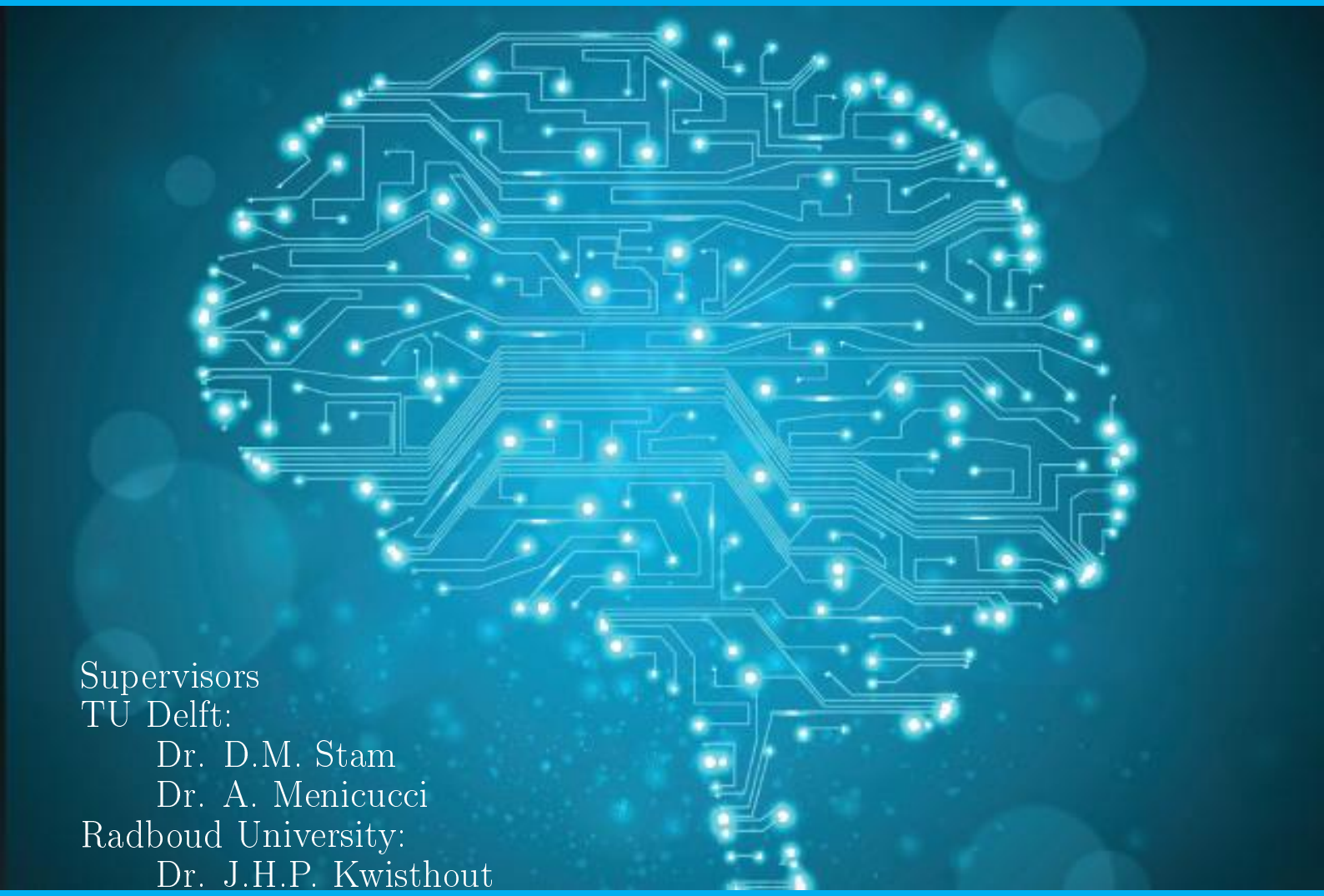
Leveraging brain adaptation to increase radiation resistance of neuromorphic space hardware

AE5810 Thesis Project: baseline report

Akke Toeter



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AE5810 Thesis Project: baseline report

by

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Systems Engineering component of the AE5810 Thesis
at the Delft University of Technology.

Student number: 1507958
Project Planning duration: September 20, 2020 – September 24, 2021
Version: 0.1

TU Delft

Contents

Chapter 1

Introduction

This document presents the baseline for the AE5810 Thesis Project of the Space Flight Master at the Faculty of Aerospace Engineering of Delft University of Technology and the SOW-MKI92 Research Project of the Master in Artificial Intelligence at the faculty of Social Sciences of Radboud University. Its purpose is to identify the 2-5 most feasible design options that can be used to determine whether the principle of brain adaptation can be leveraged in neuromorphic space hardware.

The baseline report presents the Functional Flow Diagram (FFD) and Functional Break-down Diagram (FBD) in chapter 2 and chapter 3 respectively. These function descriptions of the system that is to be designed, is then used to generate the Requirements Discovery Tree (RDT) in chapter 4. Next, the resource allocation and budget breakdown presented in chapter 5. This is followed by the technical risk assessment in chapter 6. From the RDT, the Design Options Structuring Tree (DOT) is generated in chapter 4. Contingency management is applied in chapter 8. A market analysis is presented in chapter 9, and the sustainable development strategy is presented in chapter 10. To ensure this work is performed with sufficient quality, the reporting and quality control is presented in chapter 11. The baseline is concluded in chapter 12.

Chapter 2

Functional Flow Diagram

To gain insight in the system that is to be designed, a Functional Flow Diagram is generated. This FFD presents the high level functions that the system should be able to perform, in chronological order. These functions are presented in the flow diagram of fig. 2.1.

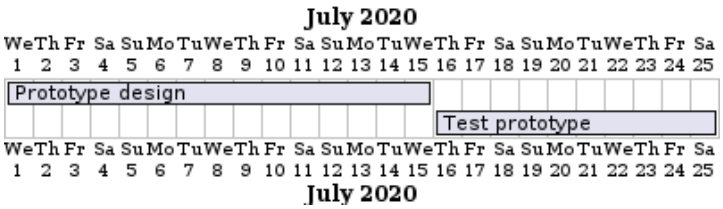

Figure 2.1: A functional flow diagram with the high-level functions of the system that is to be designed.

1. Initialise & start space-related function on neuromorphic architecture without brain adaptation implementation.
2. Initialise & start space-related function on neuromorphic architecture with brain adaptation implementation.
3. Endure modelled space radiation on neuromorphic architecture.
4. Measure space-related function performance without brain adaptation implementation.
5. Measure space-related function performance with brain adaptation implementation.
6. Report any performance difference between with- and without brain adaptation.
7. Determine significance of difference.

From these high level functions, a more detailed functional breakdown diagram is generated.

2.1. Detailed Functional Flow Diagram

1. Initialise & start space-related function on neuromorphic architecture without brain adaptation implementation.
   (a) Boot/initialise neuromorphic architecture
   (b) Load function.
   (c) Load function data.
2. Initialise & start space-related function on neuromorphic architecture with brain adaptation implementation.
   (a) Boot/initialise neuromorphic architecture

   (b) Load brain adaptation.
   (c) Load space-related function.
   (d) Load space-related function data.

3. Render and endure modelled space radiation on neuromorphic architecture.
   (a) Determine simulated space radiation pattern of neuromorphic space architecture.
   (b) Expose neuromorphic architecture to simulated space radiation pattern.
   (c) Complete space-related function.
   (d) Return results of space-related performance.

4. Measure space-related function performance without brain adaptation implementation.
   (a) Retrieve space-related function output.
   (b) Convert space-related function output to score.

5. Measure space-related function performance with brain adaptation implementation.

6. (a) Retrieve space-related function output.
   (b) Convert space-related function output to score.

7. Report difference between the scores of the architectures with- and without brain adaptation.

8. Determine significance of difference.

2.1.1. Description

1. Initialise & start space-related function on neuromorphic architecture without brain adaptation implementation.
   - To run a function on the neuromorphic architecture, it will have to be booted and initialised. The function that will be ran on the neuromorphic architecture is space related, to increase the level of representativeness of this study, in terms of space applications. The initalisation allows for loading the space related function that is to be executed. Additionally, the space related function may require (training) data on which it is ran. For example, a Martian rover that has a function that identifies rocks in its environment, may be partially simulated by loading a (labelled) dataset of Martian images.

     To run the function without brain adaptation implementation, allows for the creation of a baseline to which the brain adaptation performance can be compared.

2. Initialise & start space-related function on neuromorphic architecture with brain adaptation implementation.
   - The intialisation of the brain adaptation implementation can occur, before, during or after the loading of the space related function. Which of these options is selected depends on the more detailed design process.

3. Endure modelled space radiation on neuromorphic architecture.
   - The radiation robustness of the neuromorphic architecture can be tested by exposing the neuromorphic architecture to the radiation that it would experience in a space application. To determine what this radiation is, a relevant space mission and space function are selected. The time, position and orientation of the spacecraft in such a mission is then used to derive the radiation pattern to which the radiation may be exposed. This radiation is pattern is then used to determine to which (simulated) radiation the neuromorphic architecture will be exposed.

4. Measure space-related function performance without brain adaptation implementation.

- The performance of the space related function without brain adaptation implementaiton on the neuromorphic architecture is measured before, during and/or after radiation exposure. Which of these measuring moments are used, is still to be determined by the detailed design process. This measurement then serves as a comparison baseline to put the impact of the brain adaptation implementation into context.

5. Measure space-related function performance with brain adaptation implementation.

6. - Once a baseline for comparison is established, the space related function can be ran again on the neuromorphic hardware, whilst being exposed to radiation. In this second setting, the brain adaptation implementation is used in an attempt to increase the radiation robustness of the neuromorphic architecture. The performance of the space related function is then measured before, during and/or after radiation exposure. Which of these measuring moments are used, is still to be determined by the detailed design process.

7. Report any performance difference between with- and without brain adaptation.

8. - If any performance difference is observed between the space related function with- and without brain adaptation implementation, it will be computed and stored.

9. Determine significance of difference.

10. - An analysis is performed to determine the level of significance of any observed difference.

Chapter 3

Functional Breakdown Diagram

This section presents the functional breakdown diagram of the system that is designed in this thesis project. This FBD is generated using the detailed functional flow diagram of section 2.1. The FBD presents the activities in an hierarchical style.

1. Run space-related function on neuromorphic architecture.
    (a) Initialise neuromorphic architecture.
    (b) Optional: Load brain adaptation implementation.
    (c) Load space related function.
    (d) Load space related function data.
    (e) Run space related function.
    (f) Complete running space related function.
    (g) Retrieve space related function outputs.
    (h) Convert space related function outputs to performance score.
    (i) Report difference between the scores of the architectures with- and without brain adaptation.
    (j) Determine significance of difference.
2. Render and endure modelled space radiation on neuromorphic architecture.
    (a) Generate simulated space radiation pattern of neuromorphic space architecture.
    (b) Expose neuromorphic architecture to simulated space radiation pattern.
    (c) Optional: model architecture-radiation interaction.
    (d) Optional: measure architecture-radiation interaction.

Chapter 4

Requirements Discovery Tree

This section presents an overview of the requirements that are identified within this thesis project. Its purpose consists of listing the requirements that drive the design, identifying killer requirements and presenting an overview of the project requirements. section 4.1 contains the mission need statement of this project. Next, the stakeholder requirements are identified in section 4.2. The top level requirements are presented in section 4.3, and the key requirements are presented in section 4.4. From these combined requirements, the Requirements Discovery Tree is drafted in section 4.5.

4.1. Mission Need Statement

The mission need statement is generated in the project plan phase, and is included in this section again to provide the context of the requirement derivation process. The MSN is:

Increase the radiation robustness of neuromorphic space hardware by leveraging the principle of brain adaptation in neuromorphic hardware.

4.2. Stakeholder Requirements

The following stakeholder requirements are identified:

- **STKH-UNI-01** - The research that is performed shall be reproducible.
- **STKH-UNI-02** - The sustainability of the design concepts shall be taken into account in the design trade-off process.
- **STKH-SPACEBRAINS-01** - Achieve results towards the REACH research by Q2 2022.
- **STKH-SPACEBRAINS-01-a** - Achieve results towards the REACH research by either: 2022-03-01, 2022-04-07, 2022-07-01.
- **STKH-ICONS-01** - Submit paper documenting research results before April 15th, 2022.
- **STKH-ICONS-01-a** - Submit full paper of 6-8 pages, presenting original research, or submit short paper of 3-4 pages that has preliminary results.
- **STKH-ICONS-02** - Upon acceptance for a presentation, submit presentation before July 27th, 2022.
- **STKH-RADBOUD-01** - The research proceedings shall be documented and submitted in a format accepted by Dr. J.H.P. Kwisthout before the SOW-MKI92 Research Project is completed.
- **STKH-RADBOUD-02** - The research for the SOW-MKI92 Research Project shall be performed using at least 28 EC of work.
- **STKH-Delft-01** - The research proceedings shall be documented and submitted in a format accepted by Dr. D.M. Stam and Dr. A. Menicucci before the AE5810 Thesis Project is completed.
- **STKH-Delft-02** - The research for the AE5810 Thesis Project shall be performed using at least 42 EC of work.

4.3. Top Level Requirements

The following requirements for this thesis project are identified:

- **TECH-01** - Technology Readiness Level (TRL) of the used technology shall be at least TRL 4 [-].
- **TEST-01** - Radiation robustness shall be tested in terms of algorithmic performance.
- **TEST-02** - The radiation tests shall be technically and economically feasible.
- **TEST-03** - The radiation tests results shall be generated before September 2022.
- **SUS-01** - Sustainability management shall be integrated in each phase of this project.
- **SUS-02** - Sustainability shall be assessed in the design trade-off process.
- **SAF-01** - All participants involved in testing, integrating and operations shall not be exposed to serious danger.
- **ESA-01** - Throughout this project quarterly reports shall be provided to the SpaceBrains foundation and the European Space Agency (ESA).

4.4. Identification Key Requirements

The key requirements are requirements that can render the project infeasible, requirements that drive the design, and/or requirements that induce high risk to project success. Within this thesis project, the following key requirements are identified:

1. **STKH-SPACEBRAINS-01** - Achieve results towards the REACH research by Q2 2022.
2. **TECH-01** - Technology Readiness Level (TRL) of the used technology shall be at least TRL 4 [-].
3. **TEST-03** - The radiation tests results shall be generated before September 2022.

The **STKH-SPACEBRAINS-01** requirement significantly drives the design space, as physical radiation tests are not deemed feasible within the given timeframe and project constraints. **TEST-03** implies a strict time constraint that induces significant risk to project success as it limits the amount of time available for development and scheduling. **TECH-01** significantly drives the design as it limits the design space to concepts that rely only on technologies of TRL 4 and higher.

4.5. Requirements Discovery Tree

A requirements' discovery tree is currently omitted due to time constraints.

Chapter 5

Resource Allocation and Budget Breakdown

Chapter 6

Technical Risk assessment

Chapter 7

Design Option Structuring Tree

The purpose of the design option tree is to find a feasible design option that can satisfy the requirements. This selection process can be an iterative process in case no feasible design option is found in the initial design option tree. By positioning the design options in a tree format, their hierarchical structure becomes visible. The tree is structured such that the most impactful decisions are selected at the top of the tree, whereas more detailed decisions are made at lower levels of the tree. Several subsections are created to Figure 7.1 presents the design option tree for this thesis. The nodes in the tree represent design options and child leafs represent design options within a parent design option. For example, a choice may be made to use digital neuromorphic hardware, and within that design space, a particular chip may be selected. Multiple (parallel) design options may be selected. For example, a softwarematic and hardwarematic implementation may be chosen.

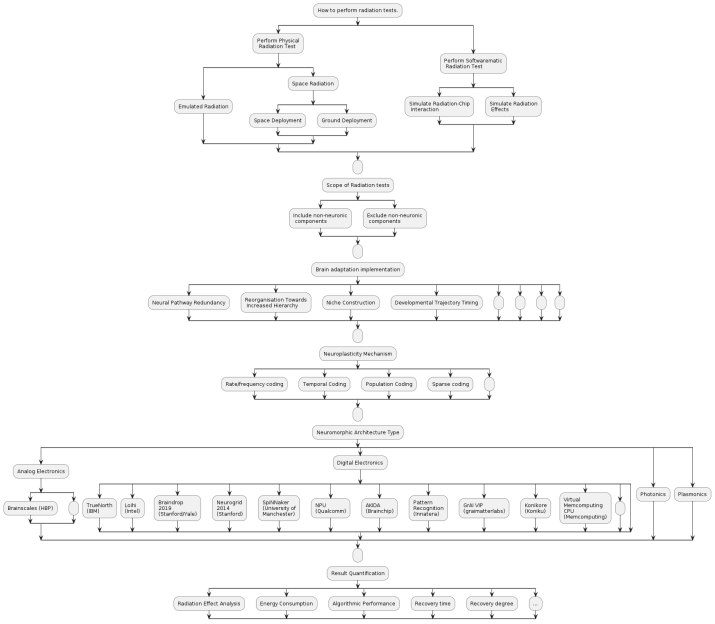

Figure 7.1: A functional flow diagram with the high-level functions of the system that is to be designed.

7.1. Pruning

With the design option tree generated, it can be pruned of options that are considered infeasible. This is done using the knowledge base that was generated in the literature study and using the requirements identified in chapter 4. The pruning process will be performed from top to bottom, which matches from high- to low hierarchical design choices.

7.1.1. Tested Functionality

At the time of writing, no specific function that is used for testing, can be eliminated.

7.1.2. How To Perform Radiation Tests

1. Starting with key requirement: **STKH-SPACEBRAINS-01** and **STKH-ICONS-01**, it is possible to eliminate the physical radiation test design option(along with its children), before the ICONS deadline of April 15th, 2022. Given the full scope of the thesis, and the **TEST-03** requirement which implies a test deadline before September 2022, a physical radiation test is still considered feasible before that time. Hence, instead of a complete termination (red), it is turned orange.

2. Continuing with the **STKH-SPACEBRAINS-01** requirement, it is considered infeasible to do a full simulation of radiation effects on the hardware components, before the ICONS deadline of April 15th, 2022. Hence, also this option will be coloured orange. Most neuromorphic chips in the DOT are proprietary, with many of the chip designs not being publically available. Since that makes it difficult to determine what the radiation effects will be on the hardware components of the chip, and how those effects, such as single-event upsets, would propagate towards influencing neurons and/or synapses. Therefore, this option is not considered feasible before April 15th 2022. It may be possible to contact manufacturers to ask how the radiation influences the neuronal- and synaptic properties. If such research is performed, it may be applied to simulate the neuromorphic hardware-radiation interaction softwarematically with sufficient accuracy to produce meaningful results.

7.1.3. Scope Of Radiation Tests

1. For the same as the last enumerated point of section 7.1.2, including the non-neuromorphic components is not considered feasible for before the ICONS deadline of April 15th, 2022. Hence, this element is also coloured orange.

7.1.4. Brain Adaptation Implementation

At the time of writing, no brain adaptation mechanisms can be eliminated.

7.1.5. Neuroplasticity Mechanism

At the time of writing, no neuroplasticitiy mechanisms can be eliminated.

7.1.6. Neuromorphic Architecture

1. Some neuromorphic architectures can be eliminated based on logistical reasons. Currently, the only direct access within this thesis project is to the Loihi. Furthermore, it may be expected that access to Pattern Recognition Chip by Innaterra may be realised after the ICONS deadline. Similarly, the Spinnaker device may become available later-on in the project. An economic feasibility assessment needs to be made on whether they should be used in physical radiation testing or not.

2. Some of the neuromorphic chip manufacturers have been contacted in the past, these contacts may allow for access to their respective chips for physical radation testing, if the intermediate results at ICONS are promising. Hence, they are kept orange.

7.1.7. Result Quantification

1. Since physical radiation testing is not deemed possible, and since the hardware diagrams of the respective neuromorphic architectures are not available, it is not deemed feasible to include a radiation effect analysis before ICONS. Therefore, this option is turned orange.

7.1.8. Pruned Design Option Tree

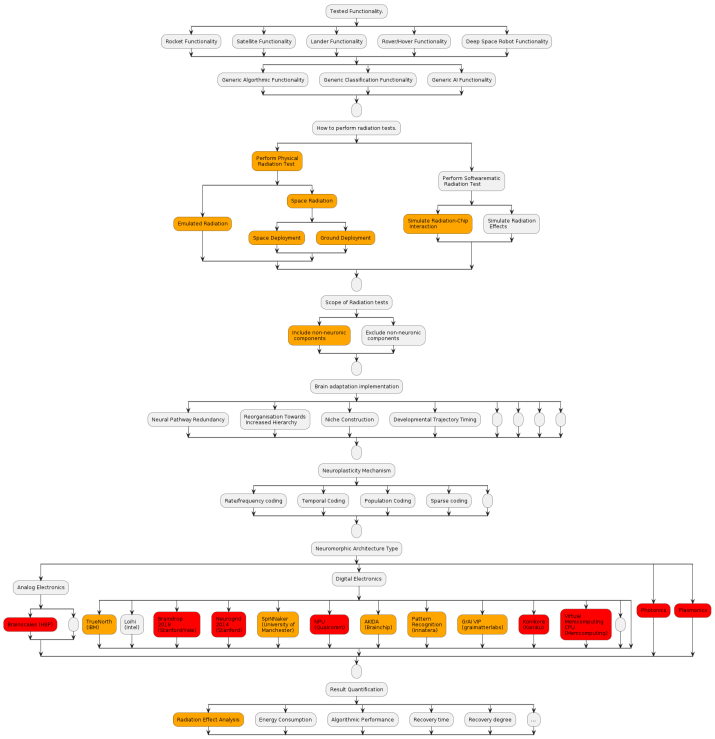

Figure 7.2: A functional flow diagram with the high-level functions of the system that is to be designed.

7.2. Preliminary Design Option Selection

With some design options eliminated, an analysis can be performed to see whether some design options are considered to be more feasible than others. This analysis is started by taking the **STKH-SPACEBRAINS-01** requirement into account, which implies results need to be produced by April 15th. This allows for expressing some design option preferences as listed in section 7.2.1 to section 7.2.7. Taking the full scope of the thesis project into account allows identification of design options that seem most feasible for a follow-up with physical radiation testing.

7.2.1. Tested Functionality Preference

For performing a generic algorithmic functionality for the following two reasons:

1. No additional dependencies such as datasets, panda packages, tensorflow etc. is required. This lowers the probability of allocating time on work that does not directly support the objective of this thesis project.
2. No preprocessing work, such as loading and/or cropping images etc., is required. This increases the amount of time that can be allocated to implementing the brain adaptation and testing its functionality.
3. Graph algorithms are typically used in space applications [**?**].
4. Thorough testing can quickly be set up for graph algorithms.

7.2.2. How To Perform Radiation Tests Preference

Using softwarematic radiation tests that simulate radiation effects.

7.2.3. Scope of Radiation Tests Preference

Excluding non-neural components from radiation effects allows for a complete focus using the expected radiation effects on the neural and synaptic properties, without having to model additional radiation interactions with (other) hardware elements. This increases the feasibility of producing results by April 15th.

7.2.4. Brain Adaptation Implementation Preference
No preference in brain adaptation implementations is expressed at the time of writing.

7.2.5. Neuroplasticity Mechanism Preference
No preference in neuroplasticity mechanisms is expressed at the time of writing.

7.2.6. Neuromorphic Architecture Type Preference
Based on availability and previous experience, a preference is expressed for the Loihi platform for the softwarematic simulation. Insights from this simulation will be used to determine the best way forward towards hardware simulations. Since the Innatera chips are expected to be available for testing in the second quarter of 2022, combined with there relatively low cost, this option is mentioned as a possible suitable candidate for physical radiation testing. The Spinnaker boards appear to have a higher unit cost.

7.2.7. Result Quantification Preference
Based on the vicinity of the ICONS deadline of April 15th, 2022, a preference is expressed for measuring the softwarematic results in terms of algorithmic performance. This is because it forms the most direct measurement that can be used to determine whether the principle of brain adaptation is indeed able to increase the radiation robustness of neuromorphic space hardware.

7.2.8. Preliminary Design Option Tree
The preferences expressed in section 7.2 are coloured green in the preliminary design option tree visualised in fig. 7.3.

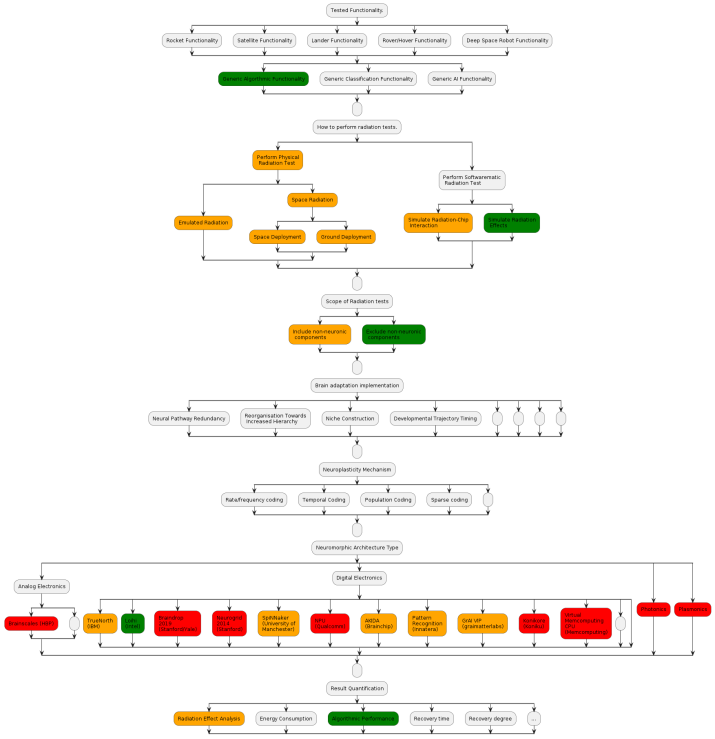

Figure 7.3: todo

7.3. Proposed Design options
To summarise, a distinction between project phases. First a softwarematic test is performed and the results are reported to the ICONS by April 15th 2022. Next, a hardwarematic test is proposed that builds upon the knowledge gained in the softwarematic tests. For the first phase, the following options are proposed to be executed during the midterm: A graph algorithm is proposed to be ran, where the radiation effects on the neuromorphic architecture are simulated as changes in the the neural and synaptic properties of the spiking

neural networks. For this first softwarematic test, the radiation effects on non-neural/traditional hardware components (such as a memory bus, CPU etc.) of the neuromorphic architecture are ignored. The Loihi platform will be used for the simulation development and radiation tests. The test results will be interpreted in terms of performance of the graph algorithm. The most feasible brain-adaptation method and neuroplasticitiy mechanism will be determined using hands-on experimentation. For the second tests, a physical radiation test is proposed. For this test, the Innatera chips are currently considered as the most feasible option. Based on the results of the softwarematic testing, a decision can be made to inlcude the traditional hardware components of the respective chips. An alternative option could be to apply extra shielding to those components, whilst saving mass on the non-shielded components though the use of brain adaptation inspired implementations.

Chapter 8

Contingency Management

Chapter 9

Market Analysis

Chapter 10

Sustainable Development Management

Chapter 11

Reporting and Quality Control

The following quality control compliance checklist can currently be generated for this project plan:

- Language Tools Grammar check applied:✔
- Language Tools Spelling check applied:✔
- CI is ran on code base used in generating this Project Plan:✗
  - Python Black Formatting Compliance:✗
  - ShellCheck Compliance:✗
  - Latex Prettier Formatting Compliance:✗
  - Python Unit Test Passing: 2 failed, 7 passed [tests]
  - Python Test Coverage %:✗[%]
  - Shell Unit Test Passing:✗[tests]
  - Shell Unit Test Coverage %:✗[%]
- Manual Quality check:
  1. Citations still have to be compiled correctly.
  2. Reproduction instructions in Appendix A should be updated.
  3. Word wrap should be applied to .uml appendices.

Chapter 12

Conclusion

Bibliography

Acronyms

ALU Arithmatic Logic Unit. 32

ANN Artificial Neural Network. 32

BIOS Basic Input/Output System. 32

BISER Built-in Soft Error Resillience. 32

BIT Binary Digit. 32

BJT Bipolar Junction Transistor. 32

CME Coronal Mass Ejection. 32

CMOS Complementary MetalâĂŞOxideâĂŞSemiconductor. 32

CPU Central Processing Unit. 21, 32

DEC Double-error Correcting Code. 32

DICE Dual Interlocked Storage Cell. 32

DNN Deep Neural Network. 32

DNU Dual-node Upset. 32

DOD Department of Defence. 32

DOT Design Options Structuring Tree. 3, 18, 32

DRAM Dynamic Random Access Memory. 32

ECC Error Correction Code. 32

ELT Enclosed Layout Transistor. 32

ESA European Space Agency. 12, 32

FBD Functional Break-down Diagram. 3, 9, 32

FET Field-Effect Transistor. 32

FFD Functional Flow Diagram. 3, 5, 32

GCD Greatest Common Divisor. 32

GCR Galactic Cosmic Ray. 32

IC Integrated Circuit. 32

INRC Intel Neuromorphic Research Community. 32

LCM Least Common Multiple. 32

LET Linear Energy Transfer. 32

MOS MetalâĂŞOxideâĂŞSemiconductor. 32

MOSFET MetalâĂŞOxideâĂŞSemiconductor Field-Effect Transistor. 32

MPNN Multilayer Perceptron Neural Network. 32

MSN Mission Need Statement. 11, 32

NVM Non-Volatile Memory. 32

POS Project Objective Statement. 32

RAM Random Access Memory. 32

RDT Requirements Discovery Tree. 3, 11, 32

ROM Read Only Memory. 32

SEC Single-error Correcting Code. 32

SEE Single-event Effects. 32

SEGR Single-event Gate Rupture. 32

SEIB Single-event Induced Burnout. 32

SEL Single-event Latch-up. 32

SER Soft-error Rate. 32

SERL Soft-error Resilient Latch. 32

SES Single-event Snapback. 32

SET Single-event Transient. 32

SEU Single-event Upset. 32

SNN Spiking Neural Network. 32

SNU Single-node Upset. 32

SPE Solar Particle Events. 32

SPENVIS Space Environment Information System. 32

SRAM Static Random Access Memory. 32

STDP Spike-Timing-Dependent Plasticity. 32

TLB Translation Lookaside Buffer. 32

TMR Tripple-mode Redundancy. 32

VLSI Very-Large-Scale Integration. 32

WBS Work Break-down Structure. 32

WFD Work Flow Diagram. 32

Glossary

(electron) holes In the context of doped semiconductors, holes are positions where electron acceptors are located, in other words, they are holes at which the electron can go.. 32

CPU cache A small hardware memory unit that is faster than the main memory and closer to the Arithmatic Logic Unit.. 32

data cache A cache that is designed to increase the speed with which data is fetched and stored.. 32

formula A mathematical expression. 32

instruction cache A cache that is designed to increase the speed with which instructions are fetched.. 32

latex Is a mark up language specially suited for scientific documents. 32

mathematics Mathematics is what mathematicians do. 32

memory cell A fundamental/basic unit in computing that is used to store information.. 32

n-type semiconductor A semiconductor that is doped with electron donors. 32

p-n junction A boundary interface of two a p-type semiconductor and a n-type semiconductor. 32

p-type semiconductor A semiconductor that is doped with electron acceptors. 32

primary memory A form of memory that is only accessible to the Central Processing Unit which reads instructions from it and executes those instructions.. 32

prompt charge The charge that is collected by means of funnelling.. 32

random-access A memory type that has access times that are independent of its physical location.. 32

unipolar transistors Unipolar transistors are transistors that use either electrons or electron holes as charge carriers and not the combination of the two.. 32

Nomenclature

c	Speed of light in a vacuum inertial frame
h	Planck constant

.1. Appendix

This is a manual appendix.

.2. Appendix Hardcoded_data.py

```
# Specify hardcoded output data.
from .latex_export_code import export_code_to_latex
from .latex_compile import compile_latex
from .plantuml_generate import generate_all_dynamic_diagrams
from .plantuml_compile import
    ↪ compile_diagrams_in_dir_relative_to_root
from .plantuml_to_tex import export_diagrams_to_latex


class Hardcoded_data:
    """ """

    def __init__(self):

        # Specify code configuration details
        # TODO: include as optional arguments.
        self.await_compilation = True
        self.verbose = True
        self.gantt_extension = ".uml"
        self.diagram_extension = ".png"

        # Filenames.
        self.main_latex_filename = "report.tex"
        self.export_data_dirname = "export_data"
        self.diagram_dir = "Diagrams"
        self.plant_uml_java_filename = "plantuml.jar"

        # Folder names.
        self.dynamic_diagram_dir = "Dynamic_diagrams"
        self.static_diagram_dir = "Static_diagrams"

        # Specify paths relative to root.
        self.path_to_export_data_from_root = f"src/{self.
            ↪ export_data_dirname}"
        self.jar_path_relative_from_root = (
            f"{self.path_to_export_data_from_root}/{self.
                ↪ plant_uml_java_filename}"
        )
        self.diagram_output_dir_relative_to_root = f"latex/
            ↪ Images/{self.diagram_dir}"

        # Path related variables
        self.relative_src_filepath_from_root = f"src/"
        self.append_export_code_to_latex = True
        self.path_to_dynamic_gantts = f"{self.
            ↪ path_to_export_data_from_root}/{self.diagram_dir
            ↪ }/{self.dynamic_diagram_dir}"
        self.path_to_static_gantts = f"{self.
            ↪ path_to_export_data_from_root}/{self.diagram_dir
            ↪ }/{self.static_diagram_dir}"
```

.3. Appendix __main__.py

```
## Entry point for this project, runs the project code and
   ↪ exports data if
# export commmands are given to the cli command that invokes
   ↪ this script.


## Import used functions.
# Project code imports.
from .create_planar_triangle_free_graph import get_graph
from .neumann import compute_mtds
from .neumann_a_t_0 import compute_mtds_a_t_0
from .arg_parser import parse_cli_args

# Export data import.
from .export_data.export_data import export_data


## Parse command line interface arguments to determine what
   ↪ this script does.
args = parse_cli_args()


## Run main code.
G = get_graph(args, False)
# compute_mtds(G)
compute_mtds_a_t_0(G)


## Run data export code if any argument is given.
if not all(arg is None for arg in [args.l, args.dd, args.sd,
   ↪ args.c2l, args.ec2l]):
    export_data(args)
```