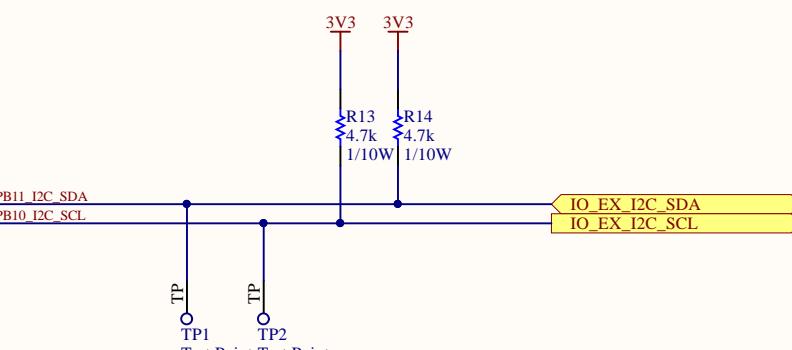
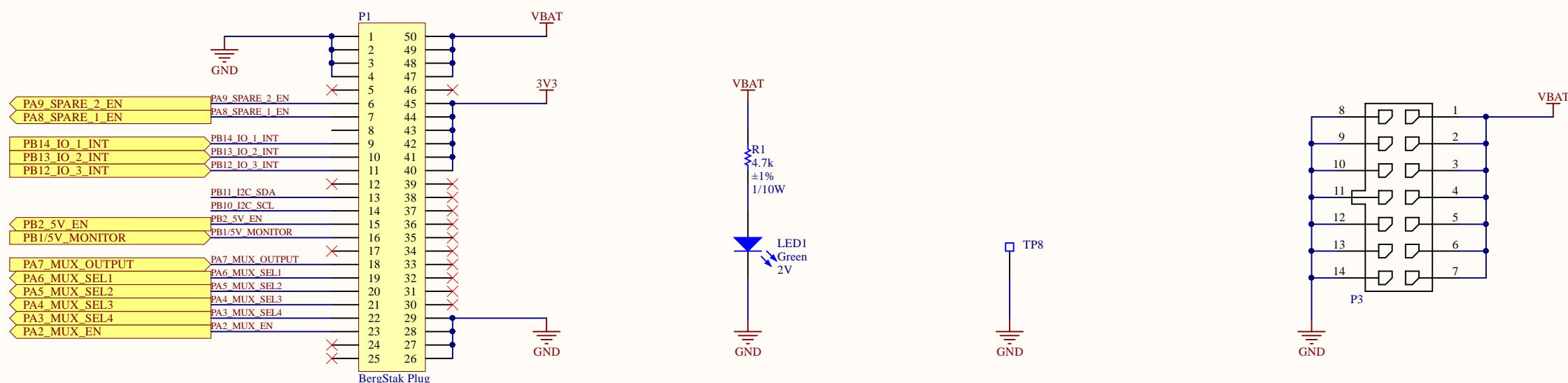
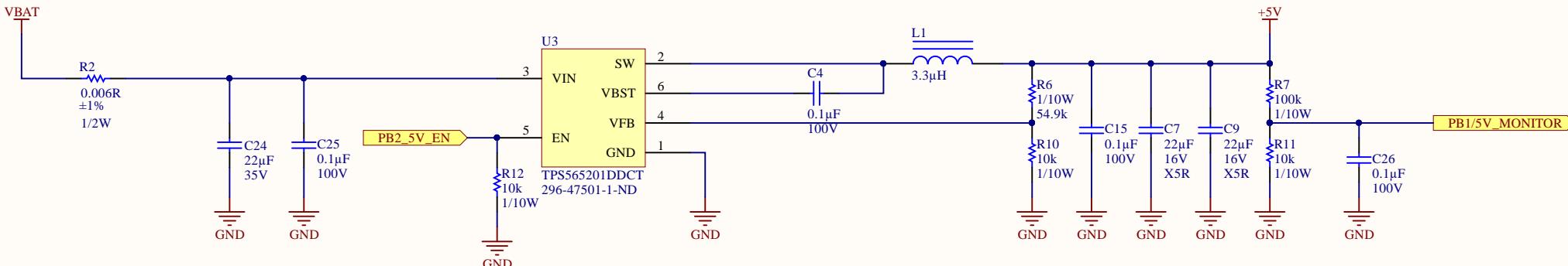
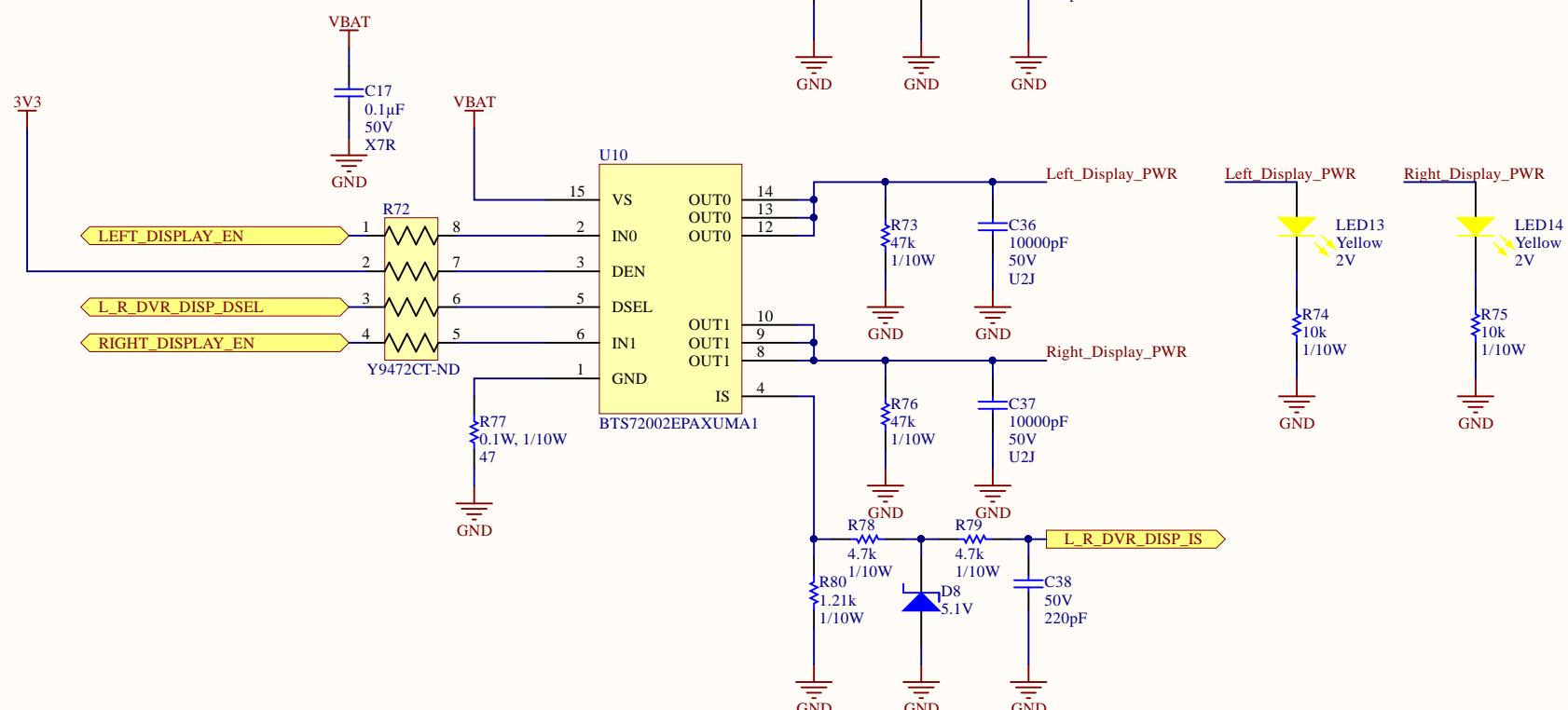
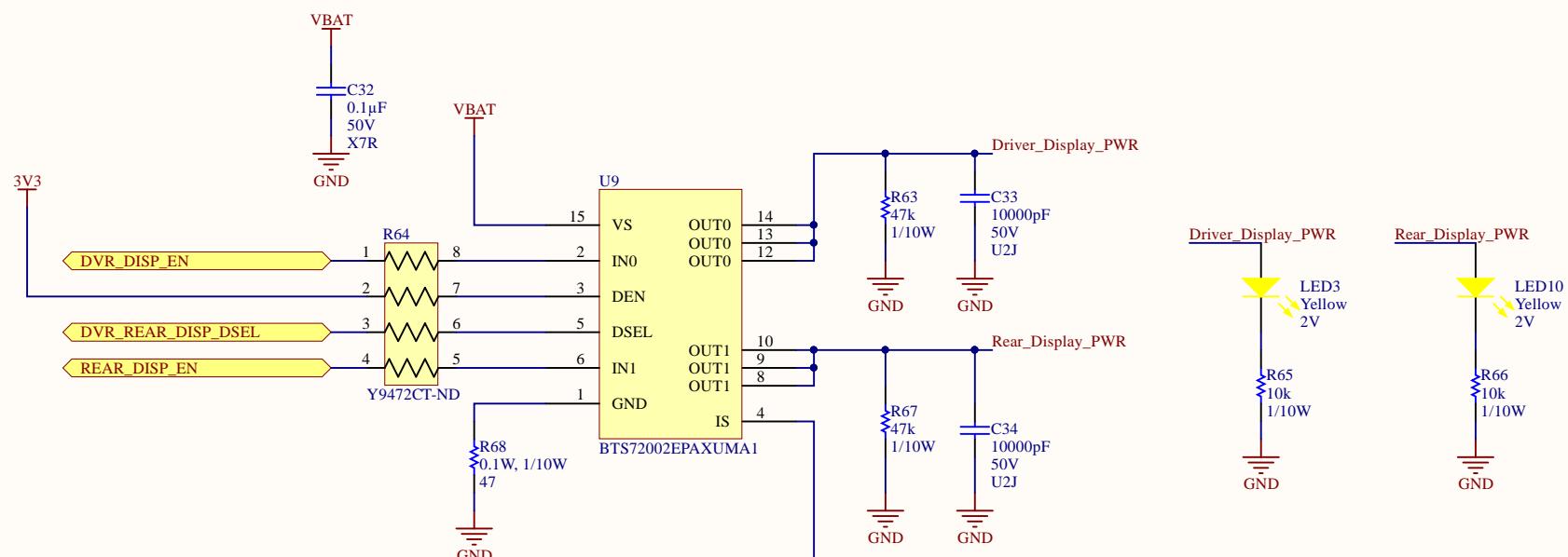


Regulator



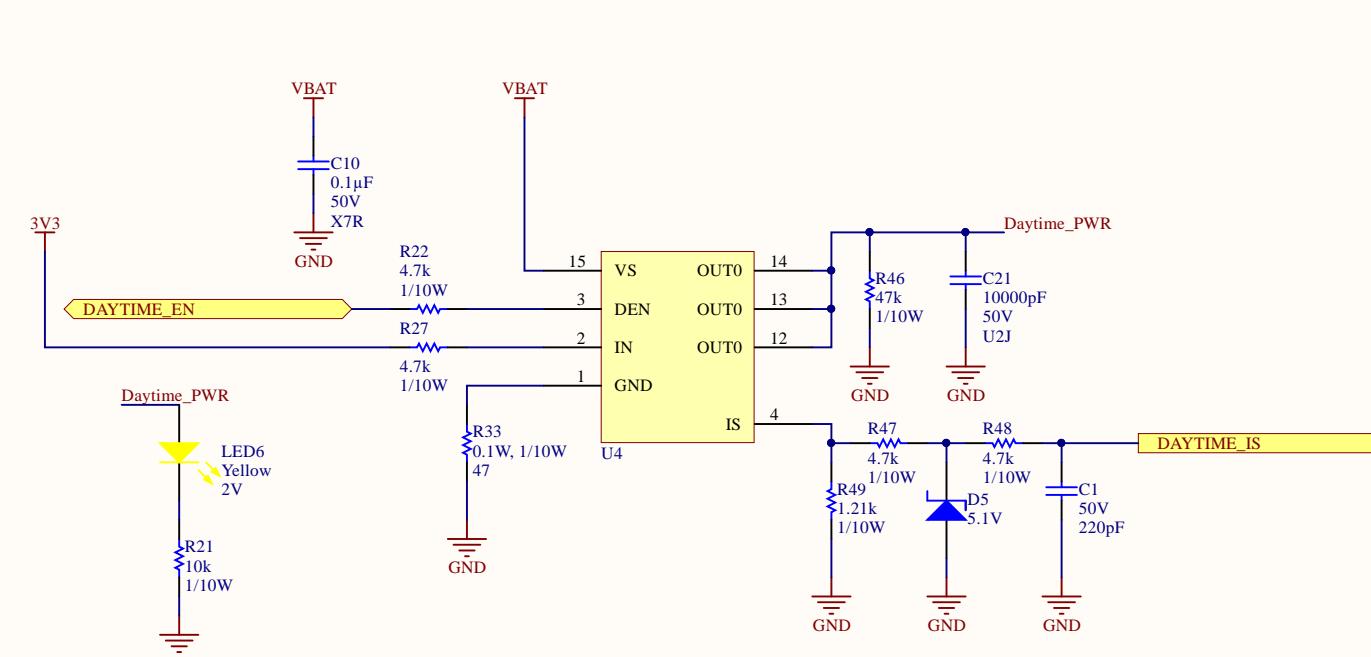
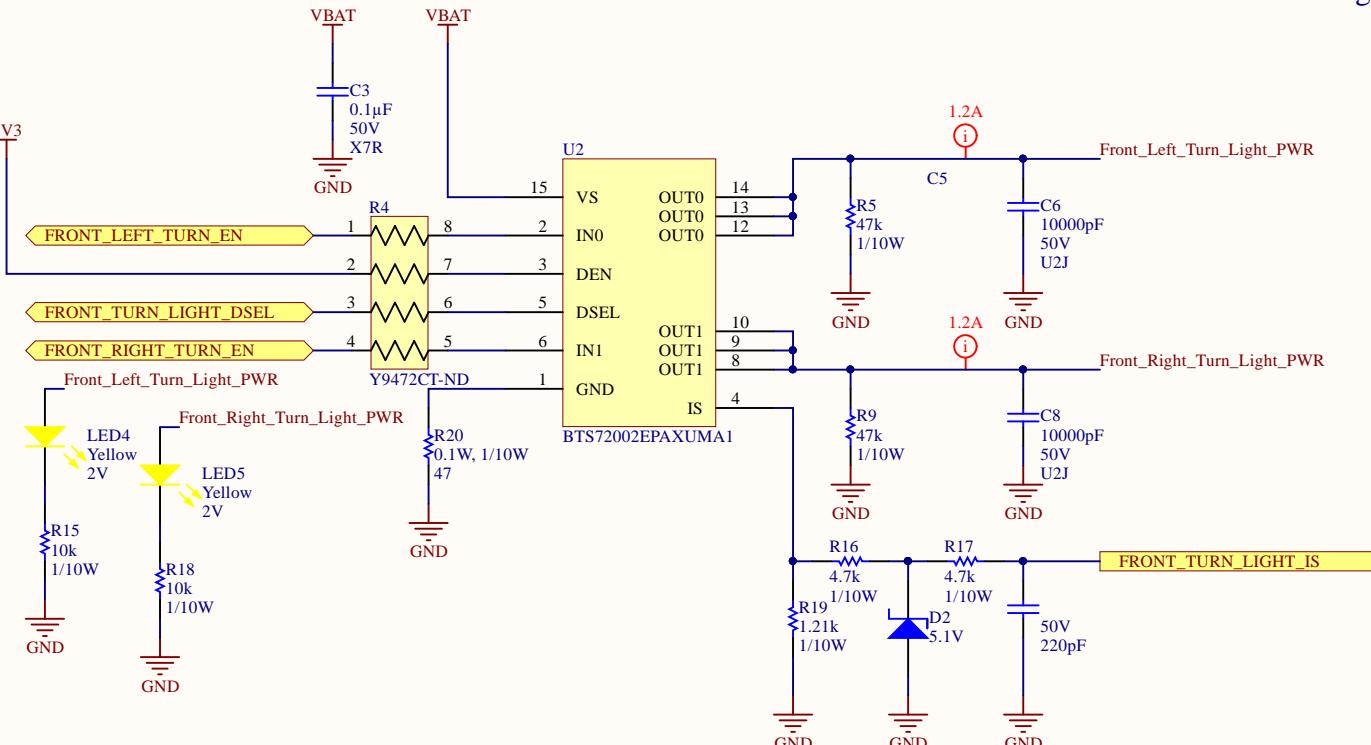
PROJECT	MSXIV_Front_Power_Distribution.PrjPcb
DOCUMENT	Controller Board Interface
PART NUMBER	VARIANT [No Variations]
DRAWN BY	REVISION
LAST MODIFIED	2020-02-09
SHEET *	OF *

A



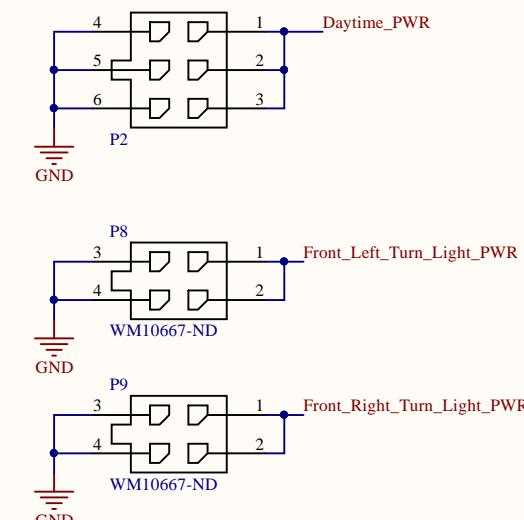
PROJECT	MSXIV_Front_Power_Distribution.PrjPcb
DOCUMENT	Title
PART NUMBER	VARIANT [No Variations]
DRAWN BY	REVISION
LAST MODIFIED	2020-02-09
SHEET *	OF *

Front light

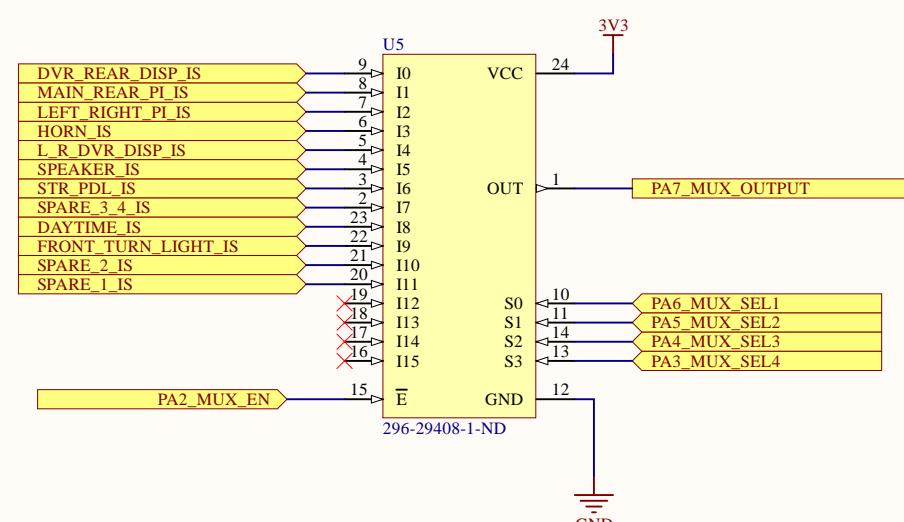
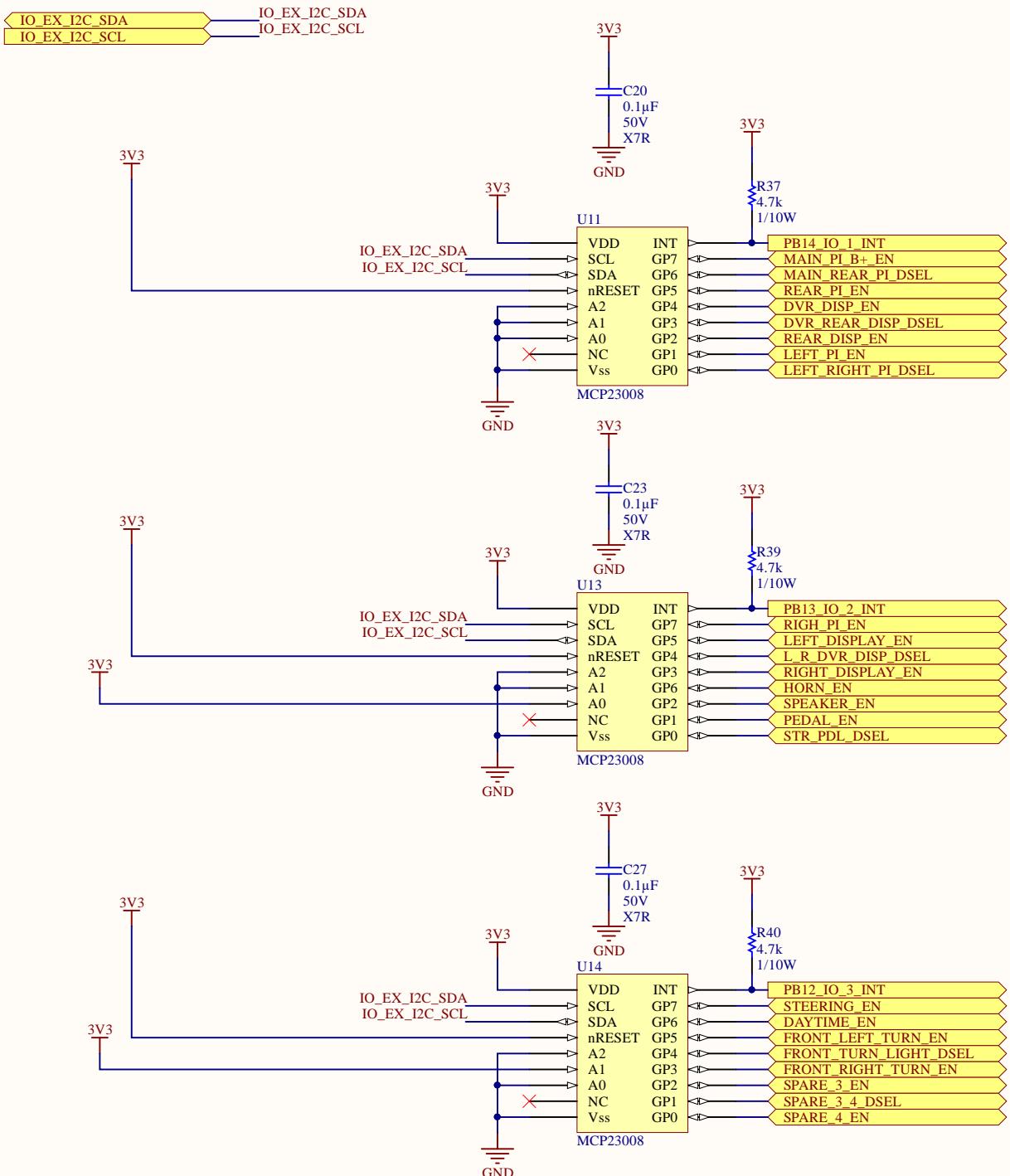


Light Load
Nominal Current: 1.2 A
Overload Current: 8.7 - 11.5 A
Based on the specs of BTS7200-2EPC

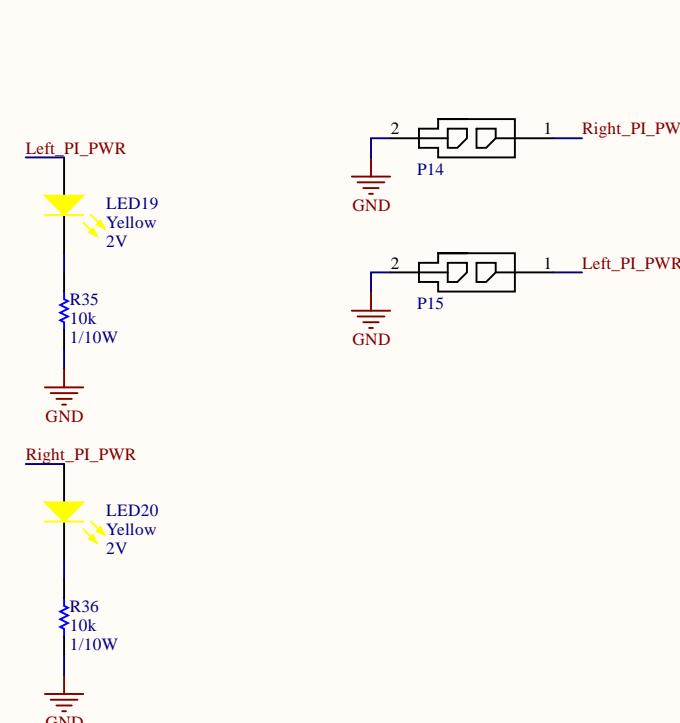
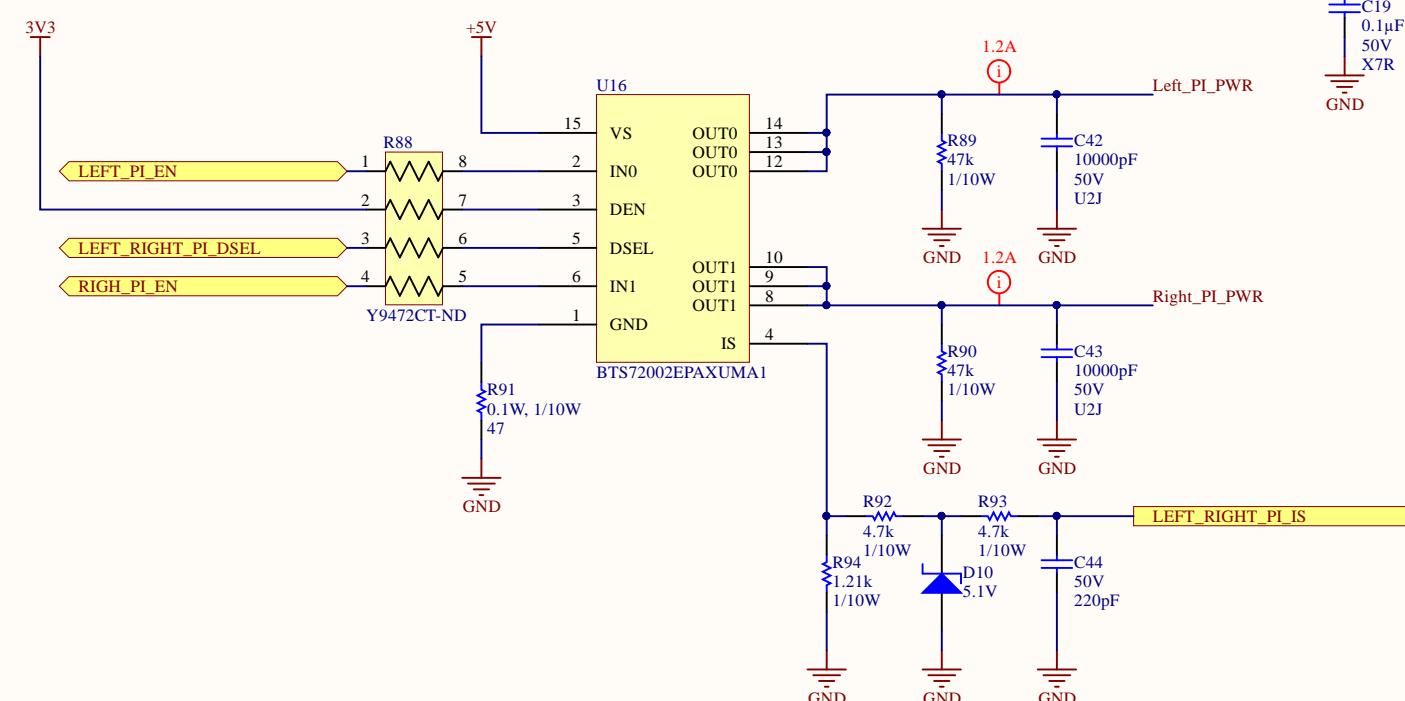
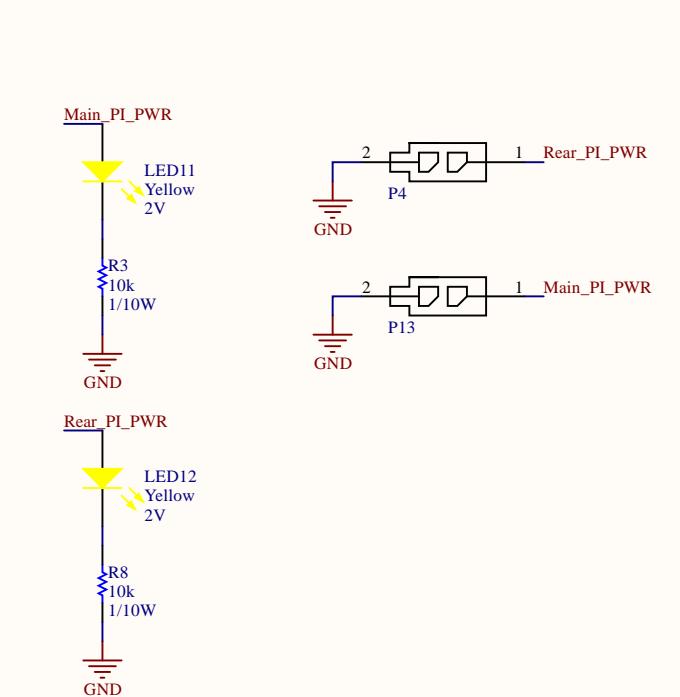
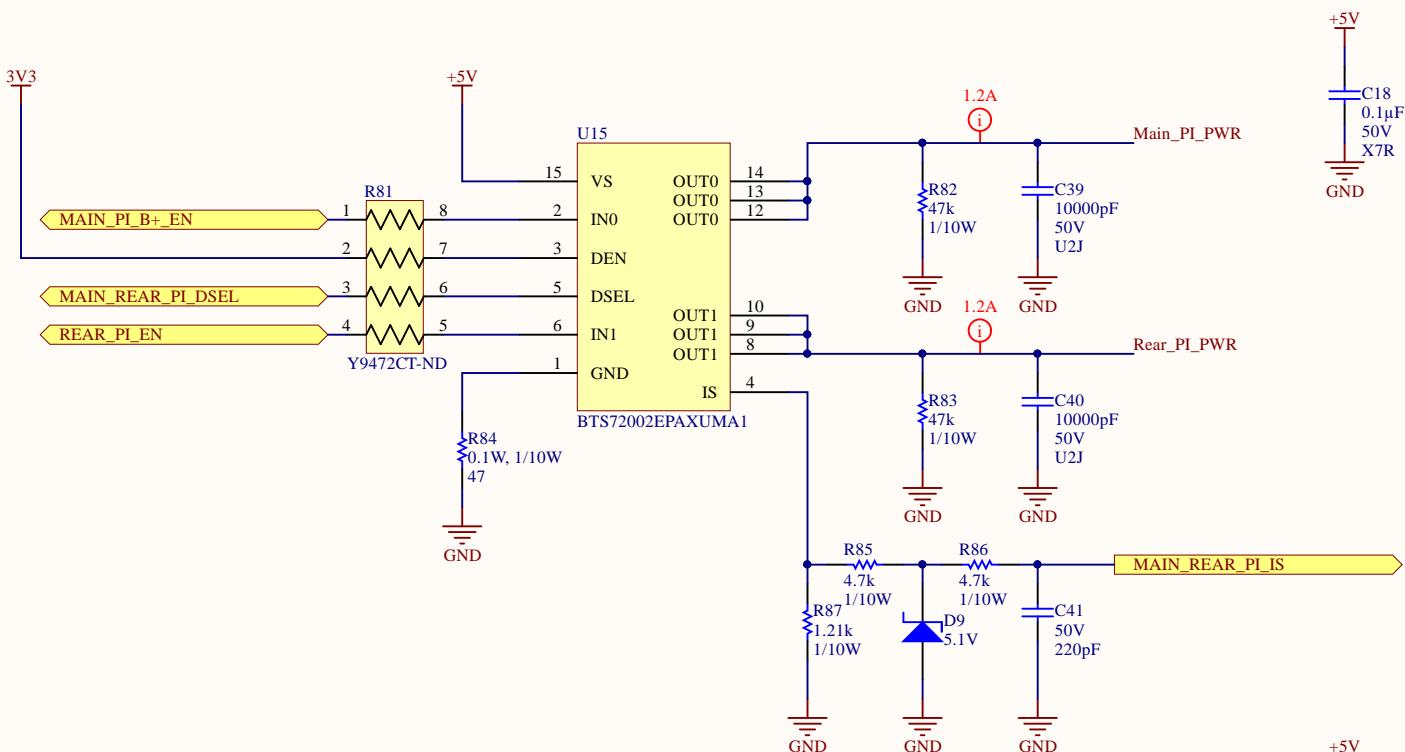
Draws 4mA when active



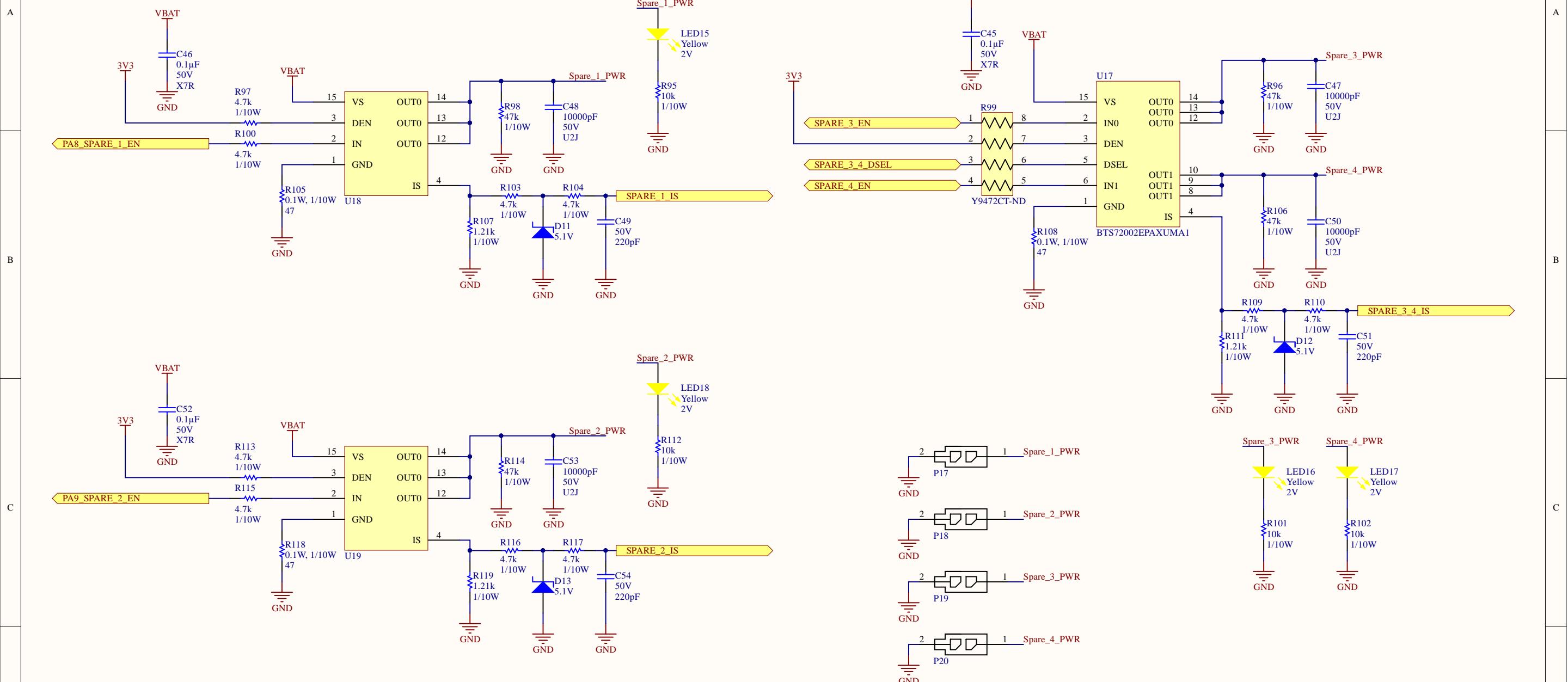
PROJECT	MSXIV_Front_Power_Distribution.PrjPcb	MIDNIGHT SUN
DOCUMENT	Title	
PART NUMBER	VARIANT [No Variations]	
DRAWN BY	REVISION	
LAST MODIFIED	2020-02-09	SHEET * OF *
hardware@uwmidsun.com		Engineering 5 - 1002 University of Waterloo (519) 888-4567 x32978



PROJECT	MSXIV_Front_Power_Distribution.PrdPcb
DOCUMENT	Title
PART NUMBER	VARIANT [No Variations]
DRAWN BY	REVISION
LAST MODIFIED	2020-02-09
SHEET *	OF *

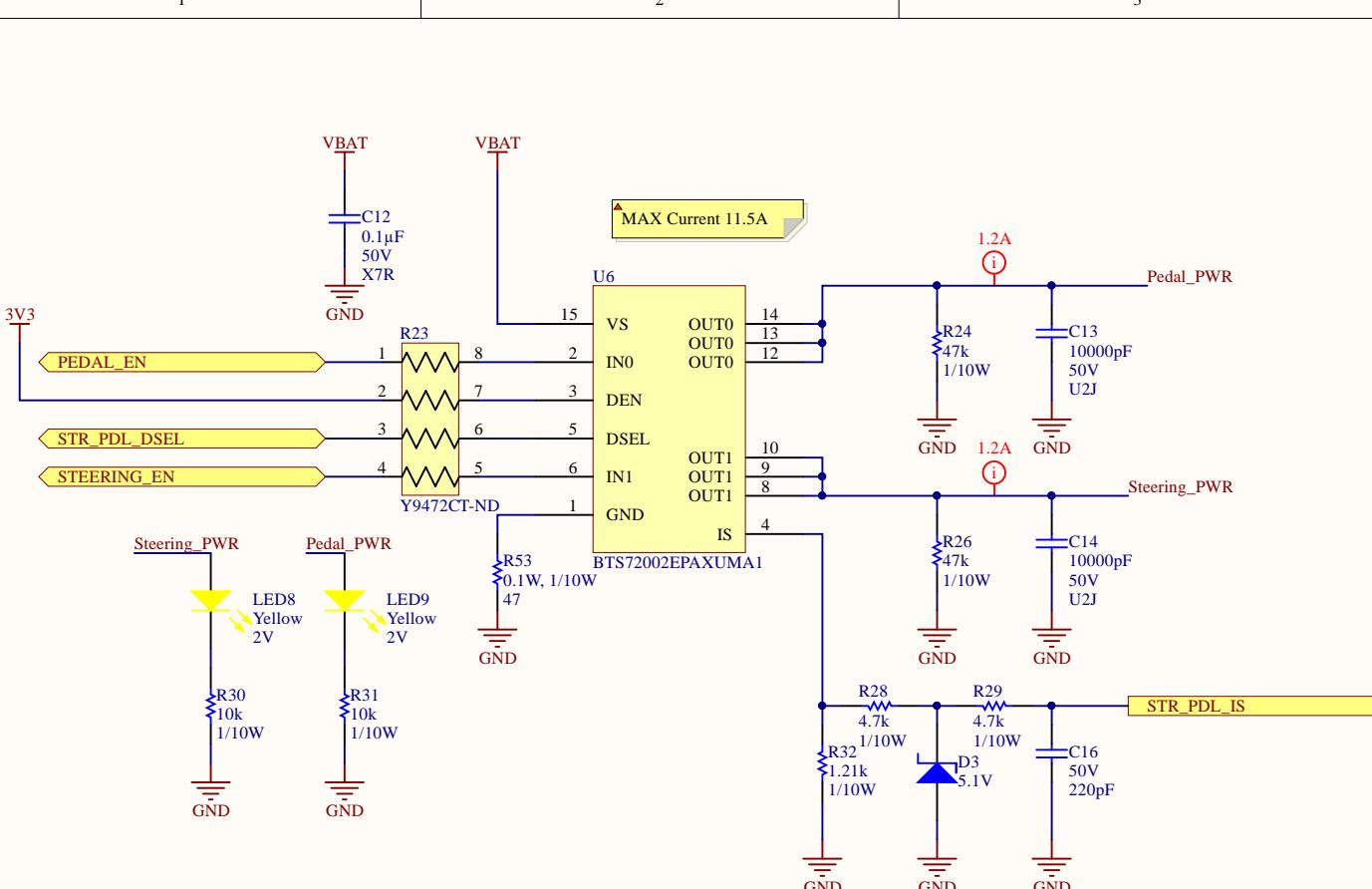


PROJECT	MSXIV_Front_Power_Distribution.PrjPcb
DOCUMENT	Title
PART NUMBER	VARIANT [No Variations]
DRAWN BY	REVISION
LAST MODIFIED	2020-02-09
SHEET *	OF *

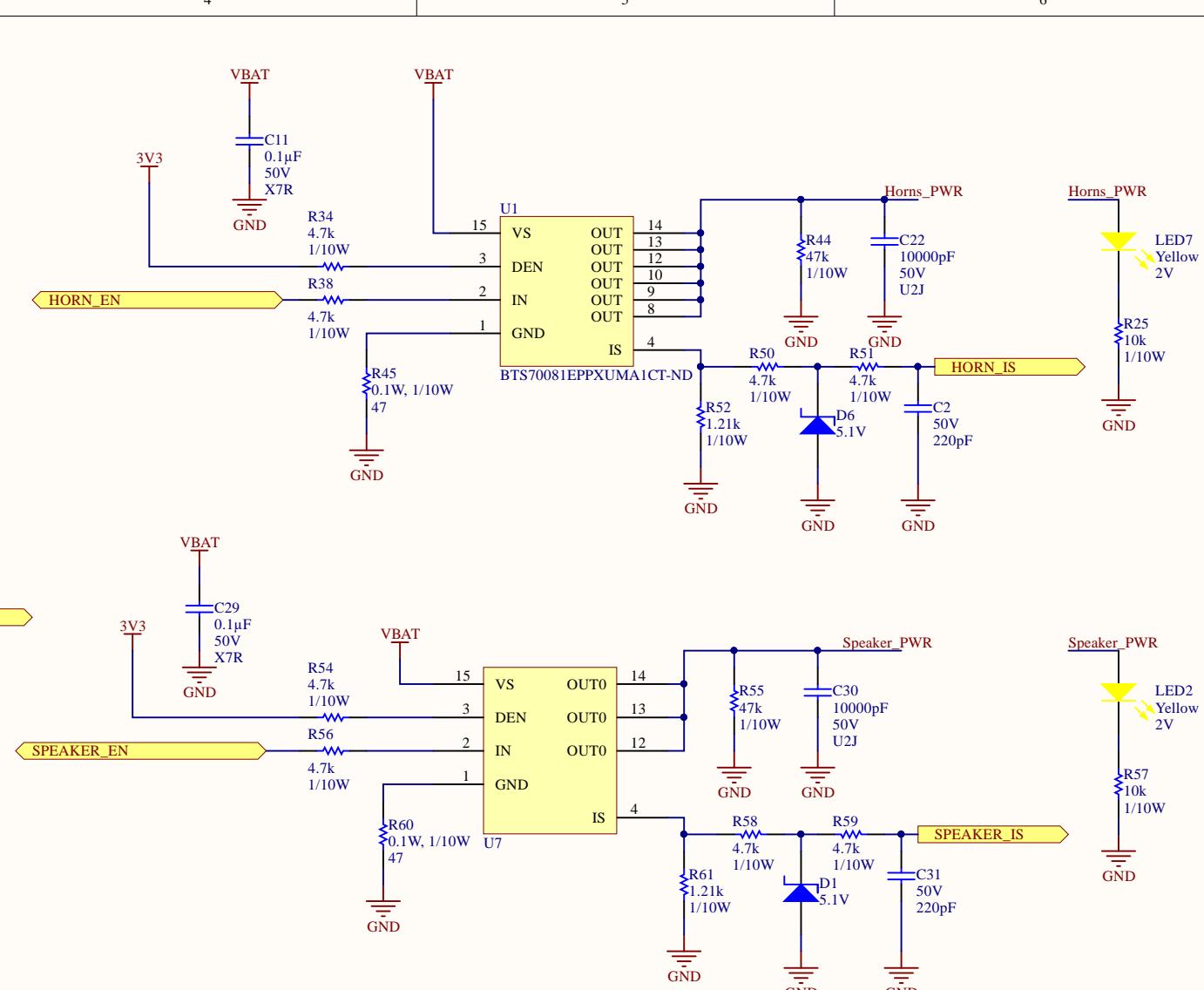


PROJECT	MSXIV_Front_Power_Distribution.PrjPcb	MIDNIGHT SUN
DOCUMENT	Title	
PART NUMBER	VARIANT [No Variations]	
DRAWN BY	REVISION	
LAST MODIFIED	2020-02-09	

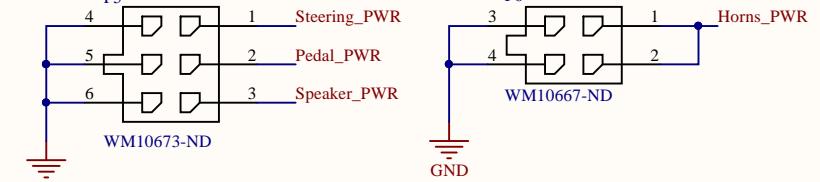
A



B



C



PROJECT MSXIV_Front_Power_Distribution.PrjPcb

DOCUMENT Title

PART NUMBER VARIANT [No Variations]

DRAWN BY REVISION

LAST MODIFIED 2020-02-09 SHEET * OF *



Engineering 5 - 1002
University of Waterloo
(519) 888-4567 x32978
hardware@uwmidsun.com

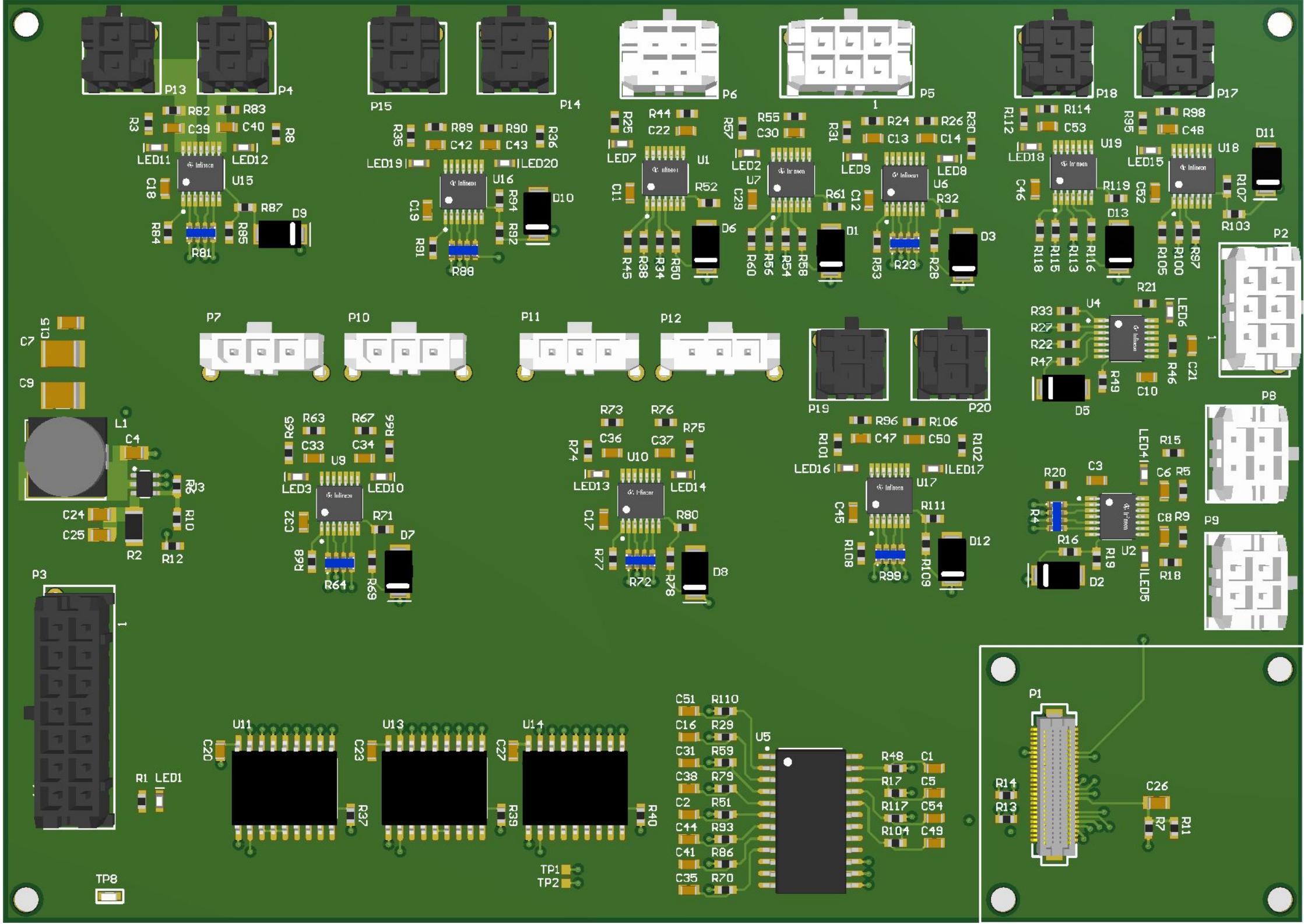
Bill of Materials

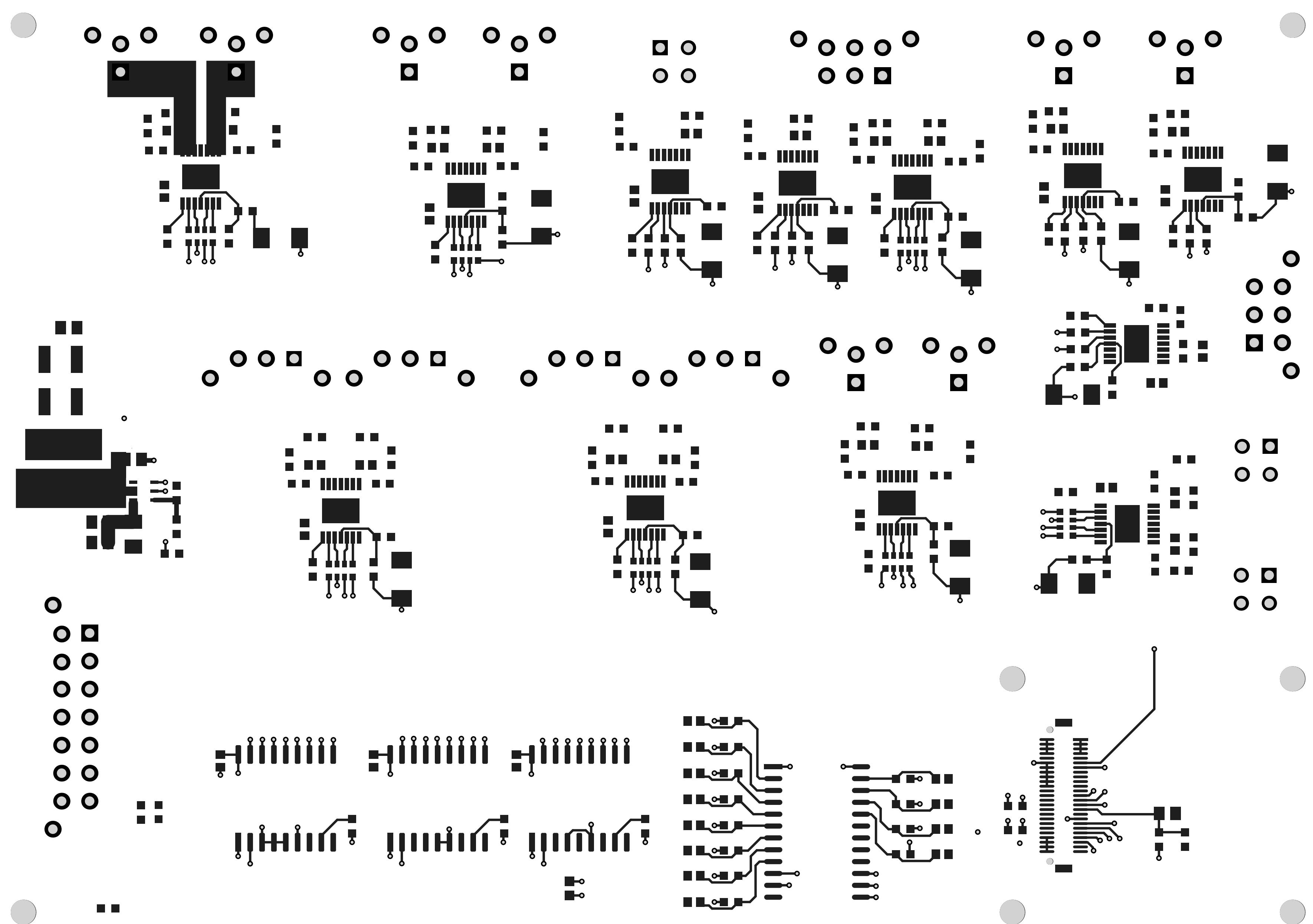
Project:	SXIV_Front_Power_Distribution.PrjPcb
Revision:	<Parameter ProjectRevision not found>
Project Lead:	<Parameter ProjectAuthor not found>
Generated On:	2020-02-09 12:59 PM
Production Quantity:	1
Currency	CAD
Total Parts Count:	238

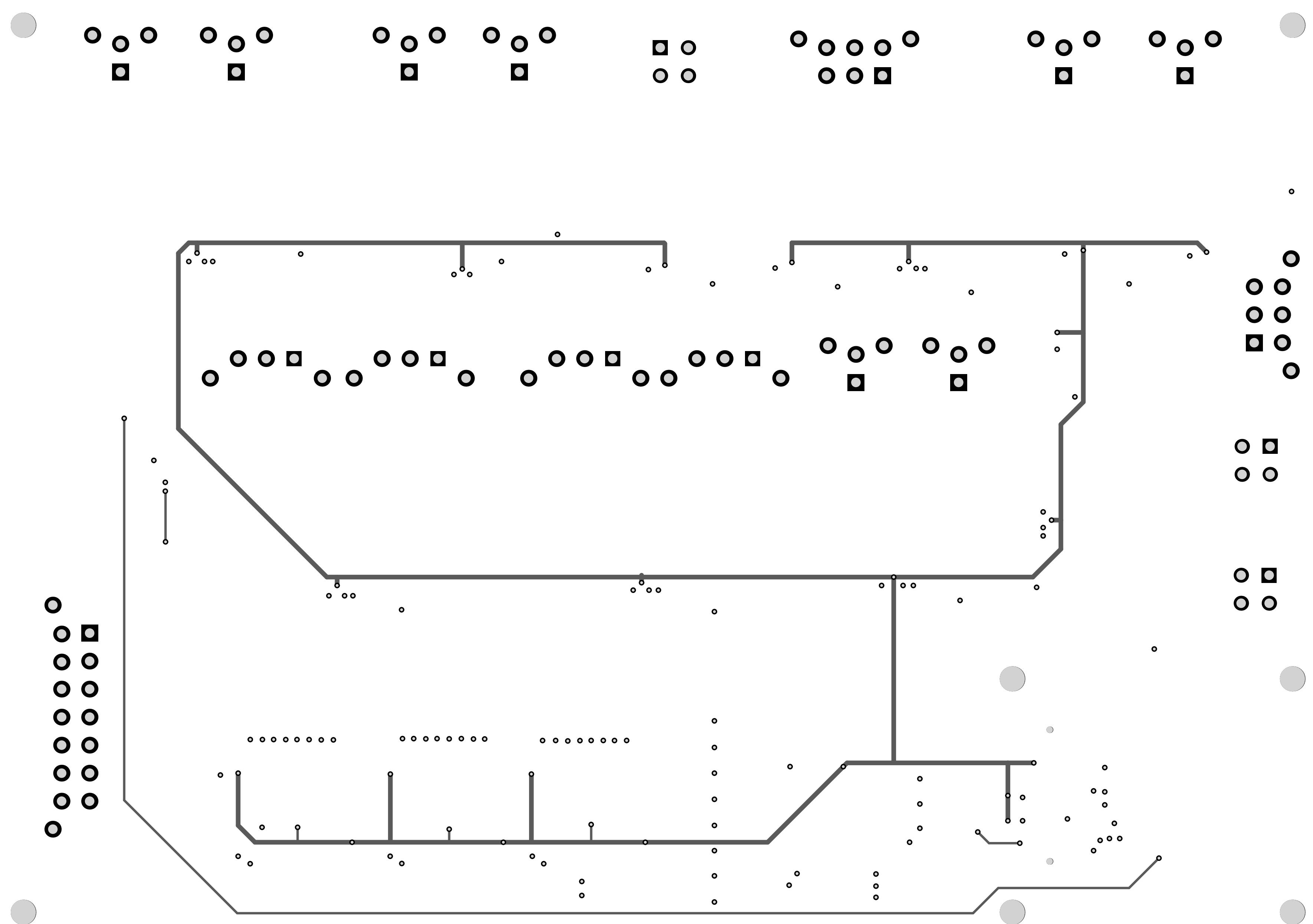
LibRef	Designator	Manufacturer 1	Manufacturer Part Number 1	Supplier 1	Supplier Part Number 1	Supplier Unit Price 1	Quantity	Supplier Subtotal 1
CAP CER 220PF 50V COG/NP0 0603	.C5,C16,C31,C35,C38,C41,C44,C49,C5	KEMET	C0603C221J5GACAUTO	Digi-Key	399-6868-1-ND	0.1171	12	\$ 1.41
CAP CER 0.1UF 50V 10% X7R 0603	I2,C17,C18,C19,C20,C23,C27,C29,C32	Kyocera AVX	0603C104KAT2A	Digi-Key	478-5052-1-ND	0.09315	15	\$ 1.40
CAP CER 0.1UF 100V 10% X7R 0805	C4,C15,C25,C26	Murata	GCM21BR2A104KA37L	Digi-Key	490-4789-1-ND	0.42582	4	\$ 1.70
CAP CER 10nF 50V 5% X7R 0603	22,C30,C33,C34,C36,C37,C39,C40,C42	KEMET	C0603C103J5ACTU	Digi-Key	399-13384-1-ND	0.29807	19	\$ 5.66
CAP CER 22uF 16V ±20% X5R 1210	C7,C9	Murata	GRM32ER61C226ME20L	Digi-Key	490-1881-1-ND	2.59	2	\$ 5.19
CAP CER 22uF 35V X5R 0805	C24	TDK	C2012X5R1V226M125AC	Digi-Key	445-14428-1-ND	1.52	1	\$ 1.52
DIODE ZENER 5.1V 500mW SOD123	D2,D3,D5,D6,D7,D8,D9,D10,D11,D12,D	ON Semiconductor	MMSZ2531BT16	Digi-Key	IMS2531BT1GO SCT-N	0.2169	12	\$ 2.60
IND 3.3uH 5.2A 20MOHM SMD	L1	TDK	VLP8040T-3R3N	Digi-Key	445-6581-1-ND		1	
LED GREEN CLEAR 2V 0603	LED1	Wurth Electronics	150060V7S7500	Digi-Key	732-4980-1-ND	0.1863	1	\$ 0.19
LED YELLOW CLEAR 2.1V 0603	LED9,LED10,LED11,LED12,LED13,LED14	Wurth Electronics	150060Y7S7500	Digi-Key	732-4981-1-ND	0.1863	19	\$ 3.54
CONN 50POS Bergstak Plug 0.02"	P1	Amphenol FCI	10132797-055100LF	Digi-Key	609-5226-1-ND	1.92	1	\$ 1.92
CONN 6POS MICRO-FIT3mm	P2,P5	Molex	0430450627	Digi-Key	WM10673-ND		2	
CONN 14POS MICRO-FIT3mm	P3	Molex	43045-1427	Digi-Key	WM10707-ND	4.34	1	\$ 4.34
CONN 2POS MICRO-FIT3mm	P4,P13,P14,P15,P17,P18,P19,P20	Molex	0430450227	Digi-Key	WM10657-ND	1.12	8	\$ 8.94
CONN 4POS MICRO-FIT3mm	P6,P8,P9	Molex	43045-0427	Digi-Key	WM10667-ND	1.78	3	\$ 5.35
CONN 3POS MICROFIT	P7,P10,P11,P12	Molex	43650-0315	Digi-Key	WM19118-ND	1.37	4	\$ 5.48
RES 4.7K OHM 1% 1/10W 0603	R51,R54,R56,R58,R59,R69,R70,R78,R	Yageo Phycomp	RC0603FR-0742KL	Digi-Key	311-4.70KHRC TND	0.03061	40	\$ 1.22
RES 0.006 OHM 1% 1/2W 1206	R2	Panasonic	ERJMP2KF6M0U	Digi-Key	P1933CT N		1	
RES 10K OHM 1% 1/10W 0603	21,R25,R30,R31,R35,R36,R57,R65,R66,	Yageo Phycomp	RC0603FR-0710K	Digi-Key	311-10.0KHRC TND	0.03061	22	\$ 0.67
RES ARRAY 4 RES 4.7K OHM 1206	R4,R23,R64,R72,R81,R88,R99	Panasonic	EXB-38V472JV	Digi-Key	Y9472CT N	0.13307	7	\$ 0.93
RES 47K OHM 1% 1/10W 0603	5,R55,R63,R67,R73,R76,R82,R83,R89,R	Panasonic	ERJ3EKF4702V	Digi-Key	P47.0KHCT N	0.07585	19	\$ 1.44
RES 54.9K OHM 1% 1/10W 0603	R6	Panasonic	ERJ-3EKF5492V	Digi-Key	P54.9KHCT N	0.13307	1	\$ 0.13
RES 100K OHM 5% 1/8W 0603	R7	Yageo	RC0603JR-07100KL	Digi-Key	311-100KGRC TND	0.13307	1	\$ 0.13
RES 1.21K OHM 1% 1/10W 0603	R49,R52,R61,R71,R80,R87,R94,R107,R	Yageo	RC0603FR-071K2L	Digi-Key	311-1.21KHRC TND	0.03061	12	\$ 0.37
RES SMD 47 OHM 1% 1/10W 0603	R45,R53,R60,R68,R77,R84,R91,R105,R	Yageo	AC0603FR-0747RL	Digi-Key	311-47LDCT N	0.03726	12	\$ 0.45
Test Point 0603 SMD	TP8	CGS - TE CONNECTIVITY	RCU-0C	Digi-Key	A106145CT N	0.31937	1	\$ 0.32
IC LOAD SWITCH BT570081EPXXUMA1	U1	Infineon	BT570081EPXXUMA1	Digi-Key	S70081EPXXUMA1CT-N	2.78	1	\$ 2.78
LOAD SWITCH BT57200-2EPAPG-TSDSO-14	U2,U6,U9,U10,U15,U16,U17	Infineon	BT572002EPAXUMA1	Digi-Key	S72002EPAXUMA1CT-N	1.73	7	\$ 12.11
REG BUCK 4.5V TO 1.7V, 5A, SYNCHRONOUS	U3	Texas Instruments	TPS565201DDCT	Digi-Key	296-47501-1-ND	2.05	1	\$ 2.05
LOAD SWITCH BT570401EPAPG-TSDSO-14-	U4,U7,U18,U19	Infineon	BT570401EPAXUMA1	Digi-Key	S70401EPAXUMA1CT-N	1.77	4	\$ 7.08
IC MUX/DEMUX 1X16 24SSOP	U5	Texas Instruments	CD74HC4067M6	Digi-Key	296-29408-1-ND	0.99802	1	\$ 1.00
IC I/O EXPANDER I2C 8B 18SOIC	U11,U13,U14	Microchip	MCP23008T-E/SO	Digi-Key	MCP23008T-E/SO CT N	1.36	3	\$ 4.07



Total: \$ 83.99







Design Rules Verification Report

Filename : D:\Josh9\Documents\Midnight Sun\hardware\MSXIV_FrontPowerDistribution\Fr

Warnings 0
Rule Violations 500

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=0.254mm) (All), (All)	0
Short-Circuit Constraint (Allowed=No) (All), (All)	0
Un-Routed Net Constraint (All)	143
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.254mm) (Max=2mm) (Preferred=0.254mm) (All)	0
Hole Size Constraint (Min=0.025mm) (Max=2.54mm) (All)	7
Hole To Hole Clearance (Gap=0.254mm) (All), (All)	0
Minimum Solder Mask Sliver (Gap=0.254mm) (All), (All)	188
Silk To Solder Mask (Clearance=0.254mm) (IsPad), (All)	136
Silk to Silk (Clearance=0.254mm) (All), (All)	20
Net Antennae (Tolerance=0mm) (All)	6
Height Constraint (Min=0mm) (Max=25.4mm) (Preferred=12.7mm) (All)	0
Total	500

Un-Routed Net Constraint ((All))
Un-Routed Net Constraint: Net VBAT Between Pad C3-1(119.175mm,48mm) on Component Side And Pad C10-1(123.225mm,59.2mm) on Component Side [Unplated] And Pad C10-1(123.225mm,59.2mm) or
Un-Routed Net Constraint: Net VBAT Between Pad U4-15(121.7mm,63.4mm) on Component Side [Unplated] And Pad C10-1(123.225mm,59.2mm) or
Un-Routed Net Constraint: Net VBAT Between Pad C11-1(67.921mm,79.875mm) on Component Side And Pad U1-15(71.75mm,80.75mm) on Component Side [Unplated] And Pad C11-1(67.921mm,79.875mm) or
Un-Routed Net Constraint: Net VBAT Between Pad C12-1(93.8mm,79.075mm) on Component Side And Pad U6-15(97.7mm,80.15mm) on Component Side [Unplated] And Pad C12-1(93.8mm,79.075mm) or
Un-Routed Net Constraint: Net VBAT Between Pad U7-15(85.4mm,80.6mm) on Component Side [Unplated] And Pad C12-1(93.8mm,79.075mm) or
Un-Routed Net Constraint: Net Pedal_PWR Between Pad C13-1(94.975mm,85.1mm) on Component Side And Pad R24-1(94.975mm,87mm) or
Un-Routed Net Constraint: Net Pedal_PWR Between Pad C13-1(94.975mm,85.1mm) on Component Side And Pad U6-14(95.75mm,83mm) on Component Side [Unplated] And Pad C13-1(94.975mm,85.1mm) or
Un-Routed Net Constraint: Net Steering_PWR Between Pad R26-1(99.325mm,87mm) on Component Side And Pad C14-1(99.425mm,85.1mm) or
Un-Routed Net Constraint: Net Steering_PWR Between Pad C14-1(99.425mm,85.1mm) on Component Side And Pad U6-8(99.65mm,83mm) or
Un-Routed Net Constraint: Net VBAT Between Pad C17-1(65.1mm,44.45mm) on Component Side And Pad U10-15(69.1mm,45.8mm) on Component Side [Unplated] And Pad C17-1(65.1mm,44.45mm) or
Un-Routed Net Constraint: Net VBAT Between Pad U9-15(36.5mm,45.5mm) on Component Side [Unplated] And Pad C17-1(65.1mm,44.45mm) or
Un-Routed Net Constraint: Net Horns_PWR Between Pad C22-1(73.35mm,85.9mm) on Component Side And Pad R44-1(73.35mm,87.8mm) on Component Side [Unplated] And Pad C22-1(73.35mm,85.9mm) or
Un-Routed Net Constraint: Net Horns_PWR Between Pad U1-9(73.05mm,83.6mm) on Component Side [Unplated] And Pad C22-1(73.35mm,85.9mm) or
Un-Routed Net Constraint: Net VBAT Between Pad U1-15(71.75mm,80.75mm) on Component Side [Unplated] And Pad C29-1(81.2mm,79.175mm) or
Un-Routed Net Constraint: Net VBAT Between Pad C29-1(81.2mm,79.175mm) on Component Side And Pad U7-15(85.4mm,80.6mm) on Component Side [Unplated] And Pad C29-1(81.2mm,79.175mm) or
Un-Routed Net Constraint: Net Speaker_PWR Between Pad C30-1(85.025mm,85.7mm) on Component Side And Pad R55-1(85.025mm,87.5mm) or
Un-Routed Net Constraint: Net Speaker_PWR Between Pad U7-12(84.75mm,83.45mm) on Component Side [Unplated] And Pad C30-1(85.025mm,85.7mm) or
Un-Routed Net Constraint: Net VBAT Between Pad U17-15(96.05mm,46.35mm) on Component Side [Unplated] And Pad C3-1(119.175mm,48mm) or
Un-Routed Net Constraint: Net VBAT Between Pad C3-1(119.175mm,48mm) on Component Side And Pad U2-15(120.7mm,44.1mm) on Component Side [Unplated] And Pad C3-1(119.175mm,48mm) or
Un-Routed Net Constraint: Net VBAT Between Pad R2-1(14.275mm,41.675mm) on Component Side And Pad C32-1(32.6mm,44.175mm) on Component Side [Unplated] And Pad R2-1(14.275mm,41.675mm) or
Un-Routed Net Constraint: Net VBAT Between Pad C32-1(32.6mm,44.175mm) on Component Side And Pad U9-15(36.5mm,45.5mm) on Component Side [Unplated] And Pad C32-1(32.6mm,44.175mm) or
Un-Routed Net Constraint: Net Rear_Display_PWR Between Pad C34-1(38.525mm,50.4mm) on Component Side And Pad R67-1(38.525mm,53.4mm) or
Un-Routed Net Constraint: Net Rear_Display_PWR Between Pad U9-8(38.45mm,48.35mm) on Component Side [Unplated] And Pad C34-1(38.525mm,50.4mm) or
Un-Routed Net Constraint: Net Left_Display_PWR Between Pad C36-1(66.675mm,51mm) on Component Side And Pad R73-1(66.775mm,54.3mm) or
Un-Routed Net Constraint: Net Left_Display_PWR Between Pad C36-1(66.675mm,51mm) on Component Side And Pad U10-14(67.15mm,48.65mm) or
Un-Routed Net Constraint: Net Right_Display_PWR Between Pad C37-1(71.025mm,51mm) on Component Side And Pad R76-1(71.025mm,54.3mm) or
Un-Routed Net Constraint: Net Right_Display_PWR Between Pad C37-1(71.025mm,51mm) on Component Side And Pad U10-8(71.05mm,48.65mm) or
Un-Routed Net Constraint: Net Left_PI_PWR Between Pad C42-1(47.575mm,84.4mm) on Component Side And Pad R89-1(47.675mm,86.3mm) or
Un-Routed Net Constraint: Net Left_PI_PWR Between Pad C42-1(47.575mm,84.4mm) on Component Side And Pad U16-14(47.95mm,82.15mm) or
Un-Routed Net Constraint: Net Right_PI_PWR Between Pad C43-1(52.125mm,84.4mm) on Component Side And Pad R90-1(52.125mm,86.2mm) or
Un-Routed Net Constraint: Net Right_PI_PWR Between Pad U16-8(51.85mm,82.15mm) on Component Side [Unplated] And Pad C43-1(52.125mm,84.4mm) or
Un-Routed Net Constraint: Net VBAT Between Pad U10-15(69.1mm,45.8mm) on Component Side [Unplated] And Pad C45-1(92.1mm,45.175mm) or
Un-Routed Net Constraint: Net VBAT Between Pad C45-1(92.1mm,45.175mm) on Component Side And Pad U17-15(96.05mm,46.35mm) on Component Side [Unplated] And Pad C45-1(92.1mm,45.175mm) or
Un-Routed Net Constraint: Net VBAT Between Pad C46-1(111.8mm,80.25mm) on Component Side And Pad U19-15(115.95mm,81.4mm) on Component Side [Unplated] And Pad C46-1(111.8mm,80.25mm) or
Un-Routed Net Constraint: Net VBAT Between Pad U6-15(97.7mm,80.15mm) on Component Side [Unplated] And Pad C46-1(111.8mm,80.25mm) or
Un-Routed Net Constraint: Net Spare_3_PWR Between Pad C47-1(93.625mm,52.55mm) on Component Side And Pad R96-1(93.7mm,54.55mm) or
Un-Routed Net Constraint: Net Spare_3_PWR Between Pad C47-1(93.625mm,52.55mm) on Component Side And Pad U17-14(94.1mm,49.2mm) or
Un-Routed Net Constraint: Net GND Between Pad R101-2(90.45mm,52.625mm) on Component Side And Pad C47-2(92.275mm,52.55mm) on Component Side [Unplated] And Pad R101-2(90.45mm,52.625mm) or
Un-Routed Net Constraint: Net Spare_1_PWR Between Pad C48-1(126.875mm,86.1mm) on Component Side And Pad R98-1(126.875mm,88mm) or
Un-Routed Net Constraint: Net Spare_1_PWR Between Pad U18-14(126.85mm,83.85mm) on Component Side [Unplated] And Pad C48-1(126.875mm,86.1mm) or
Un-Routed Net Constraint: Net Spare_4_PWR Between Pad R106-1(97.975mm,54.35mm) on Component Side And Pad C50-1(98.075mm,52.45mm) or
Un-Routed Net Constraint: Net Spare_4_PWR Between Pad U17-8(98mm,49.2mm) on Component Side [Unplated] And Pad C50-1(98.075mm,52.45mm) or
Un-Routed Net Constraint: Net VBAT Between Pad C52-1(124.8mm,79.9mm) on Component Side And Pad U18-15(128.8mm,81mm) on Component Side [Unplated] And Pad C52-1(124.8mm,79.9mm) or
Un-Routed Net Constraint: Net VBAT Between Pad U19-15(115.95mm,81.4mm) on Component Side [Unplated] And Pad C52-1(124.8mm,79.9mm) or
Un-Routed Net Constraint: Net VBAT Between Pad U4-15(121.7mm,63.4mm) on Component Side [Unplated] And Pad C52-1(124.8mm,79.9mm) or
Un-Routed Net Constraint: Net Spare_2_PWR Between Pad C53-1(113.875mm,86.4mm) on Component Side And Pad R114-1(113.875mm,88.3mm) or
Un-Routed Net Constraint: Net Spare_2_PWR Between Pad C53-1(113.875mm,86.4mm) on Component Side And Pad U19-14(114mm,84.25mm) or
Un-Routed Net Constraint: Net Rear_Display_PWR Between Pad U9-8(38.45mm,48.35mm) on Component Side [Unplated] And Pad C53-1(113.875mm,86.4mm) or
Un-Routed Net Constraint: Net NetLED10_2 Between Pad LED10-2(41.75mm,48.41mm) on Component Side And Pad R66-1(41.9mm,50.425mm) or
Un-Routed Net Constraint: Net NetLED1_1 Between Pad R1-2(15.1mm,12.425mm) on Component Side And Pad LED1-1(17mm,12.475mm) on Component Side [Unplated] And Pad R1-2(15.1mm,12.425mm) or
Un-Routed Net Constraint: Net Main_PI_PWR Between Pad LED11-1(17.45mm,84.09mm) on Component Side And Pad U15-14(19.55mm,84.1mm) or
Un-Routed Net Constraint: Net NetLED11_2 Between Pad R3-1(15.8mm,85.925mm) on Component Side And Pad LED11-2(15.95mm,84.09mm) or

Un-Routed Net Constraint ((All))
Un-Routed Net Constraint: Net Rear_PI_PWR Between Pad U15-8(23.45mm,84.1mm) on Component Side [Unplated] And Pad R8-1(29.6mm,84.825mm) or
Un-Routed Net Constraint: Net NetLED12_2 Between Pad LED12-2(26.85mm,84.11mm) on Component Side And Pad R8-1(29.6mm,84.825mm) or
Un-Routed Net Constraint: Net Left_Display_PWR Between Pad LED13-1(65.25mm,48.69mm) on Component Side And Pad U10-14(67.15mm,48.65mm) or
Un-Routed Net Constraint: Net NetLED13_2 Between Pad R74-1(63.4mm,50.425mm) on Component Side And Pad LED13-2(63.75mm,48.69mm) or
Un-Routed Net Constraint: Net Right_Display_PWR Between Pad U10-8(71.05mm,48.65mm) on Component Side [Unplated] And Pad R75-1(74.4mm,50.425mm) or
Un-Routed Net Constraint: Net NetLED14_2 Between Pad LED14-2(74.25mm,48.61mm) on Component Side And Pad R75-1(74.4mm,50.425mm) or
Un-Routed Net Constraint: Net Spare_1_PWR Between Pad LED15-1(125.05mm,83.79mm) on Component Side And Pad U18-14(126.85mm,83.85mm) or
Un-Routed Net Constraint: Net NetLED15_2 Between Pad R95-1(123.5mm,86.025mm) on Component Side And Pad LED15-2(123.55mm,83.79mm) or
Un-Routed Net Constraint: Net Spare_3_PWR Between Pad LED16-1(92.3mm,49.34mm) on Component Side And Pad U17-14(94.1mm,49.2mm) or
Un-Routed Net Constraint: Net NetLED16_2 Between Pad R101-1(90.45mm,51.075mm) on Component Side And Pad LED16-2(90.8mm,49.34mm) or
Un-Routed Net Constraint: Net Spare_4_PWR Between Pad U17-8(98mm,49.2mm) on Component Side [Unplated] And Pad LED17-1(100mm,49.26mm) or
Un-Routed Net Constraint: Net NetLED17_2 Between Pad LED17-2(101.5mm,49.26mm) on Component Side And Pad R102-1(103.9mm,51.025mm) or
Un-Routed Net Constraint: Net Spare_2_PWR Between Pad LED18-1(112.15mm,84.19mm) on Component Side And Pad U19-14(114mm,84.25mm) or
Un-Routed Net Constraint: Net NetLED18_2 Between Pad R112-1(110.6mm,86.4mm) on Component Side And Pad LED18-2(110.65mm,84.19mm) or
Un-Routed Net Constraint: Net Left_PI_PWR Between Pad LED19-1(45.85mm,82.39mm) on Component Side And Pad U16-14(47.95mm,82.15mm) or
Un-Routed Net Constraint: Net NetLED19_2 Between Pad R35-1(44.2mm,84.625mm) on Component Side And Pad LED19-2(44.35mm,82.39mm) or
Un-Routed Net Constraint: Net Right_PI_PWR Between Pad U16-8(51.85mm,82.15mm) on Component Side [Unplated] And Pad R36-1(58.2mm,84.525mm) or
Un-Routed Net Constraint: Net NetLED20_2 Between Pad LED20-2(55.125mm,82.41mm) on Component Side And Pad R36-1(58.2mm,84.525mm) or
Un-Routed Net Constraint: Net Speaker_PWR Between Pad LED2-1(81.6mm,83.5mm) on Component Side And Pad U7-14(83.45mm,83.45mm) or
Un-Routed Net Constraint: Net NetLED2_2 Between Pad LED2-2(80.1mm,83.5mm) on Component Side And Pad R57-1(80.1mm,85.425mm) or
Un-Routed Net Constraint: Net NetLED3_2 Between Pad R65-1(31mm,50.225mm) on Component Side And Pad LED3-2(31.25mm,48.39mm) or
Un-Routed Net Constraint: Net NetLED4_2 Between Pad LED4-2(123.6mm,49.4mm) on Component Side And Pad R15-1(126.025mm,51mm) or
Un-Routed Net Constraint: Net NetLED5_2 Between Pad LED5-2(123.7mm,39mm) on Component Side And Pad R18-1(125.725mm,39.1mm) or
Un-Routed Net Constraint: Net NetLED6_2 Between Pad R21-1(124.575mm,67.2mm) on Component Side And Pad LED6-2(126.4mm,67mm) or
Un-Routed Net Constraint: Net Horns_PWR Between Pad LED7-1(67.85mm,84.49mm) on Component Side And Pad U1-14(69.8mm,83.6mm) or
Un-Routed Net Constraint: Net NetLED7_2 Between Pad R25-1(66.3mm,86.125mm) on Component Side And Pad LED7-2(66.35mm,84.49mm) or
Un-Routed Net Constraint: Net Steering_PWR Between Pad U6-8(99.65mm,83mm) on Component Side [Unplated] And Pad LED8-1(101.6mm,82.9mm) or
Un-Routed Net Constraint: Net NetLED8_2 Between Pad LED8-2(103.1mm,82.9mm) on Component Side And Pad R30-1(104.9mm,83.225mm) or
Un-Routed Net Constraint: Net Pedal_PWR Between Pad LED9-1(93.25mm,83.09mm) on Component Side And Pad U6-14(95.75mm,83mm) or
Un-Routed Net Constraint: Net NetLED9_2 Between Pad R31-1(91.4mm,85.025mm) on Component Side And Pad LED9-2(91.75mm,83.09mm) or
Un-Routed Net Constraint: Net Rear_Display_PWR Between Pad R67-1(38.525mm,53.4mm) on Component Side And Pad P10-3(40.9mm,61.8mm) or
Un-Routed Net Constraint: Net PB13_IO_2_INT Between Track (52.37mm,12.4mm)(54mm,12.4mm) on Component Side And Pad R13-1(52.37mm,12.4mm) or
Un-Routed Net Constraint: Net PB12_IO_3_INT Between Track (67.54mm,12.4mm)(69.1mm,12.4mm) on Component Side And Pad R12-1(67.54mm,12.4mm) or
Un-Routed Net Constraint: Net Left_Display_PWR Between Pad P11-1(65.6mm,61.8mm) on Multi-Layer And Pad R73-1(66.775mm,54.3mm) or
Un-Routed Net Constraint: Net Right_Display_PWR Between Pad R76-1(71.025mm,54.3mm) on Component Side And Pad P12-3(74.6mm,61.8mm) or
Un-Routed Net Constraint: Net Right_PI_PWR Between Pad R90-1(52.125mm,86.2mm) on Component Side And Pad P14-1(55.6mm,92.5mm) or
Un-Routed Net Constraint: Net VBAT Between Pad P1-50(112.1mm,21mm) on Component Side And Pad U2-15(120.7mm,44.1mm) on Component Side
Un-Routed Net Constraint: Net Left_PI_PWR Between Pad P15-1(43.8mm,92.5mm) on Multi-Layer And Pad R89-1(47.675mm,86.3mm) on Component Side
Un-Routed Net Constraint: Net Spare_1_PWR Between Pad R98-1(126.875mm,88mm) on Component Side And Pad P17-1(126.9mm,92.1mm) or
Un-Routed Net Constraint: Net Spare_2_PWR Between Pad R114-1(113.875mm,88.3mm) on Component Side And Pad P18-1(113.9mm,92.1mm) or
Un-Routed Net Constraint: Net PB14_IO_1_INT Between Track (37.475mm,12.4mm)(37.7mm,12.175mm) on Component Side And Pad R14-1(37.475mm,12.4mm) or
Un-Routed Net Constraint: Net Spare_3_PWR Between Pad P19-1(91.65mm,59.25mm) on Multi-Layer And Pad R96-1(93.7mm,54.45mm) on Component Side
Un-Routed Net Constraint: Net Spare_4_PWR Between Pad R106-1(97.975mm,54.35mm) on Component Side And Pad P20-1(102.65mm,59.25mm) or
Un-Routed Net Constraint: Net VBAT Between Pad P3-2(9.6mm,29.4mm) on Multi-Layer And Pad P3-1(9.6mm,32.4mm) on Multi-Layer
Un-Routed Net Constraint: Net VBAT Between Pad P3-1(9.6mm,32.4mm) on Multi-Layer And Pad R2-1(14.275mm,41.675mm) on Component Side
Un-Routed Net Constraint: Net VBAT Between Pad P3-3(9.6mm,26.4mm) on Multi-Layer And Pad P3-2(9.6mm,29.4mm) on Multi-Layer
Un-Routed Net Constraint: Net VBAT Between Pad P3-4(9.6mm,23.4mm) on Multi-Layer And Pad P3-3(9.6mm,26.4mm) on Multi-Layer
Un-Routed Net Constraint: Net VBAT Between Pad P3-5(9.6mm,20.4mm) on Multi-Layer And Pad P3-4(9.6mm,23.4mm) on Multi-Layer
Un-Routed Net Constraint: Net VBAT Between Pad P3-6(9.6mm,17.4mm) on Multi-Layer And Pad P3-5(9.6mm,20.4mm) on Multi-Layer
Un-Routed Net Constraint: Net VBAT Between Pad P3-7(9.6mm,14.4mm) on Multi-Layer And Pad P3-6(9.6mm,17.4mm) on Multi-Layer
Un-Routed Net Constraint: Net VBAT Between Pad P3-7(9.6mm,14.4mm) on Multi-Layer And Pad R1-1(15.1mm,13.975mm) on Component Side
Un-Routed Net Constraint: Net Steering_PWR Between Pad P5-1(94.5mm,92.1mm) on Multi-Layer And Pad R26-1(99.325mm,87mm) on Component Side

Un-Routed Net Constraint (All)
Un-Routed Net Constraint: Net Pedal_PWR Between Pad P5-2(91.5mm,92.1mm) on Multi-Layer And Pad R24-1(94.975mm,87mm) on Component Side
Un-Routed Net Constraint: Net Speaker_PWR Between Pad R55-1(85.025mm,87.5mm) on Component Side And Pad P5-3(88.5mm,92.1mm) or
Un-Routed Net Constraint: Net Horns_PWR Between Pad P6-2(70.7mm,92.1mm) on Multi-Layer And Pad P6-1(73.7mm,92.1mm) on Multi-Layer
Un-Routed Net Constraint: Net Horns_PWR Between Pad R44-1(73.35mm,87.8mm) on Component Side And Pad P6-1(73.7mm,92.1mm) on Multi-Layer
Un-Routed Net Constraint: Net NetR108_1 Between Pad R108-1(93mm,40.875mm) on Component Side And Pad U17-1(94.1mm,43.5mm) on Component Side
Un-Routed Net Constraint: Net NetR20_1 Between Pad R20-1(114.9mm,47.5mm) on Component Side And Pad U2-1(117.85mm,46.05mm) on Component Side
Un-Routed Net Constraint: Net NetD9_2 Between Pad R85-1(24.5mm,74.125mm) on Component Side And Via (32.2mm,73mm) from Component Side to
Un-Routed Net Constraint: Net GND Between Pad TP8-1(10.8mm,2.9mm) on Component Side And Pad TP8-1(12.35mm,2.9mm) on Component Side
Un-Routed Net Constraint: Net Right_Display_PWR Between Pad U10-10(69.75mm,48.65mm) on Component Side [Unplated] And Pad U10-10(69.75mm,48.65mm)
Un-Routed Net Constraint: Net Left_Display_PWR Between Pad U10-13(67.8mm,48.65mm) on Component Side [Unplated] And Pad U10-13(67.8mm,48.65mm)
Un-Routed Net Constraint: Net Left_Display_PWR Between Pad U10-14(67.15mm,48.65mm) on Component Side [Unplated] And Pad U10-14(67.15mm,48.65mm)
Un-Routed Net Constraint: Net Right_Display_PWR Between Pad U10-9(70.4mm,48.65mm) on Component Side [Unplated] And Pad U10-9(70.4mm,48.65mm)
Un-Routed Net Constraint: Net Horns_PWR Between Pad U1-12(71.1mm,83.6mm) on Component Side [Unplated] And Pad U1-10(72.4mm,83.6mm) or
Un-Routed Net Constraint: Net Horns_PWR Between Pad U1-10(72.4mm,83.6mm) on Component Side [Unplated] And Pad U1-9(73.05mm,83.6mm) or
Un-Routed Net Constraint: Net Horns_PWR Between Pad U1-13(70.45mm,83.6mm) on Component Side [Unplated] And Pad U1-12(71.1mm,83.6mm) or
Un-Routed Net Constraint: Net Horns_PWR Between Pad U1-14(69.8mm,83.6mm) on Component Side [Unplated] And Pad U1-13(70.45mm,83.6mm) or
Un-Routed Net Constraint: Net GND Between Pad U13-3(44.32mm,10mm) on Component Side And Pad U13-4(45.59mm,10mm) on Component Side
Un-Routed Net Constraint: Net Right_PI_PWR Between Pad U16-10(50.55mm,82.15mm) on Component Side [Unplated] And Pad U16-10(50.55mm,82.15mm)
Un-Routed Net Constraint: Net Left_PI_PWR Between Pad U16-13(48.6mm,82.15mm) on Component Side [Unplated] And Pad U16-13(48.6mm,82.15mm)
Un-Routed Net Constraint: Net Left_PI_PWR Between Pad U16-14(47.95mm,82.15mm) on Component Side [Unplated] And Pad U16-14(47.95mm,82.15mm)
Un-Routed Net Constraint: Net Right_PI_PWR Between Pad U16-9(51.2mm,82.15mm) on Component Side [Unplated] And Pad U16-8(51.85mm,82.15mm)
Un-Routed Net Constraint: Net Spare_4_PWR Between Pad U17-10(96.7mm,49.2mm) on Component Side [Unplated] And Pad U17-9(97.35mm,49.2mm)
Un-Routed Net Constraint: Net Spare_3_PWR Between Pad U17-13(94.75mm,49.2mm) on Component Side [Unplated] And Pad U17-12(95.4mm,49.2mm)
Un-Routed Net Constraint: Net Spare_3_PWR Between Pad U17-14(94.1mm,49.2mm) on Component Side [Unplated] And Pad U17-13(94.75mm,49.2mm)
Un-Routed Net Constraint: Net Spare_4_PWR Between Pad U17-9(97.35mm,49.2mm) on Component Side [Unplated] And Pad U17-8(98mm,49.2mm) or
Un-Routed Net Constraint: Net Horns_PWR Between Pad U1-9(73.05mm,83.6mm) on Component Side [Unplated] And Pad U1-8(73.7mm,83.6mm) or
Un-Routed Net Constraint: Net Spare_1_PWR Between Pad U18-13(127.5mm,83.85mm) on Component Side [Unplated] And Pad U18-13(127.5mm,83.85mm)
Un-Routed Net Constraint: Net Spare_1_PWR Between Pad U18-14(126.85mm,83.85mm) on Component Side [Unplated] And Pad U18-14(126.85mm,83.85mm)
Un-Routed Net Constraint: Net Spare_2_PWR Between Pad U19-13(114.65mm,84.25mm) on Component Side [Unplated] And Pad U19-13(114.65mm,84.25mm)
Un-Routed Net Constraint: Net Spare_2_PWR Between Pad U19-14(114mm,84.25mm) on Component Side [Unplated] And Pad U19-14(114mm,84.25mm)
Un-Routed Net Constraint: Net Steering_PWR Between Pad U6-10(98.35mm,83mm) on Component Side [Unplated] And Pad U6-9(99mm,83mm) or
Un-Routed Net Constraint: Net Pedal_PWR Between Pad U6-13(96.4mm,83mm) on Component Side [Unplated] And Pad U6-12(97.05mm,83mm) or
Un-Routed Net Constraint: Net Pedal_PWR Between Pad U6-14(95.75mm,83mm) on Component Side [Unplated] And Pad U6-13(96.4mm,83mm) or
Un-Routed Net Constraint: Net Steering_PWR Between Pad U6-9(99mm,83mm) on Component Side [Unplated] And Pad U6-8(99.65mm,83mm) or
Un-Routed Net Constraint: Net Speaker_PWR Between Pad U7-13(84.1mm,83.45mm) on Component Side [Unplated] And Pad U7-12(84.75mm,83.45mm)
Un-Routed Net Constraint: Net Speaker_PWR Between Pad U7-14(83.45mm,83.45mm) on Component Side [Unplated] And Pad U7-13(84.1mm,83.45mm)
Un-Routed Net Constraint: Net Rear_Display_PWR Between Pad U9-10(37.15mm,48.35mm) on Component Side [Unplated] And Pad U9-10(37.15mm,48.35mm)
Un-Routed Net Constraint: Net Rear_Display_PWR Between Pad U9-9(37.8mm,48.35mm) on Component Side [Unplated] And Pad U9-9(37.8mm,48.35mm)
Un-Routed Net Constraint: Net NetC4_1 Between Via (16.475mm,50.896mm) from Component Side to Solder Side And Via (17.701mm,48.55mm) from

Hole Size Constraint (Min=0.025mm) (Max=2.54mm) (All)
Hole Size Constraint: (2.7mm > 2.54mm) Pad Free-(108.4mm,2.5mm) on Multi-Layer Actual Hole Size = 2.7mm
Hole Size Constraint: (2.7mm > 2.54mm) Pad Free-(108.4mm,27.5mm) on Multi-Layer Actual Hole Size = 2.7mm
Hole Size Constraint: (2.7mm > 2.54mm) Pad Free-(138.4mm,2.5mm) on Multi-Layer Actual Hole Size = 2.7mm
Hole Size Constraint: (2.7mm > 2.54mm) Pad Free-(138.4mm,27.5mm) on Multi-Layer Actual Hole Size = 2.7mm
Hole Size Constraint: (2.7mm > 2.54mm) Pad Free-(138.4mm,97.5mm) on Multi-Layer Actual Hole Size = 2.7mm
Hole Size Constraint: (2.7mm > 2.54mm) Pad Free-(2.5mm,2.5mm) on Multi-Layer Actual Hole Size = 2.7mm
Hole Size Constraint: (2.7mm > 2.54mm) Pad Free-(2.5mm,97.5mm) on Multi-Layer Actual Hole Size = 2.7mm

Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All)
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U6-12(97.05mm,83mm) on Component Side And Pad U6-13(96.4mm,83mm)
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U6-13(96.4mm,83mm) on Component Side And Pad U6-14(95.75mm,83mm)
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U6-2(96.4mm,77.3mm) on Component Side And Pad U6-3(97.05mm,77.3mm)
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U6-3(97.05mm,77.3mm) on Component Side And Pad U6-4(97.7mm,77.3mm)
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U6-4(97.7mm,77.3mm) on Component Side And Pad U6-5(98.35mm,77.3mm)
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U6-5(98.35mm,77.3mm) on Component Side And Pad U6-6(99mm,77.3mm) or
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U6-6(99mm,77.3mm) on Component Side And Pad U6-7(99.65mm,77.3mm) or
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U6-8(99.65mm,83mm) on Component Side And Pad U6-9(99mm,83mm) or
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U7-1(83.45mm,77.75mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U7-10(86.05mm,83.45mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U7-10(86.05mm,83.45mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U7-11(85.4mm,83.45mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U7-12(84.75mm,83.45mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U7-13(84.1mm,83.45mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U7-2(84.1mm,77.75mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U7-3(84.75mm,77.75mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U7-4(85.4mm,77.75mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U7-5(86.05mm,77.75mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U7-6(86.7mm,77.75mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U7-8(87.35mm,83.45mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U9-1(34.55mm,42.65mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U9-10(37.15mm,48.35mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U9-10(37.15mm,48.35mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U9-11(36.5mm,48.35mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U9-12(35.85mm,48.35mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U9-13(35.2mm,48.35mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U9-2(35.2mm,42.65mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U9-3(35.85mm,42.65mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U9-4(36.5mm,42.65mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U9-5(37.15mm,42.65mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U9-6(37.8mm,42.65mm) on Component Side And Pac
Minimum Solder Mask Sliver Constraint: (0.2mm < 0.254mm) Between Pad U9-8(38.45mm,48.35mm) on Component Side And Pac

Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)
Silk To Solder Mask Clearance Constraint: (0.169mm < 0.254mm) Between Arc (113.038mm,45.438mm) on Top Overlay And Pad R81-1(20.22mm,74.2mm)
Silk To Solder Mask Clearance Constraint: (0.012mm < 0.254mm) Between Arc (20.162mm,73.738mm) on Top Overlay And Pad R84-1(35.22mm,38.4mm)
Silk To Solder Mask Clearance Constraint: (0.012mm < 0.254mm) Between Arc (35.162mm,37.938mm) on Top Overlay And Pad R64-1(35.22mm,38.4mm)
Silk To Solder Mask Clearance Constraint: (0.012mm < 0.254mm) Between Arc (48.562mm,71.838mm) on Top Overlay And Pad R88-1(48.62mm,72.3mm)
Silk To Solder Mask Clearance Constraint: (0.012mm < 0.254mm) Between Arc (67.762mm,38.238mm) on Top Overlay And Pad R72-1(67.82mm,38.7mm)
Silk To Solder Mask Clearance Constraint: (0.012mm < 0.254mm) Between Arc (94.762mm,38.838mm) on Top Overlay And Pad R99-1(94.82mm,39.3mm)
Silk To Solder Mask Clearance Constraint: (0.012mm < 0.254mm) Between Arc (96.362mm,72.638mm) on Top Overlay And Pad R23-1(96.42mm,73.1mm)
Silk To Solder Mask Clearance Constraint: (0.247mm < 0.254mm) Between Pad D10-2(58mm,74.9mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.247mm < 0.254mm) Between Pad D11-2(136.8mm,79.736mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.247mm < 0.254mm) Between Pad D1-2(89.7mm,70.9mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.247mm < 0.254mm) Between Pad D12-2(102.8mm,37.436mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.247mm < 0.254mm) Between Pad D13-2(120.9mm,71.336mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.247mm < 0.254mm) Between Pad D2-2(112.311mm,37.7mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.247mm < 0.254mm) Between Pad D3-2(104mm,70.436mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.247mm < 0.254mm) Between Pad D5-2(112.836mm,57.925mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.247mm < 0.254mm) Between Pad D6-2(76.2mm,71.336mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.247mm < 0.254mm) Between Pad D7-2(43mm,36.136mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.247mm < 0.254mm) Between Pad D8-2(75mm,36mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.247mm < 0.254mm) Between Pad D9-2(32.064mm,74.7mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Pad L1-1(6.8mm,47.9mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Pad L1-1(6.8mm,47.9mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Pad L1-1(6.8mm,47.9mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.125mm < 0.254mm) Between Pad L1-2(6.8mm,52.6mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Pad L1-2(6.8mm,52.6mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Pad L1-2(6.8mm,52.6mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED10-2(41.75mm,48.41mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED11-2(15.95mm,84.09mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED1-2(17mm,13.975mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED12-2(26.85mm,84.11mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED13-2(63.75mm,48.69mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED14-2(74.25mm,48.61mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED15-2(123.55mm,83.79mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED16-2(90.8mm,49.34mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED17-2(101.5mm,49.26mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.215mm < 0.254mm) Between Pad LED18-1(112.15mm,84.19mm) on Component Side And Text "LED18"
Silk To Solder Mask Clearance Constraint: (0.215mm < 0.254mm) Between Pad LED18-2(110.65mm,84.19mm) on Component Side And Text "LED18"
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED18-2(110.65mm,84.19mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED19-2(44.35mm,82.39mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED20-2(55.125mm,82.41mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED2-2(80.1mm,83.5mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED3-2(31.25mm,48.39mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED4-2(123.6mm,49.4mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED5-2(123.7mm,39mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED6-2(126.4mm,67mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED7-2(66.35mm,84.49mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED8-2(103.1mm,82.9mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.209mm < 0.254mm) Between Pad LED9-2(91.75mm,83.09mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.223mm < 0.254mm) Between Pad P1-1(115.7mm,21mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.223mm < 0.254mm) Between Pad P1-25(115.7mm,9mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.239mm < 0.254mm) Between Pad P1-26(112.1mm,9mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.213mm < 0.254mm) Between Pad P13-0(15.9mm,96.44mm) on Multi-Layer And Track
Silk To Solder Mask Clearance Constraint: (0.236mm < 0.254mm) Between Pad P13-0(15.9mm,96.44mm) on Multi-Layer And Track

Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Pad U3-2(14.275mm,47.6mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Pad U3-3(14.275mm,46.65mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.24mm < 0.254mm) Between Pad U3-3(14.275mm,46.65mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.24mm < 0.254mm) Between Pad U3-4(16.525mm,46.65mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Pad U3-4(16.525mm,46.65mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Pad U3-5(16.525mm,47.6mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.24mm < 0.254mm) Between Pad U3-6(16.525mm,48.55mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.1mm < 0.254mm) Between Pad U3-6(16.525mm,48.55mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Pad U5-1(82.8mm,18.085mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad U5-10(82.8mm,6.655mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad U5-11(82.8mm,5.385mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad U5-12(82.8mm,4.115mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Pad U5-13(92.2mm,4.115mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Pad U5-14(92.2mm,5.385mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Pad U5-15(92.2mm,6.655mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Pad U5-16(92.2mm,7.925mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Pad U5-17(92.2mm,9.195mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Pad U5-18(92.2mm,10.465mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Pad U5-19(92.2mm,11.735mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad U5-2(82.8mm,16.815mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Pad U5-20(92.2mm,13.005mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Pad U5-21(92.2mm,14.275mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Pad U5-22(92.2mm,15.545mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Pad U5-23(92.2mm,16.815mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.225mm < 0.254mm) Between Pad U5-24(92.2mm,18.085mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad U5-3(82.8mm,15.545mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad U5-4(82.8mm,14.275mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad U5-5(82.8mm,13.005mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad U5-6(82.8mm,11.735mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad U5-7(82.8mm,10.465mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad U5-8(82.8mm,9.195mm) on Component Side And Track
Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad U5-9(82.8mm,7.925mm) on Component Side And Track

Silk To Silk (Clearance=0.254mm) (All),(All)
Silk To Silk Clearance Constraint: (0.098mm < 0.254mm) Between Text "1" (102.45mm,64.65mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.098mm < 0.254mm) Between Text "1" (113.7mm,97.5mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.098mm < 0.254mm) Between Text "1" (12.7mm,97.9mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.098mm < 0.254mm) Between Text "1" (126.7mm,97.5mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.198mm < 0.254mm) Between Text "1" (13.3mm,32.3mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.198mm < 0.254mm) Between Text "1" (130.6mm,63.6mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.098mm < 0.254mm) Between Text "1" (25.1mm,97.9mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.098mm < 0.254mm) Between Text "1" (43.6mm,97.9mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.098mm < 0.254mm) Between Text "1" (55.4mm,97.9mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.098mm < 0.254mm) Between Text "1" (91.45mm,64.65mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.198mm < 0.254mm) Between Text "1" (94.4mm,88.4mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.198mm < 0.254mm) Between Text "14" (4.1mm,13.8mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "6" (139.55mm,69.75mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "6" (88.25mm,97.35mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.248mm < 0.254mm) Between Text "LED10" (39.7mm,46.8mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.197mm < 0.254mm) Between Text "LED15" (122mm,82.2mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.097mm < 0.254mm) Between Text "LED18" (109.1mm,82.7mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.251mm < 0.254mm) Between Text "P20" (104.8mm,55.5mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (0.248mm < 0.254mm) Between Text "P20" (104.8mm,55.5mm) on Top Overlay And Track
Silk To Silk Clearance Constraint: (Collision < 0.254mm) Between Text "R6" (19.9mm,47.975mm) on Top Overlay And Text "U3" (20.4mm,46.9mm) on Top

Net Antennae (Tolerance=0mm) (All)
Net Antennae: Track (17.126mm,14.274mm)(25mm,6.4mm) on Signal
Net Antennae: Via (13.3mm,55.4mm) from Component Side to Solder Side
Net Antennae: Via (16.475mm,50.896mm) from Component Side to Solder Side
Net Antennae: Via (17.701mm,48.55mm) from Component Side to Solder Side
Net Antennae: Via (23.6mm,17.2mm) from Component Side to Solder Side
Net Antennae: Via (28.06mm,11.6mm) from Component Side to Solder Side

