Coroutines and simulated latency

References/second vs Total concurrent references offered

- 300M
- 273M
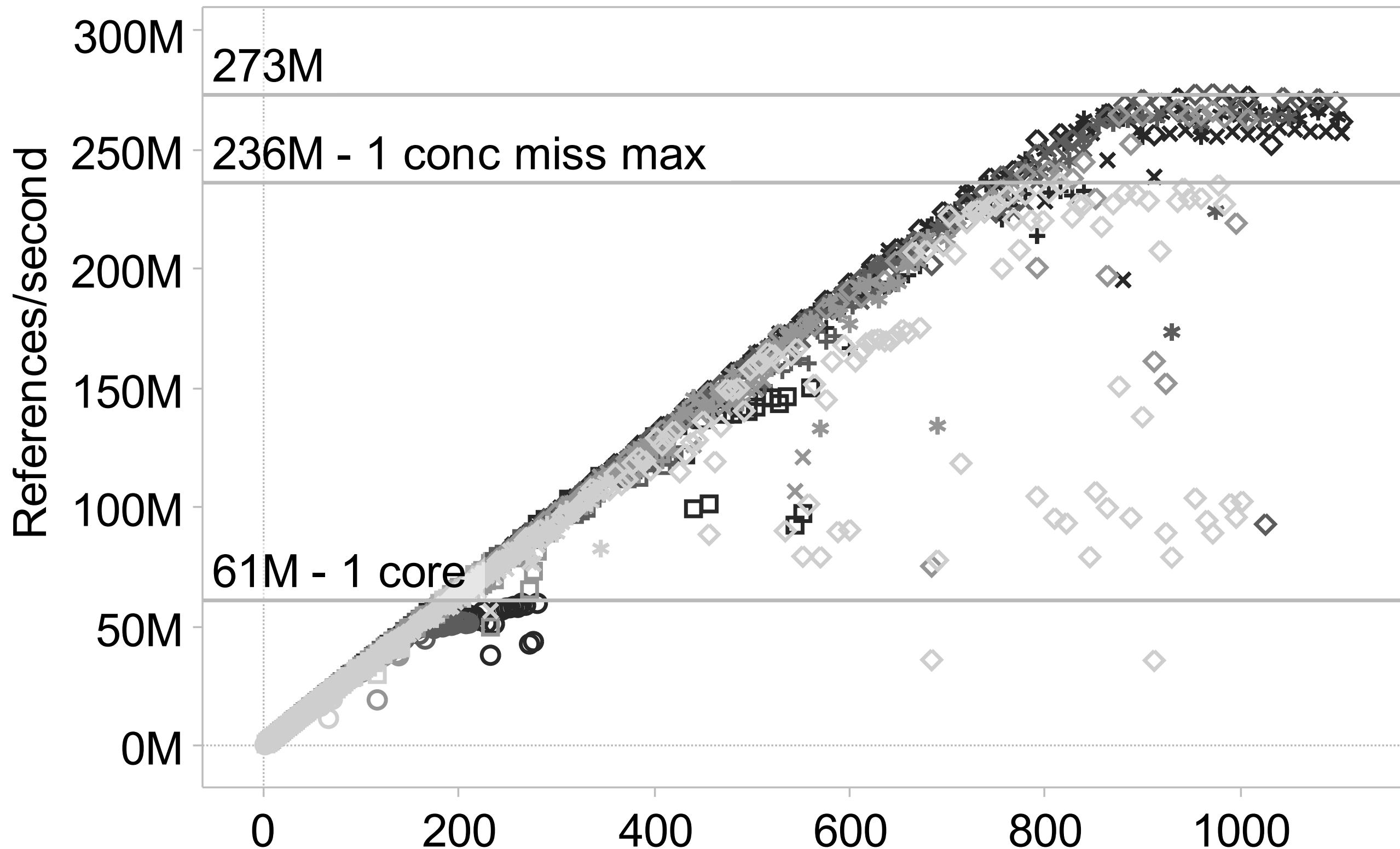- 250M
- 236M - 1 conc miss max
- 200M
- 150M
- 100M
- 61M - 1 core
- 50M
- 0M

Legend:
- ○ 1 core
- □ 2 cores
- + 3 cores
- × 4 cores
- * 5 cores
- ◇ 6 cores

- 1 ref/context
- 2 ref/context
- 3 ref/context
- 4 ref/context