Coroutines and simulated latency 300M 273M 236M - 1 conc miss max 250M References/second 200M 175M 150M 100M 1 core □ 2 cores 50M + 3 cores 1 ref/context × 4 cores 2 ref/context * 5 cores ■ 3 ref/context ■ 4 ref/context ♦ 6 cores **OM** 1000 200 400 600 008 Total concurrent references offered