

Grappa: Bridging the gap between mass-market clusters and irregular applications

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Mass-market computer systems are designed to exploit spatial locality via cache and local memory to achieve high efficiency. Unfortunately, in irregular applications like graph analysis, spatial locality is often difficult—if not impossible—to express.

Graph analytics applications from domains such as business, national security, machine learning, data-driven science, and social network computing are increasingly important. These graphs are large; fast response requires multi-node systems. Partitioning these graphs so that communication between cluster nodes is minimized is challenging. As system size grows, this communication becomes the bottleneck and performance degrades.

Tolerate global memory latency

Memory operations that reference remote memory are turned into active messages directed to a *delegate core* on the remote node. After issuing the message, the requester yields the processor until the reply arrives. The delegate core performs the operation. Read-modify-write cycles are performed entirely on the delegate.

Tolerate local memory latency

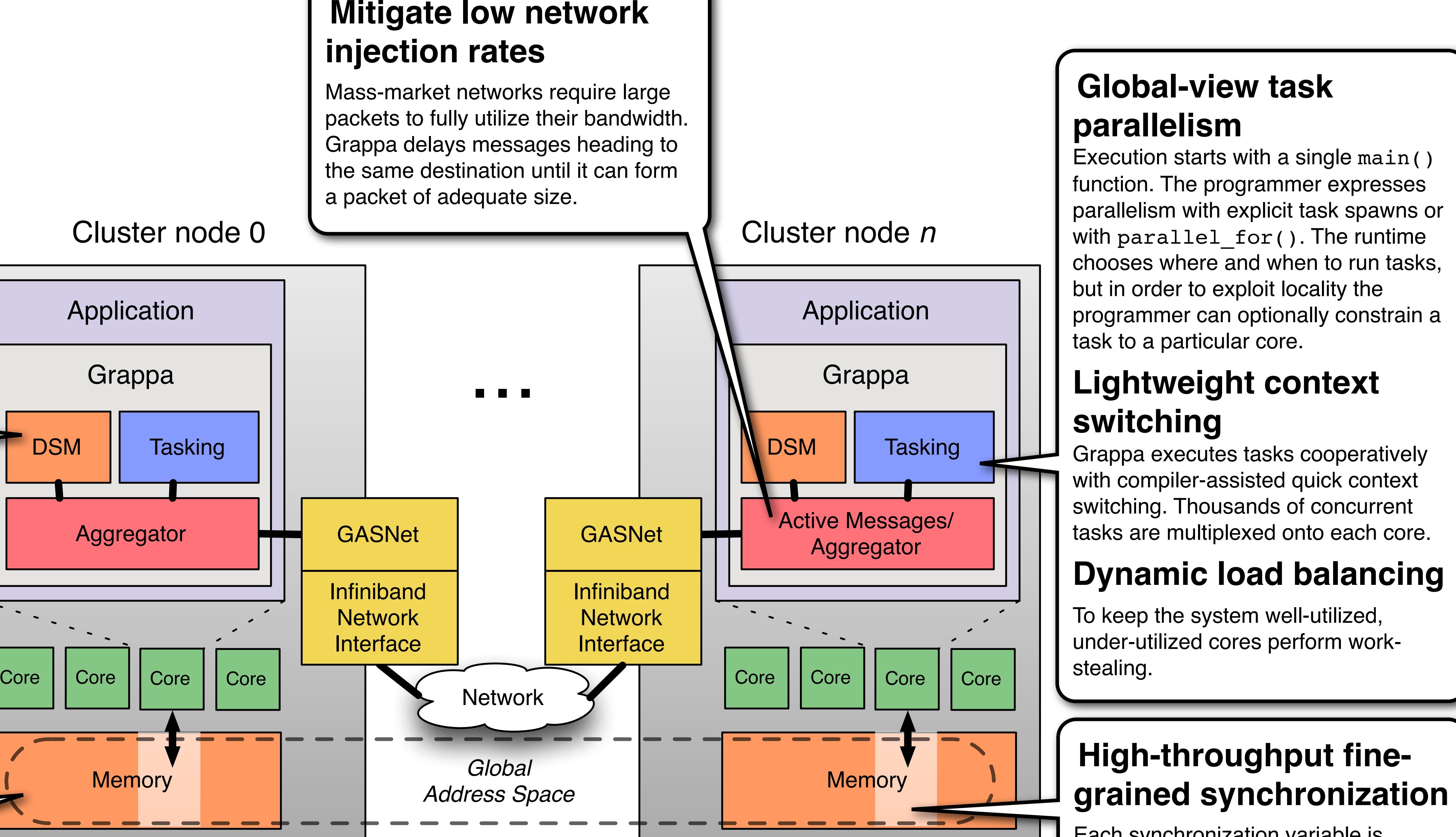
By issuing software prefetches and yielding on likely cache misses, Grappa exposes more memory parallelism to the processor and increases node-local random access bandwidth.

Exploit locality when available

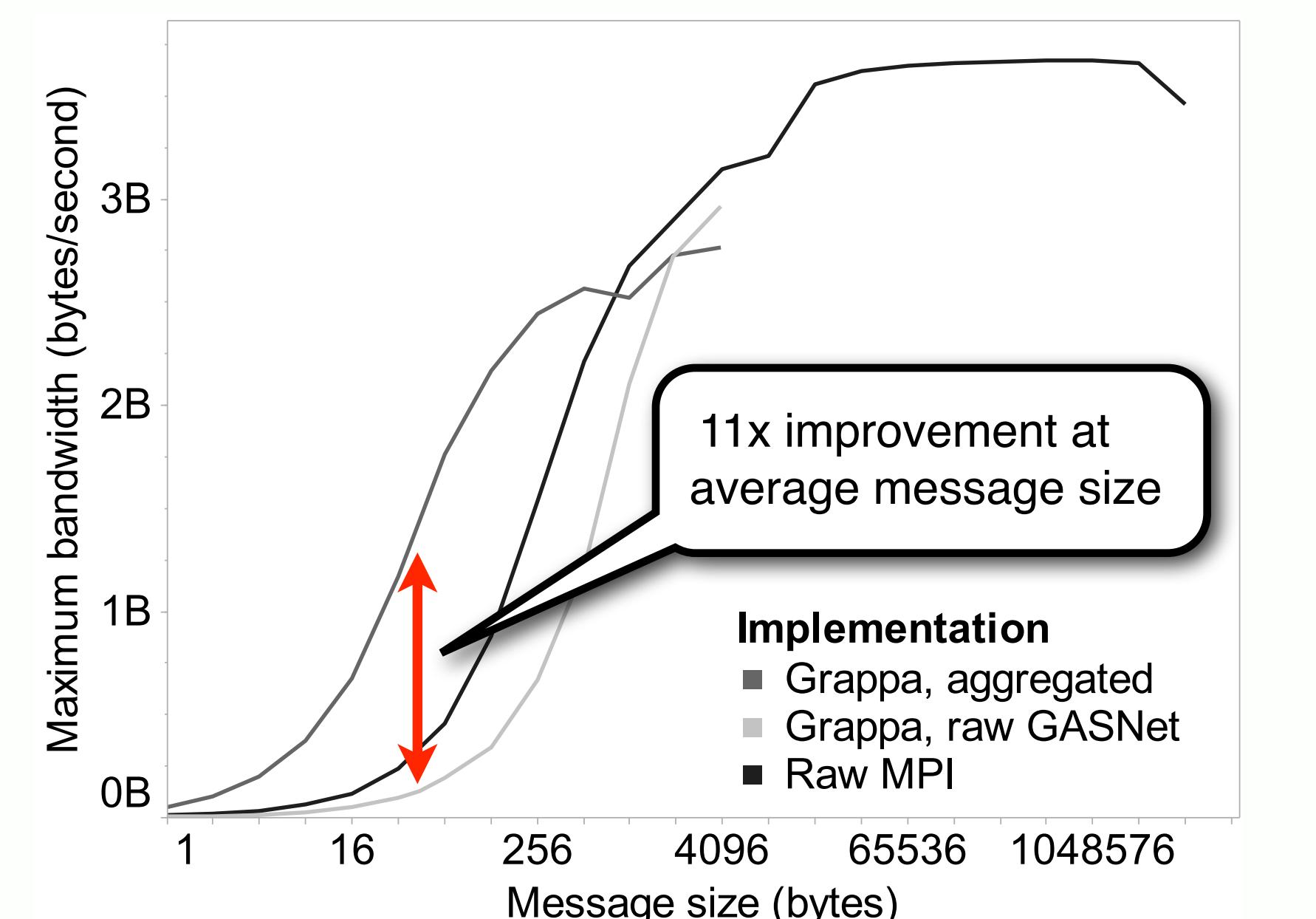
When there is locality to be exploited, global memory can be accessed through a software caching layer with user-controllable granularity.

Global shared memory

Grappa's shared heap is constructed out of chunks allocated on each node in the system. Contiguous addresses are spread across the chunks in a block-cyclic fashion.



Message aggregation



Benchmarks

Hardware:

- Grappa: PNNL's AMD Interlagos cluster, 2.1-GHz, 64GB/node, Mellanox 40Gb/s Infiniband
- XMT: PNNL's 128-processor Cray XMT-1

