



IF

ID

IE

MEM

WB

I1      ADD R1, R2, R3



I2      ADD R4, R5, R6



I3      BEQ L1

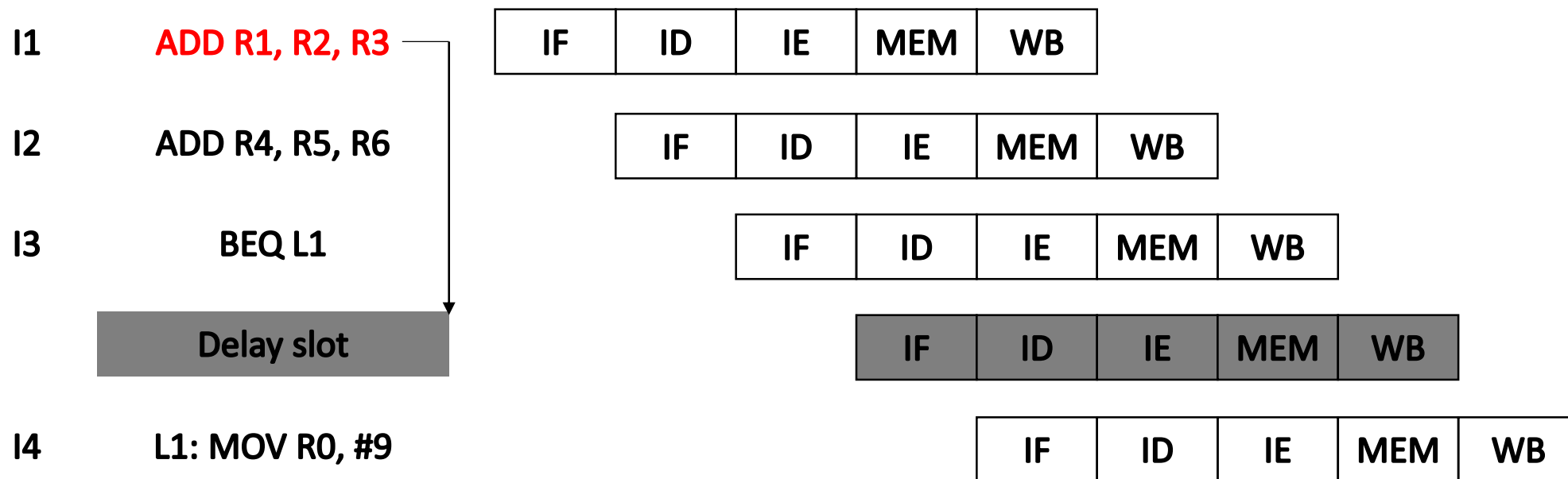
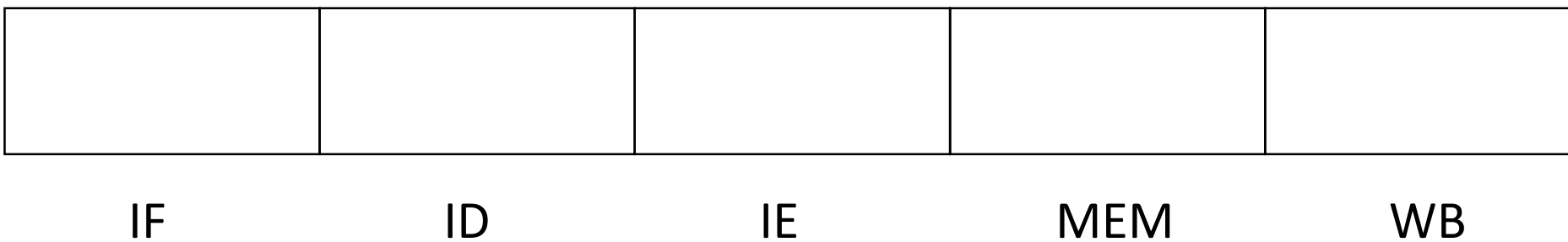


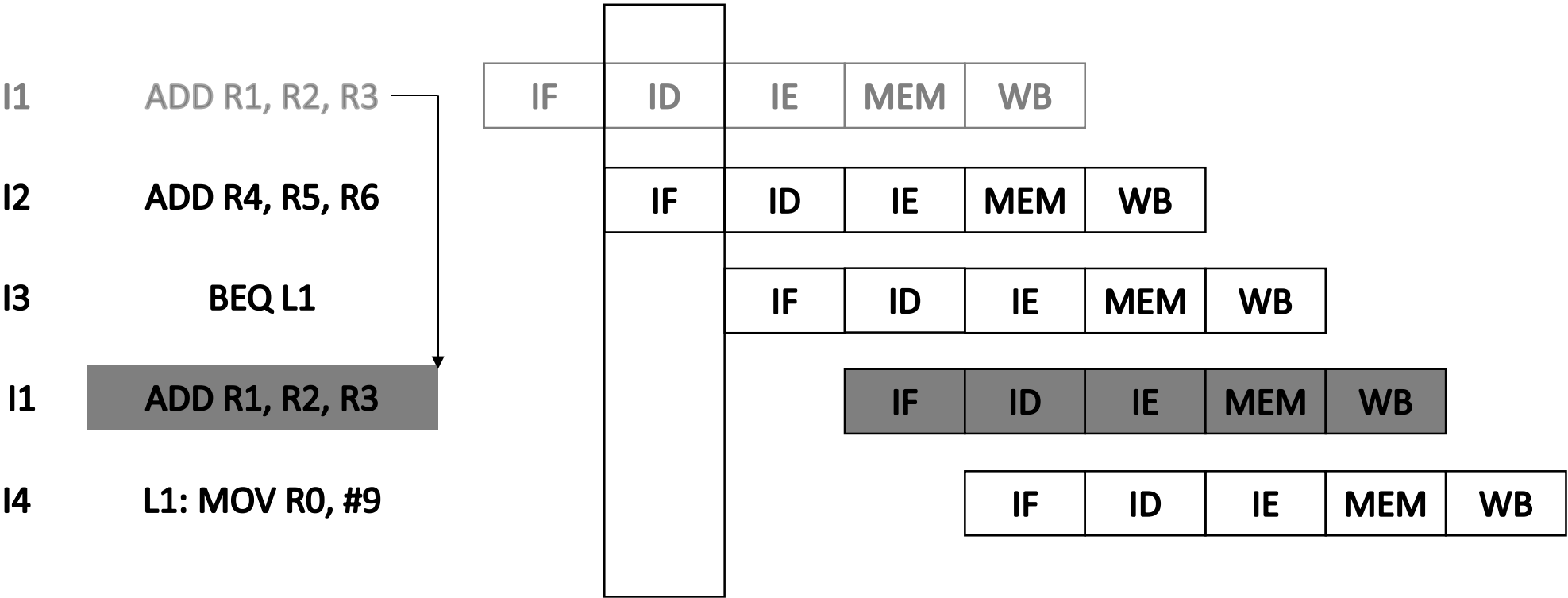
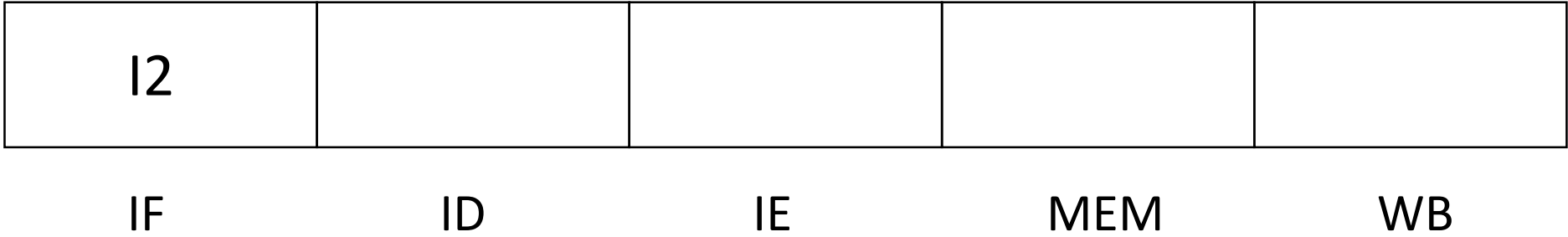
Delay slot



I4      L1: MOV R0, #9









IF

ID

IE

MEM

WB

I1

ADD R1, R2, R3



I2

ADD R4, R5, R6



I3

BEQ L1



I1

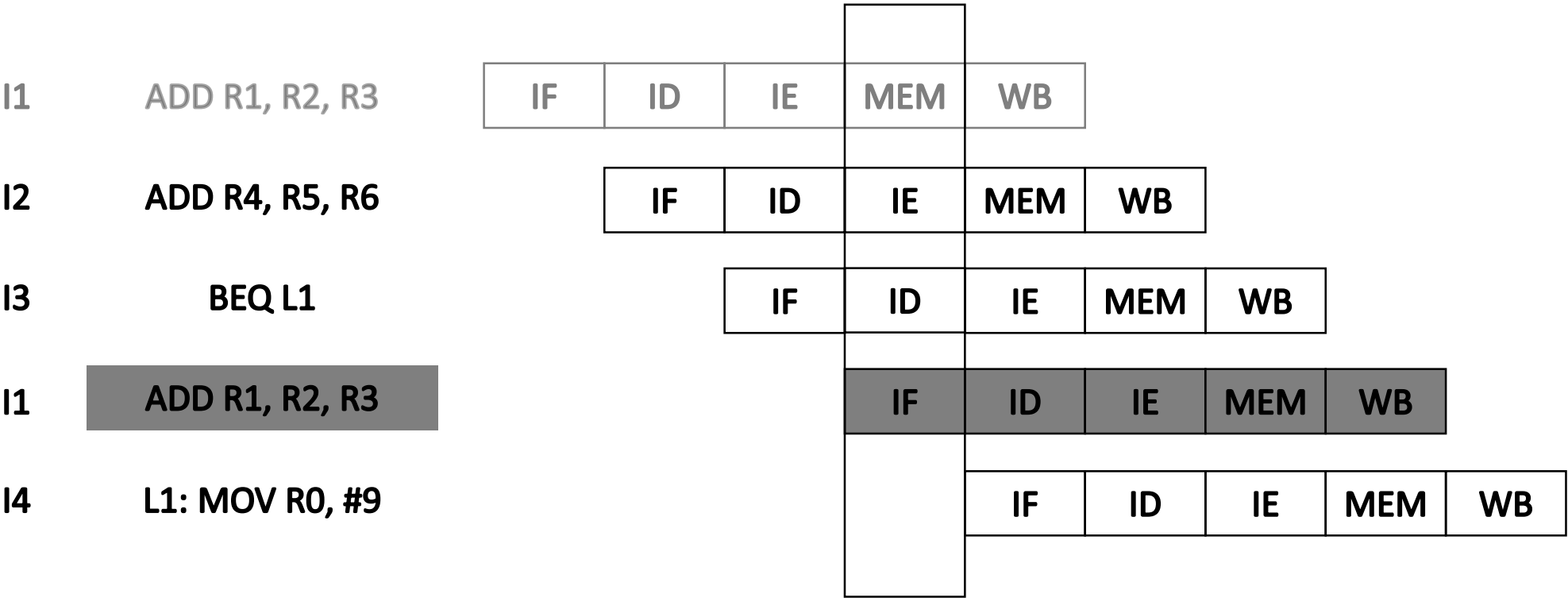
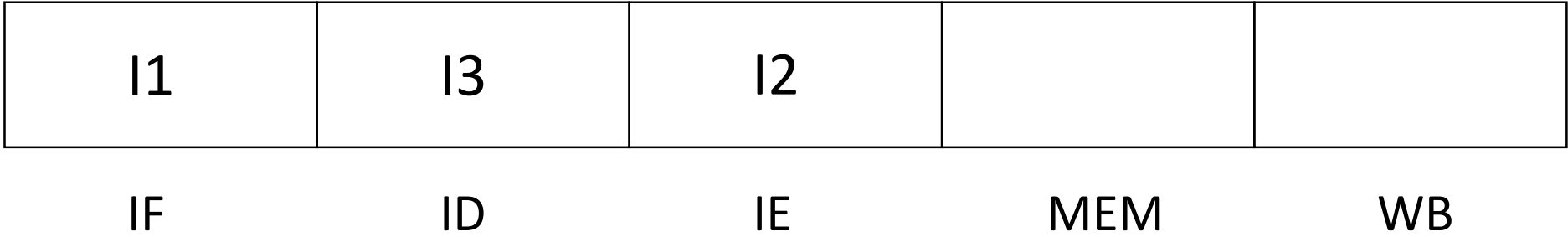
ADD R1, R2, R3

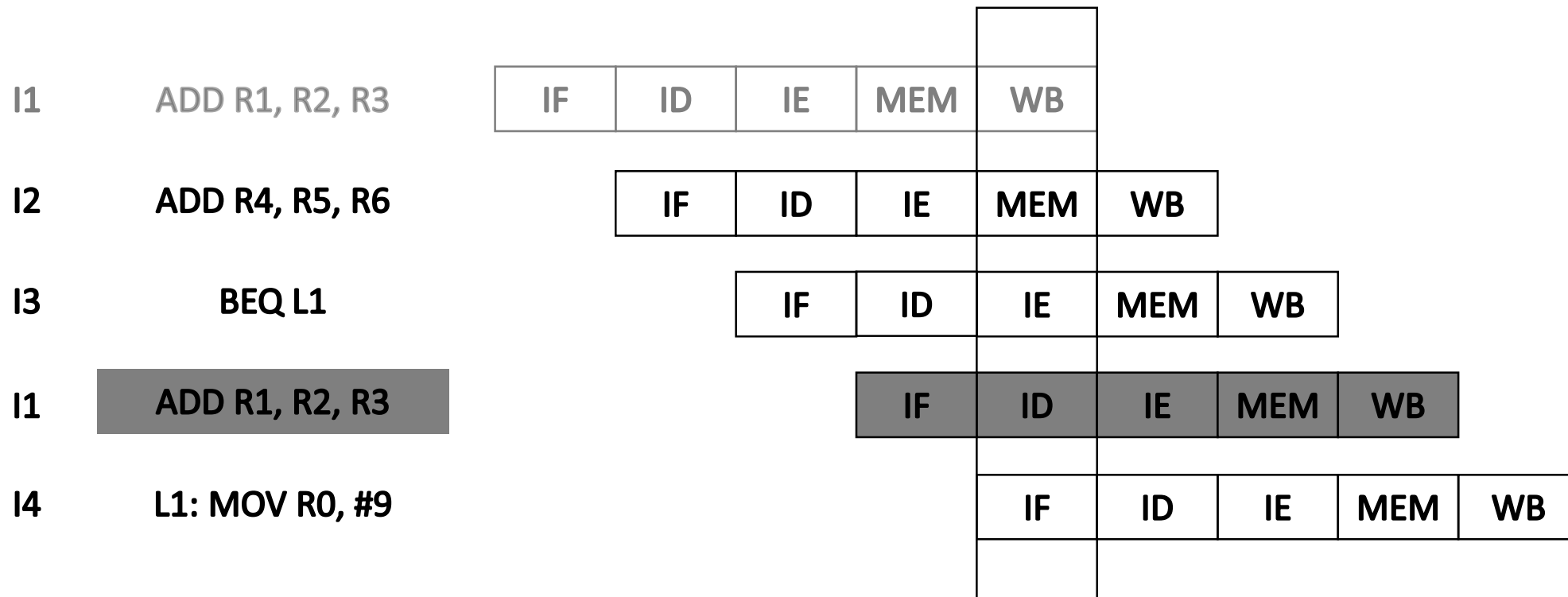
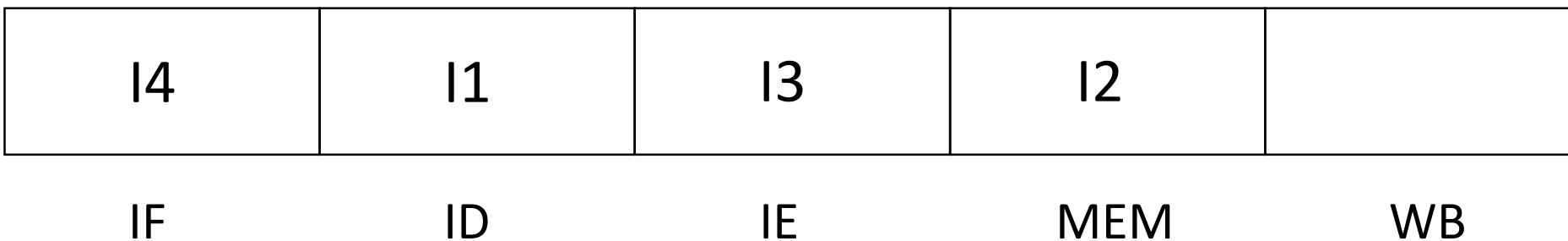


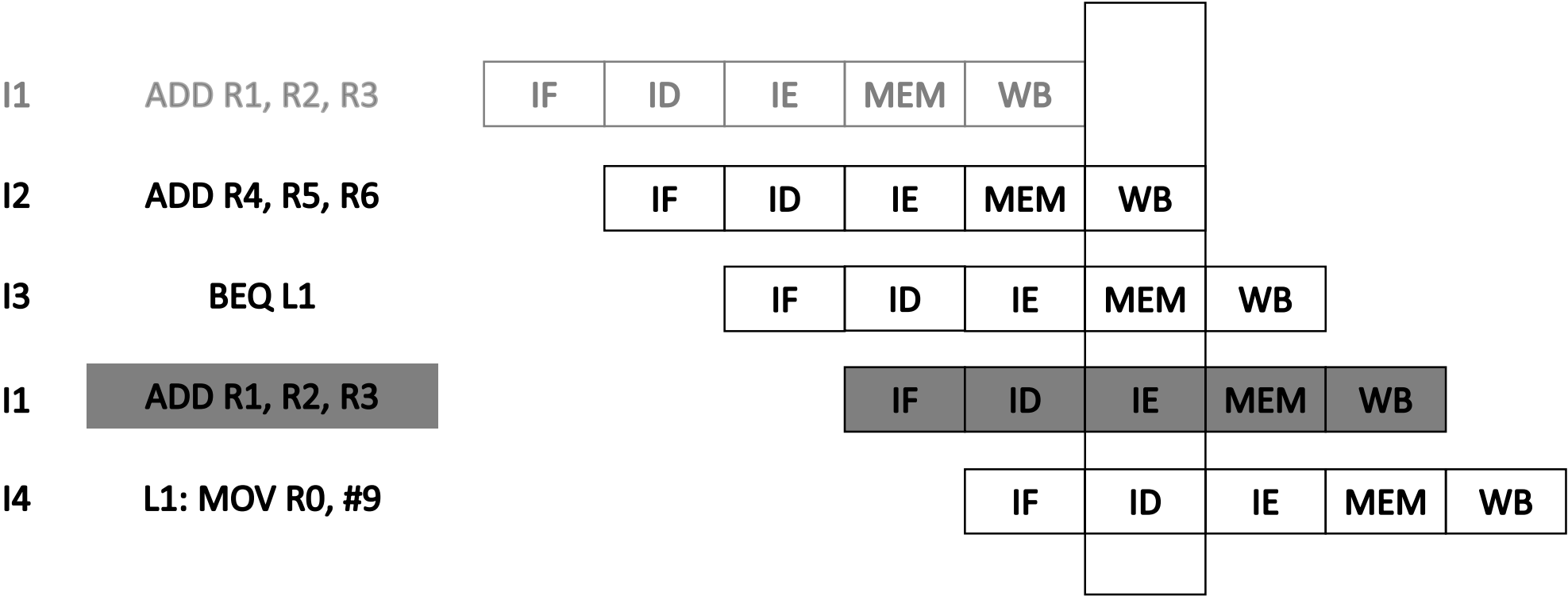
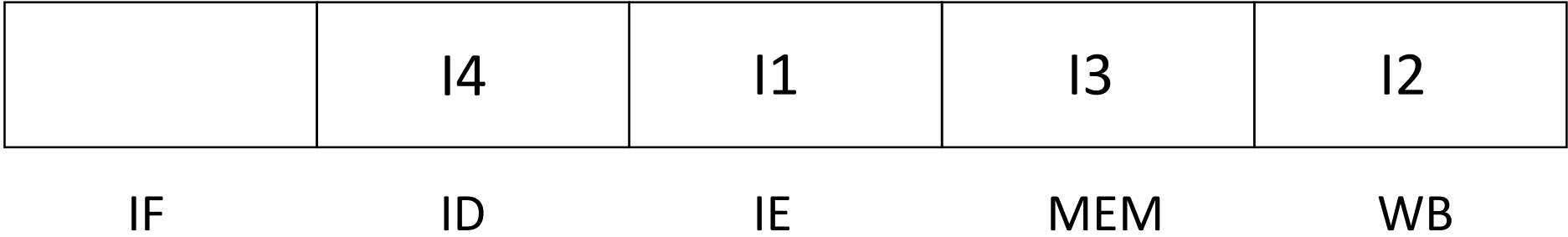
I4

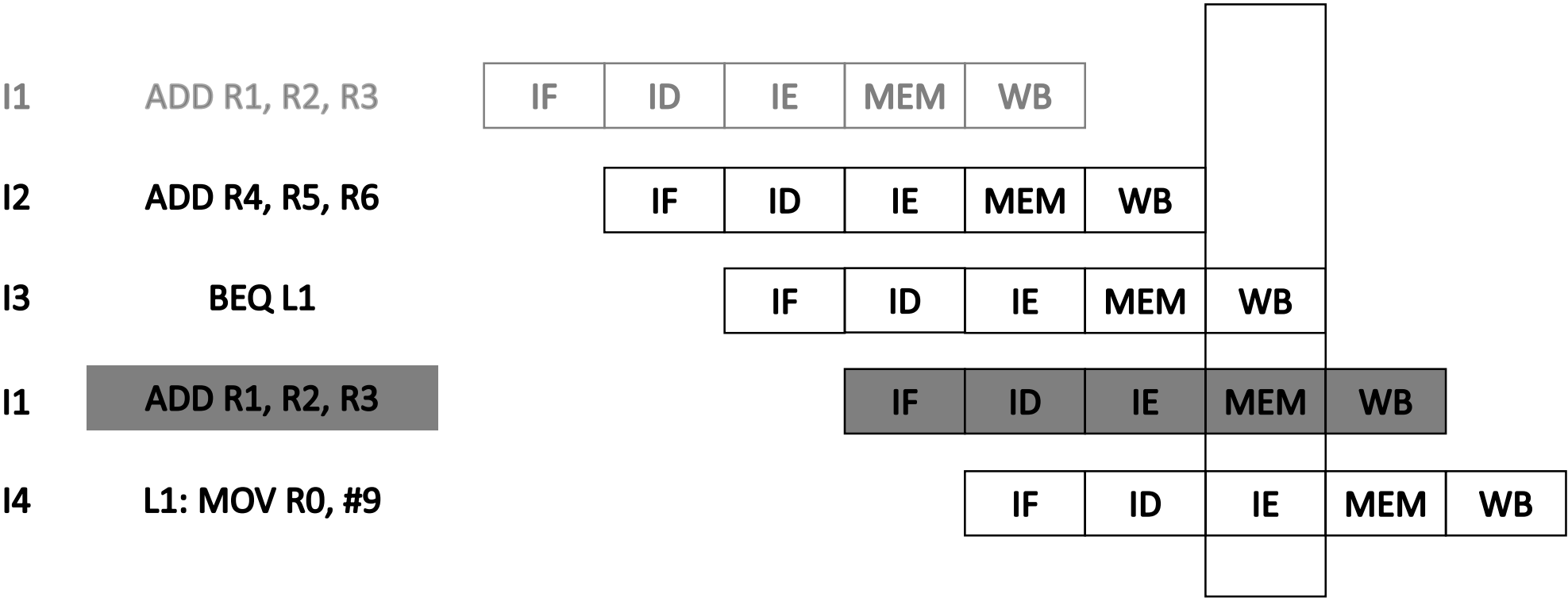
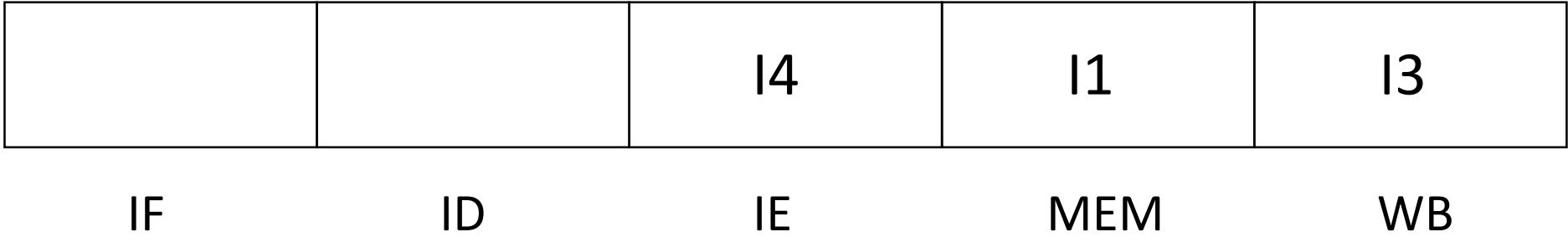
L1: MOV R0, #9



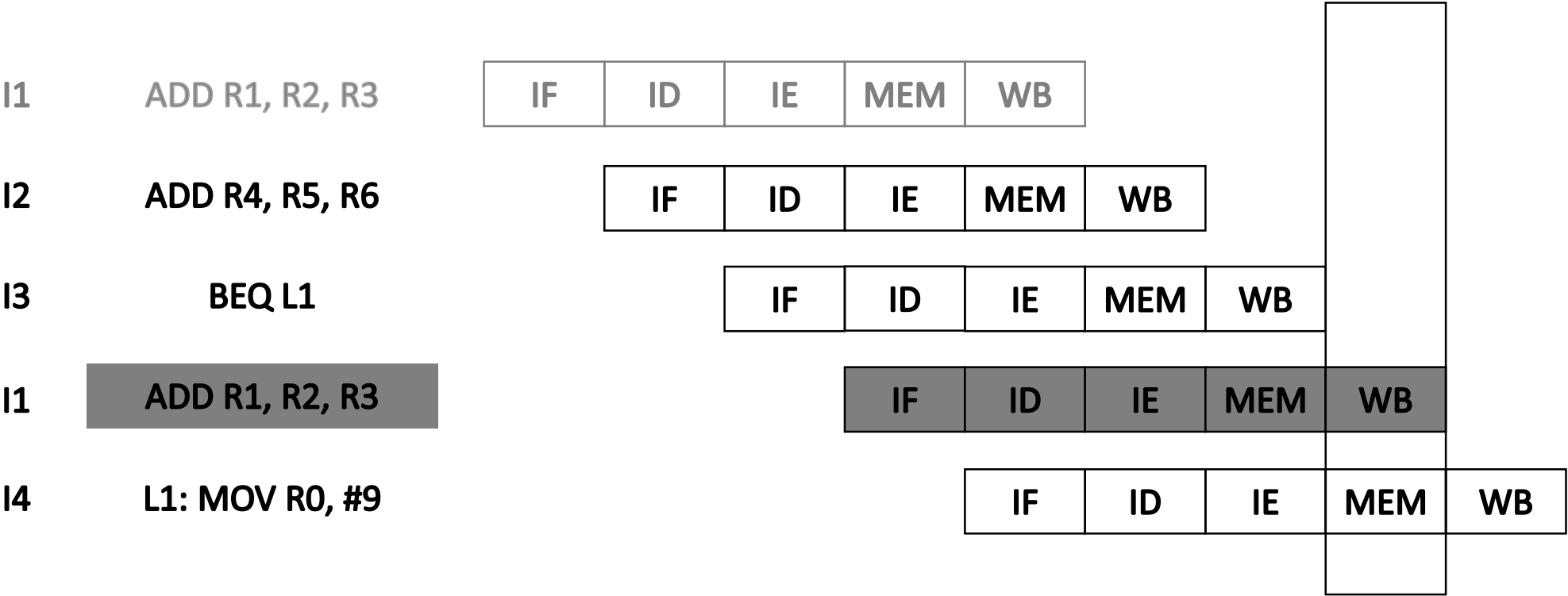
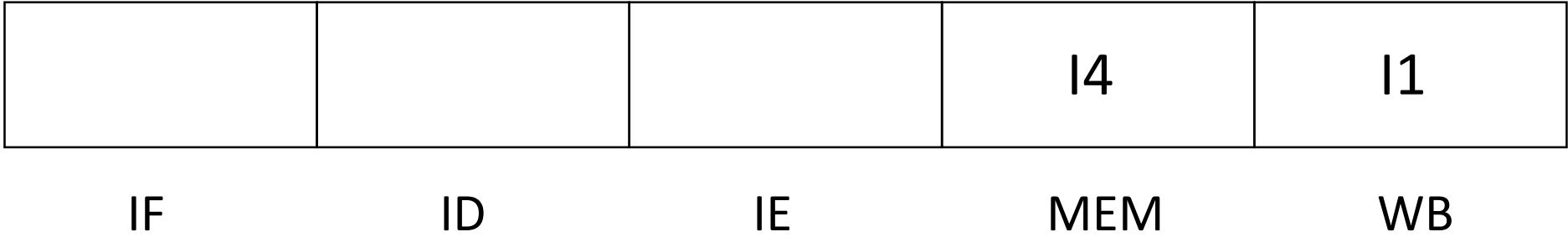




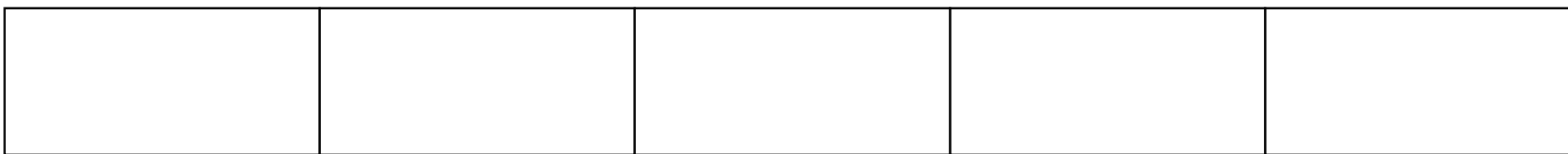












IF

ID

IE

MEM

WB

I1

ADD R1, R2, R3



I2

ADD R4, R5, R6



I3

BEQ L1



I1

ADD R1, R2, R3



I4

L1: MOV R0, #9

