



**Department of Computer Science & Engineering
Microprocessor & Computer Architecture**

UNIT 1 Assignment Part1

Unit-1: Introduction to Microprocessor & ISA

1. Differentiate between computer organization and computer architecture.
2. Differentiate between RISC and CISC architecture with two examples to each.
3. Is it possible to modify the contents of CPSR? If so how? Explain.
(Explore if not taught in the class)
4. How can IRQ interrupt be disabled or enabled? Write an example for the same. Also, explain whether the processor accepts the other exceptions while IRQ is executed?
5. The byte addressable memory system starts from location with an address 0x00306090. The system uses little endian storage. Show the memory allocation for the following data items declared in an ARM memory organization.
 - i. .byte 20, 50, 38, 45
 - ii. ..word 78650A09