UE20CS201 DIGITAL DESIGN AND COMPUTER ORGANIZATION

Lecture 48–52 notes

Reetinder Sidhu

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• Syllabus covered Microarchitecture: Single-Cycle, Multi-Cycle Processor: 7.4 (exclude 7.3.3, 7.3.4, 7.4.3) from [1]

References

[1] S. Harris and D. Harris. Digital Design and Computer Architecture 2nd Edition. Elsevier Science, 2012. ISBN: 9780128009116.