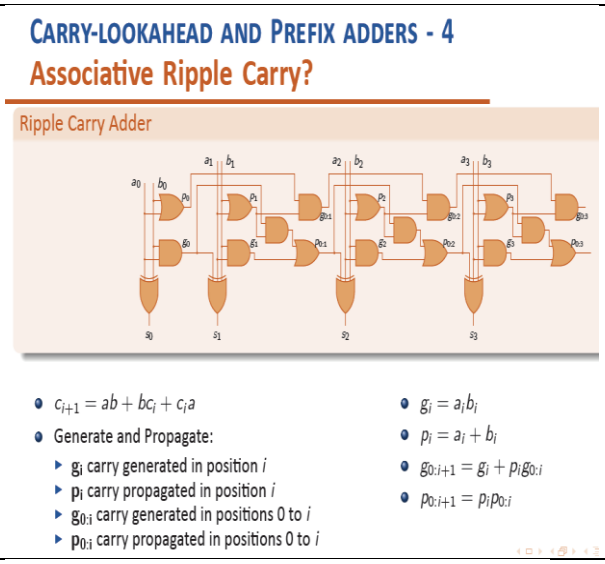
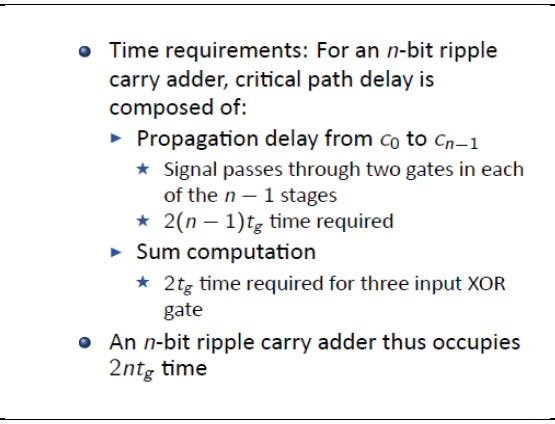
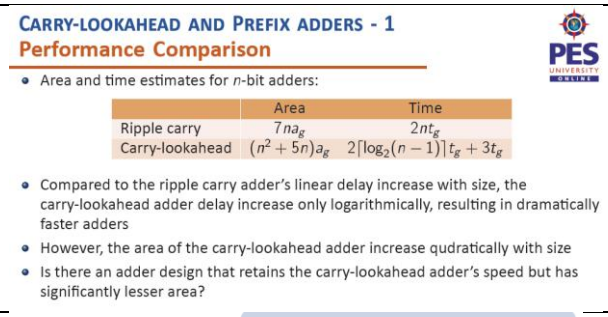
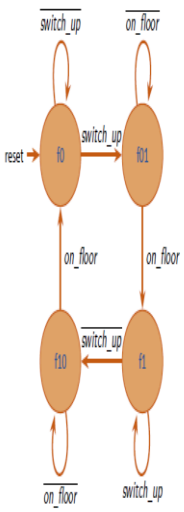
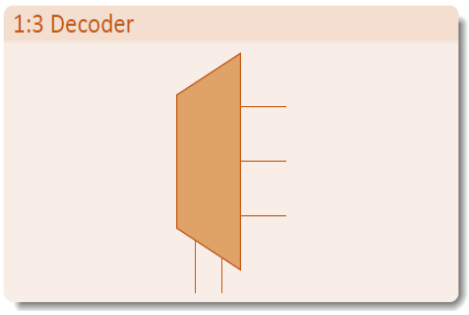
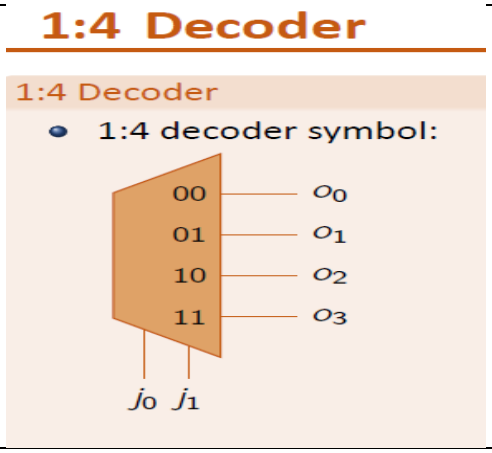
PES UNIVERSITY
3rd SEMESTER

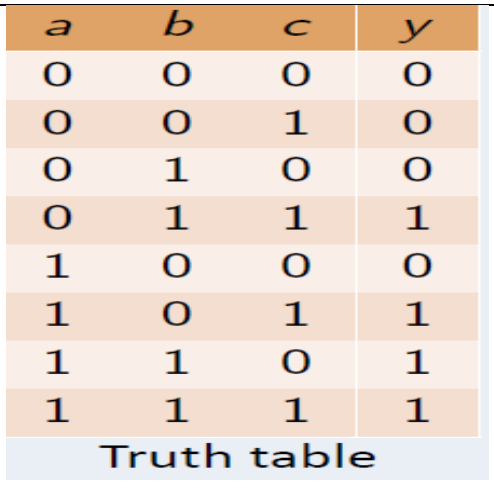
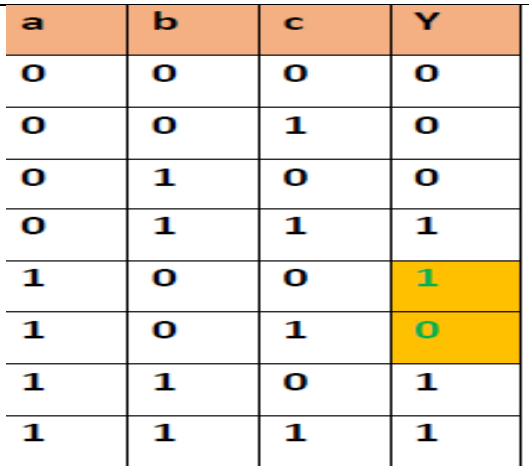
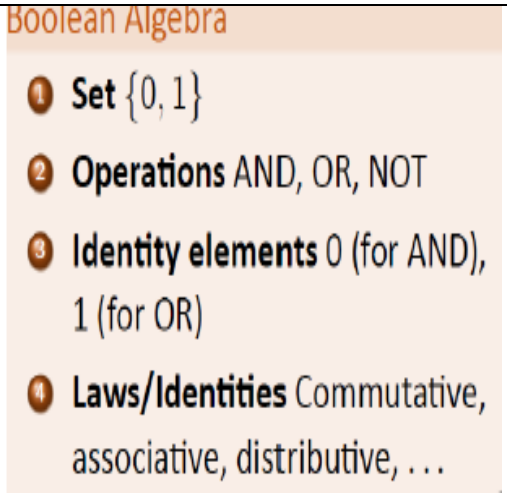
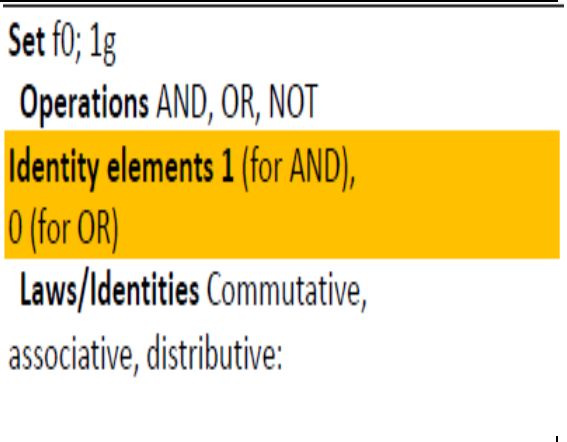
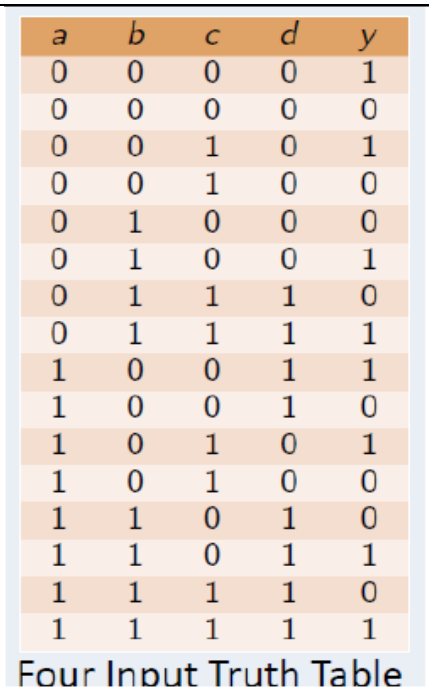
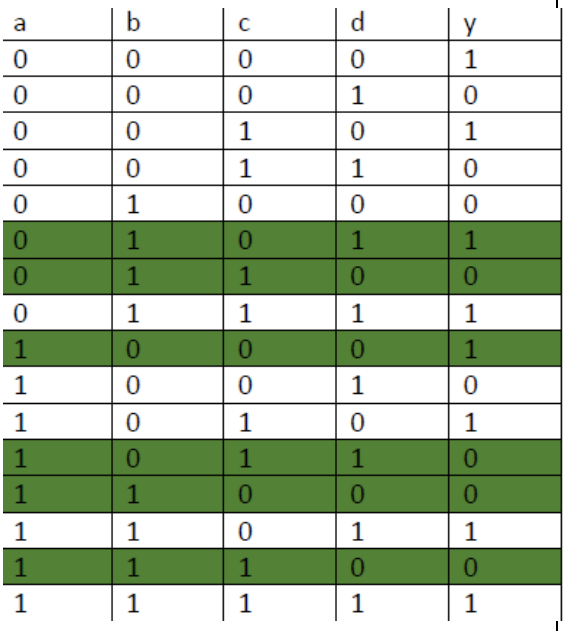
Errata for
DIGITAL DESIGN AND COMPUTER ORGANISATION
Slides Uploaded to PESU ACADEMY Portal

Errata Last updated 27th October 2020

This list is a work in progress. Some of the following corrections may be revised, and additional corrections will probably be added.

Sl. No	Lecture Number	Content in the slide	To be Corrected as
1.	UNIT3 Lecture 34 Slide 16		In the figure the labels p0: i and g0:1 need to be interchanged.
2.	UNIT3 Lecture 31 Slide 5		Propagation delay from c0 to c(n-1) should be 3(n-1)tg instead of 2(n-1) tg because although the carry signal passes through two gates in each stage, one of the gates has three inputs which counts as two 2 input gates..

3.	UNIT3 Lecture 31 Slide 34,35		log2(n-1) should be replaced by log2(n).
4.	UNIT3 Lecture 24 Slide 42 till Slide 83	 Elevator Example State Tansition Table Current State: s_3 s_2 s_1 s_0 | Inputs: switch_up, on_floor | Next State: s_3' s_2' s_1' s_0' 0 0 0 1 | 0 0 | 0 0 0 1 0 0 0 1 | 0 1 | 0 0 0 1 0 0 0 1 | 1 0 | 0 0 1 0 0 0 0 1 | 1 1 | 0 0 1 0 0 0 1 0 | 0 0 | 0 0 1 0 0 0 1 0 | 0 1 | 0 1 0 0 0 0 1 0 | 1 0 | 0 0 1 0 0 0 1 0 | 1 1 | 0 1 0 0 0 1 0 0 | 0 0 | 1 0 0 0 0 1 0 0 | 0 1 | 1 0 0 0 0 1 0 0 | 1 0 | 0 1 0 0 0 1 0 0 | 1 1 | 0 1 0 0 1 0 0 0 | 0 0 | 1 0 0 0 1 0 0 0 | 0 1 | 0 0 0 1 1 0 0 0 | 1 0 | 1 0 0 0 1 0 0 0 | 1 1 | 0 0 0 1	on_first should be equal to 1 in state 3(0100) instead of the incorrectly mentioned on_first should be equal to 1 in state 4(1000) 'lift_down' be 1 in state 4 (1000) according to the state diagram
5.	UNIT 2, Lecture 14 Slide 20		To be corrected as 2:3 decoder
6.	UNIT 2 Lecture 14 Slide 12-16		To be corrected as 2:4 decoder

7.	UNIT1, Lecture 3, Slide 12		
8.	UNIT1, Lecture 4, Slide 13		
9.	UNIT1, Lecture 8 Slide 4 till Slide 16,Also same error in Slide 40		

			The min terms for table are F(a ,b, c, d)=Σ(0,2,5,7,8,10,13,15)