

1. Explain in detail how cache coherence in multiprocessor environment is taken care of. Briefly describe different protocols
2. Explain with the help of a neat diagram, shared memory architecture in multicore systems, give suitable examples
3. Explain with the help of a neat diagram, distributed memory architecture in multicore systems, give suitable examples
4. Explain with the help of a neat diagram, hybrid memory architecture in multicore systems, give suitable examples
5. Explain with suitable sketches, architectural difference between single core and multicore architectures with relevant examples