

OCTOBER 2020: IN SEMESTER ASSESSMENT

B. Tech. (SEMESTER TEST – 1)

UE18CS302 - Operating System

Time: 2 Hrs	Answer All Questions	Max Marks: 60
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1.	a)	Clearly explain different states of the process drawing state diagram	4																											
	b)	Precisely explain fork(), exec(), and wait() system calls including parameters and return value	3																											
	c)	Precisely and clearly explain Primary benefits of Multiprocessor Based Systems	3																											
2.	a)	<p>Suppose that the following processes arrive for execution at the times indicated. Each process will run for the amount of time listed as Burst Time. In answering the questions, use non-preemptive scheduling, and base all decisions on the information you have at the time the decision must be made.</p> <table><tr><td>Process</td><td>Arrival Time</td><td>Burst Time</td></tr><tr><td>P1</td><td>0.0</td><td>8</td></tr><tr><td>P2</td><td>0.4</td><td>4</td></tr><tr><td>P3</td><td>1.0</td><td>1</td></tr></table> <ol style="list-style-type: none">1. What is the average turnaround time for these processes with the First Come First Serve (FCFS) scheduling algorithm?2. What is the average turnaround time for these processes with the Shortest Job First (SJF) scheduling algorithm?3. The SJF algorithm is supposed to improve performance, but notice that we chose to run process P1 at time 0 because we did not know that two shorter processes would arrive soon. Compute what the average turnaround time will be if the CPU is left idle for the first 1 unit and then SJF scheduling is used. Remember that processes P1 and P2 are waiting during this idle time, so their waiting time may increase. This algorithm could be known as <i>future-knowledge scheduling</i>.	Process	Arrival Time	Burst Time	P1	0.0	8	P2	0.4	4	P3	1.0	1	5 (1+2+2)															
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P1	0.0	8																												
P2	0.4	4																												
P3	1.0	1																												
b)	<p>The following processes are being scheduled using a pre-emptive, round-robin scheduling algorithm. Each process is assigned a numerical priority, with a higher number indicating a higher relative priority. In addition to the processes listed below, the system also has an idle task (which consumes no CPU resources and is identified as <i>P_{idle}</i>). This task has priority 0 and is scheduled whenever the system has no other available processes to run. The length of a time quantum is 10 units. If a process is pre-empted by a higher-priority process, the pre-empted process is placed at the end of the queue</p> <table><tr><td>Thread</td><td>Priority</td><td>Burst</td><td>Arrival</td></tr><tr><td><i>P₁</i></td><td>40</td><td>20</td><td>0</td></tr><tr><td><i>P₂</i></td><td>30</td><td>25</td><td>25</td></tr><tr><td><i>P₃</i></td><td>30</td><td>25</td><td>30</td></tr><tr><td><i>P₄</i></td><td>35</td><td>15</td><td>60</td></tr><tr><td><i>P₅</i></td><td>5</td><td>10</td><td>100</td></tr><tr><td><i>P₆</i></td><td>10</td><td>10</td><td>105</td></tr></table> <ol style="list-style-type: none">1. What is the turnaround time for each thread?2. What is the waiting time for each thread?3. What is the CPU utilization rate?	Thread	Priority	Burst	Arrival	<i>P₁</i>	40	20	0	<i>P₂</i>	30	25	25	<i>P₃</i>	30	25	30	<i>P₄</i>	35	15	60	<i>P₅</i>	5	10	100	<i>P₆</i>	10	10	105	5(2+2+1)
Thread	Priority	Burst	Arrival																											
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3.	a)	Precisely and clearly explain four conditions to occur simultaneously for deadlock	4																													
	b)	Precisely and clearly explain primary benefits of multithreading an applications	3																													
	c)	Consider a system consisting of four resources of the same type that are shared by three processes, each of which needs at most two resources. Show that the system is deadlock-free.	3																													
4.	a)	Give the correct and concise structure of reader and writer processes using semaphores for the first reader writer problem where no reader be kept waiting unless a writer has already obtained permission to use the shared object. Obviously, readers can access the shared object simultaneously.	5 (3+2)																													
	b)	Give a clear, correct and complete algorithm for deadlock detection among n processes and m resources type with multiple instances and mention the complexity of the algorithm.	5 (4+1)																													
5.	a)	Clearly Explain all the Steps to Service a Page Fault in a demand paging based system	4																													
	b)	Clearly differentiate between internal fragmentation and external fragmentation with example	3																													
	c)	Concisely describe and differentiate between Frame Allocation and Page replacement	3																													
6	a)	Given five memory partitions of 100 KB, 500 KB, 200 KB, 300 KB, and 600 KB (in order), 1. How would each of the first-fit, best-fit, and worst-fit algorithms place processes of 212 KB, 417 KB, 112 KB, and 426 KB (in order) 2. Which algorithm makes the most efficient use of memory?	5 (4+1)																													
	b)	Consider the segment table as follows : <table><tr><td>Segment</td><td>Base</td><td>Length</td></tr><tr><td>0</td><td>219</td><td>600</td></tr><tr><td>1</td><td>2300</td><td>14</td></tr><tr><td>2</td><td>90</td><td>100</td></tr><tr><td>3</td><td>1327</td><td>580</td></tr><tr><td>4</td><td>1352</td><td>96</td></tr></table> What are the Physical addresses for the following logical address? <table><tr><td>Seg#</td><td>Offset</td></tr><tr><td>0</td><td>430</td></tr><tr><td>1</td><td>10</td></tr><tr><td>2</td><td>500</td></tr><tr><td>3</td><td>400</td></tr><tr><td>4</td><td>112</td></tr></table>	Segment	Base	Length	0	219	600	1	2300	14	2	90	100	3	1327	580	4	1352	96	Seg#	Offset	0	430	1	10	2	500	3	400	4	112
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