

### DIGITAL DESIGN AND COMPUTER ORGANIZATION

#### Finite State Machines - 2

#### **Reetinder Sidhu**

Department of Computer Science and Engineering



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#### FINITE STATE MACHINES - 2

#### **Course Outline**



- Digital Design
  - Combinational logic design
  - Sequential logic design
    - ★ Finite State Machines 2
- Computer Organization
  - Architecture (microprocessor instruction set)
  - Microarchitecure (microprocessor operation)

#### Concepts covered

Finite State Machine Design Example

#### FINITE STATE MACHINES - 2

#### How to Design Synchronous Sequential Logic Circuits? (Moore FSM)

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- Determine inputs and outputs
- State transition diagram
- Encoding tables
  - State
  - Output
- State transition table
- Output table
- Logic minimization
  - State transition table yields Boolean formulas for next state logic
  - Output table yields Boolean formulas for output logic
- Logic circuit construction





Source: platformliftco

 Problem is to design the control logic for a lift in a building of two floors: ground and first





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- Problem is to design the control logic for a lift in a building of two floors: ground and first
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- Control logic should also output signals indicating which floor the lift is on





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  - on\_first is 1 when lift is on first floor and 0 otherwise
- Elevator initially on ground floor





Source: platformliftco



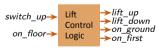
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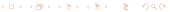
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- Elevator initially on ground floor





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#### State Transition Diagram

- A visual representation of an FSM
  - States represent by circles (called nodes/vertices)
  - Transitions between states represented by directed line segments (called arcs/edges/arrows
  - Each edge is labeled with labeled with a Boolean formula of inputs corresponding to the transition

- In each clock cycle, only one state is active
- Active state transitions occur at the rising edge of the clock signal
- If a state is active in the current clock cycle, and it has an outgoing transition labeled with a Boolean formula which is 1 in that clock cycle, then the destination state of the transition will be the active state in the next clock cycle

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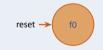
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**Elevator Example State Transition Diagram** 

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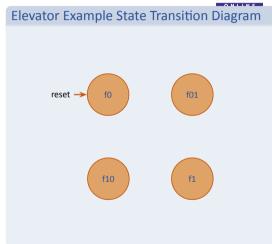
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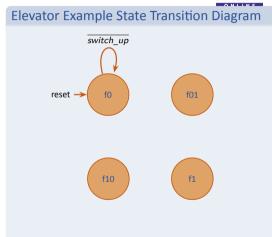


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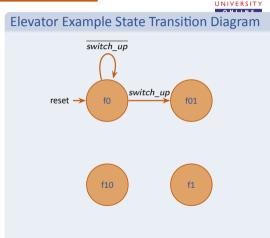
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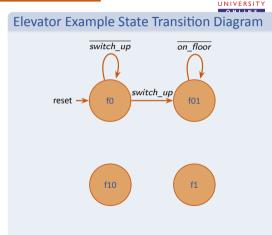
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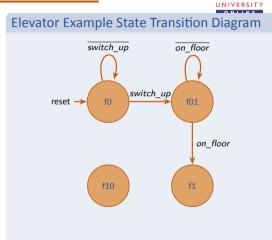
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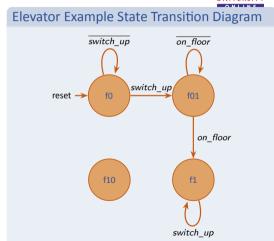
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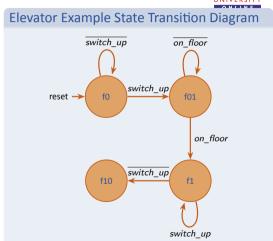
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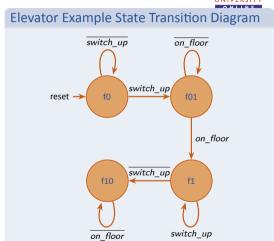
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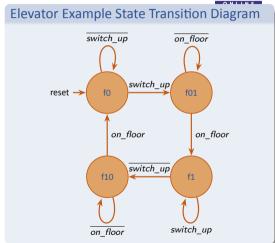


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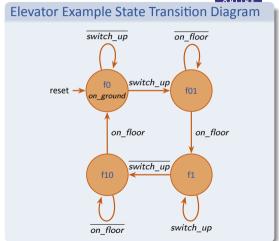






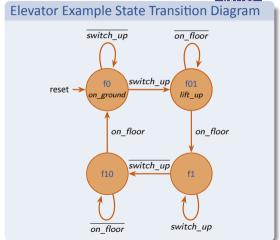


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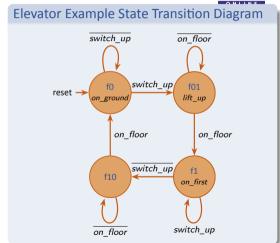


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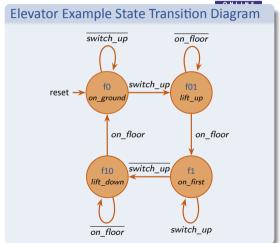


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# Elevator State Encoding Table State Encoding $(s_1 s_0)$ f0 00 f01 01 f1 11 f10 10

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f0	00
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#### **Elevator Output Encoding Tables**

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State	Encoding $(s_1 s_0)$
f0	00
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f1	11
f10	10

#### **Elevator Output Encoding Tables**

on\_ground

Meaning	Encoding
Lift on ground floor	1
Lift anywhere else	0

# FINITE STATE MACHINES - 2 State and Output Encoding Tables

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on\_ground

Meaning Encoding

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Lift anywhere else 0

on_first	
Meaning	Encoding
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Lift anywhere else	0

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State Encodi	$ng(s_1 s_0)$
	00
	01
	11
	10

#### **Elevator Output Encoding Tables**

•	on_ground		•	on_first	
	Meaning	Encoding		Meaning	Encoding
	Lift on ground floor	1		Lift on first floor	1
	Lift anywhere else	0		Lift anywhere else	0

lift\_up

Meaning	Encoding
Lift going from ground to first floor	1
Lift anywhere else	0

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f01	01
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f10	10

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on_ground	(	•	on_first	
Meaning	Encoding		Meaning	Encoding
Lift on ground floor	1		Lift on first floor	1
Lift anywhere else	0		Lift anywhere else	0

lift\_up

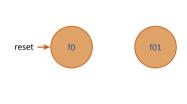
Meaning	Encoding
Lift going from ground to first floor	1
Lift anywhere else	0

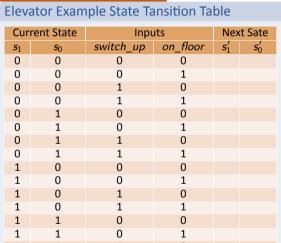
Iift\_down

Meaning	Encoding
Lift going from first to ground floor	1
Lift anywhere else	0

### **State Transition Table**

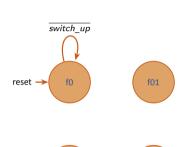






### **State Transition Table**



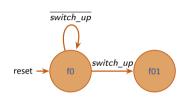


f10

			_			
Elevator Example State Tansition Table						
Cur	rent State	Inputs		Next Sate		
<b>s</b> <sub>1</sub>	<b>s</b> 0	switch_up	on_floor	$s_1'$	$s_0'$	
0	0	0	0	0	0	
0	0	0	1	0	0	
0	0	1	0			
0	0	1	1			
0	1	0	0			
0	1	0	1			
0	1	1	0			
0	1	1	1			
1	0	0	0			
1	0	0	1			
1	0	1	0			
1	0	1	1			
1	1	0	0			
1	1	0	1			
1	1	1	0			

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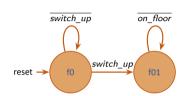




Cur	rent State		Inputs		t Sate
<b>s</b> <sub>1</sub>	<b>s</b> <sub>0</sub>	switch_up	on_floor	$s_1'$	$s_0'$
0	0	0	0	0	0
0	0	0	1	0	0
0	0	1	0	0	1
0	0	1	1	0	1
0	1	0	0		
0	1	0	1		
0	1	1	0		
0	1	1	1		
1	0	0	0		
1	0	0	1		
1	0	1	0		
1	0	1	1		
1	1	0	0		
1	1	0	1		
1	1	1	0		
1	1	1	1		

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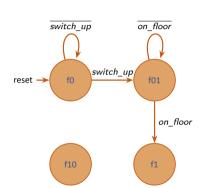




Curi	rent State	Inpu	uts	Nex	t Sate
$s_1$	<b>s</b> <sub>0</sub>	switch_up	on_floor	$s_1'$	$s_0'$
0	0	0	0	0	0
0	0	0	1	0	0
0	0	1	0	0	1
0	0	1	1	0	1
0	1	0	0	0	1
0	1	0	1		
0	1	1	0	0	1
0	1	1	1		
1	0	0	0		
1	0	0	1		
1	0	1	0		
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1	1	0	0		
1	1	0	1		
1	1	1	0		
1	1	1	1		

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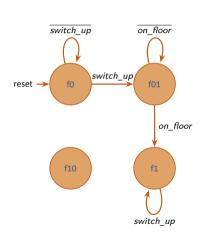




Cur	rent State	Inpu	ıts	Nex	t Sate
<b>s</b> <sub>1</sub>	<b>s</b> <sub>0</sub>	switch_up	on_floor	$s_1'$	<b>s</b> ' <sub>0</sub>
0	0	0	0	0	0
0	0	0	1	0	0
0	0	1	0	0	1
0	0	1	1	0	1
0	1	0	0	0	1
0	1	0	1	1	1
0	1	1	0	0	1
0	1	1	1	1	1
1	0	0	0		
1	0	0	1		
1	0	1	0		
1	0	1	1		
1	1	0	0		
1	1	0	1		
1	1	1	0		
1	1	1	1		

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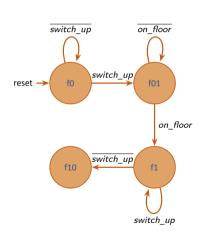




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Cur	rent State	Inpu	uts	Nex	ct Sate			
<b>s</b> <sub>1</sub>	<b>s</b> <sub>0</sub>	switch_up	on_floor	$s_1'$	$s_0'$			
0	0	0	0	0	0			
0	0	0	1	0	0			
0	0	1	0	0	1			
0	0	1	1	0	1			
0	1	0	0	0	1			
0	1	0	1	1	1			
0	1	1	0	0	1			
0	1	1	1	1	1			
1	0	0	0					
1	0	0	1					
1	0	1	0					
1	0	1	1					
1	1	0	0					
1	1	0	1					
1	1	1	0	1	1			
1	1	1	1	1	1			

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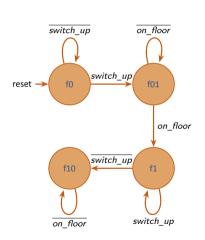




Cur	Current State Inputs				t Sate
<b>s</b> <sub>1</sub>	<b>s</b> <sub>0</sub>	switch_up	on_floor	$s_1'$	$s_0'$
0	0	0	0	0	0
0	0	0	1	0	0
0	0	1	0	0	1
0	0	1	1	0	1
0	1	0	0	0	1
0	1	0	1	1	1
0	1	1	0	0	1
0	1	1	1	1	1
1	0	0	0		
1	0	0	1		
1	0	1	0		
1	0	1	1		
1	1	0	0	1	0
1	1	0	1	1	0
1	1	1	0	1	1
1	1	1	1	1	1

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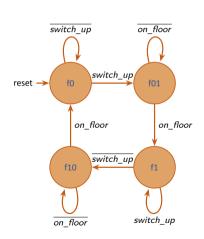




Cur	rent State	Inpu	ıts	Nex	t Sate
<i>s</i> <sub>1</sub>	<b>s</b> <sub>0</sub>	switch_up	on_floor	$s_1'$	$s_0'$
0	0	0	0	0	0
0	0	0	1	0	0
0	0	1	0	0	1
0	0	1	1	0	1
0	1	0	0	0	1
0	1	0	1	1	1
0	1	1	0	0	1
0	1	1	1	1	1
1	0	0	0	1	0
1	0	0	1		
1	0	1	0	1	0
1	0	1	1		
1	1	0	0	1	0
1	1	0	1	1	0
1	1	1	0	1	1
1	1	1	1	1	1

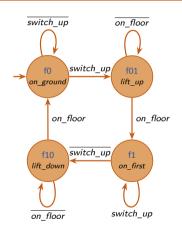
### **State Transition Table**





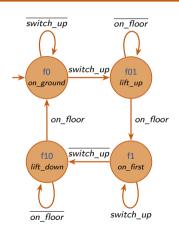
Cur	rent State	nt State Inputs			
<b>s</b> <sub>1</sub>	<b>s</b> <sub>0</sub>	switch_up	on_floor	$s_1'$	<b>s</b> '_0
0	0	0	0	0	0
0	0	0	1	0	0
0	0	1	0	0	1
0	0	1	1	0	1
0	1	0	0	0	1
0	1	0	1	1	1
0	1	1	0	0	1
0	1	1	1	1	1
1	0	0	0	1	0
1	0	0	1	0	0
1	0	1	0	1	0
1	0	1	1	0	0
1	1	0	0	1	0
1	1	0	1	1	0
1	1	1	0	1	1
1	1	1	1	1	1





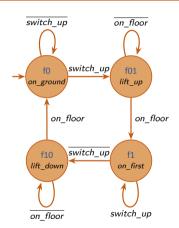
Sta	ate	Outputs				
$s_1$	<i>s</i> <sub>0</sub>	on_ground	on_first	lift_up	lift_down	
0	0					
0	1					
1	0					
1	1					





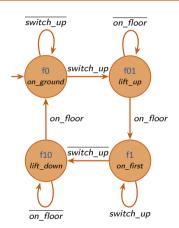
Sta	ate	Outputs				
$s_1$	<i>s</i> <sub>0</sub>	on_ground	on_first	lift_up	lift_down	
0	0	1				
0	1					
1	0					
1	1					





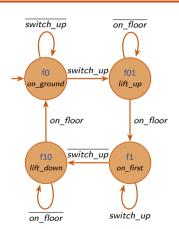
State		Outputs				
$s_1$	<i>s</i> <sub>0</sub>	on_ground	on_first	lift_up	lift_down	
0	0	1				
0	1	0				
1	0	0				
1	1	0				





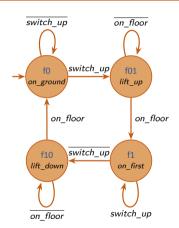
State		Outputs				
$s_1$	<i>s</i> <sub>0</sub>	on_ground	on_first	lift_up	lift_down	
0	0	1				
0	1	0		1		
1	0	0				
1	1	0				





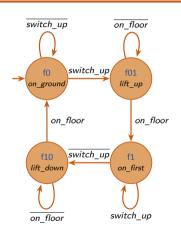
Sta	ate	Outputs				
$s_1$	<i>s</i> <sub>0</sub>	on_ground	on_first	lift_up	lift_down	
0	0	1		0		
0	1	0		1		
1	0	0		0		
1	1	0		0		





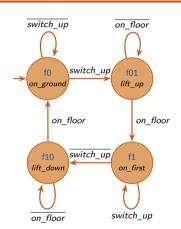
	State		Outputs				
S	1	<i>s</i> <sub>0</sub>	on_ground	on_first	lift_up	lift_down	
C	)	0	1		0		
C	)	1	0		1		
1	1	0	0		0		
1	1	1	0	1	0		





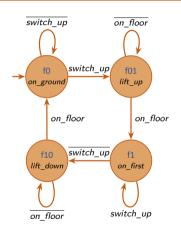
State		Outputs				
$s_1$	<i>s</i> <sub>0</sub>	on_ground	on_first	lift_up	lift_down	
0	0	1	0	0		
0	1	0	0	1		
1	0	0	0	0		
1	1	0	1	0		





Sta	ate	Outputs				
$s_1$	<i>s</i> <sub>0</sub>	on_ground	on_first	lift_up	lift_down	
0	0	1	0	0		
0	1	0	0	1		
1	0	0	0	0	1	
1	1	0	1	0		





State		Outputs				
$s_1$	<i>s</i> <sub>0</sub>	on_ground	on_first	lift_up	lift_down	
0	0	1	0	0	0	
0	1	0	0	1	0	
1	0	0	0	0	1	
1	1	0	1	0	0	



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State transition table:

State transition table.									
Cur	rent State	Inpu	Next Sate						
<b>s</b> 1	<b>s</b> 0	switch_up	on_floor	$s_1'$	$s_0'$				
0	0	0	0	0	0				
0	0	0	1	0	0				
0	0	1	0	0	1				
0	0	1	1	0	1				
0	1	0	0	0	1				
0	1	0	1	1	1				
0	1	1	0	0	1				
0	1	1	1	1	1				
1	0	0	0	1	0				
1	0	0	1	0	0				
1	0	1	0	1	0				
1	0	1	1	0	0				
1	1	0	0	1	0				
1	1	0	1	1	0				
1	1	1	0	1	1				
1	1	1	1	1	1				



State transition table:

Ì		rent State	Inputs		Next Sate	
	<b>s</b> 1	<b>s</b> 0	switch_up	on_floor	$s_1'$	$s_0'$
ĺ	0	0	0	0	0	0
	0	0	0	1	0	0
	0	0	1	0	0	1
	0	0	1	1	0	1
	0	1	0	0	0	1
	0	1	0	1	1	1
	0	1	1	0	0	1
	0	1	1	1	1	1
	1	0	0	0	1	0
	1	0	0	1	0	0
	1	0	1	0	1	0
	1	0	1	1	0	0
	1	1	0	0	1	0
	1	1	0	1	1	0
	1	1	1	0	1	1
	1	1	1	1	1	1

## Minimized Boolean Formula • K-map for $s_1'$ : switch\_up on\_floor 00 01 11 10 **S**1 **S**0 00 01 11 10

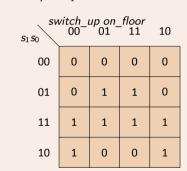


State transition table:

	rent State	Inputs		Next Sate	
<b>s</b> <sub>1</sub>	<b>s</b> 0	switch_up	on_floor	$s_1'$	$s_0'$
0	0	0	0	0	0
0	0	0	1	0	0
0	0	1	0	0	1
0	0	1	1	0	1
0	1	0	0	0	1
0	1	0	1	1	1
0	1	1	0	0	1
0	1	1	1	1	1
1	0	0	0	1	0
1	0	0	1	0	0
1	0	1	0	1	0
1	0	1	1	0	0
1	1	0	0	1	0
1	1	0	1	1	0
1	1	1	0	1	1
1	1	1	1	1	1

#### Minimized Boolean Formula

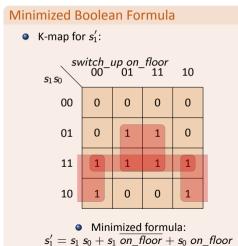
• K-map for  $s_1'$ :





State transition table:

Cur	rent State	Inpu	ıts	Next Sate	
<b>s</b> 1	<b>S</b> 0	switch_up	on_floor	$s_1'$	$s_0'$
0	0	0	0	0	0
0	0	0	1	0	0
0	0	1	0	0	1
0	0	1	1	0	1
0	1	0	0	0	1
0	1	0	1	1	1
0	1	1	0	0	1
0	1	1	1	1	1
1	0	0	0	1	0
1	0	0	1	0	0
1	0	1	0	1	0
1	0	1	1	0	0
1	1	0	0	1	0
1	1	0	1	1	0
1	1	1	0	1	1
1	1	1	1	1	1



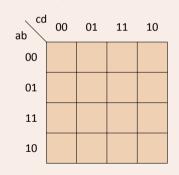


State transition table:

Cur	rent State	Inputs		Next Sate	
<b>s</b> 1	<b>S</b> 0	switch_up	on_floor	$s_1'$	$s_0'$
0	0	0	0	0	0
0	0	0	1	0	0
0	0	1	0	0	1
0	0	1	1	0	1
0	1	0	0	0	1
0	1	0	1	1	1
0	1	1	0	0	1
0	1	1	1	1	1
1	0	0	0	1	0
1	0	0	1	0	0
1	0	1	0	1	0
1	0	1	1	0	0
1	1	0	0	1	0
1	1	0	1	1	0
1	1	1	0	1	1
1	1	1	1	1	1

#### Minimized Boolean Formula

• K-map for  $s_0'$ :



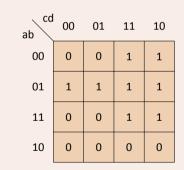


State transition table:

Cur	rent State	Inputs		Next Sate	
<b>s</b> <sub>1</sub>	<b>s</b> 0	switch_up	on_floor	$s_1'$	$s_0'$
0	0	0	0	0	0
0	0	0	1	0	0
0	0	1	0	0	1
0	0	1	1	0	1
0	1	0	0	0	1
0	1	0	1	1	1
0	1	1	0	0	1
0	1	1	1	1	1
1	0	0	0	1	0
1	0	0	1	0	0
1	0	1	0	1	0
1	0	1	1	0	0
1	1	0	0	1	0
1	1	0	1	1	0
1	1	1	0	1	1
1	1	1	1	1	1

#### Minimized Boolean Formula

• K-map for  $s_0'$ :



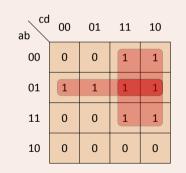


State transition table:

Current State		Inputs		Next Sate	
<b>s</b> <sub>1</sub>	<b>s</b> 0	switch_up	on_floor	$s_1'$	$s_0'$
0	0	0	0	0	0
0	0	0	1	0	0
0	0	1	0	0	1
0	0	1	1	0	1
0	1	0	0	0	1
0	1	0	1	1	1
0	1	1	0	0	1
0	1	1	1	1	1
1	0	0	0	1	0
1	0	0	1	0	0
1	0	1	0	1	0
1	0	1	1	0	0
1	1	0	0	1	0
1	1	0	1	1	0
1	1	1	0	1	1
1	1	1	1	1	1

#### Minimized Boolean Formula

• K-map for  $s_0'$ :



Minimized formula:

$$s_0' = \overline{s_1} \, s_0 + \overline{s_1} \, switch\_up + s_0 \, switch\_up$$

## **Logic Diagram**



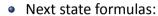
- $s_1' = s_1 s_0 + s_1 \overline{on\_floor} + s_0 on\_floor$   $s_0' = \overline{s_1} s_0 + \overline{s_1} switch up + s_0 switch up$



### Output formulas:

- ightharpoonup on  $ground = \overline{s_1} \overline{s_0}$
- ightharpoonup on\_first =  $s_1 s_0$
- lift  $up = \overline{s_1}s_0$
- ▶ lift  $down = s_1 \overline{s_0}$

## **Logic Diagram**



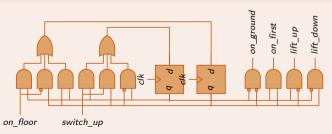
- $s_1' = s_1 s_0 + s_1 \overline{on\_floor} + s_0 on\_floor$   $s_0' = \overline{s_1} s_0 + \overline{s_1}$  switch  $up + s_0$  switch up



### Output formulas:

- ightharpoonup on ground  $= \overline{s_1} \, \overline{s_0}$
- ightharpoonup on first =  $s_1 s_0$
- lift  $up = \overline{s_1}s_0$
- ▶ lift  $down = s_1 \overline{s_0}$

#### **Elevator Example Logic Diagram**



Think About It



Design of a Moore FSM for traffic light controller (section 3.4.1 of your textbook)