

Assignment questions on 4th unit

- 1.Explain with diagrams : Flynn's Classification of SIMD,SIMD,MISD and MIMD
- 2.Discuss in detail about symmetric and distributed shared memory architecture
- 3.Draw and explain the structure of a superscalar processor
4. Differentiate between UMA and NUMA architectures, with suitable diagrams(if any)
- 5.explain with a neat diagram , symmetric shared memory architecture
- 6.Explain the various approaches to Parallel Programming.
- 7.Differentiate symmetric memory architecture and distributed memory architecture.
- 8.Explain in detail the distributed shared memory architecture highlighting the directory based cache coherence protocol.
9. Explain with examples different ILP techniques.
10. What is Parallelism? Explain its different types with appropriate examples.
- 11.Mention different approaches to parallel programming
12. Explain different memory architecture with an example each
13. Address translation time needs to be reduced , Explain the meaning of the statement, also describe how this is achieved

14. what are the entries of TLB? Explain TLBs role in reducing the address translation time