

UE20CS201  
DIGITAL DESIGN AND COMPUTER  
ORGANIZATION  
Lecture 29–30 notes

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- **Syllabus covered** Memory arrays: 5.5, 5.5.1 from [1](also brief descriptions of SRAM, DRAM,ROM and register file (done in lab) with one application each (cache,main memory etc.), no need to explain their structure).

## References

- [1] S. Harris and D. Harris. *Digital Design and Computer Architecture 2<sup>nd</sup> Edition*. Elsevier Science, 2012. ISBN: 9780128009116.