

UE20CS201  
DIGITAL DESIGN AND COMPUTER  
ORGANIZATION  
Lecture 3 notes  
  
Reetinder Sidhu  
  
PES University

- Boolean formulas are covered in 2.2.1 in [1]. The slides describe Boolean formulas more formally which simplifies explanation of combinational logic.
- Truth table from logic circuit is covered in 2.3.4 in [1].

## References

- [1] S. Harris and D. Harris. *Digital Design and Computer Architecture 2<sup>nd</sup> Edition*. Elsevier Science, 2012. ISBN: 9780128009116.