



Embedded UHF RFID Transponder into the Layout of a Printed Circuit Board

Dr. Stoyan Iliev
Kathrein RFID
CST European User Group Meeting 2011
19 May 2011, Munich



RFID

- 1. UHF RFID transponder theory
- 2. Murata MagicStrap types
- 3. Design of embedded transponder
 - a. Import of existing layout
 - b. Assessment of recommended antenna designs
 - c. CST based optimised design
- 4. Comparison of simulation and read range measurement

KATHREIN

5. Conclusion

1. UHF RFID transponder theory



RFID

Simplified transponder equivalent circuit

$R_{A}+jX_{A}$ $R_{L}+jX_{L}$

Power delivered to the load

$$P_L = \frac{1}{2} \frac{\left| V_0 \right|^2}{4R_A} \cdot g(\Delta, \Gamma)$$

Transmission coefficient (TC)

$$g(\Delta, \Gamma) = \frac{4\Delta}{(1+\Delta)^2 + \Gamma^2}$$

$$\Delta = \frac{R_L}{R_A}, \Gamma = \frac{X_A + X_L}{R_A}.$$

Transmission coefficient:

shows the amount of power delivered to the load

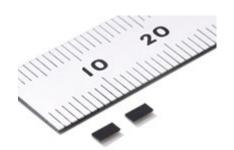
TC equals 0 (0%) – no power is transferred to the load

TC equals 1 (100%) – the available power is transferred to the load

2. Murata MagicStrap® Types



RFID



SMD RFID module, which incorporates a standard IC and enables the traceability of PCBs from the first manufacturing step

Impedances at minimum operating power

	•							
			Type1		Type2		Type3	Type4
MAGICSTRAP®			LXMS31ACNA		LXMS31ACNA	1	LXMS31ACNA	LXMS31ACNA
P/N			- 009		- 010	ı	- 011	- 012
			LXMS31ACNB		LXMS31ACNB		LXMS31ACNB	LXMS31ACNB
Parameter			- 019		- 020		- 021	- 022
Impedance value	@866.5 MHz	R	15		12		25	80
		Х	-45	Ī	-107	l	-200	-405
	@915.0 MHz	R	25		12		25	80
		Х	-45		-107		-200	-420
	@953.0	R	30		9		20	60
	MHz	Х	-48		-105		-195	-425

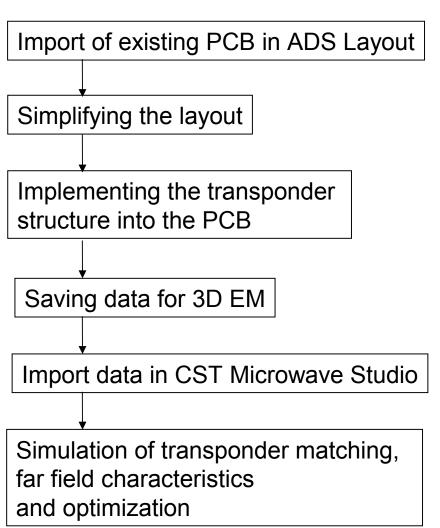
MAGICSTRAP® Technical Data Sheet

3. Design of the embedded transponder



RFID

<u>Design steps</u>



Goal

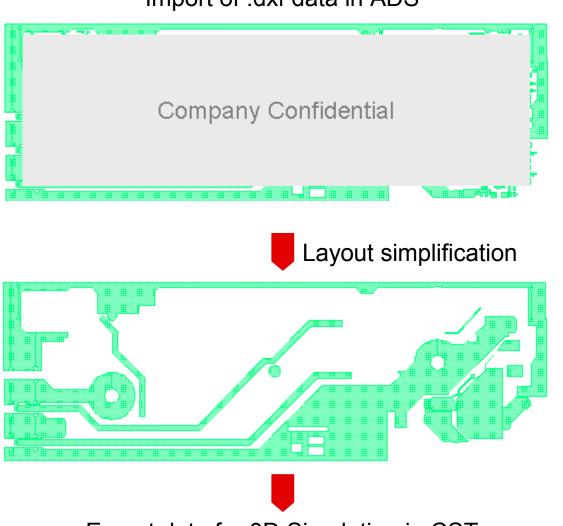
To obtain a high TC, not only for a single PCB, but also for the boards in a panel with multiple PCBs

3.a Import of existing layout



RFID

Import of .dxf data in ADS

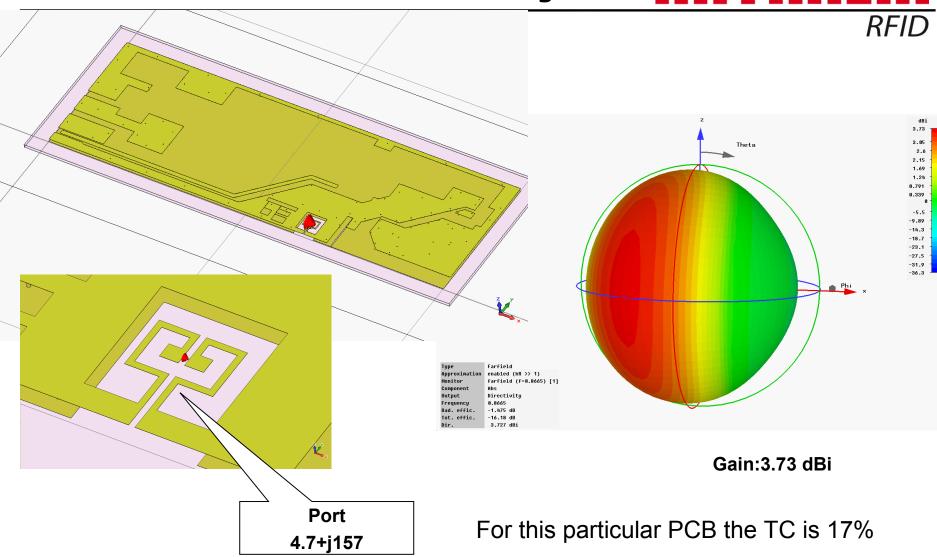


Interoperability between ADS Layout und CST Microwave Studio offers excellent export and import capabilities

Export data for 3D Simulation in CST

3.b Assessment of recommended design



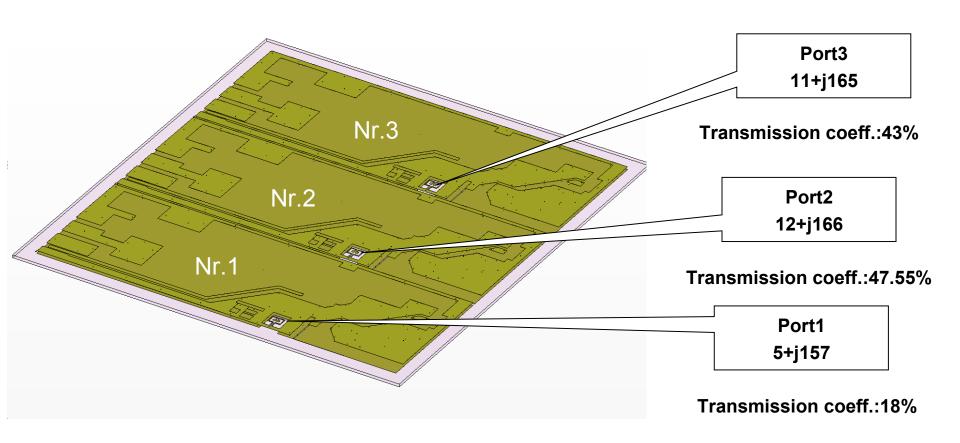


3.b Assessment of recommended design



RFID

Port impedances of recommended antenna designs in a panel with multiple PCBs

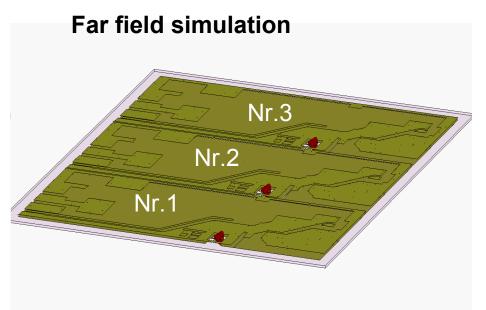


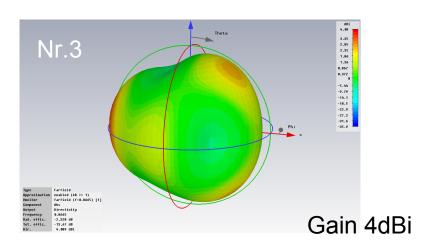
The port impedances show rather good matching between Murata Chip and recommended antenna design

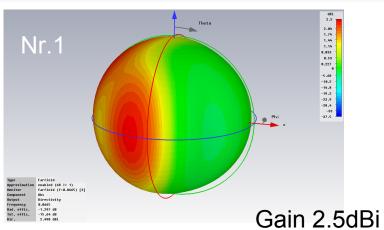
3.b Assessment of recommended design

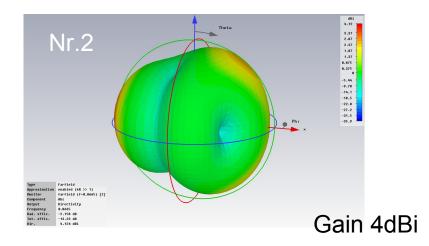


RFID





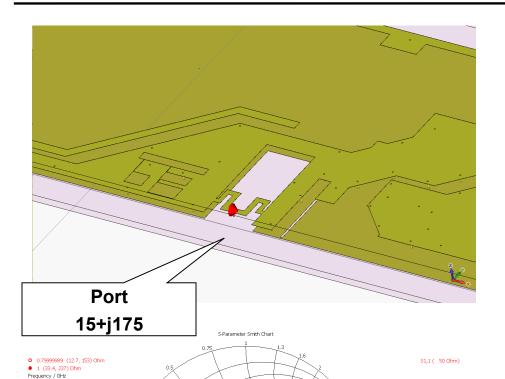


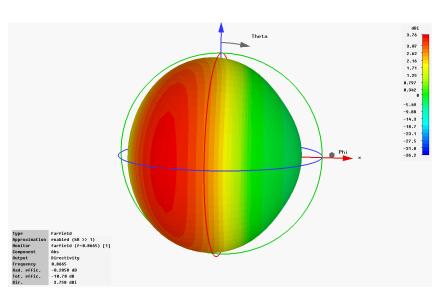


3.c CST based optimised design



RFID





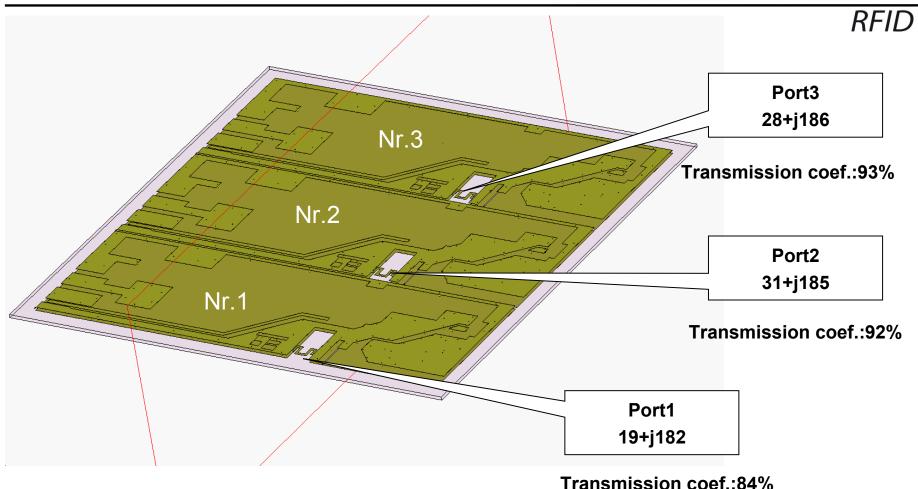
Gain:3.76 dBi

Transmission coef.:67%

Q 0.864032 (15.884624, 175.000961) Ohm

3.c CST based optimised design

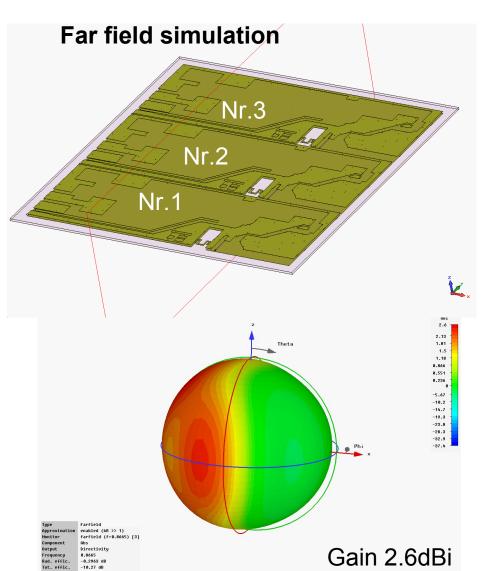


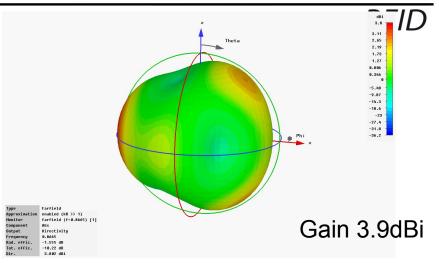


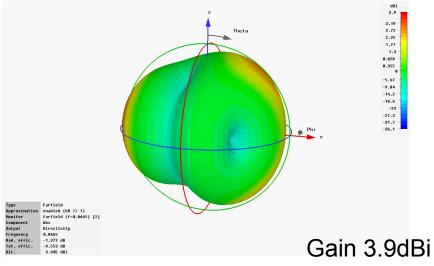
The goal is to provide good matching in the panel with multiple PCBs
This way a good read performance can be achieved at the production lines

3.c CST based optimised design



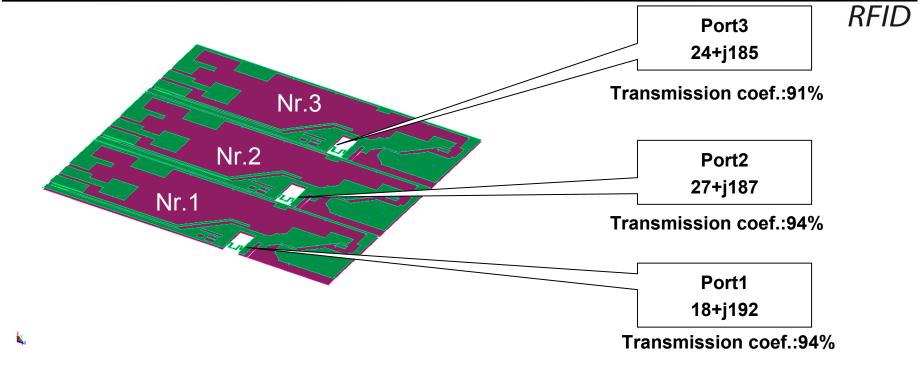






4. Comparison simulation in MoM





Transmission coefficient Recommended design				
	CST			
Port 1	18%			
Port 2	48%			
Port 3	43%			

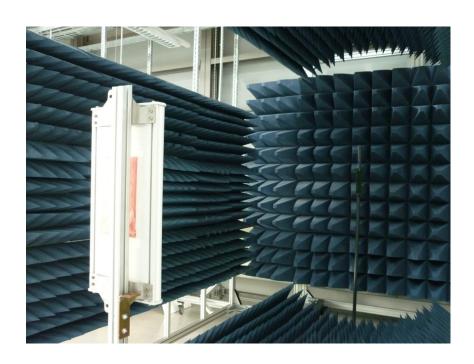
Transmission coefficient Optimised design					
	CST	MoM			
Port 1	84%	94%			
Port 2	92%	94%			
Port 3	93%	91%			

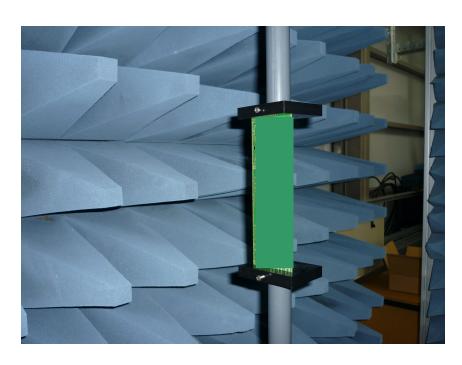
The comparison of simulation results shows a good agreement

4. Read range measurement



RFID





Read range based on 2 W ERP

Recommended design	Optimised design
4 m	6.9 m

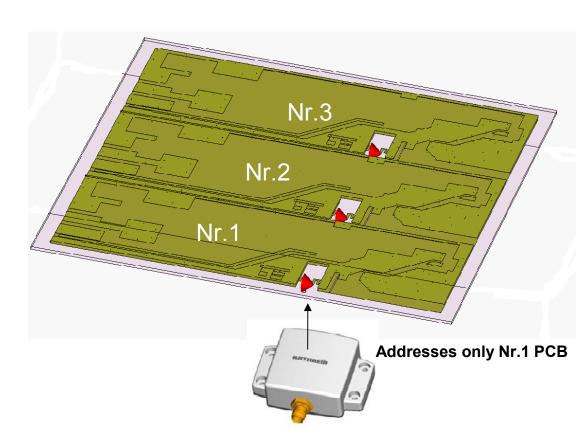
4. Selectivity with Ultra Low Range Antenna



RFID



- Limited read range < 10 cm
- Extremely high selectivity (typ. 5 cm)
- Extremely low gain (< -30 dBi)
- Dimensions: 7 cm x 9 cm
- High mechanical robustness (IP67)
- Ideal for conveyor belts and access systems
- No interference by metallic objects in the vicinity



The Ultra Low Range Antenna reads selectively a single board in a panel with multiple PCBs

5. Conclusion



CST Microwave Studio is a very good tool for embedding and analysing UHF RFID transponders in the layout of existing boards

The recommended designs from Murata show a relative good matching to chip impedances

The layout of a single board can be further optimised to match the chip impedance and achieve a greater read range

The Low Range Antennas ensure very high selectivity for addressing a single board in a panel with multiple PCBs