

# Exploiting Qualcomm WLAN And Modem Over-The-Air

- Xiling Gong, Peter Pi
- Tencent Blade Team

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#### About Us

#### Xiling Gong (@GXiling)

Senior security researcher at Tencent Blade Team.

Focus on Android Security, Qualcomm Firmware Security.

Speaker of BlackHat, CanSecWest.

#### Peter Pi(@tencent\_blade)

Senior security researcher at Tencent Blade Team.

Find many vulnerabilities of vendors like Google, Microsoft, Apple, Qualcomm, Adobe and Tesla.

The #1 Researcher of Google Android VRP in year 2016.

Speaker of BlackHat, CanSecWest, HITB, GSEC and Hitcon.

#### About Tencent Blade Team



- Founded by Tencent Security Platform Department in 2017
- Focus on security research in the areas of AloT, Mobile devices, Cloud virtualization, Blockchain, etc
- Report 200+ vulnerabilities to vendors such as Google, Apple, Microsoft, Amazon
- We talked about how to break Amazon Echo at DEFCON26
- Blog: https://blade.tencent.com

# Agenda

- Introduction and Related Work
- The Debugger
- Reverse Engineering and Attack Surface
- Vulnerability and Exploitation
- Escaping into Modem
- Escaping into Kernel
- Stability of Exploitation
- Conclusions

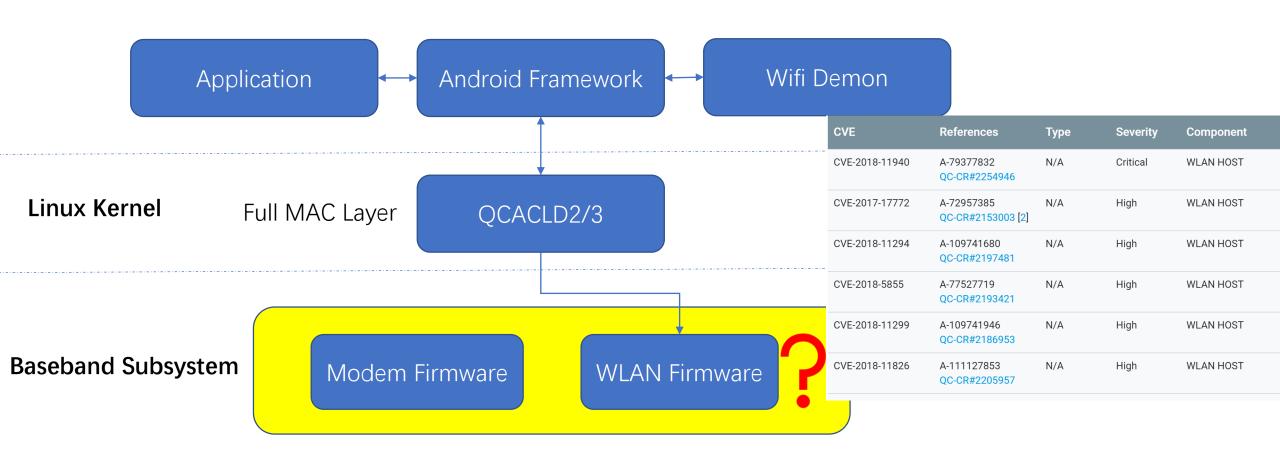
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#### Introduction

- Broadcom WIFI Chip
  - 2017, Gal Beniamini
    - Over The Air: Exploiting Broadcom's Wi-Fi Stack
  - 2017, Nitay Artenstein, BlackHat USA 2017
    - BROADPWN: REMOTELY COMPROMISING ANDROID AND IOS VIA A BUG IN BROADCOM'S WI-FI CHIPSETS
- Marvel WIFI Chip
  - 2019, Denis Selyanin
    - Zero Nights 2018, Researching Marvell Avastar Wi-Fi: from zero knowledge to overthe-air zero-touch RCE
    - Blog 2019, Remotely compromise devices by using bugs in Marvell Avastar Wi-Fi: from zero knowledge to zero-click RCE
- How about Qualcomm WIFI?

# Qualcomm WLAN (MSM8998)



# Agenda

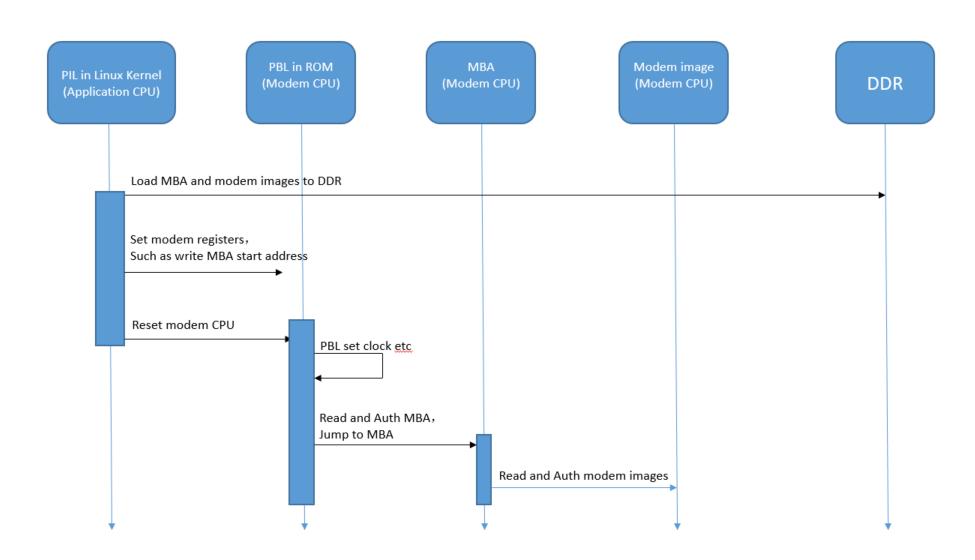
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# MBA and Modem images

- Modem Boot Authenticator
- mba.mbn
- modem.mdt
- modem.b00 modem.b20
- Image format

```
sailfish:/firmware/radio $ ls -l
total 55824
                              244 1980-01-01 00:00 mba.b00
                             4828 1980-01-01 00:00 mba.mdt
                              884 1980-01-01 00:00 modem.b00
                             5224 1980-01-01 00:00 modem.b01
                             5460 1980-01-01 08:00 modem.b02
                           196608 1980-01-01 08:00 modem.b03
                          3288155 1980-01-01 08:00 modem.b05
                           163280 1980-01-01 08:00 modem.b06
                           735936 1980-01-01 08:00 modem.b07
                          2081092 1980-01-01 08:00
             system root 15348816 1980-01-01 08:00 modem.b09
                           488448 1980-01-01 08:00 modem.b11
                          7234272 1980-01-01 08:00 modem.b13
                            82368 1980-01-01 08:00 modem.b15
                           529821 1980-01-01 08:00 modem.b16
                                  1980-01-01 08:00
                                  1980-01-01 08:00 modem.mdt
                            16384 2011-07-06 01:30 modem_pr
                              472 1980-01-01 00:00 gdsp6m.gdb
                              27 1980-01-01 00:00 radiover.cfg
                               27 1980-01-01 00:00 version.cfg
         – 1 system root
sailfish:/firmware/radio $
```

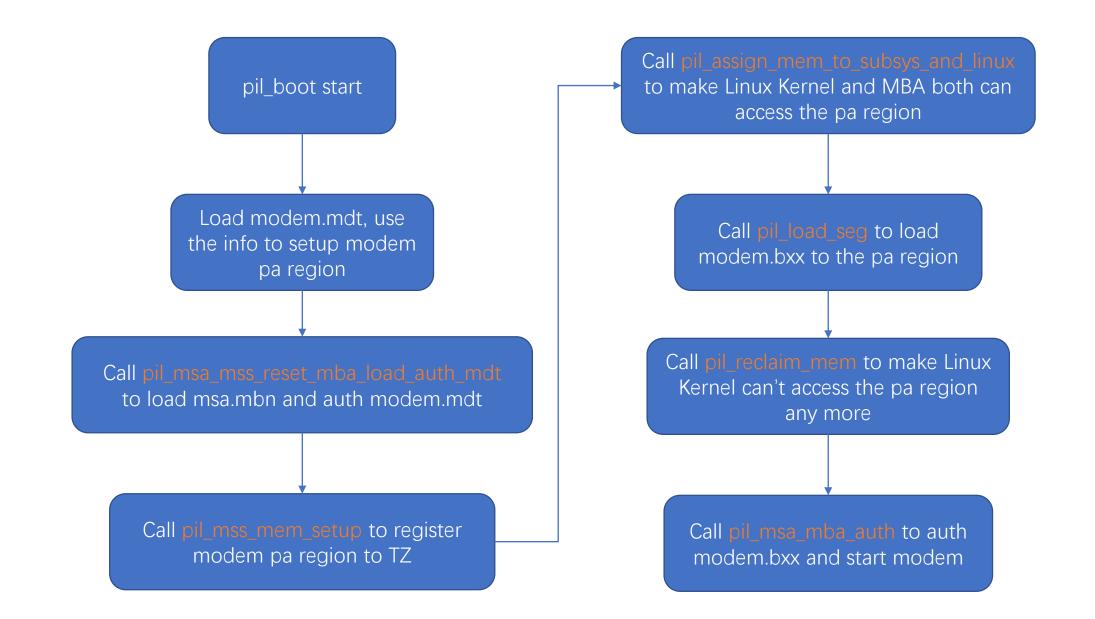
#### Modem Secure Boot



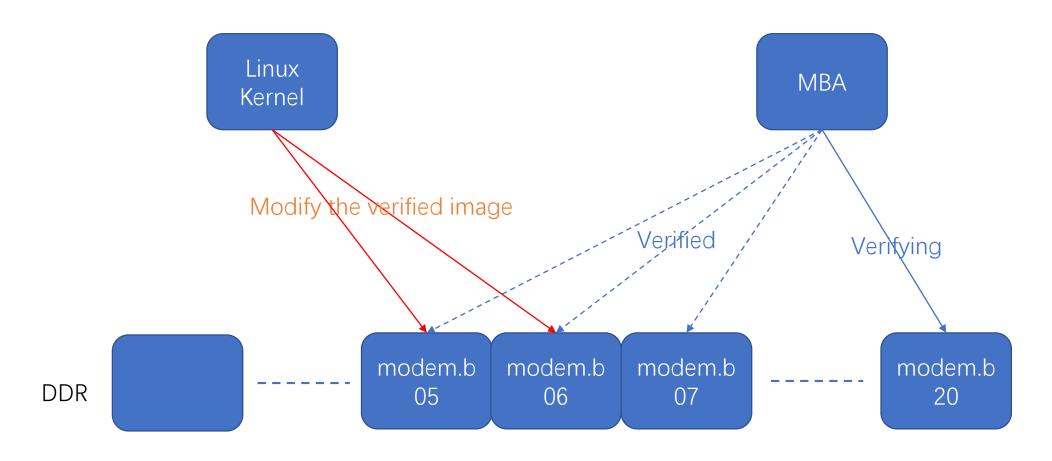
# pil\_boot

- The pil\_boot function in Linux Kernel describes the boot flow of modem.
- Load mba.mbn, modem.mdt and modem.bxx to physical memory.
- Trigger MBA and modem images to be verified and run in Modem Processor.
- Linux Kernel can restart Modem Processor at any time, will hit pil\_boot each time when restart.

### pil\_boot



# TOCTOU Vulnerability

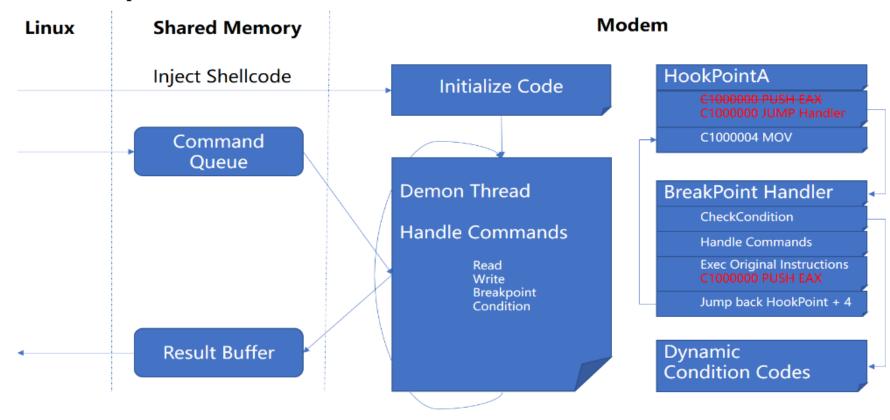


#### POC

```
@@ -840,24 +868,33 @@ int pil_boot(struct pil_desc *desc)
           goto err_deinit_image;
   if (desc->subsys_vmid > 0) {
       ret = pil_reclaim_mem(desc, priv->region_start,
               (priv->region_end - priv->region_start),
               desc->subsys_vmid);
       if (ret) {
           pil_err(desc, "Failed to assign %s memory, ret - %d\n",
           goto err_deinit_image;
       hyp_assign = false;
   ret = desc->ops->auth_and_reset(desc);
   if (ret) {
       pil_err(desc, "Failed to bring out of reset\n");
       goto err_auth_and_reset;
   pil_info(desc, "Brought out of reset\n");
   if (modem_dbg_cfg) { // just a switch can be set in userspace to enable our test
       list_for_each_entry(seg, &desc->priv->segs, list) {
           pil_modify_seg(desc, seg); // self defined function to modify segments
   if (modem_dbg_cfg == 0) {
       if (desc->subsys_vmid > 0) {
           ret = pil_reclaim_mem(desc, priv->region_start,
                   (priv->region_end - priv->region_start),
                   desc->subsys_vmid);
           if (ret) {
               pil_err(desc, "Failed to assign %s memory, ret - %d\n",
                           desc->name, ret);
               goto err_deinit_image;
           hyp_assign = false;
```

# Debug Server Injection

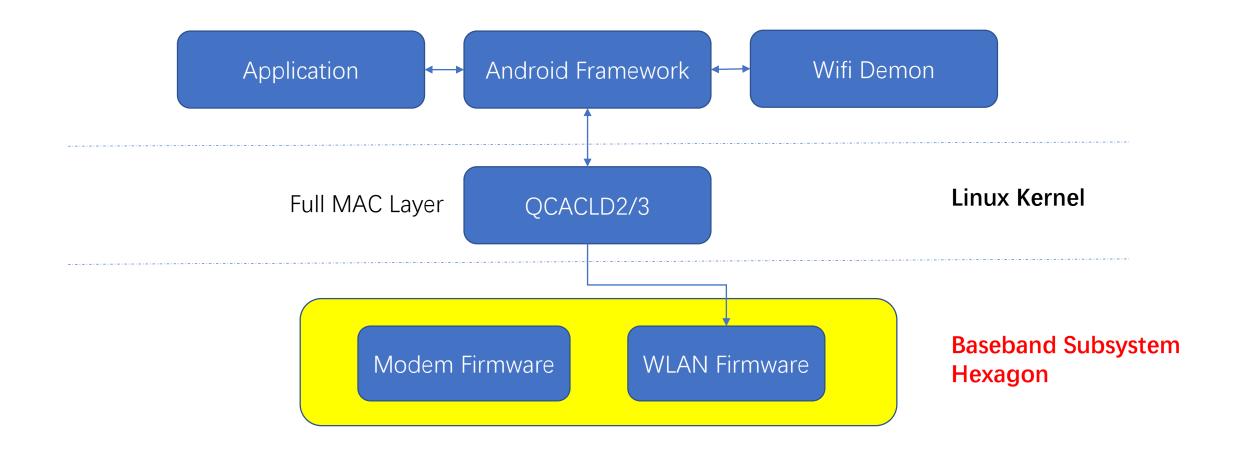
#### **Implementation**



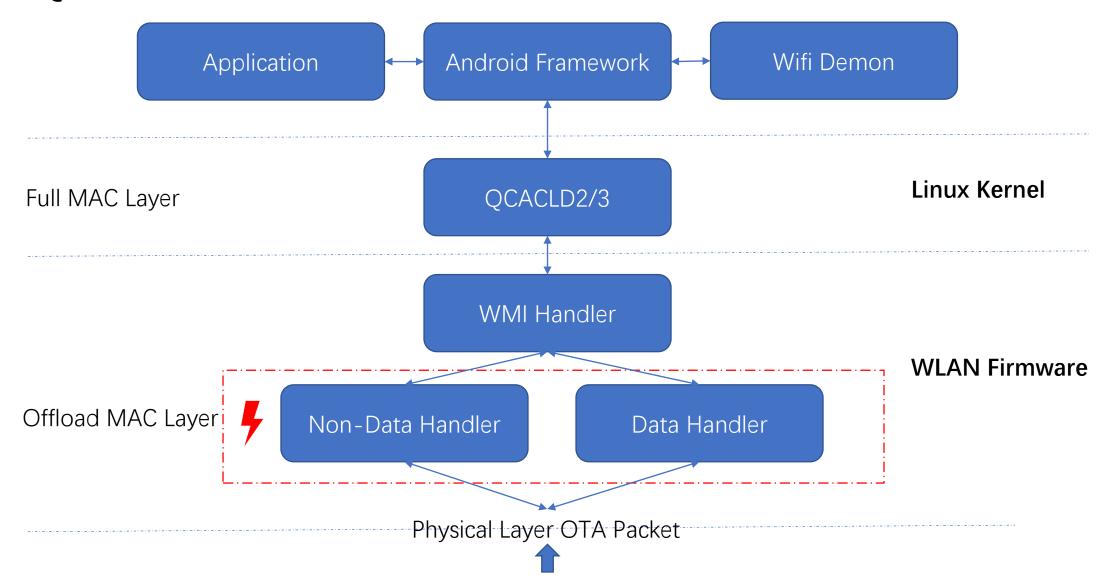
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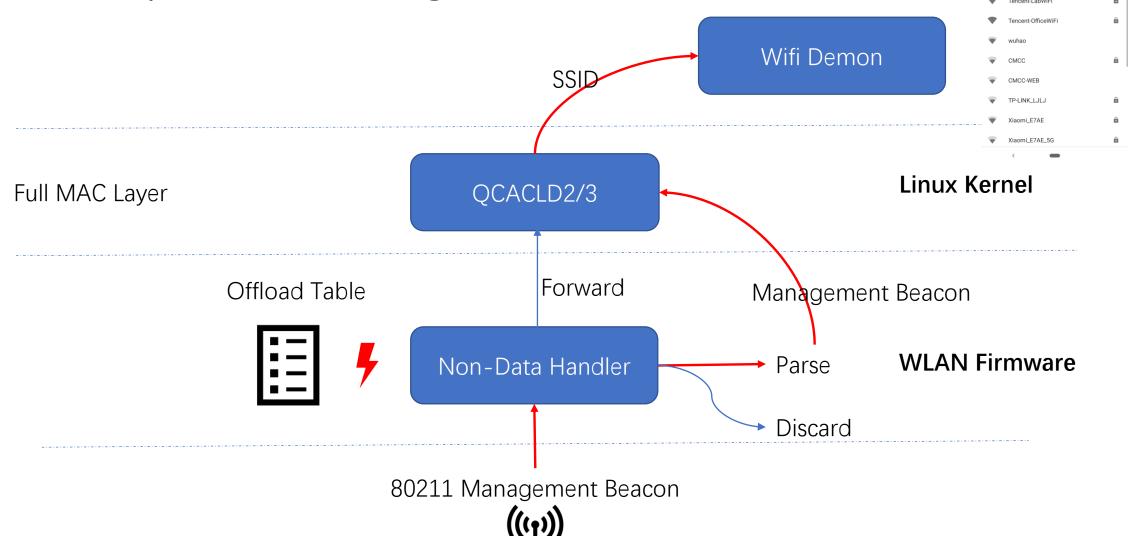
# Qualcomm WLAN



# Qualcomm WLAN Architecture



# Example - Management Beacon



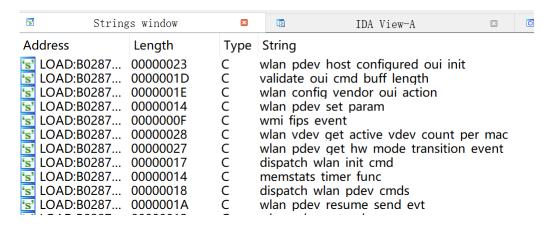
使用 WLAN

#### Firmware

- Modem load WLAN Firmware from /vendor/firmware/wlanmdsp.mbn
- IDA Disassembler
  - https://github.com/programa-stic/hexag00n/tree/master/ida
  - https://github.com/gsmk/hexagon
- Qualcomm SDK
  - https://developer.qualcomm.com/software/hexagon-dsp-sdk/tools
- Instruction Reference
  - <a href="https://developer.qualcomm.com/download/hexagon/hexagon-v5x-programmers-reference-manual.pdf?referrer=node/6116">https://developer.qualcomm.com/download/hexagon/hexagon-v5x-programmers-reference-manual.pdf?referrer=node/6116</a>

# Reverse Engineering – Hint From Qualcomm

#### **String Table**



#### **Import Function**

```
f qurt exception raise nonfatal
f msg v2 send 2
f msg v2 send 3
f MMPM Init Ext
f DALSYS Init
f Diag LSM Init
f dog hb task
f dog hb init
f msg v2 send var
hexagon udivsi3
```

#### **WMI Handler**

drivers/staging/fw-api-fw/wmi\_unified.h

```
* Command IDs and commange events
typedef enum {
   /** initialize the wlan sub system */
   WMI_INIT_CMDID = 0x1,
   /* Scan specific commands */
   /** start scan request to FW */
   WMI_START_SCAN_CMDID = WMI_CMD_GRP_START_ID(WMI_GRP_SCAN),
    /** stop scan request to FW */
   WMI STOP SCAN CMDID.
   /** full list of channels as defined by the regulatory that will be used by scanner
   WMI_SCAN_CHAN_LIST_CMDID,
   /** overwrite default priority table in scan scheduler
   WMI SCAN SCH PRIO TBL CMDID,
   /** This command to adjust the priority and min.max rest time
    * of an on ongoing scan request.
   WMI SCAN UPDATE REQUEST CMDID,
```

# Reverse Engineering

- Targets To Reverse
  - WMI Handlers
    - Handle WMI commands from Linux Kernel
    - Send back WMI indication to Linux Kernel

Offload MAC Layer

- Offload Handlers
  - Handle OTA Packets

Non-Data Handler

Data Handler

Physical Layer OTA Packet

#### WMI Handlers

drivers/staging/fw-api-fw/wmi\_unified.h

```
* Command IDs and commange events
typedef enum {
   /** initialize the wlan sub system */
   WMI_INIT_CMDID = 0x1,
   /* Scan specific commands */
   /** start scan request to FW */
                                                                                 0x03001
   WMI_START_SCAN_CMDID = WMI_CMD_GRP_START_ID(WMI_GRP_SCAN),
   /** stop scan request to FW */
   WMI STOP SCAN CMDID,
   /** full list of channels as defined by the regulatory that will be used by scanner
   WMI_SCAN_CHAN_LIST_CMDID,
   /** overwrite default priority table in scan scheduler */
   WMI_SCAN_SCH_PRIO_TBL_CMDID,
   /** This command to adjust the priority and min.max_rest_time
    * of an on ongoing scan request.
   WMI_SCAN_UPDATE_REQUEST_CMDID,
                                                                                                                 dd sub B00269F4
                                                          LOAD: B0301D00 F4 69 02 B0
                                                                                                                 dw 0x3001
                                                          LOAD: B0301D04 01 30
                                                                                                                 dd 0
                                                          LOAD:B0301D06 00 00 00 00
```

#### Offload Handlers

```
S LOAD:B0288... 00000020 C offldmgr protocol data handler C offldmgr non data handler
```

```
LOAD:B0288C55 5F 6F 66 66+a_offldmgr_prot:db "_offldmgr_protocol_data_handler"

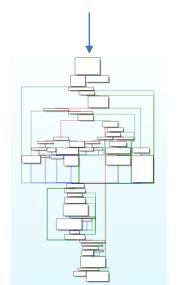
LOAD:B0288C55 6C 64 6D 67+ ; DATA XREF: _offldmgr_protocol_data_handler+B0^r

LOAD:B0288C75 5F 6F 66 66+a_offldmgr_non_:db "_offldmgr_non_data_handler"

LOAD:B0288C75 6C 64 6D 67+ ; DATA XREF: _offldmgr_non_data_handler+32C^r

LOAD:B0288C75 72 5F 6E 6F+ ; _offldmgr_non_data_handler+32C^r

LOAD:B0288C75 6C 5F 64 61+ db 0
```



```
loc_B0013218:
                                    R22 = R21
                                   R23 = add (R19, 0x10)
                    loc B0013220:
                     { R21 = memw (R23 + 0xFFFFFFF0)
                      if (cmp.eq (r21.new, 0)) jump:nt loc_B00132B8
                             R2 = memw (R23 + 0xFFFFFFF8) }
                             [ P0 = bitsclr (R2, R20)
  if (P0.new) jump:nt loc_B00132B8 }
                      callr R21
                      R1 = R17
                       RØ = memw (R23 + ØxFFFFFFF4) }
                      R21 = R0
                      if (cmp.eq (r21.new, 0)) jump:nt loc_B00132B4 }
                  R1 = 0x2444
                  R0 = 0x590; memw (sp + 0) = R22 }
                   memw (sp + 8) = R16; memw (sp + 4) = R21}
                   call sub B0227D84
                   memw (R29 + 0xC) = R20 }
                   immext
                  R1:R0 = combine (4, 0xB0288DD4)
memw (sp + 4) = R21; memw (sp + 8) = R16 }
call sub_B00032AC
                   memw (sp + \thetaxC) = R2\theta ; memw (sp + \theta) = R22 }
                  R2 = add (R23, 0xFFFFFFFE)
                  p0 = cmp.eq (R21, 2) ; if (p0.new) jump:nt loc_B001328C ]
                                 { p0 = cmp.eq (R21, 1) ; if (!p0.new) jump:nt loc_B00132B4 }
                                                                    jump loc_B00132B4
memh (R23 + 0) += 1
loc_B001328C:
{ memh (R2 + 0) += 1 }
 immext
 R2 = memw (gp + 0xB0306134) }
 callr R2 }
 P0 = cmp.eq (R0, 1)
 if (P0.new) R0 = 0xB0288C75 ; "_offldmgr_non_data_handler'
 if (P0.new) R1 = 0x620 }
 immext
 if (P0) call sub B00115F4
                                           1111
```

# Sample Offload Handler

```
sub B0004C2C:
DataPtr = R17
DataPtr1 = R21
{ call sub B02859C4
  allocframe (0x30) }
\{ R16 = R2 ; R19 = R0 \}
\{ R2 = memw (R16 + 0) \}
\{ R2 = memw (R2 + 0x10) \}
{ DataPtr = memub (R2 + 0x58)
  R3 = memub (R2 + 0x59) 
{ DataPtr |= asl (R3, 8)
  R4 = memub (R2 + 0x5B)
 R5 = memub (R2 + 0x5A) 
\{ R5 \mid = asl (R4, 8) \}
{ DataPtr |= asl (R5, 0x10) }
{ R3 = memub (DataPtr + 0) }
\{ R18 = and (R3, 0xF0) \}
 R1 = and (R3, 0xC)
                        ; management
  if (!cmp.eq (r1.new, 0)) jump:t loc_B0004DF4 }
            \{ P0 = cmp.eq (R18, 0x80) \}
              DataPtr1 = memub (R2 + 0x5C)
              R22 = memub (R2 + 0x5D) }
              if (P0) jump loc_B0004C84 }
```

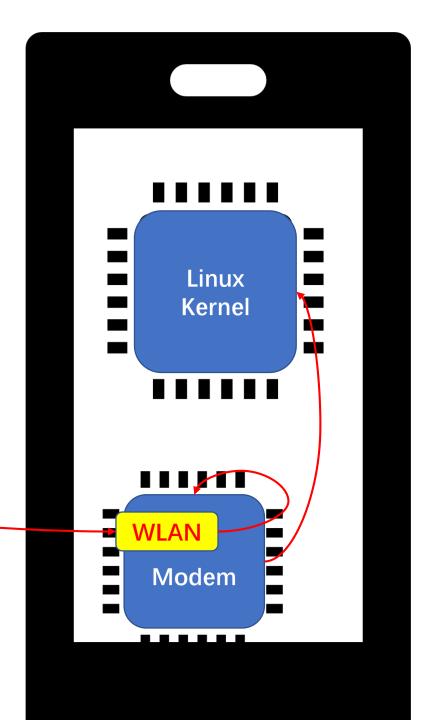
OTA Packet Data Pointer = [0x5B | 0x5A | 0x59 | 0x58]

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# The Roadmap





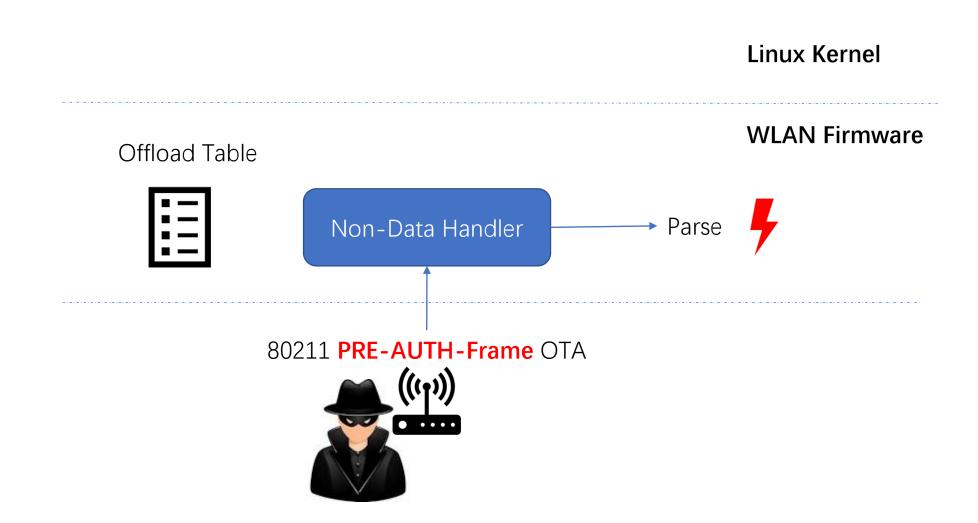
# Mitigation Table (WLAN & Modem)

Mitigation	Status
Heap ASLR	Υ
Heap Cookie	Υ
Stack Cookie	Υ
W^X	Υ
FRAMELIMIT*	Υ
FRAMEKEY**	Υ
Code & Global Data ASLR	N
CFI	Ν

<sup>\*</sup>FRAMELIMIT Register - if SP < FRAMELIMIT throw exception

<sup>\*\*</sup>FRAMEKEY Register - Return Address XOR FRAMEKEY. A random integer different for every thread

# The Vulnerability (CVE-2019-10540)



# The Vulnerability (CVE-2019-10540)

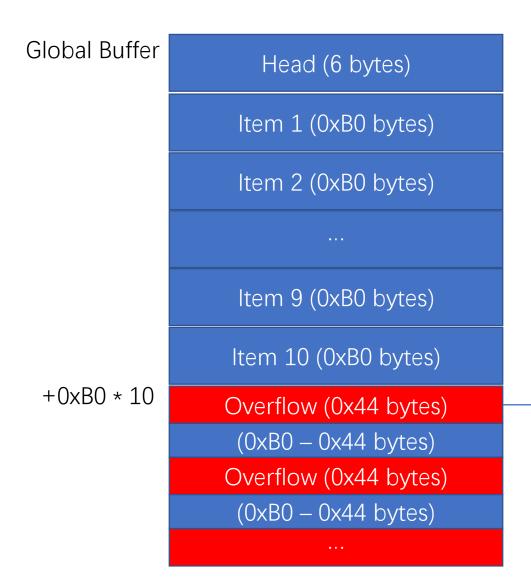
```
Copy items from packet into Global Static Buffer.
Max Item Count = 10
Send 11 items -> Overflow!
     [GLOBAL] char *GlobalBuffer[10 * 0xB0 + 6];
     unsigned int itemCount = 0;
     for (unsigned int i = 0; i < Length; i += 0x44) {
            memcpy (GlobalBuffer + 6 + itemCount * 0xB0,
                       OTA DataPtr + i,
                        0x44);
            itemCount++;
```

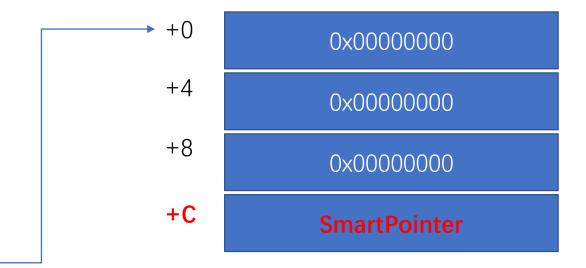
<sup>\*</sup> Translated and simplified the code flow

#### Data & Address of Overflow

Global Buffer Head (6 bytes) Item 1 (0xB0 bytes) Item 2 (0xB0 bytes) Item 9 (0xB0 bytes) Item 10 (0xB0 bytes) +0xB0 \* 10Overflow (0x44 bytes) (0xB0 - 0x44 bytes)Overflow (0x44 bytes) (0xB0 - 0x44 bytes)

# Smart Pointer Around Overflow Memory





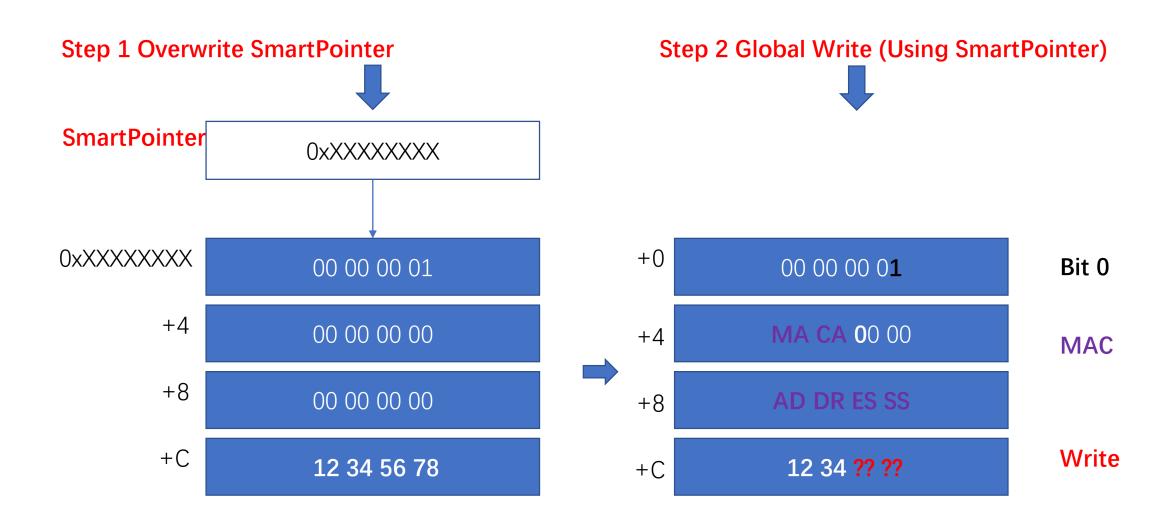
# Usage Of Smart Pointer

```
Char **AddressOfSmartPointer = GlobalBuffer + 6 + 0xB0 * 11 + 0xC;
char *SmartPointer = *AddressOfSmartPointer;
char *MacAddress = OTA DataPtr + 0x10;
char *BYTE C = OTA DataPtr + 0x10 + 0x20;
char *BYTE D = OTA DataPtr + 0x10 + 0x21;
char *BYTE 14 = OTA DataPtr + 0x10 + 0x22;
if (TestBit(SmartPointer, 0) == 1) {
        if (memcmp(SmartPointer + 6, MacAddress, 6) == 0) {
                *(SmartPointer + 0xC) = *BYTE_C;
                *(SmartPointer + 0xD) = *BYTE D;
                *(SmartPointer + 0x14) = *BYTE_14;
```

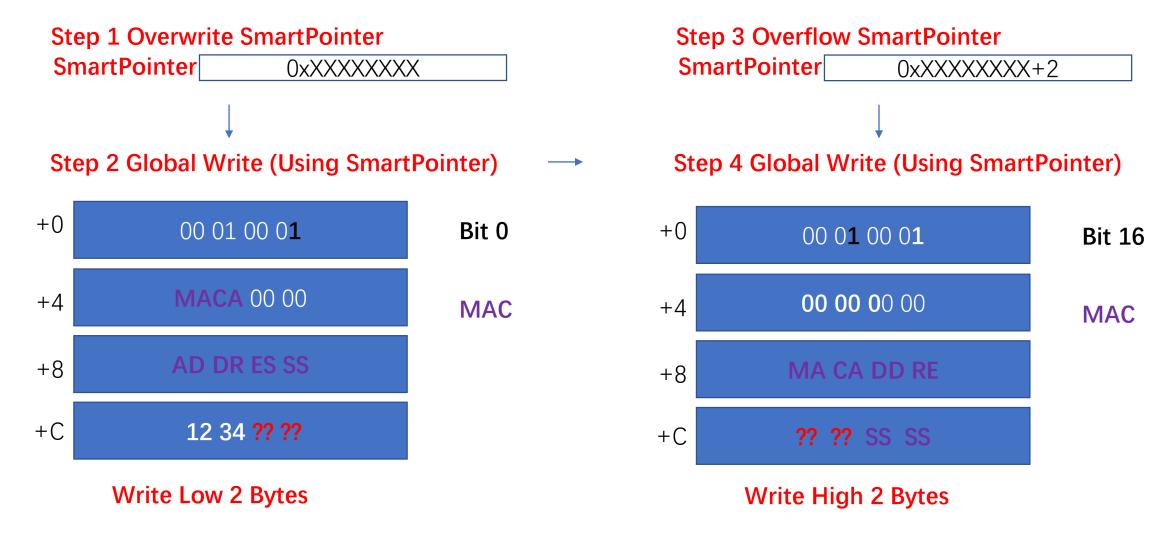
# Usage Of Smart Pointer

```
Char **AddressOfSmartPointer = GlobalBuffer + 6 + 0xB0 * 11 + 0xC;
char *SmartPointer = *AddressOfSmartPointer; // ← Overwrite with vulnerability
char *MacAddress = OTA DataPtr + 0x10;
char *BYTE C = OTA DataPtr + 0x10 + 0x20;
char *BYTE D = OTA DataPtr + 0x10 + 0x21;
char *BYTE 14 = OTA DataPtr + 0x10 + 0x22;
if (TestBit(SmartPointer, 0) == 1) {
                                                  // \leftarrow The only constraint, Bit0 == 1
        if (memcmp(SmartPointer + 6, MacAddress, 6) == 0)
                                                  // ← From OTA Data, could be bypass
                *(SmartPointer + 0xC) = *BYTE_C; // ← Overwrite 0xC
                *(SmartPointer + 0xD) = *BYTE_D; // ← Overwrite 0xD
                *(SmartPointer + 0x14) = *BYTE_14;
```

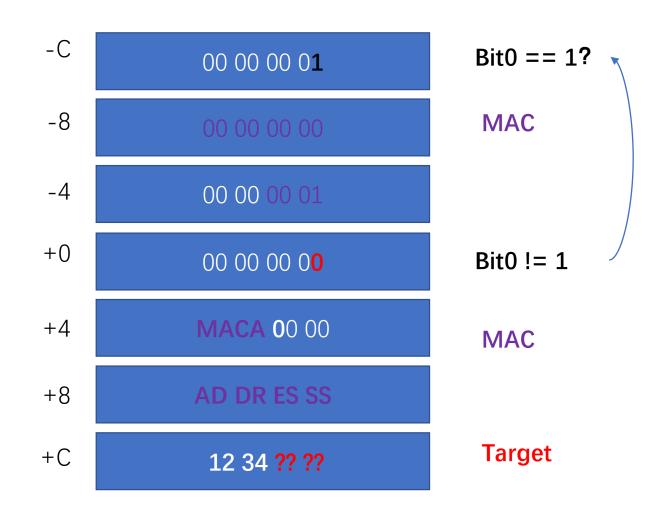
#### Global Write With Constraint



# Global Write With Constraint How to write 4 bytes?



# Global Write With Constraint The Bit0 != 1?



### Control PC & R0

Address	Value	
00	0x00010000	
+04	0x00010001	
+08	0x00000000	
+0C	0x0000001	
+10	0x00000000	
+14	0x00000000	
+18	0x00000000	
+1C	0x00000000	
+20	0x00000000	
+24	0x12345678(PC)	
+28	0x87654321(R0)	

+08	0x00000000
+0C	0x000 <mark>1</mark> 0001
+10	0x000 <b>1</b> 000 <b>1</b> <
+14	0x00000000
+18	0x000 <b>1</b> 000 <b>1</b>
+1C	0x000 <b>1</b> 000 <b>1</b> <

Value

0x00010000

0x00010001

0x0000000

**TARGET PC** 

**TARGET R0** ←

**SmartPointer** 

Address

+00

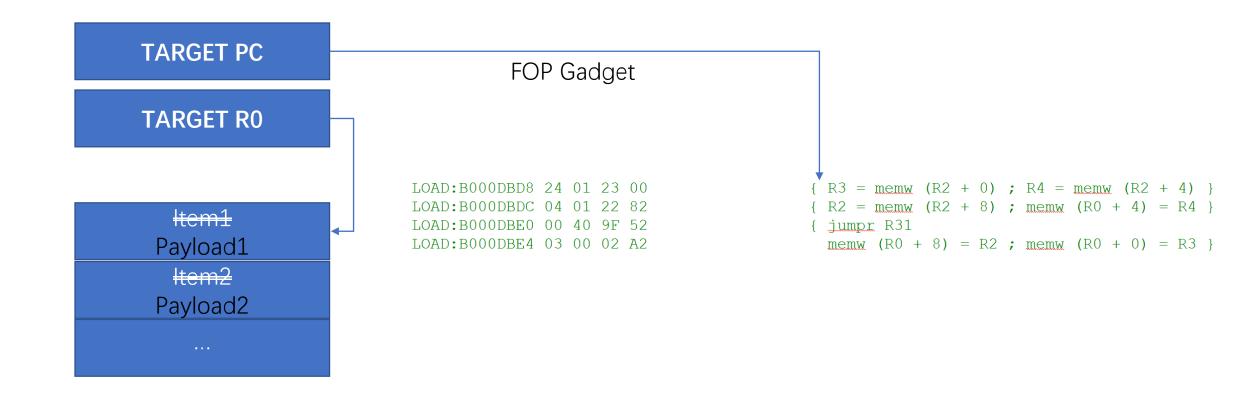
+04

+20

+24

+28

### Transform To Arbitrary Write

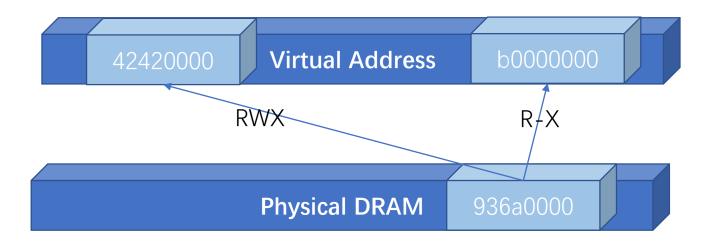


### Run Useful FOP Gadget

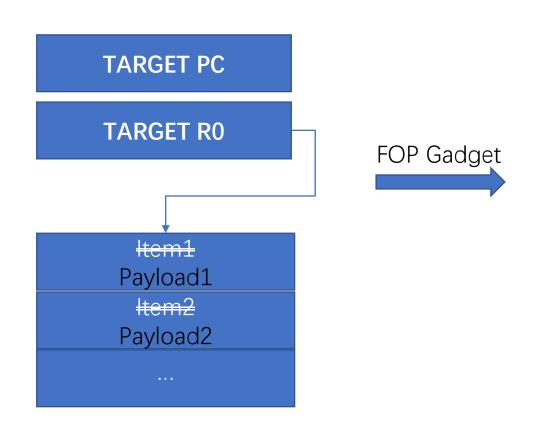
**Function Pointer(PC) Step 1 Arbitrary Write** Overwrite function pointer → **Step 2 Arbitrary Write** Overwrite data pointer → Data Pointer (R0) <del>Item1</del> Step 3 Send payload packet and trigger the PC  $\rightarrow$ Payload1 <del>Item2</del> Payload2

### Memory Mapping RWX

### **CreateMapping(args, ...)** R0 = 0x42420000Virtual Address R1 = 0x936a0000Physical Address R2 = 0x1000Size R3 = 4Unknown R4 = 7Permission RWX

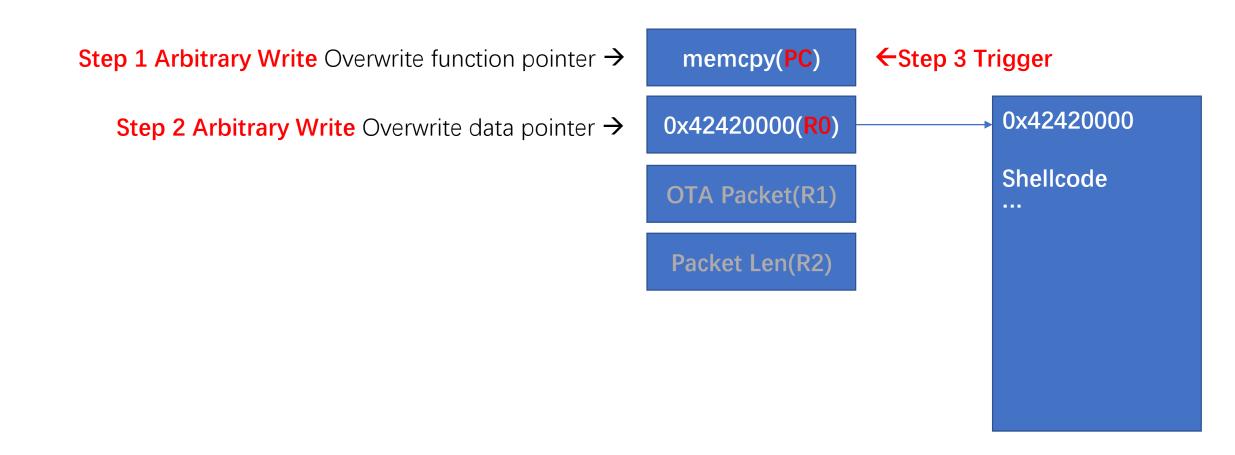


### Memory Mapping RWX

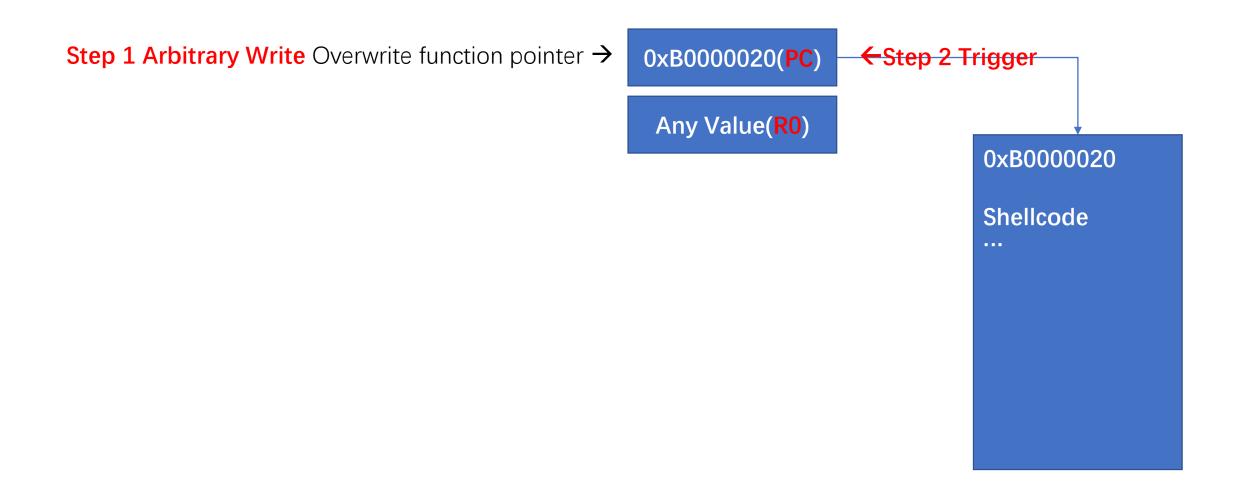


### CreateMapping(args, ···) R0 = 0x42420000Virtual Address R1 = 0x936a0000Physical Address R2 = 0x1000Size R3 = 4Unknown R4 = 7Permission RWX

### Copy Shellcode to 0x42420000



## Trigger Shellcode

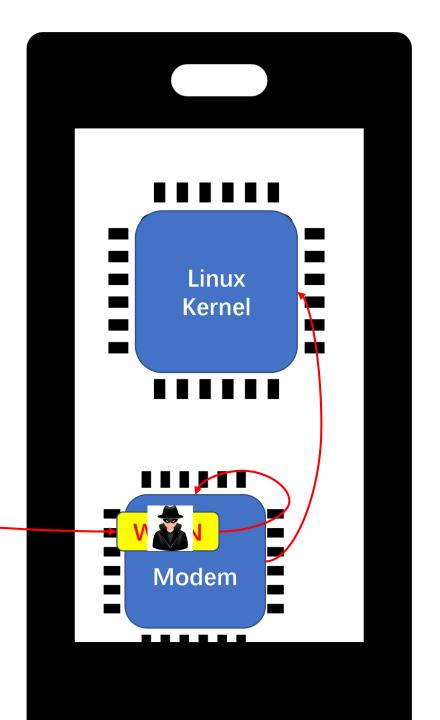


### Agenda

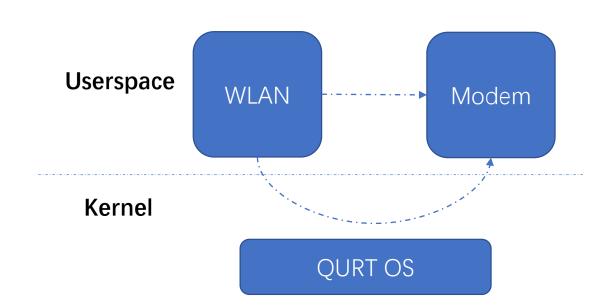
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### From WLAN to Modem



Actions From WLAN	Eligible?
TLB Set*	N
Write Modem Data	Ν
Call Modem Complex Function**	N
Call Modem Simple Code Snippet***	Υ
Map Modem Memory	Υ

<sup>\*</sup> TLB is a Hexagon Instruction to modify the Memory Page Attribute

<sup>\*\*</sup> Complex Function uses the resource of Modem, or calls System Call

<sup>\*\*\*</sup> Simple Code Snippet mean code has only register operation

### Map Modem Memory into WLAN

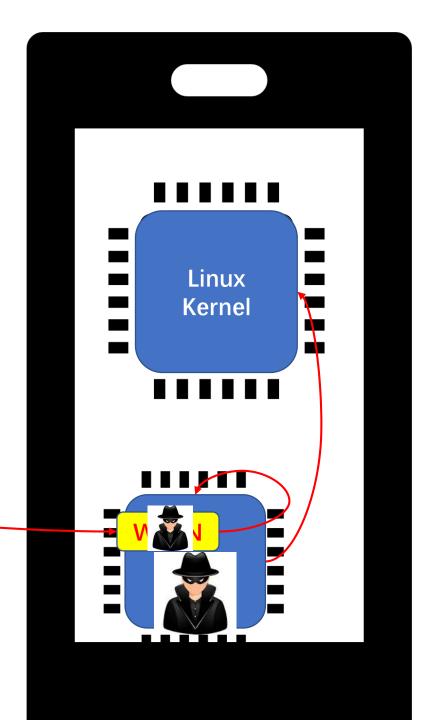
# WLAN Process Virtual Address WLAN Modem Modem Virtual Address WLAN Modem Physical Address

### Agenda

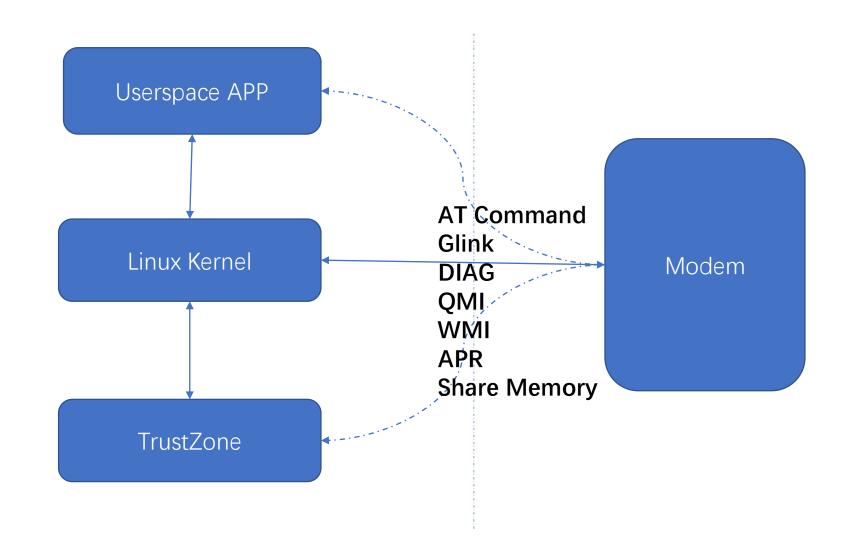
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### The Attack Surfaces



We've found

An arbitrary memory read/write vulnerability
Could bypass all the mitigations of Linux Kernel
From Modem into Linux Kernel

- In these attack surfaces
- But we are unable to disclose the detail now

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### Deliver the Payload Over-The-Air







**Pixel 2XL** 



commands payload1 payload2 payload3 payload4 payload5 payload6 payload7 payload8 payload9 payload10 payload11

a payload12 payload13 payload14 payload15 payload16 payload17 payload18 payload19 payload20 payload21 payload22 payload23

Packet Losing Rate 90%+!

## Deliver the Payloads Using Pixel2











#### Pixel 2XL

# The Roadmap

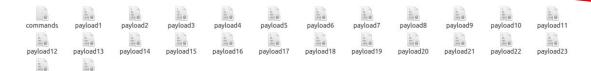


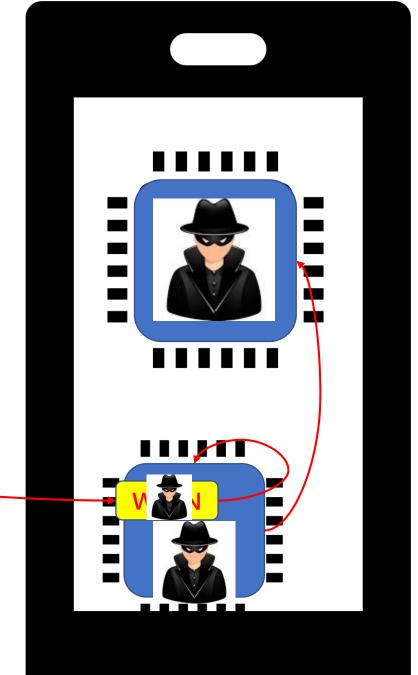
Pixel 2



payload24

payload25





### Demo

### Future Works

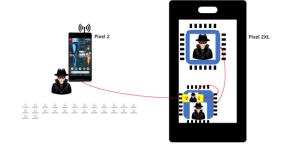
- There are still lots of mystery in the WLAN.
  - We were only reversed a small part of the code
  - Lots of functions are unknown
- How to fuzz the WLAN Firmware?
  - Reverse engineering is quite...
  - How to fuzz closed source target and Hexagon architecture effectively?
- Translate Hexagon Instruction to C?
  - IDA/Ghidra F5 plugin?

### Timeline

- 2019-2-14 Find the Modem debug vulnerability on MSM8998
- 2019-3-24 Find the WLAN issue and report to Google
- 2019-3-28 Google forwards the issue to Qualcomm
- 2019-4-24 Google confirms the WLAN issue as Critical
- 2019-5-08 Find the WLAN into Linux Kernel issue and report to Google
- 2019-5-24 Google confirms the WLAN into Linux Kernel issue
- 2019-5-28 Submit the full exploit chain (OTA→WLAN→Kernel) to Google
- 2019-6-04 Google reply unable to reproduce the full exploit chain
- 2019-6-17 Improve the stability and submit to Google
- 2019-7-19 CVE Assigned by Google
- 2019-7-20 Qualcomm confirms issues will be fixed before October
- 2019-8-0? Google release the fix for Google Pixel2/Pixel3

### Takeaways

- The full exploit chain into Android Kernel
  - OTA → WLAN → Modem → Kernel



- The Qualcomm WLAN vulnerability and exploitation
- The Qualcomm Baseband Debugger

# THANK YOU



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