

# OverSketch: Approximate Matrix Multiplication for the Cloud

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**Abstract**—We propose OverSketch, an approximate algorithm for distributed matrix multiplication in serverless computing. OverSketch leverages ideas from matrix sketching and high-performance computing to enable cost-efficient multiplication that is resilient to faults and straggling nodes pervasive in low-cost serverless architectures. We establish statistical guarantees on the accuracy of OverSketch and empirically validate our results by solving a large-scale linear program using interior-point methods and demonstrate a 34% reduction in compute time on AWS Lambda.

**Keywords**—serverless computing, straggler mitigation, sketched matrix multiplication

## 1. INTRODUCTION

Matrix multiplication is a frequent computational bottleneck in fields like scientific computing, machine learning, graph processing, etc. In many applications, such matrices are very large, with dimensions easily scaling up to millions. Consequently, the last three decades have witnessed the development of many algorithms for parallel matrix multiplication for High Performance Computing (HPC). During the same period, technological trends like Moore’s law made arithmetic operations faster and, as a result, the bottleneck for parallel computation shifted from computation to communication. Today, the cost of moving data between nodes exceeds the cost of arithmetic operations by orders of magnitude, and this gap is increasing exponentially with time [1]–[3]. This has led to the popularity of communication-avoiding algorithms for parallel computation [3], [4].

In the last few years, there has been a paradigm shift from HPC towards distributed computing on the cloud due to extensive and inexpensive commercial offerings. In spite of developments in recent years, server-based cloud computing is inaccessible to a large number of users due to complex cluster management and a myriad of configuration tools. Serverless computing<sup>1</sup> has recently begun to fill this void by abstracting away the need for maintaining servers and thus removing the need for complicated cluster management while providing greater elasticity and easy scalability [5]–[7]. Some examples are Amazon Web Services (AWS) based Lambda, Microsoft Azure functions, and Google

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<sup>1</sup>The term ‘serverless’ is a misnomer, servers are still used for computation but their maintenance and provisioning is hidden from the user.

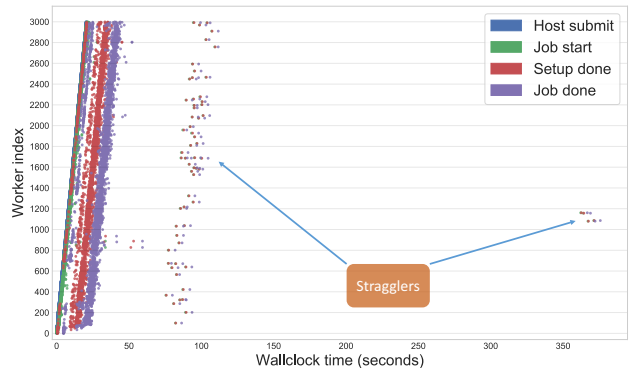


Figure 1: Job times for 3000 AWS Lambda nodes where the median job time is around 40 seconds, and around 5% of the nodes take 100 seconds, and two nodes take as much as 375 seconds to complete the same job.

Cloud Functions. Large-scale matrix multiplication, being embarrassingly parallel and frequently encountered, is a natural fit for serverless computing.

Existing distributed algorithms for HPC/server-based systems cannot, in general, be extended to serverless computing due to the following crucial differences between the two architectures:

- Workers in the serverless setting, unlike cluster nodes, do not communicate amongst themselves. They read/write data directly from/to a single data storage entity (for example, cloud storage like AWS S3) and the user is only allowed to submit prespecified jobs and does not have any control over the management of workers [5]–[7].
- Distributed computation in HPC/server-based systems is generally limited by the number of workers at disposal. However, in serverless systems, the number of inexpensive workers can easily be scaled into the thousands, but these low-commodity nodes are generally limited by the amount of memory and lifespan available.
- Unlike HPC, nodes in the cloud-based systems suffer degradation due to system noise which can be a result of limited availability of shared resources, network latency, hardware failure, etc. [8], [9]. This causes variability in job times, which results in subsets of slower nodes, often called *stragglers*, which significantly slow the computation. Time statistics for worker job times are plotted in Figure 1 for AWS Lambda. Notably, there are a few workers

( $\sim 5\%$ ) that take much longer than the median job time, thus decreasing the overall computational efficiency of the system. Distributed algorithms robust to such unreliable nodes are desirable in cloud computing.

#### A. Main Contributions

This paper bridges the gap between communication-efficient algorithms for distributed computation and existing methods for straggler-resiliency. To this end, we first analyze the monetary cost of distributed matrix multiplication for serverless computing for two different schemes of partitioning and distributing the data. Specifically, we show that row-column partitioning of input matrices requires asymptotically more communication than blocked partitioning for distributed matrix multiplication, similar to the optimal communication-avoiding algorithms in the HPC literature.

In applications like machine learning, where the data itself is noisy, solution accuracy is often traded for computational efficiency. Motivated by this, we propose OverSketch, a sketching scheme to perform blocked *approximate* matrix multiplication and prove statistical guarantees on the accuracy of the result. OverSketch has threefold advantages:

- 1) Reduced computational complexity by significantly decreasing the dimension of input matrices using sketching,
- 2) Resiliency against stragglers and faults in serverless computing by over-provisioning the sketch dimension,
- 3) Communication efficiency for distributed multiplication due to the blocked partition of input matrices.

Sketching for OverSketch requires linear time that is embarrassingly parallel. Through experiments on AWS Lambda, we show that small redundancy ( $\approx 5\%$ ) is enough to tackle stragglers using OverSketch. Furthermore, we use OverSketch to calculate the Hessian distributedly while solving a large linear program using interior point methods and demonstrate a 34% reduction in total compute time on AWS Lambda.

#### B. Related Work

Traditionally, techniques like speculative execution are used to deal with stragglers, for example, Hadoop MapReduce [10] and Apache Spark [11]. Such techniques work by detecting nodes that are running slowly or will slow down in the future and then assigning their jobs to new nodes without shutting down the original job. The node that finishes first submits its results. This has many limitations. A constant monitoring of jobs is required, which might be costly if there are many workers in the system. It is also possible that a node will straggle only towards the end of the job, and by the time the job is resubmitted, the additional time and computational overhead has already hurt the overall efficiency of the system. The situation is even worse for smaller jobs, as spinning up an extra node requires additional invocation and setup time which can exceed the job time itself.

Recently, approaches based on coding theory have been developed which cleverly introduce redundancy into the

$$\begin{array}{|c|} \hline A_1 \\ \hline A_2 \\ \hline A_1 + A_2 \\ \hline \end{array} \times \begin{array}{|c|c|c|} \hline B_1 & B_2 & B_1 + B_2 \\ \hline \end{array} = \begin{array}{|c|c|c|} \hline \text{X}C_{11} & C_{12} & C_{11} + C_{12} \\ \hline \text{X}C_{21} & \text{X}C_{22} & C_{21} + C_{22} \\ \hline C_{11} + C_{21} & C_{12} + C_{22} & C_{11} + C_{21} + C_{12} + C_{22} \\ \hline \end{array}$$

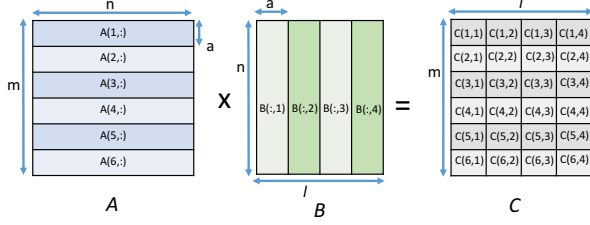
Figure 2: Matrix  $\mathbf{A}$  is divided into 2 row chunks  $\mathbf{A}_1$  and  $\mathbf{A}_2$ , while  $\mathbf{B}$  is divided into two column chunks  $\mathbf{B}_1$  and  $\mathbf{B}_2$ . During the encoding process, redundant chunks  $\mathbf{A}_1 + \mathbf{A}_2$  and  $\mathbf{B}_1 + \mathbf{B}_2$  are created. To compute  $\mathbf{C}$ , 9 workers store each possible combination of a chunk of  $\mathbf{A}$  and  $\mathbf{B}$  and multiply them. During the decoding phase, the master can recover the affected data ( $C_{11}$ ,  $C_{12}$  and  $C_{22}$  in this case) using the redundant chunks.

computation to deal with stragglers [12]–[17]. Many of these proposed schemes have been dedicated to distributed matrix multiplication [13]–[16]. In [13], the authors develop a coding scheme for matrix multiplication that uses Maximum Distance Separable (MDS) codes to code  $\mathbf{A}$  in a column-wise fashion and  $\mathbf{B}$  in a row-wise fashion, so that the resultant is a product-code of  $\mathbf{C}$ , where  $\mathbf{C} = \mathbf{A} \times \mathbf{B}$ . An illustration is shown in Figure 2. A simpler version of this has been known in the HPC community as Algorithm-Based-Fault-Tolerance (ABFT) [18]. Authors in [14] generalize the results in [13] to a  $d$ -dimensional product code with only one parity in each dimension. In [15], the authors develop polynomial codes for matrix multiplication, which is an improvement over [13] in terms of recovery threshold, that is, the minimum number of workers required to recover the product  $\mathbf{C}$ .

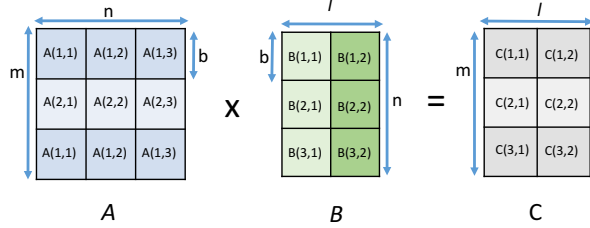
The commonality in these and other similar results is that they divide the input matrices into row and column blocks, where each worker multiplies a row block (or some combination of row blocks) of  $\mathbf{A}$  and a column block (or some combination of column blocks) of  $\mathbf{B}$ . These methods provide straggler resiliency but are not cost-efficient as they require asymptotically more communication than blocked partitioning of data, as discussed in detail in the next section. Another disadvantage of such coding-based methods is that there are separate encoding and decoding phases that require additional communication and potentially large computational burden at the master node, which may make the algorithm infeasible in some distributed computing environments.

## 2. PRELIMINARIES

There are two common schemes for distributed multiplication of two matrices  $\mathbf{A} \in \mathbb{R}^{m \times n}$  and  $\mathbf{B} \in \mathbb{R}^{n \times l}$ , as illustrated in Figures 3a and 3b. We refer to these schemes as *naive* and *blocked* matrix multiplication, respectively. Detailed steps for these schemes are provided in Algorithms 1 and 2, respectively, for the serverless setting. During naive matrix multiplication, each worker receives and multiplies an  $a \times n$  row-block of  $\mathbf{A}$  and  $n \times a$  column-block of  $\mathbf{B}$  to compute an  $a \times a$  block of  $\mathbf{C}$ . Blocked matrix multiplication consists of two phases. During the computation phase, each worker gets two  $b \times b$  blocks, one each from  $\mathbf{A}$  and  $\mathbf{B}$ , which are then multiplied by the workers. In the reduction phase, to



(a) Distributed naive matrix multiplication, where each worker multiplies a row-block of  $\mathbf{A}$  of size  $a \times n$  and a column block of  $\mathbf{B}$  of size  $n \times a$  to get an  $a \times a$  block of  $\mathbf{C}$ .



(b) Distributed blocked matrix multiplication, where each worker multiplies a sub-block of  $\mathbf{A}$  of size  $b \times b$  and a sub-block of  $\mathbf{B}$  of size  $b \times b$ .

Figure 3: An illustration of two algorithms for distributed matrix multiplication.

compute a  $b \times b$  block of  $\mathbf{C}$ , one worker gathers results of all the  $n/b$  workers from the cloud storage corresponding to one row-block of  $\mathbf{A}$  and one column-block of  $\mathbf{B}$  and adds them. For example, in Figure 3b, to get  $\mathbf{C}(1, 1)$ , results from 3 workers who compute  $\mathbf{A}(1, 1) \times \mathbf{B}(1, 1)$ ,  $\mathbf{A}(1, 2) \times \mathbf{B}(2, 1)$  and  $\mathbf{A}(1, 3) \times \mathbf{B}(3, 1)$  are added.

It is accepted in High Performance Computing (HPC) that blocked partitioning of input matrices takes less time than naive matrix multiplication [3], [4], [19]. For example, in [4], the authors propose 2.5D matrix multiplication, an optimal communication avoiding algorithm for matrix multiplication in HPC/server-based computing, that divides input matrices into blocks and stores redundant copies of them across processors to reduce bandwidth and latency costs. However, perhaps due to lack of a proper analysis for cloud-based distributed computing, existing algorithms for straggler mitigation in the cloud do naive matrix multiplication [13]–[15]. Next, we bridge the gap between cost analysis and straggler mitigation for distributed computation in the serverless setting.

### 3. COST ANALYSIS: NAIVE AND BLOCKED MULTIPLICATION

There are communication and computation costs associated with any distributed algorithm. Communication costs themselves are of two types: latency and bandwidth. For example, sending  $n$  bits requires packing them into contiguous memory and transmitting them as a message. The latency cost  $\alpha$  is the fixed overhead time spent in packing and transmitting a message over the network. Thus, to send  $Q$  messages, the total latency cost is  $\alpha Q$ . Similarly, to transmit  $K$  bits, a

#### Algorithm 1: Distributed naive matrix multiplication

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**Input:** Matrices  $\mathbf{A} \in \mathbb{R}^{m \times n}$  and  $\mathbf{B} \in \mathbb{R}^{n \times l}$   
**Result:**  $\mathbf{C} = \mathbf{A} \times \mathbf{B}$

- 1 **Initialization:** Divide  $\mathbf{A}$  into submatrices of size  $a \times n$  (row-wise division) and  $\mathbf{B}$  into submatrices of size  $n \times a$  (column-wise division)
- 2 **for**  $i=1$  to  $m/a$  **do**
- 3     **for**  $j=1$  to  $l/a$  **do**
- 4         1. Worker  $W_{ij}$  receives  $i$ -th chunk of  $\mathbf{A}$ , say  $\mathbf{A}(i, :)$ , and  $j$ -th chunk of  $\mathbf{B}$ , say  $\mathbf{B}(:, j)$
- 5         2.  $W_{ij}$  computes the  $a \times a$  chunk of  $\mathbf{C}$ , that is,  $\mathbf{C}(i, j) = \mathbf{A}(i, :) \times \mathbf{B}(:, j)$
- 6         3.  $W_{ij}$  writes  $\mathbf{C}(i, j)$  back to the cloud storage
- 7     **end**
- 8 **end**

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#### Algorithm 2: Distributed blocked matrix multiplication

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**Input:** Matrices  $\mathbf{A} \in \mathbb{R}^{m \times n}$  and  $\mathbf{B} \in \mathbb{R}^{n \times l}$   
**Result:**  $\mathbf{C} = \mathbf{A} \times \mathbf{B}$

- 1 **Initialization:** Divide  $\mathbf{A}$  into  $m/b \times n/b$  matrix and  $\mathbf{B}$  into  $n/b \times l/b$  matrix of  $b \times b$  blocks where  $b$  is the block-size
- // Computation phase:*
- 2 **for**  $i = 1$  to  $m/b$  **do**
- 3     **for**  $j = 1$  to  $l/b$  **do**
- 4         **for**  $k = 1$  to  $n/b$  **do**
- 5             1. Worker  $W_{ijk}$  gets  $(i, k)$ -th block of  $\mathbf{A}$ , say  $\mathbf{A}(i, k)$ , and  $(k, j)$ -th block of  $\mathbf{B}$ , say  $\mathbf{B}(k, j)$ , from the cloud storage
- 6             2.  $W_{ijk}$  then computes the  $b \times b$  product  $\hat{\mathbf{C}}_{ijk} = \mathbf{A}(i, k) \times \mathbf{B}(k, j)$
- 7             3. Worker writes the result  $\hat{\mathbf{C}}_{ijk}$  back to the cloud storage
- 8         **end**
- 9     **end**
- 10 **end**
- // Reduction phase:*
- 11 **for**  $i = 1$  to  $m/b$  **do**
- 12     **for**  $j = 1$  to  $l/b$  **do**
- 13         Spin a new worker, say  $W_{ij}$ , that stores an all-zero  $b \times b$  sub-block  $\mathbf{C}_{ij}$
- 14         **for**  $k = 1$  to  $n/b$  **do**
- 15             1.  $W_{ij}$  extracts the output  $\hat{\mathbf{C}}_{ijk}$  written by  $W_{ijk}$  from cloud storage
- 16             2.  $W_{ij}$  does  $\mathbf{C}_{ij} = \mathbf{C}_{ij} + \hat{\mathbf{C}}_{ijk}$
- 17         **end**
- 18          $W_{ij}$  writes  $\mathbf{C}_{ij}$  back to the cloud storage
- 19     **end**
- 20 **end**

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bandwidth cost proportional to  $K$ , given by  $\beta K$ , is associated. Letting  $\gamma$  denote the time to perform one floating point operation (FLOP), the total computing cost is  $\gamma F$ , where  $F$  is the total number of FLOPs at the node. Hence, the total time pertaining to one node that sends  $M$  messages,  $K$  bits and performs  $F$  FLOPs is

$$T_{\text{worker}} = \alpha Q + \beta K + \gamma F,$$

where  $\alpha \gg \beta \gg \gamma$ . The  $(\alpha, \beta, \gamma)$  model defined above has been well-studied and is used extensively in the HPC literature [2]–[4], [20], [21]. It is ideally suited for serverless computing, where network topology does not affect the latency costs as each worker reads/writes directly from/to

the cloud storage and no multicast gains are possible.

However, our analysis for costs incurred during distributed matrix multiplication differs from previous works in three principle ways. 1) Workers in serverless architecture cannot communicate amongst themselves, and hence, our algorithm for blocked multiplication is very different from optimal communication avoiding algorithm for HPC that involves message passing between workers [4]. 2) The number of workers in HPC analyses is generally fixed, whereas the number of workers in the serverless setting is quite flexible, easily scaling into the thousands, and the limiting factor is memory/bandwidth available at each node. 3) Computation on the inexpensive cloud is more motivated by savings in expenditure than the time required to run the algorithm. We define our cost function below.

If there are  $W$  workers, each doing an equal amount of work, the total amount of money spent in running the distributed algorithm on the cloud is proportional to

$$C_{\text{total}} = W \times T_{\text{worker}} = W(\alpha Q + \beta K + \gamma F). \quad (1)$$

Eq. (1) does not take into account the straggling costs as they increase the total cost by a constant factor (by re-running the jobs that are straggling) and hence does not affect our asymptotic analysis.

Inexpensive nodes in serverless computing are generally constrained by the amount of memory or communication bandwidth available. For example, AWS Lambda nodes have a maximum allocated memory of 3008 MB<sup>2</sup>, a fraction of the memory available in today's smartphones. Let the memory available at each node be limited to  $M$ . That is, the communication bandwidth available at each worker is limited to  $M$ , and this is the main bottleneck of the distributed system. We would like to multiply two large matrices  $\mathbf{A} \in \mathbb{R}^{m \times n}$  and  $\mathbf{B} \in \mathbb{R}^{n \times l}$  in parallel, and let  $M = O(n^\delta)$ . Note that if  $\delta \geq 2$ , one of the following will happen:

- $m = O(n)$  and  $l = O(n)$ , and the input matrices can fit into one worker's memory and parallelism is not required.
- Either  $m = \omega(n)$  or  $l = \omega(n)$  or both, and block-size for blocked matrix multiplication is  $n$ . The two schemes, naive and blocked multiplication, would exactly be the same in this case.

Thus, for all practical cases in consideration,  $\delta < 2$ .

**Theorem 3.1.** *For the cost model defined in Eq. (1), communication (i.e., latency and bandwidth) costs for blocked multiplication outperform naive multiplication by a factor of  $O(n^{1-\delta/2})$ , where the individual costs are listed in Table I.*

*Proof:* See Appendix A. ■

The rightmost column in Table I lists the ratio of communication costs for naive and blocked matrix multiplication. We note that the latter significantly outperforms the former,

<sup>2</sup>AWS Lambda limits are available at (may change over time) <https://docs.aws.amazon.com/lambda/latest/dg/limits.html>

Table I: Costs comparison for naive and blocked matrix multiplication in the serverless setting, where  $\delta < 2$ .

Cost type	Naive multiply	Blocked Multiply	Ratio: naive/blocked
Latency	$O(mn^{2(1-\delta)})$	$O(mn^{1-3\delta/2})$	$O(n^{1-\delta/2})$
Bandwidth	$O(mn^{2-\delta})$	$O(mn^{1-\delta/2})$	$O(n^{1-\delta/2})$
Computation	$O(mn)$	$O(mn)$	1

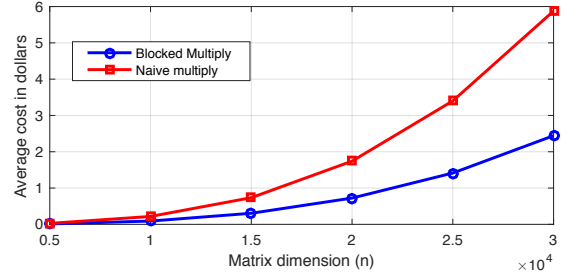


Figure 4: Comparison of AWS Lambda costs for multiplying two  $n \times n$  matrices, where each worker is limited by 3008 MB of memory and price per running worker per 100 milliseconds is \$0.000004897.

with communication costs being asymptotically worse for naive multiplication. An intuition behind why this happens is that each worker in distributed blocked multiplication does more work than in distributed naive multiplication for the same amount of received data. For example, to multiply two square matrices of dimension  $n$ , where memory at each worker limited by  $M = 2n$ ,  $a = 1$  for naive multiplication and  $b = \sqrt{n}$  for blocked multiplication. We note that the amount of work done by each worker in naive and blocked multiplication is  $O(n)$  and  $O(n^{3/2})$ , respectively. Since the total amount of work is constant and equal to  $O(n^3)$ , blocked matrix multiplication ends up communicating less during the overall execution of the algorithm as it requires fewer workers. Note that naive multiplication takes less time to complete as each worker does asymptotically less work, however, the number of workers required is asymptotically more, which is not an efficient utilization of resources and increases the expenditure significantly.

**Remark 1.** For clarity of exposition, we partition the input matrices into square-blocks of dimension  $b$ . However, optimal block dimensions for rectangular-block partitions can be found by minimizing (1) while incorporating the memory constraints of the distributed system. This is a convex problem that can be converted to a geometric program [22]. We note that the optimal block dimension found in this way is nearly square.

Figure 4 supports the above analysis where we plot the cost in dollars of multiplying two square matrices in AWS Lambda, where each node's memory is limited by 3008 MB and price per worker per 100 millisecond is \$0.000004897. However, as discussed earlier, existing schemes for straggler-resiliency in distributed matrix multiplication consider naive

multiplication which is impractical from a user's point of view. In the next section, we propose OverSketch, a scheme to mitigate the detrimental effects of stragglers for blocked matrix multiplication.

#### 4. OVERSKETCH: STRAGGLER-RESILIENT BLOCKED MATRIX MULTIPLICATION USING SKETCHING

Many of the recent advances in algorithms for numerical linear algebra have come from the technique of linear sketching, in which a given matrix is compressed by multiplying it with a random matrix of appropriate dimension. The resulting product can then act as a proxy for the original matrix in expensive computations such as matrix multiplication, least-squares regression, low-rank approximation, etc. [23]–[26]. For example, computing the product of  $\mathbf{A} \in \mathbb{R}^{m \times n}$  and  $\mathbf{B} \in \mathbb{R}^{n \times l}$  takes  $O(mnl)$  time. However, if we use  $\mathbf{S} \in \mathbb{R}^{n \times d}$  to compute the sketches, say  $\tilde{\mathbf{A}} = \mathbf{A}\mathbf{S} \in \mathbb{R}^{m \times d}$  and  $\tilde{\mathbf{B}} = \mathbf{S}^T \mathbf{B} \in \mathbb{R}^{d \times l}$ , where  $d \ll n$  is the sketch dimension, we can reduce the computation time to  $O(mdl)$  by computing an approximate product  $\mathbf{A}\mathbf{S}\mathbf{S}^T \mathbf{B}$ . This is very useful in applications like machine learning, where the data itself is noisy, and computing the exact result is not needed.

**Key idea behind OverSketch:** Sketching accelerates computation by eliminating redundancy in the matrix structure through dimension reduction. However, the coding-based approaches described in Section 2 have shown that redundancy can be *good* for combating stragglers if judiciously introduced into the computation. With these competing points of view in mind, our algorithm OverSketch works by "oversketching" the matrices to be multiplied by reducing dimensionality not to the minimum required for sketching accuracy, but rather to a slightly higher amount which simultaneously ensures both the accuracy guarantees and speedups of sketching *and* the straggler resilience afforded by the redundancy which was not eliminated in the sketch. OverSketch further reduces asymptotic costs by adopting the idea of block partitioning from HPC, suitably adapted for a serverless architecture.

Next, we propose a sketching scheme for OverSketch and describe the process of straggler mitigation in detail.

##### A. OverSketch: The Algorithm

During blocked matrix multiplication, the  $(i, j)$ -th block of  $\mathbf{C}$  is computed by assimilating results from  $d/b$  workers who compute the product  $\tilde{\mathbf{A}}(i, k) \times \tilde{\mathbf{B}}(k, j)$ , for  $k = 1, \dots, d/b$ . Thus, the computation  $\mathbf{C}(i, j)$  can be viewed as the product of the row sub-block  $\tilde{\mathbf{A}}(i, :) \in \mathbb{R}^{b \times d}$  of  $\tilde{\mathbf{A}}$  and the column sub-block  $\tilde{\mathbf{B}}(:, j) \in \mathbb{R}^{d \times b}$  of  $\tilde{\mathbf{B}}$ . An illustration is shown in Figure 5. Assuming  $d$  is large enough to guarantee the required accuracy in  $\mathbf{C}$ , we increase the sketch dimension from  $d$  to  $z = d + eb$ , where  $e$  is the worst case number of stragglers in  $N = d/b$  workers. For the example in Figure 5,  $e = 1$ . To get a better insight on  $e$ , we observe in our simulations for cloud systems like AWS lambda and EC2 that the number of stragglers is  $< 5\%$  for most runs. Thus,

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#### Algorithm 3: OverSketch: Distributed blocked matrix multiplication for the Cloud

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**Input :** Matrices  $\mathbf{A} \in \mathbb{R}^{m \times n}$  and  $\mathbf{B} \in \mathbb{R}^{n \times l}$ , sketch dimension  $z$ , straggler tolerance  $e$

**Result:**  $\mathbf{C} \approx \mathbf{A} \times \mathbf{B}$

- 1 **Sketching:** Use Algorithm 5 to obtain  $\tilde{\mathbf{A}} = \mathbf{A}\mathbf{S}$  and  $\tilde{\mathbf{B}} = \mathbf{S}^T \mathbf{B}$  distributedly
  - 2 **Block partitioning:** Divide  $\tilde{\mathbf{A}}$  into  $m/b \times z/b$  matrix and  $\tilde{\mathbf{B}}$  into  $z/b \times l/b$  matrix of  $b \times b$  blocks where  $b$  is the block-size
  - 3 **Computation phase:** Use the computation phase from Algorithm 2 to multiply  $\tilde{\mathbf{A}}$  and  $\tilde{\mathbf{B}}$ . This step invokes  $mlz/b^3$  workers, where  $z/b$  workers are used per block of  $\mathbf{C}$
  - 4 **Termination:** Stop computation when any  $d/b$  workers return their results for each of the  $ml/b^2$  blocks of  $\mathbf{C}$ , where  $d = z - eb$
  - 5 **Reduction phase:** Invoke  $ml/b^2$  workers for reduction as described in Algorithm 2 on available results
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#### Algorithm 4: Calculating Count-sketch of a matrix $\mathbf{A}$

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**Input :** Matrix  $\mathbf{A} \in \mathbb{R}^{m \times n}$  and sketch dimension  $b$

**Result:**  $\tilde{\mathbf{A}} \in \mathbb{R}^{m \times b}$ , a random Count-sketch of  $\mathbf{A}$

- 1 Multiply each column of  $\mathbf{A}$  by  $-1$  with probability 0.5
  - 2 Map each column of resultant  $\mathbf{A}$  to an integer in  $[1, b]$  uniformly randomly
  - 3 Add columns in  $\mathbf{A}$  that are mapped to the same integer. The resultant matrix is a Count-sketch of  $\mathbf{A}$
- 

if  $N = d/b = 40$ , i.e. 40 workers compute one block of  $\mathbf{C}$ , then  $e \approx 2$  is sufficient to get similar accuracy for matrix multiplication. We describe OverSketch in detail in Algorithm 3. Next, we describe how to compute the sketched matrices  $\tilde{\mathbf{A}}$  and  $\tilde{\mathbf{B}}$ .

Many sketching techniques have been proposed recently for approximate matrix computations. For example, to sketch a  $m \times n$  matrix  $\mathbf{A}$  with sketch dimension  $d$ , Gaussian projection takes  $O(mnd)$  time, Subsampled Randomized Hadamard Transform (SRHT) takes  $O(mn \log n)$  time, and Count-sketch takes  $O(nnz(\mathbf{A}))$  time, where  $nnz(\cdot)$  is the number of non-zero entries [24], [27]–[29].

Count sketch, first exploited in data streaming literature, has been widely applied to expedite large-scale matrix computations [27], [29], [30]. It is one of the most popular sketching techniques as it requires linear time to compute the matrix sketch with similar approximation guarantees for matrix multiplication. To compute the Count-sketch of  $\mathbf{A} \in \mathbb{R}^{m \times n}$  of sketch dimension  $b$ , each column in  $\mathbf{A}$  is multiplied by  $-1$  with probability 0.5 and then mapped to an integer sampled uniformly from  $\{1, 2, \dots, b\}$ . Then, to compute the sketch  $\tilde{\mathbf{A}}_c = \mathbf{A}\mathbf{S}_c$ , columns with the same mapped value are summed (see Algorithm 4 for details). An



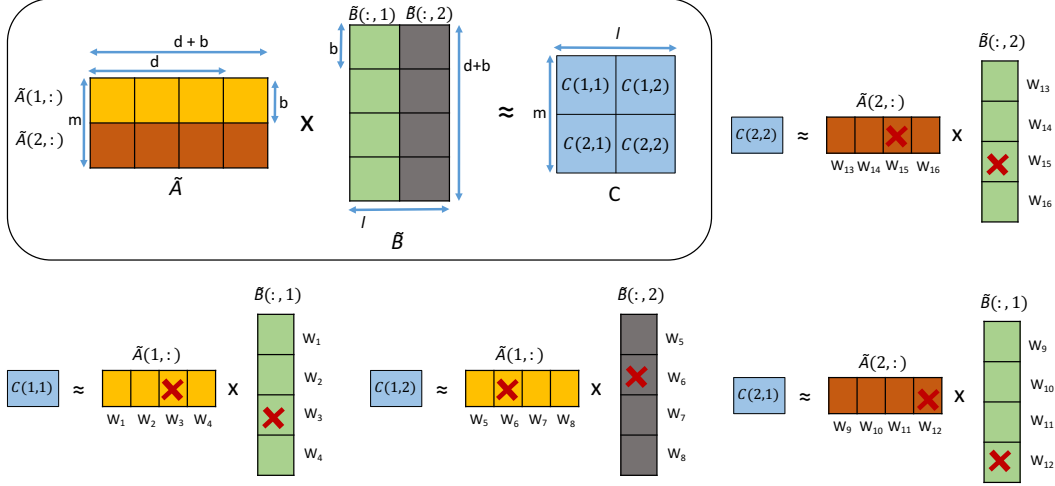


Figure 5: An illustration of multiplication of  $m \times z$  matrix  $\tilde{\mathbf{A}}$  and  $z \times l$  matrix  $\tilde{\mathbf{B}}$ , where  $z = d + b$  assures resiliency against one straggler per block of  $\mathbf{C}$ , and  $d$  is chosen by the user to guarantee a desired accuracy. Here,  $m = l = 2b$ ,  $d = 3b$ , where  $b$  is the block-size for blocked matrix multiplication. This scheme ensures one worker can be ignored while calculating each block of  $\mathbf{C}$ .

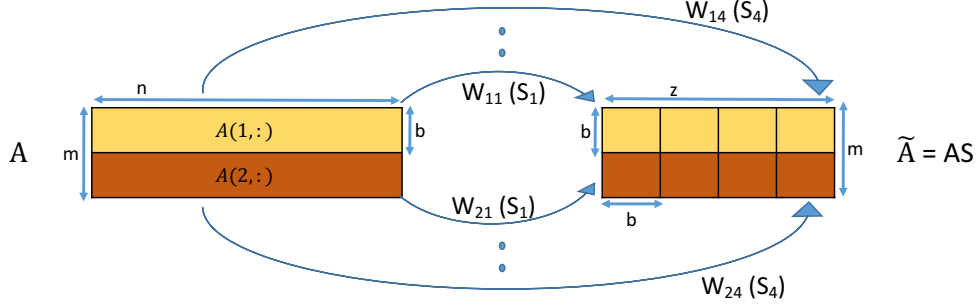


Figure 6: An illustration of sketching  $\mathbf{A} \in \mathbb{R}^{m \times n}$  in parallel using the sketch matrix in Eq. (3) with sketch dimension  $z = (N + e)b$ . Worker  $W_{ij}$  receives the row-block  $\mathbf{A}(i, :)$  of  $\mathbf{A}$  and the Count-sketch  $\mathbf{S}_j$  to compute the  $(i, j)$ -th block of  $\tilde{\mathbf{A}}$ . Sketching requires a total of  $mz/b^2$  workers. Here,  $z = 4b$ ,  $N = 3$  and  $e = 1$ , and  $\mathbf{A}$  is divided into 2 row-blocks, that is,  $m = 2b$ . Total number of workers required for distributed sketching is 8.

example of Count-sketch matrix with  $n = 9$  and  $b = 3$  is

$$\mathbf{S}_c^T = \begin{bmatrix} 0 & 0 & 0 & 1 & -1 & 0 & -1 & 0 & 0 \\ 1 & -1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 & -1 & -1 \end{bmatrix}. \quad (2)$$

Here,  $\mathbf{A}$  has 9 columns, and columns 4, 5 and 7 were mapped to 1, columns 1, 2 and 6 were mapped to 2, and columns 3, 8 and 9 were mapped to 3. Thus, the Count-sketch  $\tilde{\mathbf{A}}_c$  would have only 3 columns, which are obtained by summing the columns of  $\mathbf{A}$  with the same mapped value (after possibly multiplying with -1). The sparse structure of  $\mathbf{S}_c$  ensures that the computation of sketch takes  $O(nnz(\mathbf{A}))$  time. However, a drawback of the desirable sparse structure of Count-sketch is that it cannot be directly employed for straggler mitigation in blocked matrix multiplication as it would imply complete loss of information from a subset of columns of  $\mathbf{A}$ . For the example in (2), suppose the worker processing column 3 of  $\tilde{\mathbf{A}}_c$  be straggling. Ignoring this worker would imply that columns 3, 8 and 9 of  $\mathbf{A}$  were not considered in the

computation. This will generally lead to poor accuracy for sketched matrix multiplication.

To facilitate straggler mitigation for blocked matrix multiplication, we propose a new sketch matrix  $\mathbf{S}$ , inspired by Count-sketch, and define it as

$$\mathbf{S} = \frac{1}{\sqrt{N}}(\mathbf{S}_1, \mathbf{S}_2, \dots, \mathbf{S}_{N+e}), \quad (3)$$

where  $N = d/b$ ,  $e$  is the expected number of stragglers per block of  $\mathbf{C}$  and  $\mathbf{S}_i \in \mathbb{R}^{n \times b}$ , for  $i = 1, 2, \dots, (N + e)$ , is a Count-sketch matrix with dimension  $b$ . Thus, the total sketch-dimension for the sketch matrix in (3) is  $z = (N + e)b = d + eb$ . Computation of this sketch takes  $O(nnz(\mathbf{A})(N + e))$  time in total and can be implemented in a distributed fashion trivially, where  $(N + e)$  is the number of workers per block of  $\mathbf{C}$ . An illustration of distributed sketching of  $\mathbf{A}$  in serverless systems is described in Figure 6. For detailed steps, see Algorithm 5. A few remarks regarding OverSketch based distributed matrix multiplication are in order.

---

**Algorithm 5:** “Over” sketching  $\mathbf{A}$  in parallel using the sketch  $\mathbf{S}$  in (3) to compute  $\tilde{\mathbf{A}} = \mathbf{AS}$

---

**Input:** Matrix  $\mathbf{A} \in \mathbb{R}^{m \times n}$  and “Over” sketch dimension  $z$   
**Result:**  $\tilde{\mathbf{A}} = \mathbf{A} \times \mathbf{S}$

```

1 Initialization: Divide  $\mathbf{A}$  into row-blocks of size  $b \times n$ 
2 for  $i=1$  to  $m/b$  do
3   for  $j=1$  to  $z/b$  do
4     1. Worker  $W_{ij}$  receives  $i$ -th row-block of  $\mathbf{A}$ , say  $\mathbf{A}(i, :)$ 
5     2.  $W_{ij}$  uses Algorithm 4 to compute the  $(i, j)$ -th  $b \times b$  block of  $\tilde{\mathbf{A}}$  using Count-sketch  $\mathbf{S}_j$ , that is,  $\tilde{\mathbf{A}}(i, j) = \mathbf{A}(i, :) \times \mathbf{S}_j$ 
6     3.  $W_{ij}$  writes  $\tilde{\mathbf{A}}(i, j)$  back to the cloud storage
7   end
8 end

```

---

- **Graceful degradation:** Coding-based straggler mitigation (see Figure 2 for example) cannot tolerate more stragglers than provisioned. An advantage of using sketching schemes for computation is that more stragglers can be tolerated than initially provisioned at the cost of accuracy of the result, thus exhibiting ‘graceful degradation’.
- **Memory constrained distributed sketching:** It is assumed in Algorithm 5 that each worker can store an entire row-block of  $\mathbf{A}$  in memory to calculate its Count-sketch. That might not always be the case, especially in low-memory serverless nodes. However, since Count-sketch is a streaming based algorithm, workers can calculate the sketch by further partitioning the  $b \times n$  row-block into  $b \times b$  square blocks and copying only one block at a time (see [31] for details).
- **Straggler-resilient sketching:** We note that the stragglers can also be ignored during distributed sketching (Algorithm 5). More specifically, the blocks ignored during the sketching phase can be marked as faults/stragglers during computation phase in Algorithm 3.
- **Limitations of “Over-sampling”:** Schemes like leverage-score based sampling are also used in literature to compute approximate product of  $\mathbf{A}$  and  $\mathbf{B}$  [23], [32]. Such schemes are as efficient as Count-sketch but are not suitable for straggler-resilient blocked multiplication. For example, if a worker with a block of  $\tilde{\mathbf{A}}$  straggles, where  $\tilde{\mathbf{A}}$  is obtained by “over”-sampling the columns of  $\mathbf{A}$  according to leverage scores, results from all other workers that are working on that column-block of  $\tilde{\mathbf{A}}$  is wasted as part of the column-block is unavailable. Thus, oversampling can require huge redundancy even for a small number of stragglers.

Next, we prove statistical guarantees on the accuracy of our sketching based matrix multiplication algorithm.

#### B. OverSketch: Approximation guarantees

**Definition 4.1.** We say that an approximate matrix multiplication of two matrices  $\mathbf{A}$  and  $\mathbf{B}$  using sketch  $\mathbf{S}$ , given by  $\mathbf{ASS}^T\mathbf{B}$ , is  $(\epsilon, \theta)$  accurate if, with probability at least

$(1 - \theta)$ , it satisfies

$$\|\mathbf{AB} - \mathbf{ASS}^T\mathbf{B}\|_F^2 \leq \epsilon \|\mathbf{A}\|_F^2 \|\mathbf{B}\|_F^2.$$

Now, for blocked matrix multiplication using OverSketch and as illustrated in Figure 5, the following holds

**Theorem 4.1.** *Computing  $(\mathbf{AS}) \times (\mathbf{S}^T\mathbf{B})$  using sketch  $\mathbf{S} \in \mathbb{R}^{n \times z}$  in (3) and  $d = \frac{z}{\epsilon\theta}$ , while ignoring  $e$  stragglers among any  $\frac{z}{b}$  workers, is  $(\epsilon, \theta)$  accurate.*

*Proof:* See Appendix B.

For certain applications, the guarantee in theorem 4.1 may be too crude as the product of  $\|\mathbf{A}\|_F^2$  and  $\|\mathbf{B}\|_F^2$  in the RHS can get big for large matrices  $\mathbf{A}$  and  $\mathbf{B}$ . We can obtain a stronger result than in theorem 4.1 when  $\min(\text{rank}(\mathbf{A}), \text{rank}(\mathbf{B})) \ll n$ , for example, when  $\mathbf{A}$  is a fat matrix, or  $\mathbf{B}$  is a tall matrix. Without loss of generality, say  $\min(\text{rank}(\mathbf{A}), \text{rank}(\mathbf{B})) = \text{rank}(\mathbf{A}) = r$ . Thus,  $\|\mathbf{A}\|_2 \leq \|\mathbf{A}\|_F \leq \sqrt{r}\|\mathbf{A}\|_2$ , where  $\|\cdot\|_2$  denotes the spectral norm. Hence, with probability at least  $(1 - \theta)$

$$\|\mathbf{ASS}^T\mathbf{B} - \mathbf{AB}\|_F^2 \leq \epsilon r \|\mathbf{A}\|_2^2 \|\mathbf{B}\|_F^2.$$

Now, if we increase the sketch dimension by a factor of  $r$  to  $z = r(d + eb) = O(\frac{r}{\epsilon\theta})$ , we get

$$\|\mathbf{ASS}^T\mathbf{B} - \mathbf{AB}\|_F^2 \leq \epsilon \|\mathbf{A}\|_2^2 \|\mathbf{B}\|_F^2 \quad (4)$$

with probability  $(1 - \theta)$ , which is a better approximation for the product  $\mathbf{ASS}^T\mathbf{B}$ .

During the reduction phase, we use  $ml/b^2$  workers, which is much less than the number of workers used during the computation phase, that is,  $mlz/b^3$ . In our experiments, we observe that the possibility of stragglers reduces significantly if fewer workers are used. This is especially true for the reduction phase, as healthy running workers from the computation phase are reused, reducing the chances of stragglers. However, in the unfortunate event that stragglers are observed during reduction, speculative execution can be used, i.e. detecting and restarting the slow job. Another simple solution is to use existing coding techniques as described in Figure 2, that is, by adding one parity row-block to  $\tilde{\mathbf{A}}$  and one parity row column to  $\tilde{\mathbf{B}}$  before multiplying them, which can tolerate 3 stragglers in the worst case. However, this would require a decoding step to compensate for the missing stragglers.

## 5. EXPERIMENTAL RESULTS

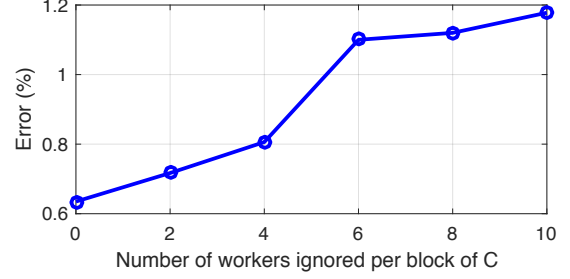
### A. Blocked Matrix Multiplication on AWS Lambda

We implement the straggler-resilient blocked matrix multiplication described above in the serverless computing platform *Pywren* [6], [7]<sup>3</sup>, on the AWS Lambda cloud system to compute an approximate  $\mathbf{C} = \mathbf{AS} \times \mathbf{S}^T\mathbf{B}$  with  $b = 2048, m = l = 10b, n = 60b$  and  $\mathbf{S}$  as defined in (3)

<sup>3</sup>A working implementation of OverSketch is available at <https://github.com/vvignupta/OverSketch>

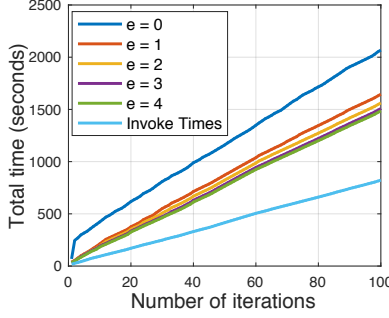


(a) Time statistics for OverSketch on AWS Lambda for the straggler profile in Figure 1

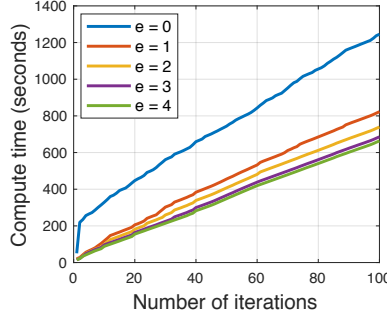


(b) Frobenius norm error for sketched matrix product

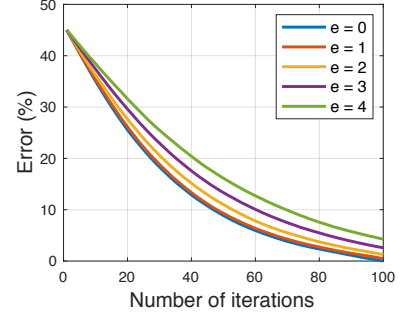
Figure 7: Time and approximation error for OverSketch with 3000 workers when  $e$ , the number of workers ignored per block of  $C$ , is varied from 0 to 10.



(a) Plot of total time (i.e., invocation time plus computation time) versus number of iterations.



(b) Plot of computation time versus number of iterations. When  $e = 1$ , algorithm takes 7 minutes less compared to when  $e = 0$ .



(c) Plot of percentage error versus iterations. Ignoring one worker per block of  $C$  has negligible affect on the convergence.

Figure 8: Time statistics and optimality gap on AWS Lambda while solving the LP in (5) using interior point methods, where  $e$  is the number of workers ignored per block of  $C$ .

with sketch dimension  $z = 30b$ . Throughout this experiment, we take  $A$  and  $B$  to be constant matrices where the entries of  $A$  are given by  $A(x, y) = x + y$  for all  $x \in [1, m]$  and  $y \in [1, n]$  and  $B = A^T$ . Thus, to compute  $(i, j)$ -th  $b \times b$  block of  $C$ , 30 nodes compute the product of  $A(i, :)$  and  $\tilde{B}(:, j)$ , where  $\tilde{A} = AS$  and  $\tilde{B} = S^T B$ . While collecting results, we ignore  $e$  workers for each block of  $C$ , where  $e$  is varied from 0 to 10.

The time statistics are plotted in Figure 7a. The corresponding worker job times are shown in Figure 1, where the median job time is around 42 seconds, and some stragglers return their results around 100 seconds and some others take up to 375 seconds. We note that the compute time for matrix multiplication reduces by a factor of 9 if we ignore at most 4 workers per 30 workers that compute a block of  $C$ . In figure 7b, for same  $A$  and  $B$ , we plot average error in matrix multiplication by generating ten instances of sketches and averaging the error in Frobenius norm,  $\frac{\|AB - ASS^T B\|_F}{\|AB\|_F}$ , across instances. We see that the average error is only 0.8% when 4 workers are ignored.

## B. Solving Optimization Problems with Sketched Matrix multiplication

Matrix multiplication is the bottleneck of many optimization problems. Thus, sketching has been applied to solve several fairly common optimization problems using second-order methods, like linear programs, maximum likelihood estimation, generalized linear models like least squares and logistic regression, semi-definite programs, support vector machines, Kernel ridge regression, etc., with essentially same convergence guarantees as exact matrix multiplication [25], [26]. As an instance, we solve the following linear program (LP) using interior point methods on AWS Lambda

$$\begin{aligned} & \underset{\mathbf{x}}{\text{minimize}} \quad \mathbf{c}^T \mathbf{x} \\ & \text{subject to} \quad \mathbf{A}\mathbf{x} \leq \mathbf{b}, \end{aligned} \quad (5)$$

where  $\mathbf{x} \in \mathbb{R}^{m \times 1}$ ,  $\mathbf{c} \in \mathbb{R}^{m \times 1}$ ,  $\mathbf{b} \in \mathbb{R}^{n \times 1}$  and  $\mathbf{A} \in \mathbb{R}^{n \times m}$  is the constraint matrix with  $n > m$ . To solve (5) using the logarithmic barrier method, we solve the following sequence of problems using Newton's method

$$\min_{\mathbf{x} \in \mathbb{R}^m} f(\mathbf{x}) = \min_{\mathbf{x} \in \mathbb{R}^m} \left( \tau \mathbf{c}^T \mathbf{x} - \sum_{i=1}^n \log(b_i - \mathbf{a}_i \mathbf{x}) \right), \quad (6)$$



where  $\mathbf{a}_i$  is the  $i$ -th row of  $\mathbf{A}$ ,  $\tau$  is increased geometrically as  $\tau = 2\tau$  after every 10 iterations and the total number of iterations is 100. The update in the  $t$ -th iteration is given by

$$\mathbf{x}_{t+1} = \mathbf{x}_t - \eta(\nabla^2 f(\mathbf{x}_t))^{-1} \nabla f(\mathbf{x}_t), \quad (7)$$

where  $\mathbf{x}_t$  is the estimate of the solution in the  $t$ -th iteration and  $\eta$  is the appropriate step-size. The gradient and Hessian for the objective in (6) are given by

$$\nabla f(\mathbf{x}) = \tau \mathbf{c} + \sum_{i=1}^n \frac{\mathbf{a}_i^T}{b_i - \mathbf{a}_i^T \mathbf{x}} \text{ and} \quad (8)$$

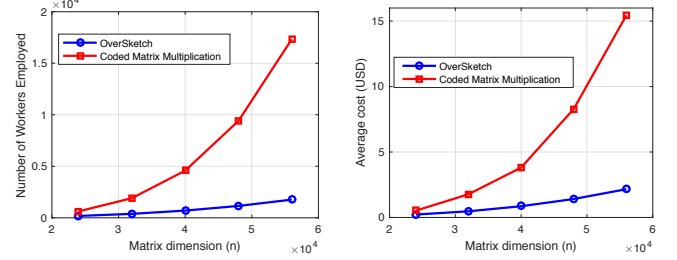
$$\nabla^2 f(\mathbf{x}) = \mathbf{A}^T \text{diag} \frac{1}{(b_i - \mathbf{a}_i^T \mathbf{x})^2} \mathbf{A}, \quad (9)$$

respectively. The square root of the Hessian is given by  $\nabla^2 f(\mathbf{x})^{1/2} = \text{diag} \frac{1}{|b_i - \mathbf{a}_i^T \mathbf{x}|} \mathbf{A}$ . The computation of Hessian requires  $O(nm^2)$  time and is the bottleneck in each iteration. Thus, we use our distributed and sketching-based blocked matrix multiplication scheme to mitigate stragglers while evaluating the Hessian approximately, i.e.  $\nabla^2 f(\mathbf{x}) \approx (\mathbf{S} \nabla^2 f(\mathbf{x})^{1/2})^T \times (\mathbf{S} \nabla^2 f(\mathbf{x})^{1/2})$ , on AWS Lambda, where  $\mathbf{S}$  is defined in (3).

We take the block size,  $b$ , to be 1000, the dimensions of  $\mathbf{A}$  to be  $n = 40b$  and  $m = 5b$  and the sketch dimension to be  $z = 20b$ . We use a total of 500 workers in each iteration. Thus, to compute each  $b \times b$  block of  $\mathbf{C}$ , 20 workers are assigned to compute matrix multiplication on two  $b \times b$  blocks. We depict the time and error versus iterations in figure 8. We plot our results for different values of  $e$ , where  $e$  is the number of workers ignored per block of  $\mathbf{C}$ . In our simulations, each iteration includes around 9 seconds of invocation time to launch AWS Lambda workers and assign tasks. In figure 8a, we plot the total time that includes the invocation time and computation time versus iterations. In 8b, we exclude the invocation time and plot just the compute time in each iteration and observe 34% savings in solving (5) when  $e = 1$ , whereas the effect on the error with respect to the optimal solution is insignificant (as shown in figure 8c).

### C. Comparison with Existing Straggler Mitigation Schemes

In this section, we compare OverSketch with an existing coding-theory based straggler mitigation scheme described in [13]. An illustration for [13] is shown in Figure 2. We multiply two square matrices  $\mathbf{A}$  and  $\mathbf{B}$  of dimension  $n$  on AWS Lambda using the two schemes, where  $\mathbf{A}(x, y) = x + y$  and  $\mathbf{B}(x, y) = x \times y$  for all  $x, y \in [1, n]$ . We limit the bandwidth of each worker by 400 MB (i.e. around 48 million entries, where each entry takes 8 bytes) for a fair comparison. Thus, we have  $3b^2 = 48 \times 10^6$ , or  $b = 4000$  for OverSketch and  $2an + a^2 = 48 \times 10^6$  for [13], where  $a$  is the size of the row-block of  $\mathbf{A}$  (and column-block of  $\mathbf{B}$ ). We vary the matrix dimension  $n$  from  $6b = 24000$  to  $14b = 56000$ . For OverSketch, we take the sketch dimension  $z$  to be  $n/2 + b$ ,



(a) Number of workers required for OverSketch and [13] (b) Cost for distributed matrix multiplication for OverSketch and [13]

Figure 9: Comparison of OverSketch with coded theory based scheme in [13] on AWS Lambda. OverSketch requires asymptotically less workers which translates to significant savings in cost.

and take  $e = 1$ , i.e., ignore one straggler per block of  $\mathbf{C}$ . For straggler mitigation in [13], we add one parity row in  $\mathbf{A}$  and one parity column in  $\mathbf{B}$ . In Figures 9a and 9b, we compare the workers required and average cost in dollars, respectively, for the two schemes. We note that OverSketch requires asymptotically fewer workers, and it translates to the cost for doing matrix multiplication. This is because the running time at each worker is heavily dependent on communication, which is the same for both the schemes. For  $n = 20000$ , the average error in Frobenius norm for OverSketch is less than 2%, and decreases as  $n$  is increased.

The scheme in [13] requires an additional decoding phase, and assume the existence of a powerful master that can store the entire product  $\mathbf{C}$  in memory and decode for the missing blocks using the redundant chunks. This is also true for the other schemes in [14]–[16]. Moreover, these schemes would fail when the number of stragglers is more than the provisioned redundancy while OverSketch has a ‘graceful degradation’ as one can get away by ignoring more workers than provisioned at the cost of accuracy of the result.

## 6. CONCLUSION

Serverless computing penetrates a large user base by allowing users to run distributed applications without the hassles of server management. We analyzed the cost of distributed computation in serverless computing for naive and blocked matrix multiplication. Through analysis and experiments on AWS Lambda, we show that the latter significantly outperforms the former. Thus, existing straggler mitigation schemes that do naive matrix multiplication are unsuitable. To this end, we develop OverSketch, a sketching based algorithm for approximate blocked matrix multiplication. Our sketching scheme requires time linear in the size of input matrices. As a distributed matrix multiplication algorithm, OverSketch has many advantages: reduction in dimension of input matrices for computational savings, and built-in straggler resiliency. Extensive experiments on AWS Lambda support our claims that OverSketch is resilient to stragglers, cost-efficient, and highly accurate for suitably chosen sketch dimension.

## APPENDIX

### A. Proof of Theorem 3.1

To compare naive and blocked multiplication, we first observe that the computation cost in (1), that is  $W \times F$ , is the same for both naive and blocked multiplication and is equal to  $O(mnl)$ , which is the total amount of work done during matrix-matrix multiplication<sup>4</sup>. Let  $W_1$  be the number of workers required for naive matrix multiplication. Then,  $W_1 = ml/a^2$ , as each worker is sent one row-block of  $\mathbf{A}$  from  $m/a$  choices, and one column-block of  $\mathbf{B}$  from  $l/a$  choices. Each worker receives  $2an$  entries and writes back  $a^2$  entries. Hence, the total communication incurred during the algorithm is  $W_1 \times (2an + a^2) = (2nml/a + ml)$ . Also, since each worker can only receive  $M$  entries, we have  $M = 2an$ , thus  $a = M/2n$ . Hence, the total bandwidth cost for naive multiplication is  $\beta \times (4n^2ml/M + ml) = O(n^{2-\delta}ml)$ . Also, the total number of messages sent during the process is  $W_1$ , and hence the total latency cost is  $O(mln^{2(1-\delta)})$ .

During the computation phase for blocked multiplication,  $W_{2,comp} = (n/b) \times ml/b^2$ , as computation of one  $b \times b$  block of  $\mathbf{C} \in \mathbb{R}^{m \times l}$  requires  $n/b$  workers, and there are a total of  $ml/b^2$  such blocks. Again, each worker receives two  $b \times b$  blocks, one from each  $\mathbf{A}$  and  $\mathbf{B}$ , and writes back a  $b \times b$  block, where  $b$  satisfies  $M = 2b^2$ . Thus, the total bandwidth cost incurred during the computation phase is  $\beta W_{2,comp} \times 3b^2 = 3\beta nml/b = O(mln^{1-\delta/2})$ . The total number of messages received by the workers is  $W_{2,comp}$ , and, hence, the latency cost is  $\alpha nml/b^3 = O(mln^{1-3\delta/2})$ . During the reduction phase, the number of workers required is  $W_{2,red} = ml/b^2$ , and each worker receives  $n/b$  blocks of size  $b \times b$  to compute one block of  $\mathbf{C}$ . Thus, for the reduction phase, the communication is  $W_{2,red} \times b^2 \times (n/b) = nml/b = O(mln^{1-\delta/2})$  and total messages sent is  $W_{2,red} \times (n/b) = mln/b^3 = O(mln^{1-3\delta/2})$ . Hence, the total latency and bandwidth costs for blocked multiplication are  $O(mln^{1-3\delta/2})$  and  $O(mln^{1-\delta/2})$ , respectively. This analysis justifies the costs summarized in Table I and proves the theorem.

### B. Proof of Theorem 4.1

The following three lemmas will assist us with the proof of Theorem 4.1.

**Lemma 1.1.** *let  $\mathbf{S}_c \in \mathbb{R}^{n \times b}$  be a Count sketch matrix. Then, for any vectors  $\mathbf{x}, \mathbf{y} \in \mathbb{R}^{n \times 1}$ , the following holds*

$$\begin{aligned} \mathbb{E}[\mathbf{x}^T \mathbf{S}_c \mathbf{S}_c^T \mathbf{y}] &= \mathbf{x}^T \mathbf{y} \\ \text{Var}[\mathbf{x}^T \mathbf{S}_c \mathbf{S}_c^T \mathbf{y}] &= \frac{1}{b} \left( \sum_{j \neq l} x_j^2 y_l^2 + \sum_{j \neq l} x_j y_j x_l y_l \right) \end{aligned} \quad (10)$$

<sup>4</sup>The computation cost for blocked matrix multiplication can be further improved by using Strassen type methods that take  $O(b^{2.38})$  to multiply two square sub-blocks of dimension  $b \times b$ , but we do not consider that advantage in this paper for clarity of exposition and to emphasize on savings just due to communication.

$$\leq \frac{1}{b} ((\mathbf{x}^T \mathbf{y})^2 + \|\mathbf{x}\|_2^2 \|\mathbf{y}\|_2^2) \leq \frac{2}{b} \|\mathbf{x}\|_2^2 \|\mathbf{y}\|_2^2. \quad (11)$$

*Proof:* See [33], Appendix A.  $\blacksquare$

**Lemma 1.2.** *Let  $\mathbf{S} = \frac{1}{\sqrt{N}}(\mathbf{S}_1, \mathbf{S}_2, \dots, \mathbf{S}_N) \in \mathbb{R}^{n \times d}$ , where  $d = Nb$  and  $\mathbf{S}_i \in \mathbb{R}^{n \times b}$  is a Count-sketch matrix that satisfies (10) and (11), for all  $i \in 1, 2, \dots, N$ . Then, for any vectors  $\mathbf{x}, \mathbf{y} \in \mathbb{R}^{n \times 1}$ , the following holds*

$$\begin{aligned} \mathbb{E}[\mathbf{x}^T \mathbf{S} \mathbf{S}^T \mathbf{y}] &= \mathbf{x}^T \mathbf{y} \\ \text{Var}[\mathbf{x}^T \mathbf{S} \mathbf{S}^T \mathbf{y}] &\leq \frac{2}{d} \|\mathbf{x}\|_2^2 \|\mathbf{y}\|_2^2. \end{aligned}$$

*Proof:* Note that,  $\mathbf{S} \mathbf{S}^T = \frac{1}{N}(\mathbf{S}_1 \mathbf{S}_1^T + \mathbf{S}_2 \mathbf{S}_2^T + \dots + \mathbf{S}_N \mathbf{S}_N^T)$ . Thus,

$$\mathbf{x}^T \mathbf{S} \mathbf{S}^T \mathbf{y} = \frac{1}{N} (\mathbf{x}^T \mathbf{S}_1 \mathbf{S}_1^T \mathbf{y} + \mathbf{x}^T \mathbf{S}_2 \mathbf{S}_2^T \mathbf{y} + \dots + \mathbf{x}^T \mathbf{S}_N \mathbf{S}_N^T \mathbf{y}),$$

and hence,  $\mathbb{E}[\mathbf{x}^T \mathbf{S} \mathbf{S}^T \mathbf{y}] = \mathbf{x}^T \mathbf{y}$  by (10) and linearity of expectation. Now,

$$\begin{aligned} \text{Var}[\mathbf{x}^T \mathbf{S} \mathbf{S}^T \mathbf{y}] &= \mathbb{E}[(\mathbf{x}^T \mathbf{S} \mathbf{S}^T \mathbf{y} - \mathbf{x}^T \mathbf{y})^2] \\ &= \mathbb{E}\left[\frac{1}{N} ((\mathbf{x}^T \mathbf{S}_1 \mathbf{S}_1^T \mathbf{y} + \mathbf{x}^T \mathbf{S}_2 \mathbf{S}_2^T \mathbf{y} + \dots + \mathbf{x}^T \mathbf{S}_N \mathbf{S}_N^T \mathbf{y}) - N \mathbf{x}^T \mathbf{y})^2\right] \\ &= \mathbb{E}\left[\frac{1}{N^2} \left(\sum_{i=1}^N (\mathbf{x}^T \mathbf{S}_i \mathbf{S}_i^T \mathbf{y} - \mathbf{x}^T \mathbf{y})\right)^2\right] \\ &= \frac{1}{N^2} \left( \sum_{i=1}^N \mathbb{E}[(\mathbf{x}^T \mathbf{S}_i \mathbf{S}_i^T \mathbf{y} - \mathbf{x}^T \mathbf{y})^2] \right. \\ &\quad \left. + \sum_{i \neq j} \mathbb{E}[(\mathbf{x}^T \mathbf{S}_i \mathbf{S}_i^T \mathbf{y} - \mathbf{x}^T \mathbf{y})(\mathbf{x}^T \mathbf{S}_j \mathbf{S}_j^T \mathbf{y} - \mathbf{x}^T \mathbf{y})] \right) \\ &= \frac{1}{N^2} \left( \sum_{i=1}^N \text{Var}[\mathbf{x}^T \mathbf{S}_i \mathbf{S}_i^T \mathbf{y}] + \sum_{i \neq j} \mathbb{E}[(\mathbf{x}^T \mathbf{S}_i \mathbf{S}_i^T \mathbf{y} - \mathbf{x}^T \mathbf{y})(\mathbf{x}^T \mathbf{S}_j \mathbf{S}_j^T \mathbf{y} - \mathbf{x}^T \mathbf{y})] \right). \end{aligned} \quad (12)$$

Noting that  $\mathbf{S}_1, \mathbf{S}_2, \dots, \mathbf{S}_N$  are independent random variables and using (10), we get

$$\begin{aligned} &\mathbb{E}[(\mathbf{x}^T \mathbf{S}_i \mathbf{S}_i^T \mathbf{y} - \mathbf{x}^T \mathbf{y})(\mathbf{x}^T \mathbf{S}_j \mathbf{S}_j^T \mathbf{y} - \mathbf{x}^T \mathbf{y})] \\ &= \mathbb{E}[\mathbf{x}^T \mathbf{S}_i \mathbf{S}_i^T \mathbf{y} - \mathbf{x}^T \mathbf{y}] \mathbb{E}[\mathbf{x}^T \mathbf{S}_j \mathbf{S}_j^T \mathbf{y} - \mathbf{x}^T \mathbf{y}] = 0 \quad \forall i \neq j. \end{aligned}$$

Now, using the above equation and (11) in (12), we get

$$\text{Var}[\mathbf{x}^T \mathbf{S} \mathbf{S}^T \mathbf{y}] = \frac{1}{N^2} \times N \times \frac{2}{b} \|\mathbf{x}\|_2^2 \|\mathbf{y}\|_2^2 = \frac{2}{d} \|\mathbf{x}\|_2^2 \|\mathbf{y}\|_2^2,$$

which proves the lemma.  $\blacksquare$

**Lemma 1.3.** *Let  $d = 2/\epsilon$ . Then, for any  $\mathbf{A} \in \mathbb{R}^{m \times n}$ ,  $\mathbf{B} \in \mathbb{R}^{n \times l}$  and  $\mathbf{S}$  as defined in lemma 1.2,*

$$\mathbb{E}[\|\mathbf{A} \mathbf{B} - \mathbf{A} \mathbf{S} \mathbf{S}^T \mathbf{B}\|_F^2] \leq \epsilon \|\mathbf{A}\|_F^2 \|\mathbf{B}\|_F^2. \quad (13)$$

*Proof:* By the property of Frobenius norm and linearity of expectation, we have

$$\mathbb{E}[\|\mathbf{A} \mathbf{B} - \mathbf{A} \mathbf{S} \mathbf{S}^T \mathbf{B}\|_F^2] = \sum_{i=1}^m \sum_{j=1}^l \mathbb{E}[\mathbf{a}^{(i)} \mathbf{b}_{(j)} - \mathbf{a}^{(i)} \mathbf{S} \mathbf{S}^T \mathbf{b}_{(j)}]^2, \quad (14)$$

where  $\mathbf{a}^{(i)}$  and  $\mathbf{b}_{(j)}$  are the  $i$ -th row and  $j$ -th columns of  $\mathbf{A}$  and  $\mathbf{B}$ , respectively. Now, using lemma 1.2 in (14), we get

$$\begin{aligned}\mathbb{E}\|\mathbf{AB} - \mathbf{ASS}^T\mathbf{B}\|_F^2 &= \sum_{i=1}^m \sum_{j=1}^k \text{Var}[\mathbf{a}^{(i)}\mathbf{SS}^T\mathbf{b}_{(j)}]^2 \\ &\leq \sum_{i=1}^m \sum_{j=1}^k \frac{2}{d} \|\mathbf{a}^{(i)}\|_2^2 \|\mathbf{b}_{(j)}\|_2^2 \\ &= \epsilon \left( \sum_{i=1}^m \|\mathbf{a}^{(i)}\|_2^2 \right) \left( \sum_{j=1}^k \|\mathbf{b}_{(j)}\|_2^2 \right) \quad (\text{as } d = 2/\epsilon) \\ &= \epsilon \|\mathbf{A}\|_F^2 \|\mathbf{B}\|_F^2,\end{aligned}$$

which is the desired result.  $\blacksquare$

We are now ready to prove theorem 4.1. As illustrated in figure 5, we can think of computation of a  $b \times b$  sub-block  $\mathbf{C}(i, j)$  as multiplication of row block  $\tilde{\mathbf{A}}(i, :)$  of  $\tilde{\mathbf{A}} = \mathbf{AS}$  and column-block  $\tilde{\mathbf{B}}(:, j)$  of  $\tilde{\mathbf{B}} = \mathbf{S}^T\mathbf{B}$ . Since we ignore upto only  $e$  workers in the calculation of a  $b \times b$  block of  $\mathbf{C}$ , the effective sketch dimension is greater than  $d = \frac{2}{e\theta}$ , and therefore, from lemma 1.3

$$\begin{aligned}\mathbb{E}\|\mathbf{A}(i, :)\mathbf{B}(:, j) - \mathbf{A}(i, :)\mathbf{S}_{ij}\mathbf{S}_{ij}^T\mathbf{B}(:, j)\|_F^2 \\ \leq e\theta \|\mathbf{A}(i, :)\|_F^2 \|\mathbf{B}(:, j)\|_F^2,\end{aligned} \quad (15)$$

for all  $i \in 1, \dots, m/b$  and  $j \in 1, \dots, l/b$ . Note that even if we applied the same sketch on  $\mathbf{A}$  and  $\mathbf{B}$  across row and column blocks, respectively,  $\mathbf{S}_{ij}$  in the above equation might end up being different for each pair  $(i, j)$  depending upon the location of stragglers, though with a common property that the sketch dimension is at least  $d$ . Now, we note that

$$\begin{aligned}\mathbb{E}\|\mathbf{ASS}^T\mathbf{B} - \mathbf{AB}\|_F^2 &= \sum_{i=1}^{m/b} \sum_{j=1}^{l/b} \mathbb{E}\|\mathbf{A}(i, :)\mathbf{B}(:, j) - \mathbf{A}(i, :)\mathbf{S}_{ij}\mathbf{S}_{ij}^T\mathbf{B}(:, j)\|_F^2 \\ &\leq e\theta \sum_{i=1}^{m/b} \sum_{j=1}^{l/b} \|\mathbf{A}(i, :)\|_F^2 \|\mathbf{B}(:, j)\|_F^2 = e\theta \|\mathbf{A}\|_F^2 \|\mathbf{B}\|_F^2.\end{aligned}$$

Now, by Markov's inequality

$$\begin{aligned}\mathbb{P}(\|\mathbf{ASS}^T\mathbf{B} - \mathbf{AB}\|_F^2 > \epsilon \|\mathbf{A}\|_F^2 \|\mathbf{B}\|_F^2) \\ \leq \frac{\mathbb{E}\|\mathbf{ASS}^T\mathbf{B} - \mathbf{AB}\|_F^2}{\epsilon \|\mathbf{A}\|_F^2 \|\mathbf{B}\|_F^2} \\ \leq \frac{e\theta \|\mathbf{A}\|_F^2 \|\mathbf{B}\|_F^2}{\epsilon \|\mathbf{A}\|_F^2 \|\mathbf{B}\|_F^2} = \theta,\end{aligned}$$

which proves the desired result.  $\blacksquare$

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