

Watt Book 2 - by the
Muse
Dept A41

2580-~~60~~
Dept A41

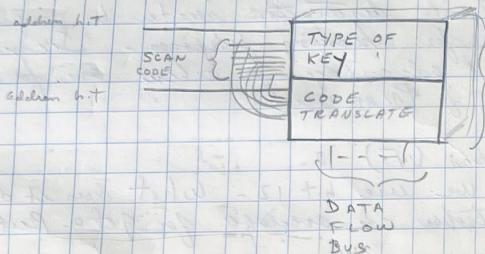
Pls return to:

J. D. GEORGE
P.O. Box 66
LOS GATOS CALIF. 95030

IBM Corp

7/25/77 KEYBOARD

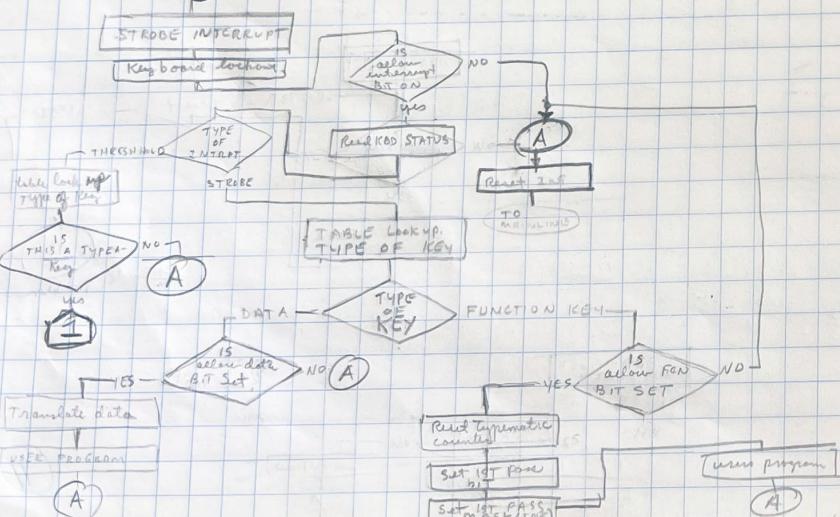
- Interrupt controller on enrolls key stroke.
 - begin process of keystroke if Keyboard interrupt is not masked off in software.
 - interrogate a KBD MAP/ROS with scan code which defines type of key (typematic function, upper/lower shift, alternate code, etc) as defined by user system.
- ROS is divided into two sections -



user Personalized ROS
or
Writable Table.

128x9
Including machine code.
(EBCDIC, ASCII, 6-bit, etc.)

- New line from KBD called Threshold will cause additional interrupts if KBD attachment typematic latch is set. (could be software latch bit) Threshold line emits a pulse each time the keyboard address is interrogated after the strobe line has been detected. Threshold line does not emit during strobe time.



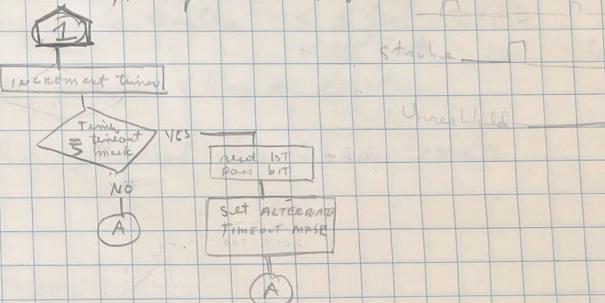
7/26. Conversation with John Fox - KBD

441-6319

Constraints of KBD - Upper/lower shift key positions should be in fixed locations. space bar; what else.

- One stroke per Keystroke.
 - Can we send back info to Keyboard after strobe to identify typematic key? NO! Must be before the fact - Too restrictive for our application.
 - Can we have a line that tells us that any key other than shift is being held down? YES
- THRESHOLD Line: does not need low speed line to KBD.
- If function keys are predefined can we get a function strobe? YES Too restrictive for our application.
 - Timing chart & logic for KBD? Being sent by J Fox.
 - If KBD lockout is used does it inhibit threshold output? YES Freezes output lines in whatever state they were in & stops scan drive.
 - On N Key Rollover, if Keyboard lockout is used + we are long enough to allow one or two additional key to be depressed, what happens?
a) do we get a strobe for each key as long as it does not exceed t? YES if lockout is not on

cont. flow from previous page



13

7/26/72 Review of New PALM.

Page 12 - More halfword - T0 | R2 | R1 | strobe |

1) R2 & R1 are reversed ~~from~~ ~~data~~ in order of operation - Do Re: Contents of Reg specified first is altered.

2) GET TO Register.

How long is data present on Bus Out from Can we Extend ~~o~~ (Hold) last E cycle to allow new data entered into destination register to propagate back to attachment for checking purposes - Seems to be of great advantage for online one for on checking. (page 15 of PALM specs)

3) Get / put instruction (F)

- a) How do we use b.t 12 - What does it do.
- b) what strobes are available for get or put.

4) Error Processing :

New KBD Attachment. See previous page

7/26/72

Status Register

b.T M : ADAPTOR READY.

Power on

KBD cable connected

b.T N : BUS OUT ERROR

a) KBD selected & bus out error detected.

b.T P : KEYBOARD DATA BUS ERROR

a) b.T M on

b) parity error detected from KBD to KBD attachment

b.T Q : ADAPTOR ENABLED

a) b.T M on

b) Set on by P.O.R or program instruction

c) reset by program (disable CMD)

b.T R : INTERRUPT REQUEST

a) Set if bits M & Q are on & strobe or threshold

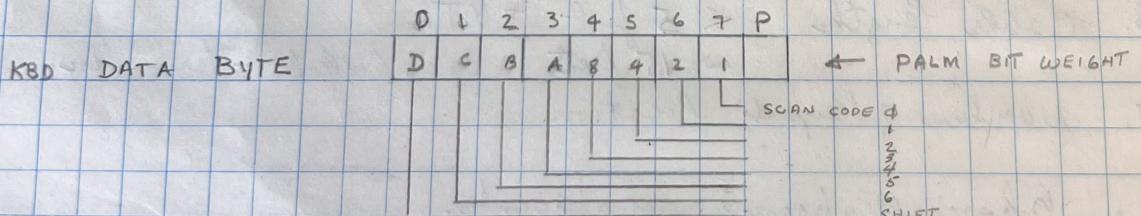
b) reset by next CMD

b.T S : LOCKOUT

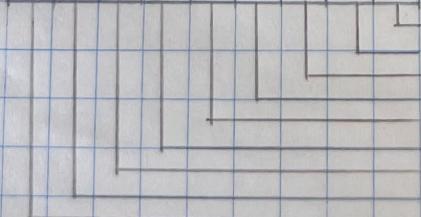
CAN BE EXTERNALLY JUMPERED TO LOCKOUT

WITH INTERRUPT TR ON. WE WILL PROGRAM

LOCKOUT. (open loop)

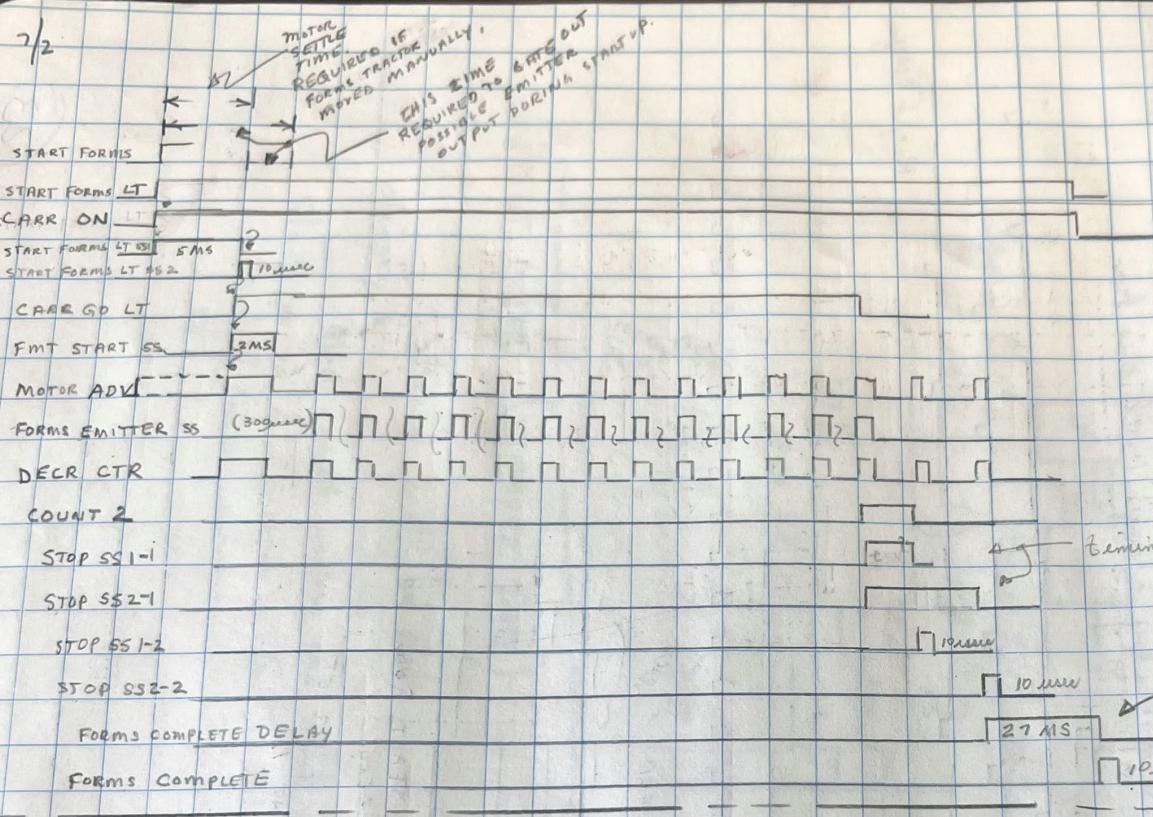


KBD STATUS BYTE

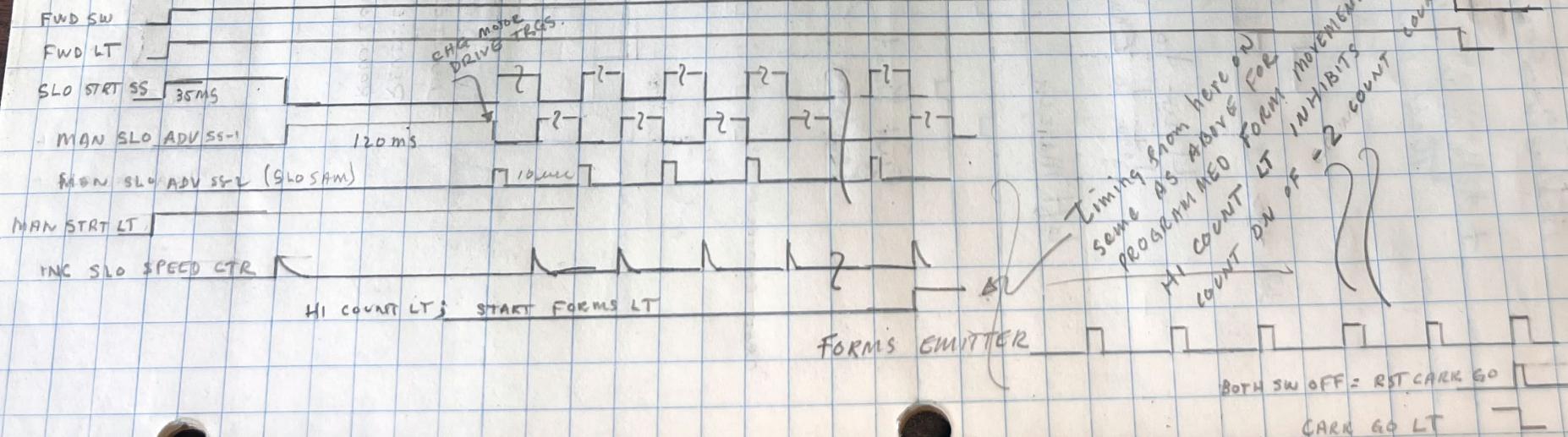


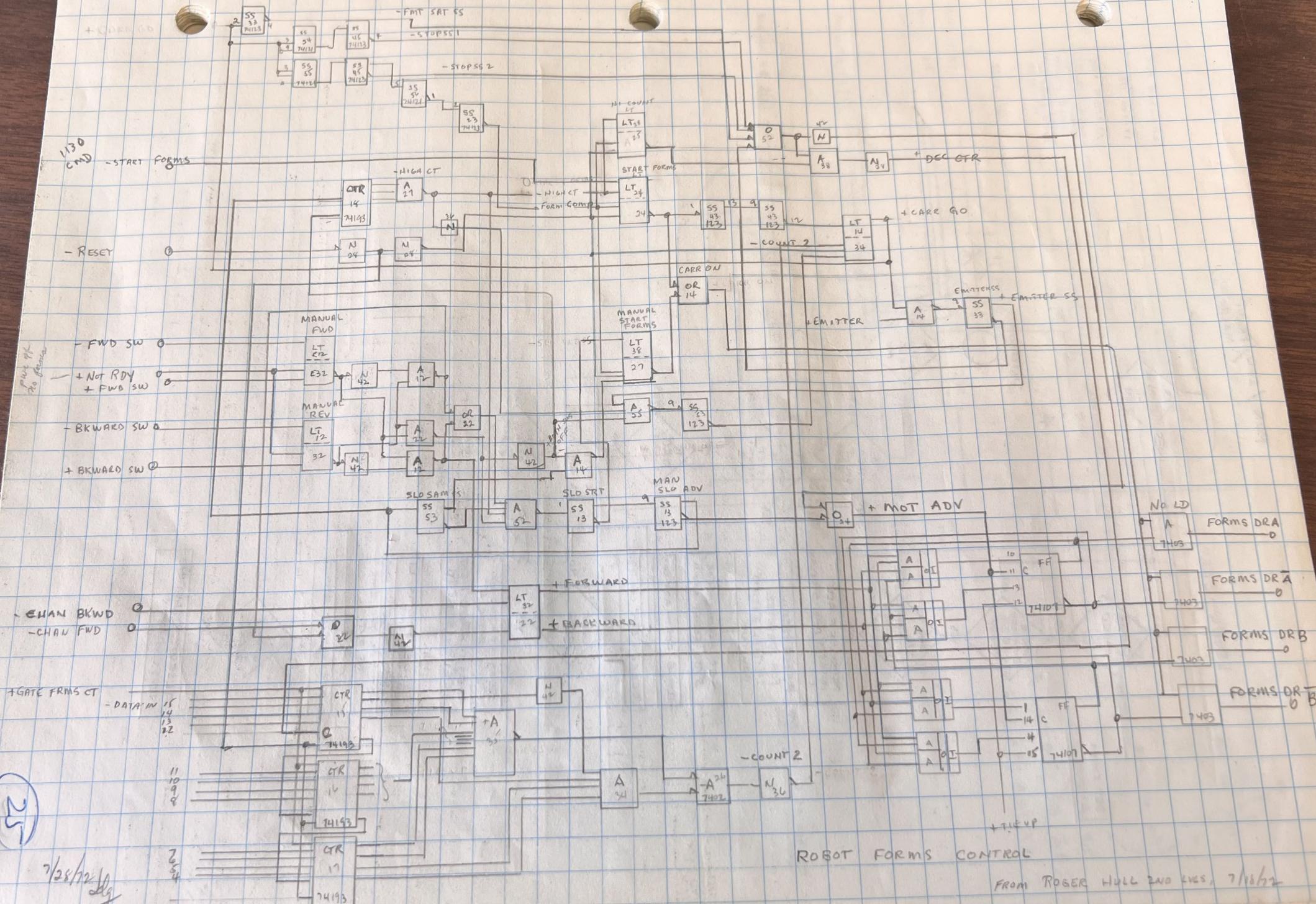
PARITY BIT
BUS OUT → KBD ERROR
KBD → ATTACHMENT PARITY ERROR
ADAPTOR ENABLED
INTERRUPT REQUEST
LOCKOUT ON
STROBE INTERRUPT
THRESHOLD INTERRUPT
ADAPTOR READY

7/2



MANUAL FWD SW

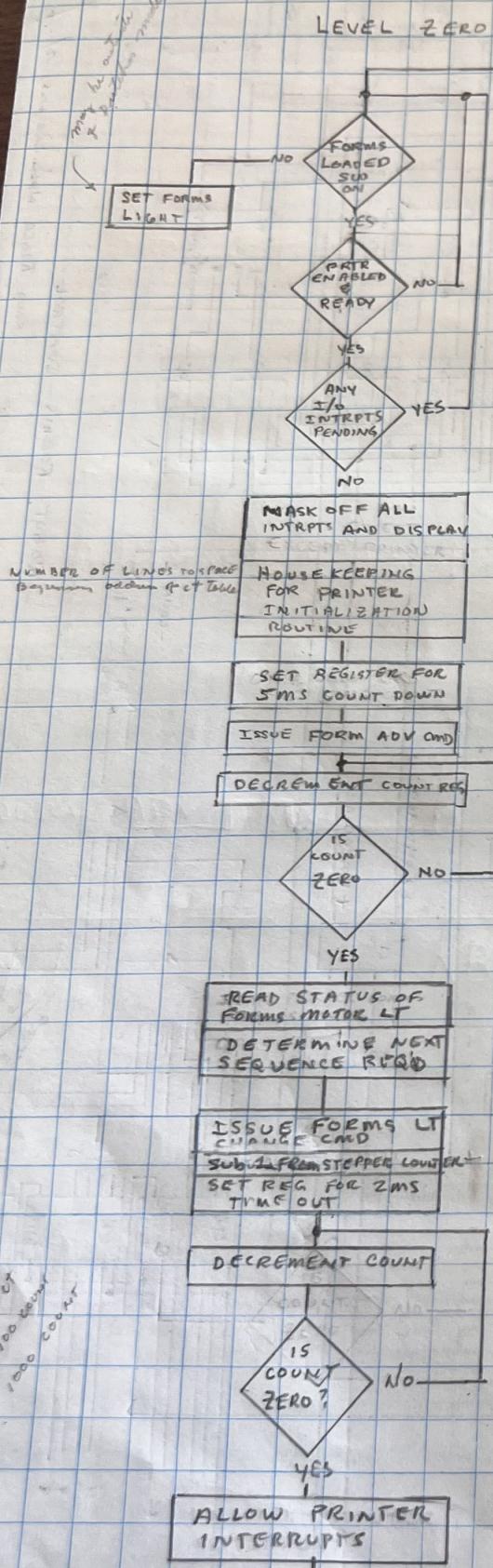




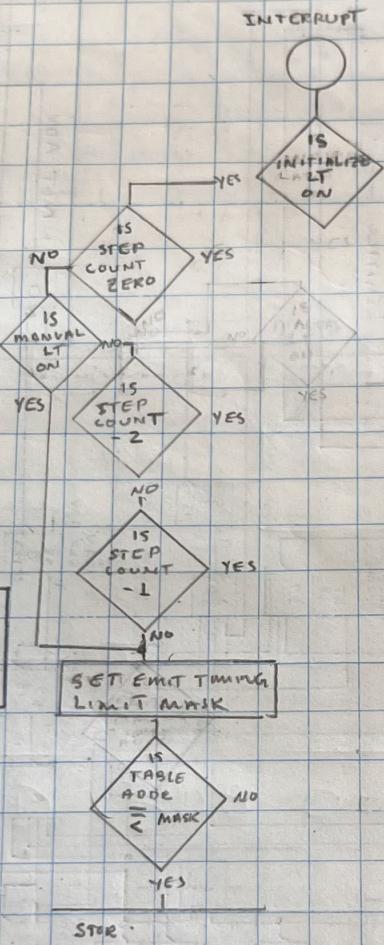
POR / IPL / PRINT MACRO

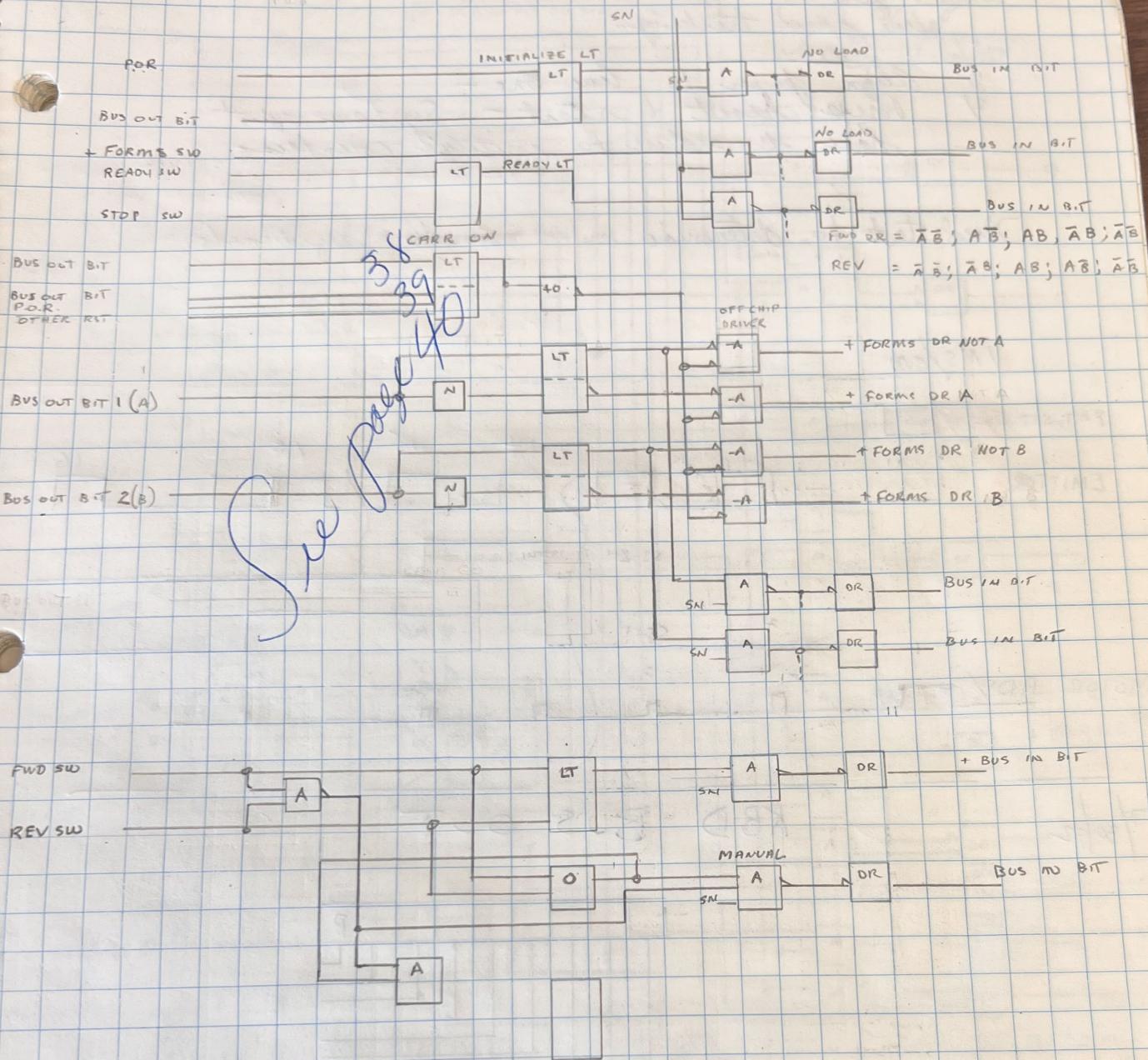
COUNTER REG
STATUS REG
N STEPS REG
EMIT TIMING TABLE ADDRESS
LINE SPACE #

LEVEL ZERO



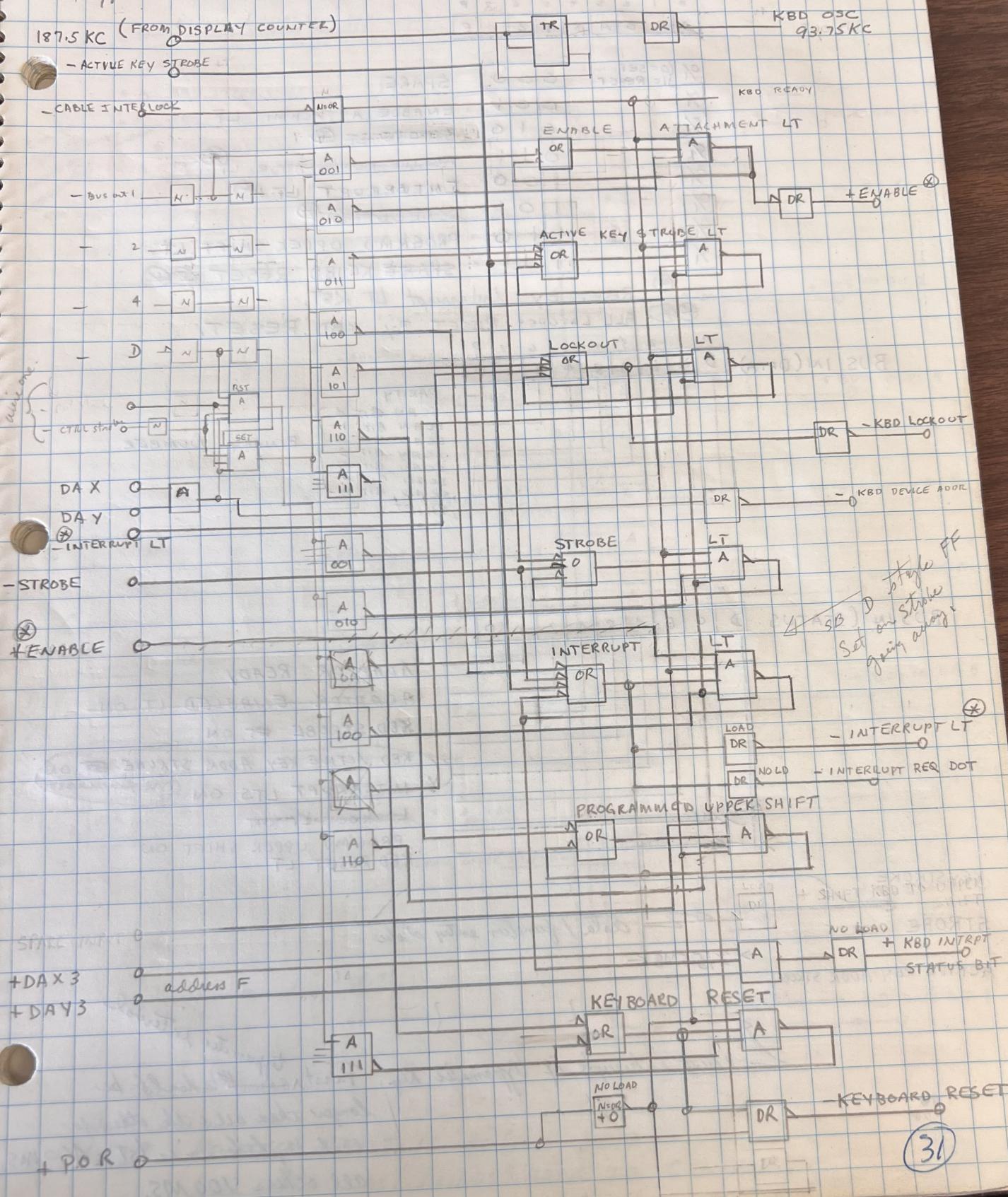
INTERRUPT





7/31/72

KBD LATCHES, Set / Reset



KBD CONTROL, BUS IN DATA AND STATUS INFORMATION.

8/31/72

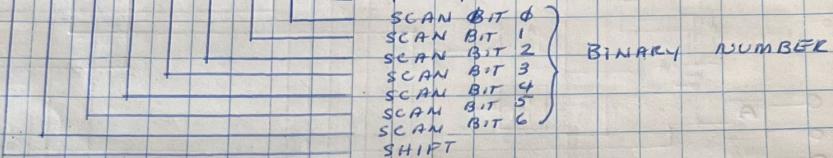
BUS OUT	0	1	2	3	4	5	6	7	P
	D	C	B	A	8	4	2	1	P

0%	0 = SET	000	SPARE
9%	1 = RESET	001	ENABLE ATTACH'MT LT
X		010	STROBE LT (X)
X		011	ACTIVE KEY(0) ADDR LT (X) (KBD ADDRESS STROBE) LT
%		100	INTERRUPT LT (X)
0%		101	LOCKOUT LT (X)
9%		110	PROG'MD UPPER SHIFT LT
9%		111	SPARE KEYBD RESET (X) (X)

~~(X)~~ RESET BY INTERRUPT LT RST.

~~(X)~~ ALL LATCHES RESET BY KBD RESET.

BUS IN (DATA)	0	1	2	3	4	5	6	7	P
	D	C	B	A	8	4	2	1	P



BUS IN (STATUS)	D	C	B	A	8	4	2	1	P
	D	C	B	A	8	4	2	1	P

ADAPTOR READY

ADAPTOR ENABLED LT ON

KBD STROBE LT ON

KBD ACTIVE KEY ADDR STROBE ~~LT~~ ON
INTERRUPT LTS ON One indicator

~~LT~~ LOCKOUT LT ON

PROG'MD UPPER SHIFT ON
KBD RESET LT

KEYSTROKE

STROBE

ACTIVE KEY ADDR STROBE

4-bus...

data / function entry stroke

$\approx 1.5-5 \text{ MS}$

count interrupt if typematic key. First ~~recent~~ typematic interval

should be longer than all of the others for each keystroke. i.e. $I_{ST} = 400 \text{ ms}$, all others 100 ms .

DB
200
156

8/1/72

FORMS CONTROL (Refer to pages 24 & 25)

1. Assume availability.

- a. Must track form being used. Need switch or operator procedure to initiate "line one" of form.
- b. 16 increments per line space.
- c. Can space up or down.
 - 1. space up sequence is \overline{AB} ; $A\overline{B}$; AB ; repeat seq.
 - 2. space down sequence is \overline{AB} ; \overline{AB} ; AB ; \overline{AB} etc
- d. Beginning and ending of carriage motion requires special attention.

Use 3 MS
for cycle timer

→ 1.0 issue Carriage GO instruction. Power to carriage motor is energized. (Mask off carriage interrupts)

- 1.1 Delay 50MS. (Allow motor to settle if previously moved by hand.)
- 1.2 determine status of motor drive latches
- 1.3 issue cmd to change motor drive latches ± 1 depending on direction desired. Value set into latches must be different than originals to cause motor movement.

Use 1 MS timer →

1.4 delay 2 MS; (ALLOW carriage interrupt)
Due to possible erroneous carriage emitter output during start up the must exhibit interrupts until motor is almost up to speed.

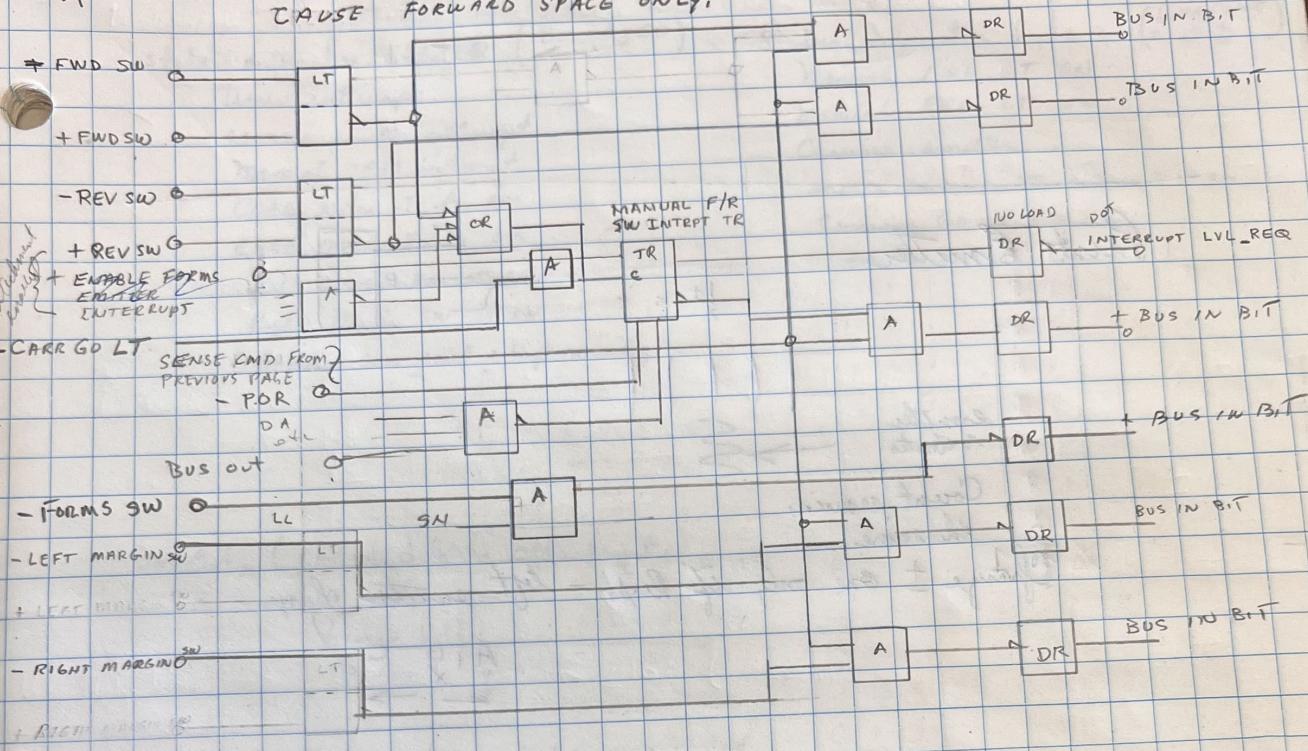
NOTE: The two delays mentioned above and any other delay required by the printer should be easily changed by Keyboard input.

- 2.0. For any given number of line spaces, the last two motor increments require special attention. In most cases the drive change to the motor will be delayed.

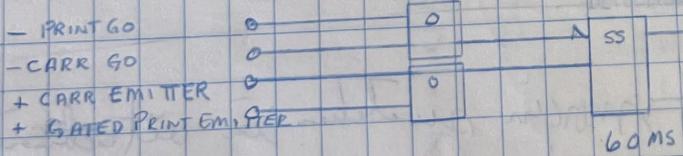
Final machine may require more than two slowdown iterations.

- 2.1 At count Plus 2 MASK Carriage Interrupt.
- 2.2 start 10KC timer.
- 2.3 present machine requires 3.2 MS delay
- 2.4 issue next carriage motor drive change.
- 2.5 Restart timer + count 4.8 MS. (present machine)
- 2.6 issue last motor increment.
- 3.0. Restart timer and count approximately 27 MS
- 3.1. END Operation.

Note: Printing may begin some where before 27 MS delay - To be defined.

8/1/72
JAGNOTE: IF both SWITCHES ARE HELD DOWN, MICRO PROGRAM WILL
CAUSE FORWARD SPACE ONLY.

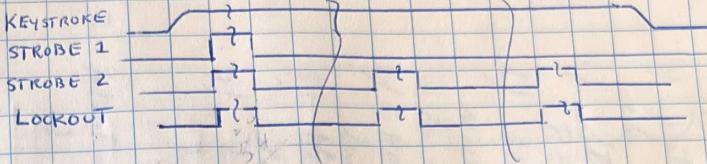
- ① Micro program will use timer to cause 250 ms delay for " n " increments for manual switches. After n increments and if switch is still depressed micro program should then advance paper at the regular speed.
to Robot $n = 14$. Appears to be too many -
- ② May be necessary to delay reset of forms emitter interrupt trigger
- ③ Micro program must not allow manual operation if print cmd is pending. If Carriage Go latch is on manual operation is disabled.



- FORMS / PRINT
NO Emitter

8/3/72
JDF

KEYBOARD FOR PALM/DAHIA



1	2	=	3 <	4 ;	5 :	6 %	7 0	8 >	9 *	10 (11)	12 -	13 +	14	15	16	68	69
1	2	1	2	3	4	5	6	7	8	9	0	-	8	<	7	8	9	68
17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	71	72	
37	→	Q	W	E	R	T	Y	U	I	O	P	@	K	4	5	6		
34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	74	75		
LOCK	A	S	D	F	G	H	J	K	L	\$	#	←	1	2	3	01		
49	50 (NON SENSE)	51	52	53	54	55	56	57	58	59	60 ?	61	62	63	77	78		
SHIFT	Z	X	C	V	B	N	M	,	.	/	SHIFT (NON SENSE)		Φ		08			
64	65 (NON SENSE)									66	67	REQUEST						
	RESET																	

L. USER TO SUPPLY OSCILLATOR DRIVE AND ± 5 VOLT AND $+8.5$ VOLT DC.

K. EIGHT(8) EXTRA BLANK KEYS S/B REQUESTED ~~REPLACEMENT FOR KEYS CABOLED 01-08.~~

A. RIGHT AND LEFT SHIFT KEYS SHOULD HAVE UNIQUE OUTPUT LINES. SHOULD NOT GENERATE STROBE 1 OR 2.

B. STROBE 1 OCCURS ONCE PER KEYSTROKE. STROBE 2 OCCURS ONCE EACH TIME AN ACTIVE (HELD DOWN) KEY IS ADDRESSED BY THE KEYBOARD SCAN LOGIC. LEADING EDGE OF STROBE 1 & 2 SHOULD COINCIDE.

C. KEYBOARD TO PROVIDE N KEY ROLL OVER. $N \geq 4$

D. SCAN CODE OUTPUT DATA LINES.

E. KEYBOARD TO ACCEPT SHIFT UP SHIFT CONTROL FROM KBD ATTACHMENT. UP SHIFT FROM ATTACHMENT WILL BE MODIFIED BY SETTING OF RIGHT SHIFT / LEFT SHIFT (ALPHA-NUMERIC) KEYS. MODIFICATION TO BE DONE BY KBD ATTACHMENT. KBD ATTACHMENT WILL DETERMINE SHIFT STATUS TO BE SENT TO CONTROLLER.

M. CABLE INTERLOCK

F. KEYBOARD STYLE: SLOPED

G. TYPOMATIC AND FUNCTION KEYS WILL BE DEFINED BY USER.

H. KEYS # 50, 61, and 65 WILL NOT CAUSE STROBE OUTPUT.

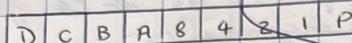
J. KEYS # 1, 17, 33, 49, 15, 16, 68, 69, 31, 32, 71, 72, 47, 48, 74, 75, 62, 63, 77, 78 to be color BLUE.
all other keys GRAY.

8/14/72

PRINTER CONTROL COMMANDS

9/14 Copy sent to Dick Prett.

CONTROL BYTE

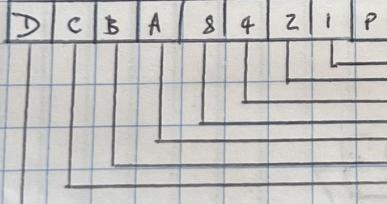
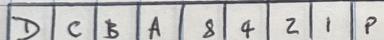


Update Plate
Copy

% 0 1	0 0 0
% 0 1	0 0 1
% 0 1	0 1 0
% 0 1	0 1 1
% 0 1	1 0 0
% 0 1	1 0 1
% 0 1	1 1 0
% 0 1	1 1 1
X 0 1	X 1 X X X X
X 0 1	I X X X X X
X 1 0	X I X X X X
X 1 0	I X X X X X
% 1 1	X % % % % %
% 0 1	1 0 0

STATUS BYTE 1
(BUS IN)

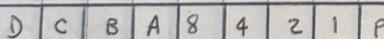
BUS OUT MUST HAVE BIT 1 ON.



FORMS Emitter INTERRUPT STATUS 3,7	
MANUAL F/R INTERRUPT	" "
PRINT Emitter INTERRUPT	" "
TIMER INTERRUPT	" "
LEFT MARGIN SW ON	" "
RIGHT MARGIN SW ON	" "
FORMS SWITCH	" "
EXPECTED INTERRUPT MISSING. (single shot)	

STATUS BYTE 2
(BUS IN)

BUS OUT MUST HAVE BIT 2 ON.



MANUAL FORWARD LT ON
MANUAL REVERSE LT ON
CARRIAGE Emitter
ENABLE CARRIAGE Emitter INTERRUPT LT
FORMS GO LT
FORMS MOTOR LT A
FORMS MOTOR LT B

STATUS BYTE 4
(BUS IN)

BUS OUT MUST HAVE BIT 4 ON



PRINT MOTOR LT A
PRINT MOTOR LT B
PRINT Emitter SINGLED SHOT
ENABLE TIMER

PR EMTTRA
PR EMTTRB
PR EMTTRC

Pages 78/79

8/22/72

Conversation with Ed -

File programs for level RBD running - He is now writing program to tie both level + controller together

- a) does not think PFS programmer has to come out.
 - b) His documentation good enough for programmer to interpret.
-

8/23

Sent KBD logics to Veen Kendrick

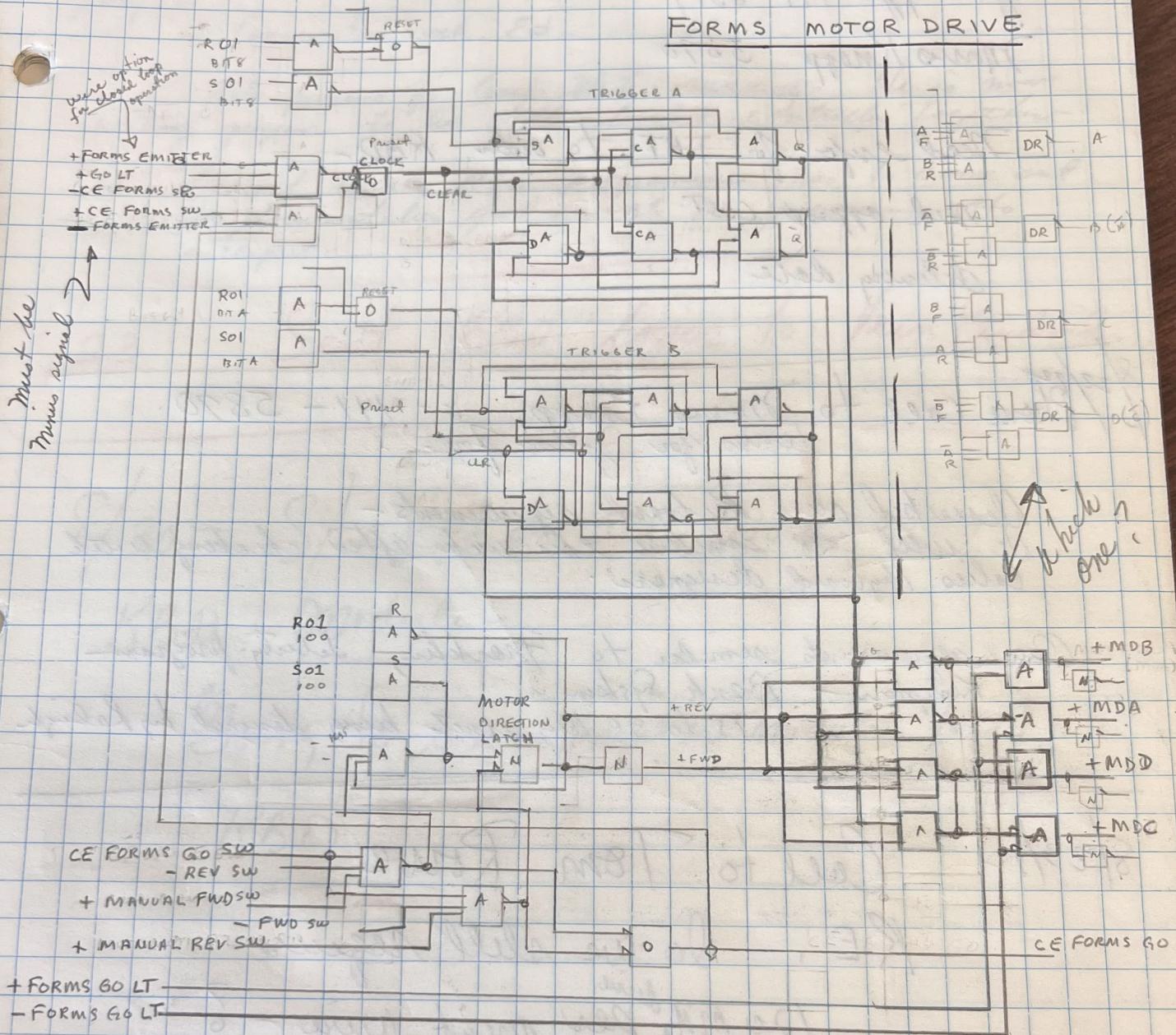
8/30-

Stopped all work on
KBD module -

Going to use printer module for

RBD

Think about CE Print Run mode -



Trigger A	0	1	1	0	0	0	0	0	0	0	0
Trigger B	0	0	1	1	0	0	0	0	0	0	0
MOTOR DIRECTION	FWD										
MDA	0	1		0	0	0	0	0	0	0	0
MDB	1	0	0	1	0	0	0	0	0	0	0
MDC	0	0	1	0	0	0	0	0	0	0	0
MDD	1	1	0	0	1	0	0	0	0	0	1

8/25/72

Discussion with Dave Nielsen! Above scheme too costly
Going back to original design, page 40.

8/26/72.

Printer
Consider following solutions for printer

Bit Don=Set

	D	C	B	A	8	4	2	1
%	0	0	X	1			0	1
%	0	0	X	1			0	0
%	0	0	X	1			1	0
	1	0	X	1	1	0	1	1

enable forms interrupt

Forms Go latch

Forms interrupt LT

Forms motor A+B

enable forms interrupt

Forms Go

Print interrupt

Print motor A/B

	D	C	B	A	8	4	2	1
%	0	0	1	X			0	1
%	0	0	1	X			0	0
%	0	0	1	X			1	0
	1	0	1	X	1	0	1	1
	%	0	1	0	X	1	0	0

enable Print interrupt

Print Go

Print interrupt

Print motor A/B

	D	C	B	A	8	4	2	1
%	1	1	X	1	X	X	1	
%	1	0	1	1	1	X	X	
%	1	X	X	1	X	X	1	X
	1	X	1	1	1	X	X	X
	%	1	1	X	X	X	1	
	%	1	1	X	X	1	X	

enable Timer interrupt

Enable 3 MS INTERRUPT

Timer interrupt

(limit ≤ 50 usec)

enable error LT

error interrupt LT

Print

8 Print wires

Forms

4 motor drive

1 Print ctrl

1 emitter

4 motor drive

1 forms SW

3 Emitters

2 margin SWs

2 emitter SWs

2 manual SWs

(3 Timeout
errors SS)

18 Bus in

8 bus in

9 Bus out

9 bus out

2 ctrl/put

1 ctrl

4 address

4 address

1 hi speed os

Timers

15.7 Kc osc

Timer bit 8

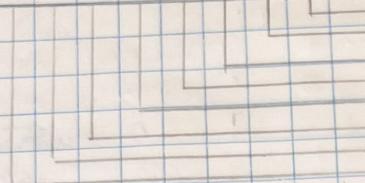
78

8/26/72

exercising.

STATUS BYTE 2

D C B A 8 4 2 1 P



status byte 1, status byte 2

TIMER INTERRUPT FORMS INTERRUPT
FORMS MOTOR LTA FORMS MOTOR LTB
ERROR INTERRUPT PRINT INTERRUPT
PRINT MOTOR LTA PRINT MOTOR LTB
PRINT MOTOR LTB

08

STATUS BYTE 1

D C B A 8 4 2 1 P



LEFT MARGIN FORMS SW
MANUAL FWD
MANUAL REV
RIGHT MARGIN PRINT EMITA
PRINT EMIT B
PRINT EMIT C

STATUS BYTE 3

D C B A 8 4 2 1 P

CONSIDER
SOFTWARE
STATUS



FORMS ENABLE
FORMS GO
PRINT ENABLE
PRINT GO
TIMER ENABLE
3MS TIMER ENABLE
ERROR INTERRUPT ENABLE

- Ready; absence of ready disables motor drive and emitter action.