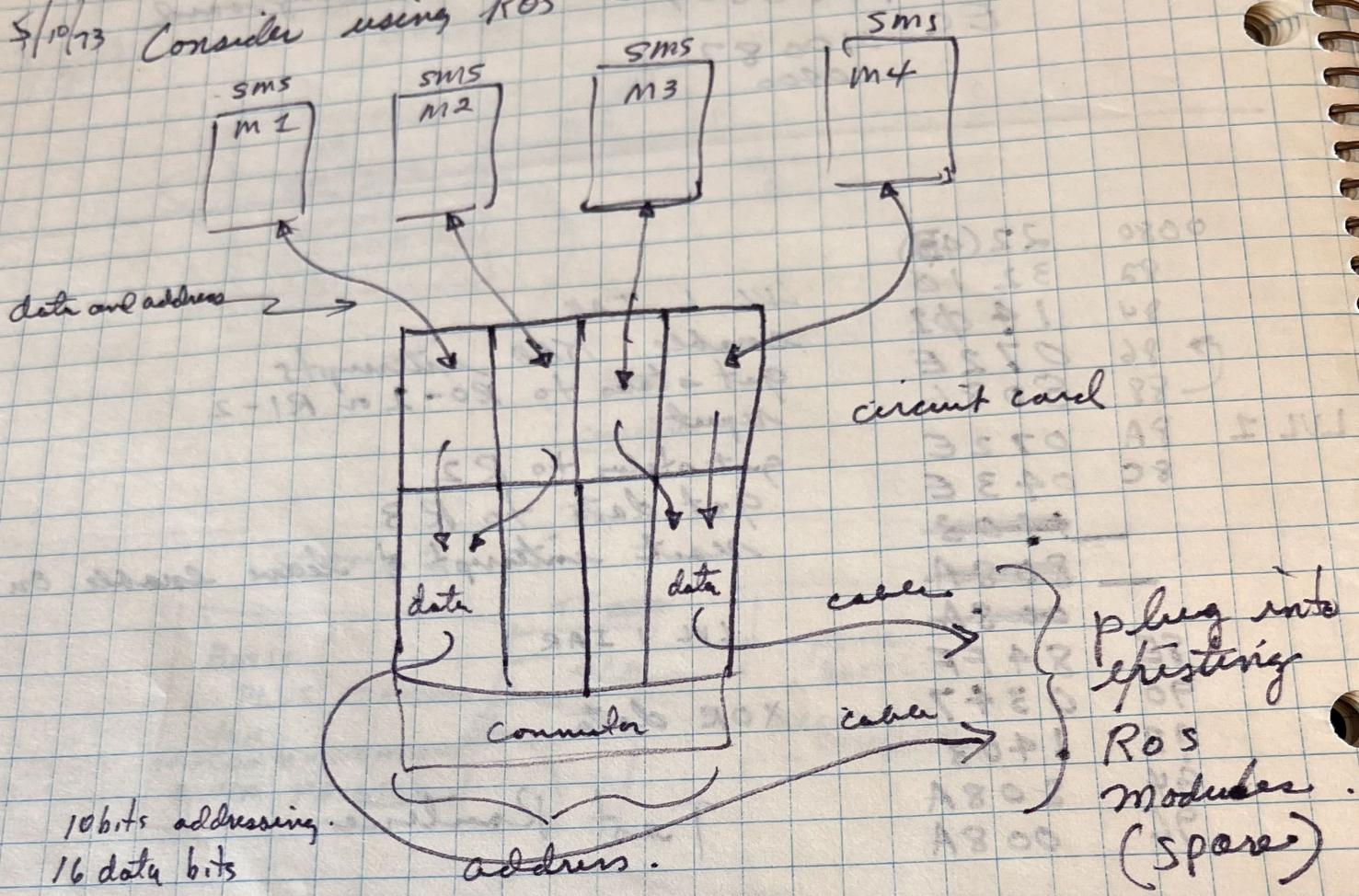


5/10/73 Consider using Ros Simulator for IPL.



Add switch to select Hex KBD
Ros or Tape Ros (SMS Simulator)

6/10

Test, status input

80 1401
82 22(49)
84 3210
86 071E
88 8086

LVL 1 071E
042E
1403
808A
008A

read status to R0-1

With no active keys status byte read
is $7\phi = \text{data key}$
 $F\phi = \text{Reset Key}$

$70 = \text{day key} -$
BIT $\phi = \phi$ due to conflict with
HEX KBD -

Try get & add Using address F

0080 1401
82 22(49)
84 3210
86 071E
88 8086

LVL 1 8A ~~FF~~ ~~0FOF~~
8C 808C
8E 808E
90 8090

go to data routine.

go to reset routine.

Jumped to 0094?

Removed Hex KBD - Status bit is now correct.
FF if data key -

National Airline 1303 - 3866

Coll. John Vokes 7-4615

Dick Lewis - 6061 (1044)

80 22(46)

82 83 C φ

83 FF

84 7324

86 C223

88 8084

8A 808E

8C 0IFF

8E 10F3

90 1401

92 6330

94 83(9C)

96 331φ

98 3212

9A 809A

9C 83FF

9E 044E

A0 0437

2 7428

4 442φ

6 1403

8 809E

A

C

E

B0

2

4

6

8

A

C

E

CO

Clear 2D

erase input 17

erase EOF 16

Test reg 3φ

A 1B

Cursor 6D

Space Bar = 4φ

83FF

852D

8617

8716

8830

891B

044E

C45B

80()CO

C49A

80()

048A

80()

0437

7428

4420

1403

80A8

80BC

0404

8480

3400

1st year
en LVL 1

entry

get to reg.

XOR

XOR mask

clear key mask

erase input "

erase EOF "

Test reg "

"

Backspace.

NO JUMP IF equal

go to clear routine-

go to Backspace

go to Test reg.

Sam Dozier 3874 3294 - APL sign on it
 Dave Sargione 3874
 2066

	TPA (elite)	K1 B12	- data set ready -
(6)	(purple)	B13	- RCV space
(3)	(orange)	D12	- clear to send
(5)	(Green)	D13	- can detect.
(8)			
(4)	(yellow)	B09	- req to send
(20)	(TAN)	B10	- transmitter ready originate mode
.	(Sw.)	D09	- originate mode
(2)	(red)	D10	- send space

? = signal ground (pink) D08

1 (white)
 10 blue
 9 brown

5 | " | 13 8-939-5263

KBD → CRT → Tape

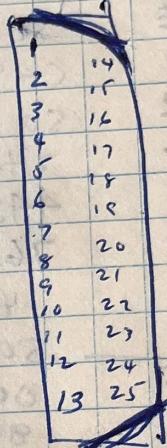
TAPE → CRT

Possibly have TP link

life size drawing of scamp.

Jae 258-9963

Anchors
Connector for TP



Female

5/12/73

HEX INPUT

0080 A000

(A800) start address 0404
 (C0FF) length count A409
 0090 +1 IAR 3910
 2243 } LI IAR 1401
 3210 } F001

008C

+1 cursor KAO INTPT D201
 F003 → D800
 1st 90 entry 2241 R2 = starting address DA01
 2A42 RA = length count 00FF
 896D cursor mask
 871B Backspace "

8600 clear R6 = half byte switch

85F0 numeric mask

83FF XOR mask

L1 Main entry

044E get data

A0 0437 reverse data bits

8B2D mask & clear key

C4BA jump not equal

A0(3F) go to clear routine

8B30 mask of request key (complete message has been issued)

C4BA

A0(3F) go to finish routine

C47A jmp if not backspace

B0 A0(17) go to backspace routine ←

C455 jump if numeric data

A0(1B) go to alpha routine

C663 jmp if 1st half byte

A0(1D) go to 2nd half byte

1ST HB 8601 set 2nd half switch

0448

0448

C0 0448 } shift left 4

0448

0844 save shifted data

Reset 1403 next interrupt

F0(2B) go to main entry

Backspace 84C0 blank character mask

7424 store blank -1

100P A600 7928 or Write cursor 0637 char switch

DO F0(0B) go to reset

HEX INPUT

D2 Alpha A408 add 9

C66B jmp if 2nd half byte

goto 1st half byte

reset half byte switch

8600 clear h.b.s 0123

0486 combine half bytes

7420 stuff +1

E0 0994 7928 write cursor

FA00 decrement count

CAA3 test for zero

F0(2B) 21 go to reset

OPCB clear 8AFF 8AFF

2241 D201

84C0 0B00

Finish F0 FA00

CAA3 repeat restart

F0(2B) 07 2241

8AFF

F0(3F) 31 go to reset

00FC 16F1 select 2048 display

F077F07B778

new input = 1

close EOF = 1

Clear 8XE8 8AFF

D201

0B00

FINISH 84C0

7420

FA00

CAA3

F0 07

D201

0B00

8AFF

F03B

5/17 Dick? Wants house - 443 - APL machine
Fishkill - 7 months ago.
input from John Fox. - gas panel

John Fox

Wants house - 27B - 3989 - Machine tech. - Ken Gerow - Mgr.

Frank Williams - 441-6424

Wants info on Bahia

Barker - Hash - UC -

5/8/73 - from Pat Smith .

SCAMP MODE - SCAMP mode (SYSTEMS mode)
TRM Terminal mode. (SCAMP is 2741 mode)
TYPE typewriter mode
PRINT enable printer
DISP enable display-
attn. attention

5/26/13

By Shifter:

Timing of present shifter design is close. Not sure what 64K storage configuration will do to bus delay -

Consider present data manipulation

1. a) move data to register.
- b) set command and count in shifter hardware
- c) get data R data, & shifted.

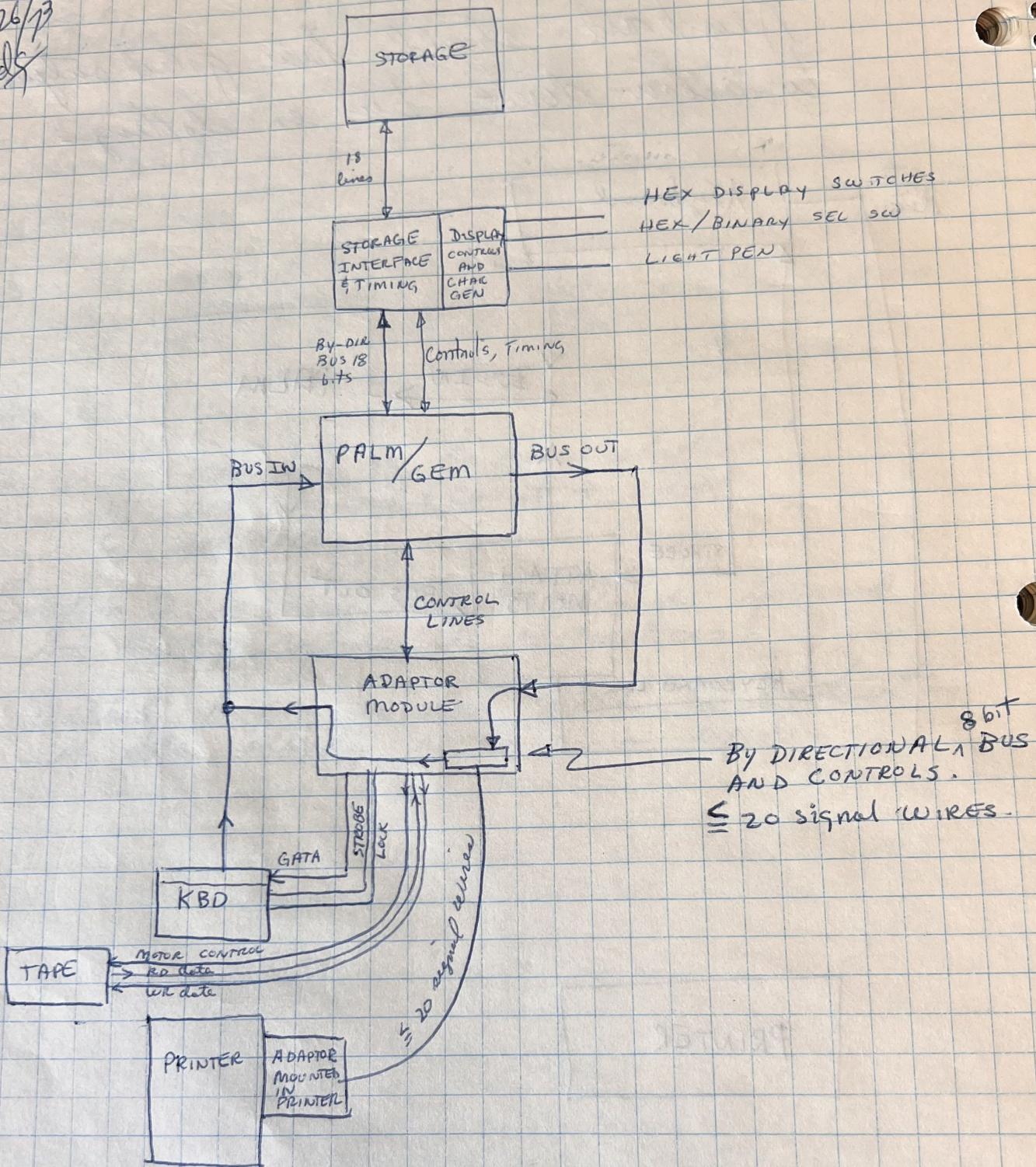
Why not:

2. a) Move data to hardware shifter.
- b) set command + Count + shift.
- c) get data.

Solution 2 is very ~~straight~~ straight forward but always requires 3 instructions.

Method 1 can be done with 2 instructions if data already resides in a register.

5/26/13
JL



QUESTION: What type of interface is required by D Mos?

D drivers can drive long lines direct.
no external drivers / receivers required.

2 EVEN

	LOW	HIGH
0	8 7 6 5 4	3 2 1
1	8 1 2 4 8	1 2 4
2	0 0 0 0 0	0 0 0
3	0 0 0 0 0	0 0 0
4	1 1 1 0 0	0 1 0
5	1 0 0 0 0	1 1 1
6	1 1 1 0 0	0 0 0
7	1 0 0 1 0	0 1 0
8	1 1 1 0 0	0 1 0
9	1 0 0 0 0	1 1 1
A	1 1 1 1 0	1 1 0
B	1 0 0 0 0	1 1 1
C	1 0 0 1 0	0 0 0
D	1 1 1 0 0	0 0 0
E	1 1 0 1 0	0 0 0
F	1 0 0 0 1	0 0 0
10	0 1 0 0 1	0 0 0
11	1 0 0 0 0	1 1 1
12	0 1 0 0 1	0 0 0
13	1 1 0 0 1	0 0 1
14	1 0 0 0 0	1 1 1
15	1 0 0 0 0	0 1 0
16	1 1 0 0 1	0 0 1
17	0 1 1 1 0	0 0 0
18	0 1 0 0 1	0 0 0
19	0 0 0 0 1	0 0 0
A	1 0 0 0 1	0 0 1
B	1 0 0 0 0	1 1 1
C	0 1 0 0 1	1 1 1
D	1 0 1 0 0	1 1 1 FB49
E	0 0 0 0 0	0 0 0 FB4B
IF	0 0 0 0 0	1 1 0 0

2 ODD

8 7 6 5 4	3 2 1
8 1 2 4 8	1 2 4
0 0 0 0 1	1 1 0
0 0 0 0 1	0 0 1
0 1 0 0 1	0 0 0
0 1 0 0 0	1 0 0
0 1 0 1 0	1 0 0
0 0 0 0 0	1 0 0
0 1 1 0 1	0 0 0
0 1 0 0 0	0 0 0
0 1 0 0 0	1 0 0
0 1 0 1 1	1 0 0
0 1 0 0 0	0 0 0
0 1 0 0 0	0 0 0
0 1 0 1 0 0 1 0	
0 0 0 0 0 0 1 0	
0 0 1 0 1 0 1 0	
0 0 0 1 0 1 0 0	
0 1 0 0 0 0 0 0	
1 0 1 1 0 0 0 0	
0 1 0 1 1 0 1 0	
1 1 1 1 0 0 0 0	
1 1 0 1 0 0 0 0	
0 1 1 0 0 1 1 0	
0 1 0 1 0 0 0 0	
1 1 0 1 0 0 0 0	
1 0 1 1 0 0 0 0	
1 1 1 1 0 0 0 0	
1 0 0 0 1 0 0 0	
0 1 1 0 0 0 0 0	
0 1 1 1 0 1 0 0	
1 0 0 0 0 0 1 0	
0 1 0 0 1 0 0 1	
0 1 0 0 1 0 0 0	
0 0 0 0 0 1 0 0	

3

Jell Faussey

280 - Wolfe - Right - Right first Right. Tantau

	Low Order							High Order						
Programmer	8	7	6	5	4	3	2	1	8	9				
Module	9	7	6	5	4	3	2	1	9		0	7	6	5
Bus Bit	9	7	6	5	4	3	2	1	9		8	1	2	4
	8	1	2	4	8	1	2	4	8		8	1	2	4
0000	0	0	1	0	0	0	0	0	0		0	0	1	0
1	0	1	0	1	0	0	0	0	1		1	1	1	1
2	0	1	1	1	1	0	0	0	0		0	0	0	0
3	1	1	1	1	1	0	1	0	0		0	1	1	0
4	0	1	1	1	1	1	0	0	0		0	0	0	0
5	1	1	1	1	1	1	0	0	0		0	1	1	0
6	0	1	1	1	1	1	1	0	0		0	0	1	0
7	1	0	0	1	1	0	0	0	0		0	0	0	0
8	0	0	0	1	1	0	0	0	0		1	1	0	1
9	0	0	0	1	1	1	0	0	0		0	1	1	0
A	1	1	1	1	1	0	0	0	0		0	1	0	0
B	0	0	1	1	1	0	1	0	0		1	0	0	1
C	1	0	0	1	1	0	0	1	0		0	1	1	0
D	1	0	0	0	0	1	1	1	1		0	1	1	0
E	0	1	1	1	0	0	1	0	0		0	1	1	0
F	1	1	1	1	0	0	0	1	0		0	1	1	0
10	1	0	0	0	0	0	1	0	0		0	1	0	1
11	1	1	1	1	1	1	0	0	0		0	1	1	0
12	0	0	0	1	0	0	0	1	1		1	0	0	1
13	1	0	0	0	0	1	1	1	1		0	1	0	0
14	0	1	0	1	0	1	1	0	0		0	0	1	1
15	0	1	1	1	1	1	0	0	0		1	1	1	1
16	1	1	0	1	1	0	0	0	0		0	0	1	1
17	1	0	0	1	1	0	0	0	0		0	0	1	0
18	1	1	0	1	0	0	0	0	0		0	0	0	0
19	1	0	0	1	0	0	0	0	0		0	0	1	0
A1	1	1	0	0	0	0	0	0	0		0	0	0	0
B1	0	0	1	1	1	0	0	0	0		0	0	1	0
C1	1	0	1	1	0	0	0	0	0		0	0	1	1
D0	0	0	0	0	0	0	0	0	0		1	1	0	1
E1	0	1	1	0	0	1	0	0	0		0	1	0	1
F1	0	1	1	0	0	0	1	0	0		0	0	0	0
											1	1	0	1
											0	0	1	0

EVEN

0-1F₁₆

| odd

O-1F₁₆

Requirements to complete 1st model & set stage for cover design -

After discussions with Ref, Darwin & Abernathy my position has changed
in respect with smoldered enclosure. ~~Although schedule is exclusive~~
would take 3 months to build after design is frozen.

Need memo to LaVear & McCarty stating delay if embedded type
must be sent for 15 model. Also delay of mailed enclosure.

PALM can be ordered from Manufacturing.com July -
Must request cores without storage and Enterprise module.

Need 2 logic designers + 2 technician to build 10 units.

Need letters from McCarthy requesting we build 10 events -

Need level 4 wiring

Must gate level 1 and 2 bits on bus in with Device address F & level 4 conditioning.

4/13/73
OPD PS for Cactus -

Dennis Robertson
Rm 412

Rochester - Leroy Schroeder, 6670
Viking - Try for Thursday -
met Al Cole.
display.

Dutchess	1973	74	75	76	77
	12.54	5.63	4.05	2.85	2.82

Bob King - 5418
~~Jerry Malagreno~~, Ron Steiger
plastic mold.
574-004-2 Rochester

George Werbitzky - Endicott -
packaging -
have one of his guys look over packaging -
Allen Cole to see him -

0/14/73 - Inputs to CL Cole

Top adaptor -
1 dutch - New
1 single shot
1 driver
1 amp
2 R Pac
1 C Pac

KBD

1 latches (Pickup)

Printer

2 wa
2 hi.

② dutches modules
③ ~~pasta~~ dutches modules } designed
1 single shot
1 7493

TPA 54

1 dutches - New
1 dutches shift register (2 7496)
1 75154
2 75150
(osc) 1/2 dutches

Shifter

1 dutches - New - Scaler
2 XOR modules
1 7400 "

Console

2 latches modules
2 driver modules
8 LEDs

Storage Interface

2 dutches modules -

Powering

1 dutches module

display

(Emerald) May be too slow -)

Character Generator.

9/18/73

Moe Knapp - Raleigh - 8441 - 5870
Keyboards / P Earlewood - Asst. Eng.
John Fox - Dick Harris
- Compak -
Dir. tools to be used for 1st yr maintenance
- 6319 -

Celico - Carnation - 54 Key no copper - lower case - Wood -
- released - No Key Rollover - has shift no shift lock -
\$1000
Motor Vehicle - 2 Key rollover - no shift lock ok.

Larry Schroder - Viking 8-456-6670 Thursday

Bob King 456-5418 8:00 AM Thursday
Main lobby - 001 -

Berry Jarvis 5239
Fred U. 5136

Wednesday

Ralph Tedball - Charlie Seward - Chet Rataiski - Art Wescoag
442-5926 442-5939 442-5678 442-5351
Six Foks - PM George Branswig - chet Rataiski.
Triangle 1st. AM - John Fry - Earl Wood

Lou Ruffino 372 - 7575 - 7669 / 7575

Mark Silneimen
Bill Morrison

820A
A900
C925
F005
8700 *easy*
A800 *clear*
C825
E082
8800
A700
C725
B082
8700
A600
C625
B082
8600
A500
C525
B082
8500
A400
C425
B082
8400
B082

6/19/73 Raleigh -

Daniel Duncan — on vacation

C52/2517 / Bldg 602

442-5405/5934

M. Richard Milroy 5779 C50