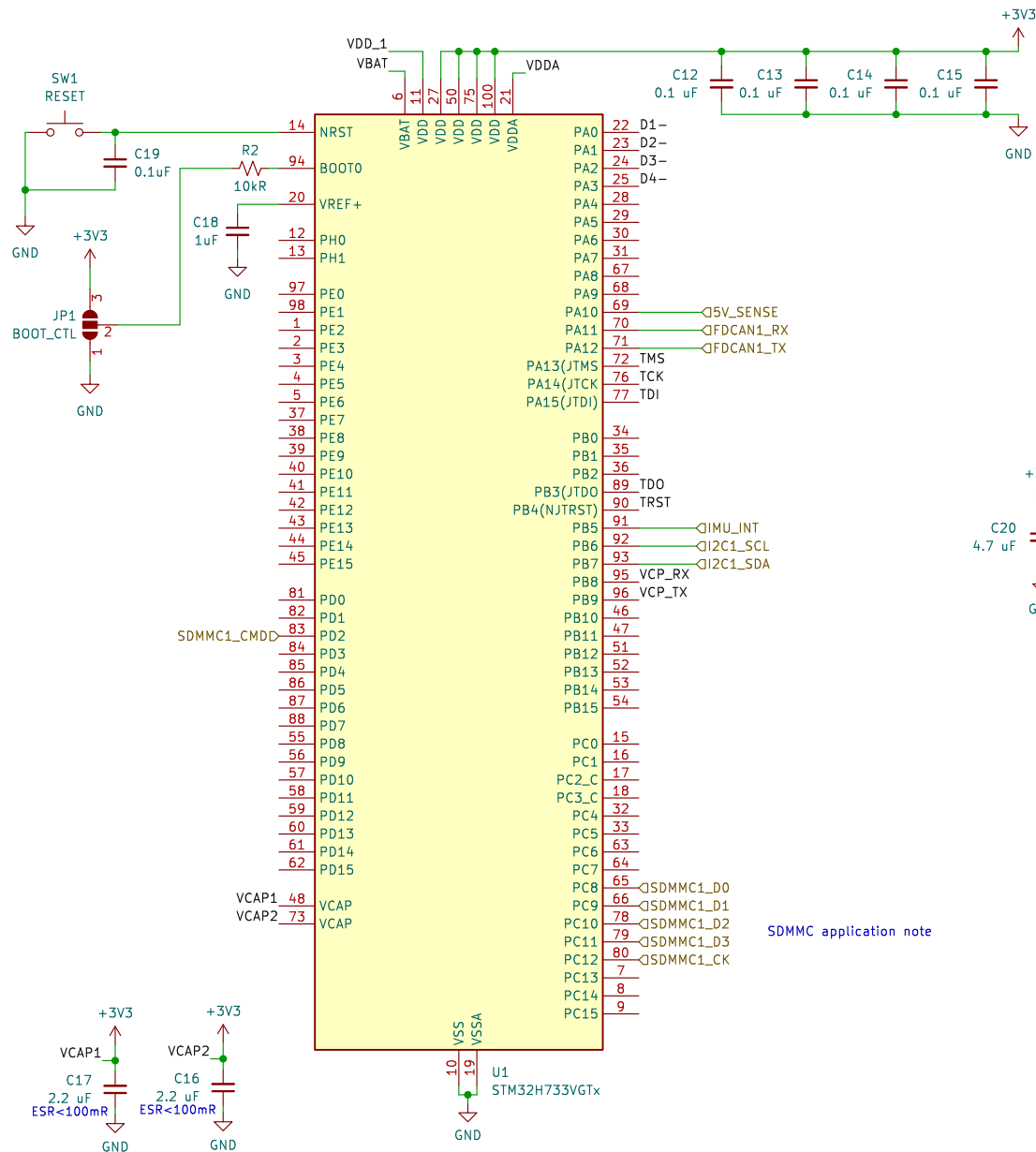
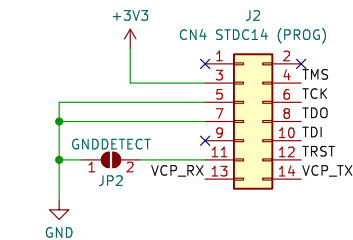
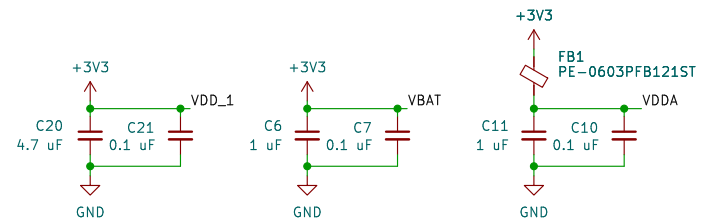
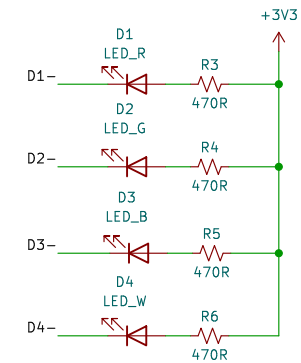


| | | |
|---------------------------|-------|---------|
| Sheet: / | | |
| File: processor.kicad_sch | | |
| Title: | | |
| Size: A4 | Date: | Rev: |
| KiCad E.D.A. kicad 7.0.7 | | Id: 1/4 |

power conn manual (link)



SDMMC application note



Notes:
VCP is virtual com port, should be connected to valid UART pins on the chip?
Pin 9 is "JRCCLK", SWD uses Pin 6 (TCK) for clock signal as far as I can tell

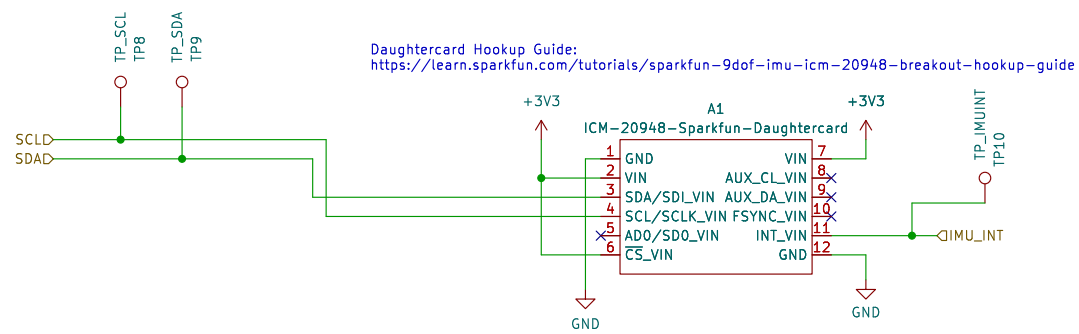
Sheet: /mcu/
File: mcu.kicad_sch

Title:

Size: A4
KiCad E.D.A. kicad 7.0.7

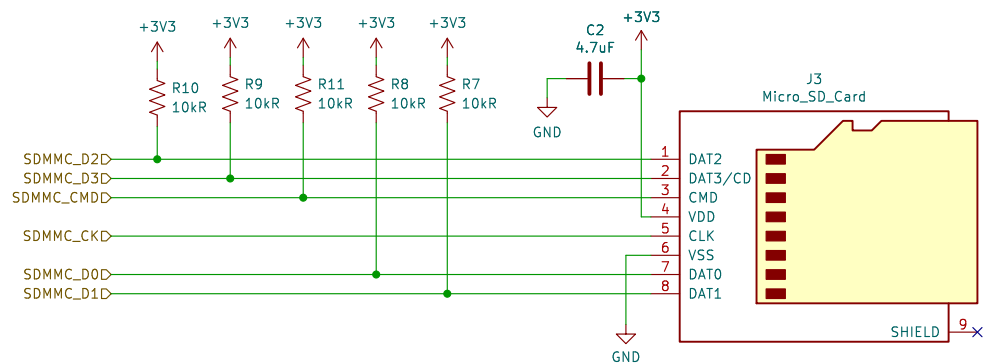
Date:

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I2C address: 0x69 By default, (0x68 if jumper on back is removed).

| | | |
|-------------------------------------|---------|------|
| Sheet: /imu/ File: imu.kicad_sch | | |
| Title: | | |
| Size: A4 | Date: | Rev: |
| KiCad E.D.A. kicad 7.0.7 | Id: 3/4 | |



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|---|---------|------|
| Sheet: /SD Card/ File: sd_card.kicad_sch | | |
| Title: | | |
| Size: A4 | Date: | Rev: |
| KiCad E.D.A. kicad 7.0.7 | Id: 4/4 | |