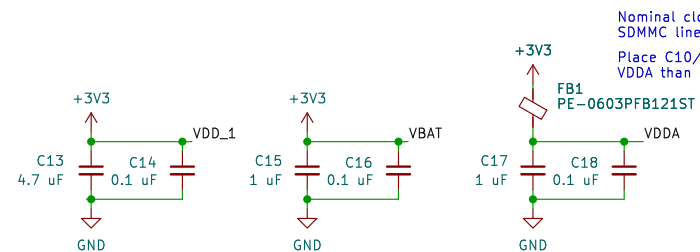
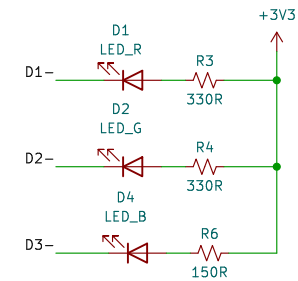
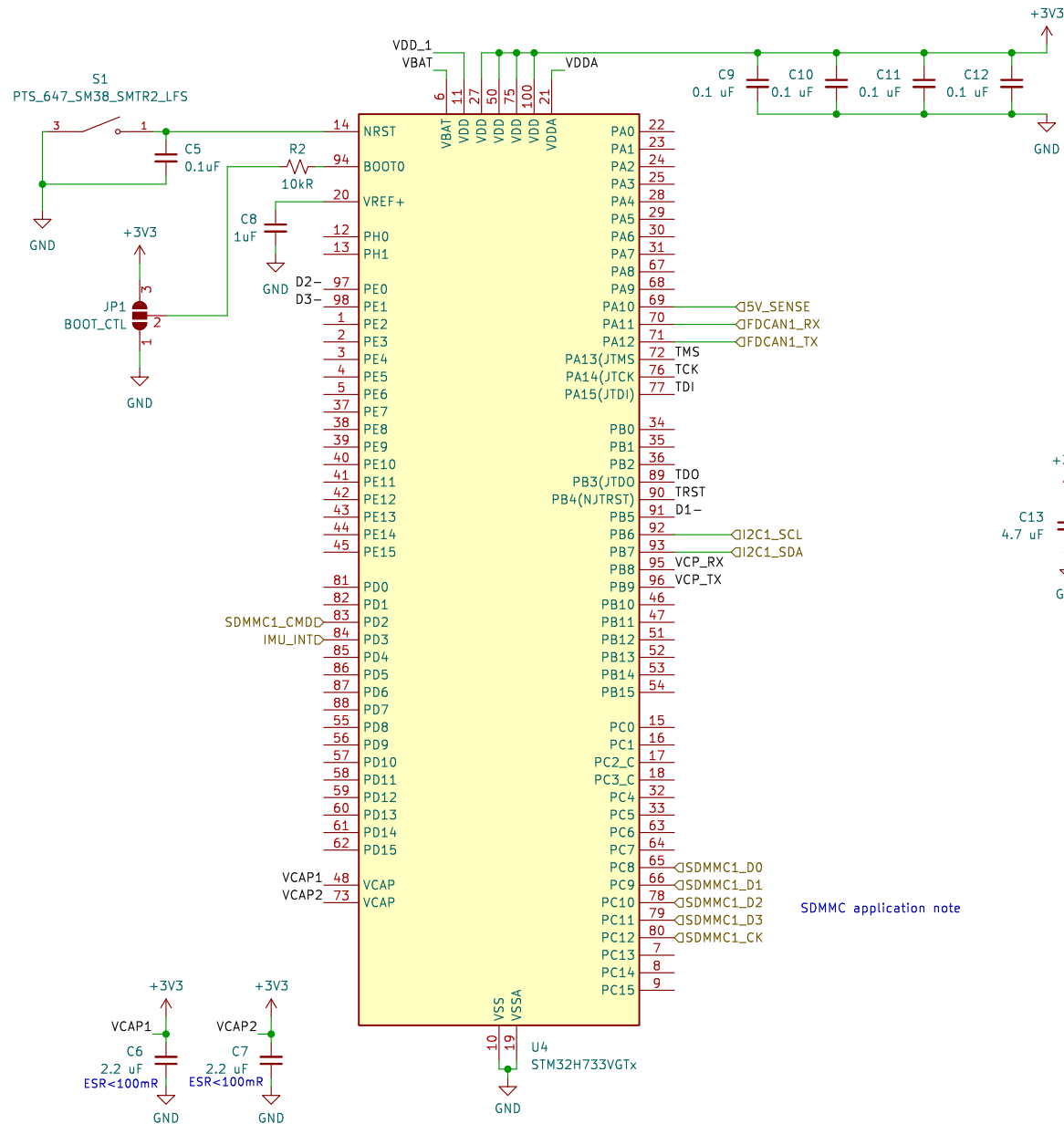
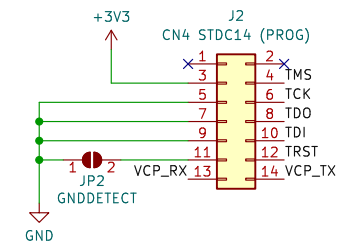


power conn manual (link)

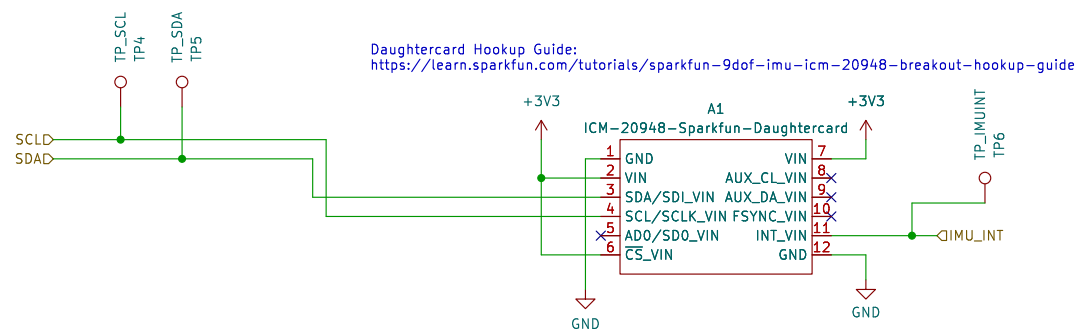


Nominal clock speed of 550 MHz
SDMMC lines at ~200 MHz
Place C10/C11 closer to
VDDA than FB1!



SDMMC application note

Sheet: /mcu/ File: mcu.kicad_sch	
Title:	
Size: A4	Date:
KiCad E.D.A. kicad 7.0.7	Rev: Id: 2/4



I2C address: 0x69 By default, (0x68 if jumper on back is removed).

Sheet: /imu/
 File: imu.kicad_sch

Title:

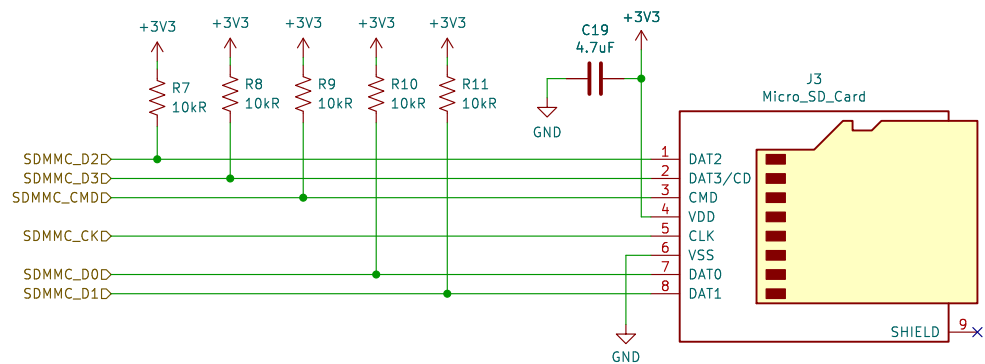
Size: A4

Date:

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Rev:

Id: 3/4



Sheet: /SD Card/
File: sd_card.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. kicad 7.0.7

Rev:

Id: 4/4