Principles and Specifications of Concurrent Systems

Leslie Lamport

Sections colored like this have

not yet been written.

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The *Principles* and *Specification* Tracks

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The One-Bit Clock

1 Introduction

1.3 Specification

- 2.1 The Clock's Behaviors
- 2.2 Describing the Behaviors 2.3 Writing the Specification

1.1 Concurrent Computation 1.2 Modeling Computation

1.4 Systems and Languages

- 2.4 The Pretty-Printed Version of Your Spec
- 2.5 Checking the Specification
- 2.6 Computing the Behaviors from the Specification 2.7 Other Ways of Writing the Behavior Specification
- 2.8 Specifying the Clock in PlusCal

The Die Hard Problem

- 3.1 Representing the Problem in TLA⁺
- 3.2 Applying TLC
- 3.3 Expressing the Problem in PlusCal

4 Euclid's Algorithm

- 4.1 The Greatest Common Divisor
 - 4.1.1 Divisors
 - 4.1.2 CHOOSE and the Maximum of a Set
 - 4.1.3 The GCD Operator
- 4.2 Comments
- 4.3 The Algorithm
- 4.4 The TLA⁺ Translation
- 4.5 Checking Safety
- 4.6 Checking Liveness
- 4.7 The Translation Revisited
- 4.8 The Grain of Atomicity
- 4.9 Why Euclid's Algorithm Is Correct
- Proving Invariance 4.9.1
 - Verifying GCD1-GCD3 4.9.2

The Generalized Die Hard Problem 5.1 The PlusCal Representation 5.2 Checking the Algorithm 5.3 The TLA⁺ Translation Alternation 6.1 The Problem 6.2 The One-Bit Clock Revisited 6.3 Specifying Alternation: Safety 6.4 Specifying Alternation: Liveness 6.5 The Two-Phase Handshake Protocol 6.6 Refinement 6.7 Refinement and Stuttering 6.7.1 Adding Steps 6.7.2Temporal Logic and Stuttering 6.7.3 A Finer-Grained Algorithm 6.8 Temporal Logic and Refinement 6.9 Alternation Revisited 6.10 Round-Robin Synchronization 6.10.1 The One-Bit Clock Revisited Again 6.10.2 An N-Valued Clock 6.10.3 An Implementation of the N-Valued Clock 6.10.4 Round-Robin Synchronization The *Principles* Track

7 Mutual Exclusion

- - 7.1 The Problem
 - 7.2 The One-Bit Protocol

 - 7.2.1 The Protocol
 - 7.2.2
 - An Assertional Proof

4.9.3 Proving Termination 4.10 Euclid's Algorithm for Sets

- 7.2.3 Using TLC to Check an Inductive Invariant
- 7.3 The Two-Process One-Bit Algorithm

 - The Two-Process Algorithm
 - 7.3.1 7.3.2
 - Busy Waiting Versus Synchronization Primitives

 - Requirement (c) 7.3.3
- 7.4 Proving Liveness
- 7.5 An Informal Proof 7.6 A More Formal Proof
- 7.7 The N-Process One-Bit Algorithm

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		7.8.3 The Atomic Bakery Algorithm
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C		8.1.4 Implementing The Bounded Channel
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 - 12.2.2 Reading Structured Proofs
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 - 12.5.1 Additional Language Features
 - 12.5.2 Importing
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 - 12.5.4 The Fine Print

13 The Bounded Buffer Proof

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13 Arithmetic and Logic

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- 14.3 Set Constructors
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