SE Sem IV

Microprocessor

Question Bank 2023 (PT -1)

1. Advantages and disadvantages of memory segmentation in 8086 microprocessor
2. Memory banking in 8086 microprocessor
3. Flag register of 8086 with suitable diagram
4. Draw and explain internal architecture of 8086
5. Functional pin diagram of 8086
6. Addressing modes of 8086 with suitable examples
7. Instruction set of 8086 with suitable examples (-Data Transfer Instructions, Logical Instructions, Arithmetic Instructions)
8. Draw and explain architecture of 8255 PPI.
9. Control word formats of 8255 PPI

NOTE:

Sample Questions related to Addressing modes and Instruction set of 8086 are as follows:

i. State with reason the addressing modes of the following instruction
   a. Mov AX, BX
   b. Mov AX, [BX + 8]
   c. IN AX, DX
   d. ADD AX,[2000]
ii. Write the mnemonic for instruction to load register DI with the base address of a data table starting at location TABLE.
iii. Which logical instruction should be used to force bits 0 and 1 of register DH high without changing any of the other bits?
iv. Determine the state of status flags after following instructions are executed

   MOV AL, 6DH;

   MOV BH, 40H;

   AND AL, BH;