



1OPTIONAL COMPONENTS TO CONFIGURE IBUFOUT AS A FILTER.
2REMOVE R7 FOR CORE-ONLY TESTS.
3FOR SINGLE SUPPLY OPERATION, REMOVE R6, SHORT OR REPLACE C3 WITH A 0Ω RESISTOR AND CONNECT THE SUPPLY GROUND OR RETURN TO THE GREEN TEST LOOP –V.
4TO CONFIGURE THE FET INPUT BUFFER FOR GAIN OF 2, INSTALL 0Ω RESISTOR AT R5 AND REMOVE RFBH.
9RFBL IS USED TO CONFIGURE THE INPUT BUFFER FOR GAIN VALUES >2×.

Figure 48. Evaluation Board Schematic

## **FUNCTIONAL BLOCK DIAGRAM**

