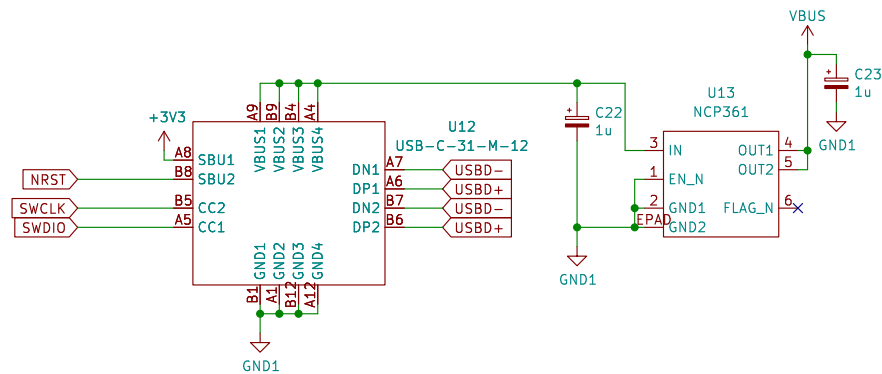
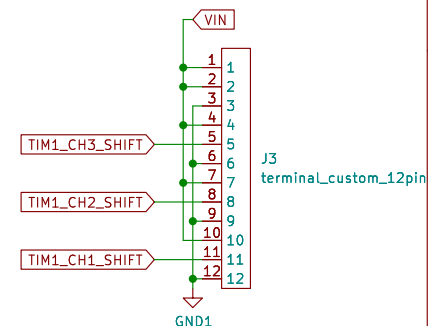


- H1 MountingHole
- H2 MountingHole
- H3 MountingHole
- H4 MountingHole

spudglo_v8p0_micro
File: spudglo_v8p0_micro.kicad_sch

spudglo_v8p0_power
File: spudglo_v8p0_power.kicad_sch



Sheet: /
File: spudglo_driver_v8p0.kicad_sch

Title:

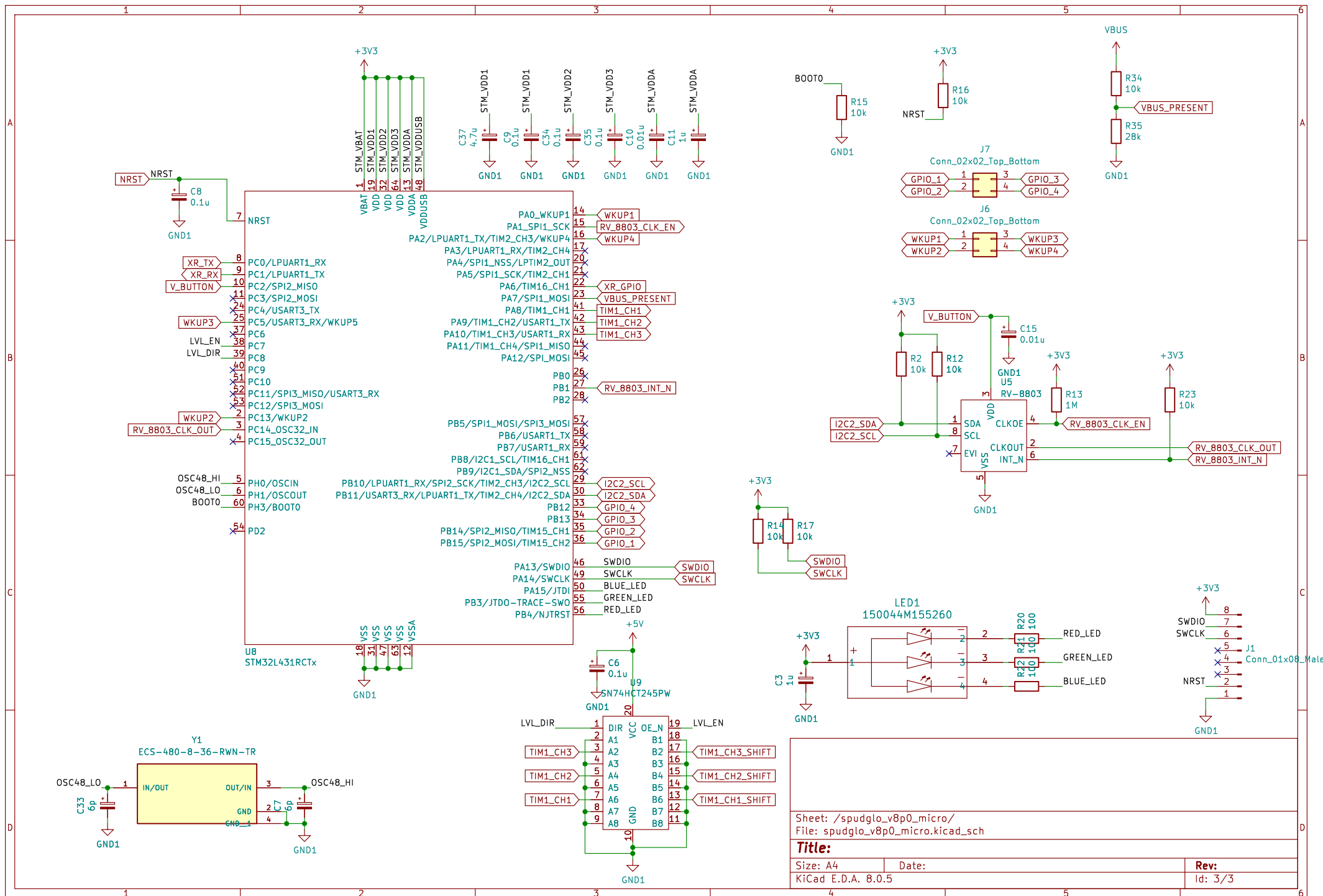
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Date:

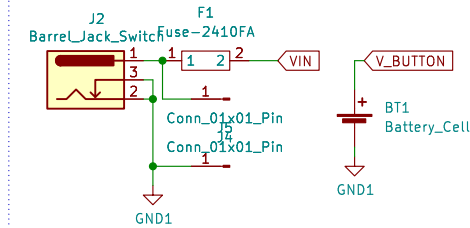
KiCad E.D.A. 8.0.5

Rev:

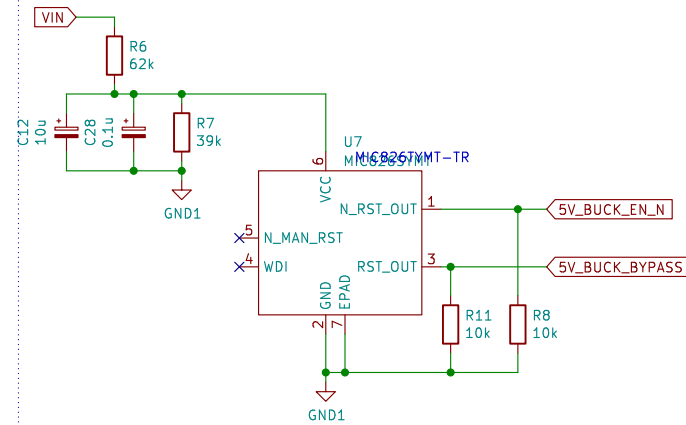
Id: 1/3



Input power



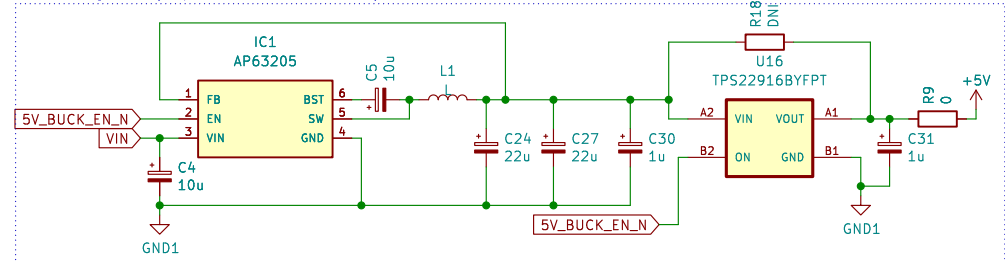
VIN select control



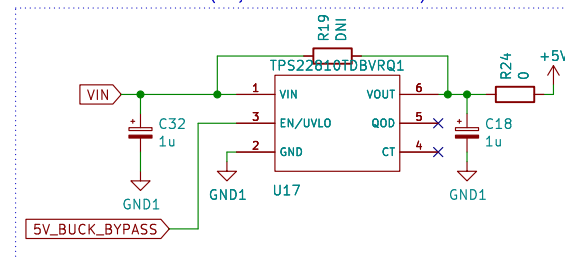
MIC826TYMT-TR

- * Threshold voltage 3.085 V typ
- * VCC min = 1 V, VCC max = 5.5 V
- * If VIN = 5 V, VCC = 2 V
- * N_RST_OUT = 0
- * RST_OUT >= 0.8 * 2 V >= 1.6 V
- * If VIN = 12 V, VCC = 4.8 V
- * N_RST_OUT = 4.8 V
- * RST_OUT = 0

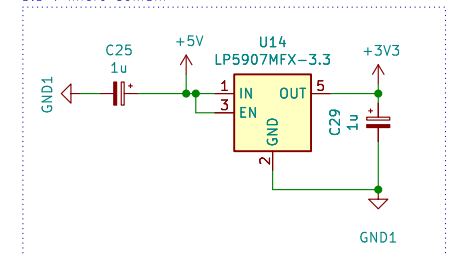
5V buck regulator (only enabled if VIN == 12 V)



VIN connection to 5V rail (only enabled if VIN == 5 V)



3.3 V micro domain



POWER STEPS

- VIN == 5 V
- * MIC826
- * N_RESET [5V_BUCK_EN_N] = 0
- * RESET [5V_BUCK_BYPASS] = - 5 * 2/5 = 2 V
- * ADP63205
- * OFF!
- * TPS22916B
- * OFF!
- * TPS22810
- * ON!
- * VIN CONNECTS TO 5V RAIL

VIN == 12 V

- * MIC826
- * N_RESET [5V_BUCK_EN_N] = 4.8 V
- * RESET [5V_BUCK_BYPASS] = 0
- * ADP63205
- * ON!
- * 5V GENERATED
- * TPS22916B
- * ON!
- * ADP 5V CONNECTS TO 5V RAIL.
- * TPS22810
- * OFF!
- * VIN CONNECTS TO 5V RAIL

Sheet: /spudglo_v8p0_power/
File: spudglo_v8p0_power.kicad_sch

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Size: A4

Date:

Rev:

KiCad E.D.A. 8.0.5

Id: 4/3