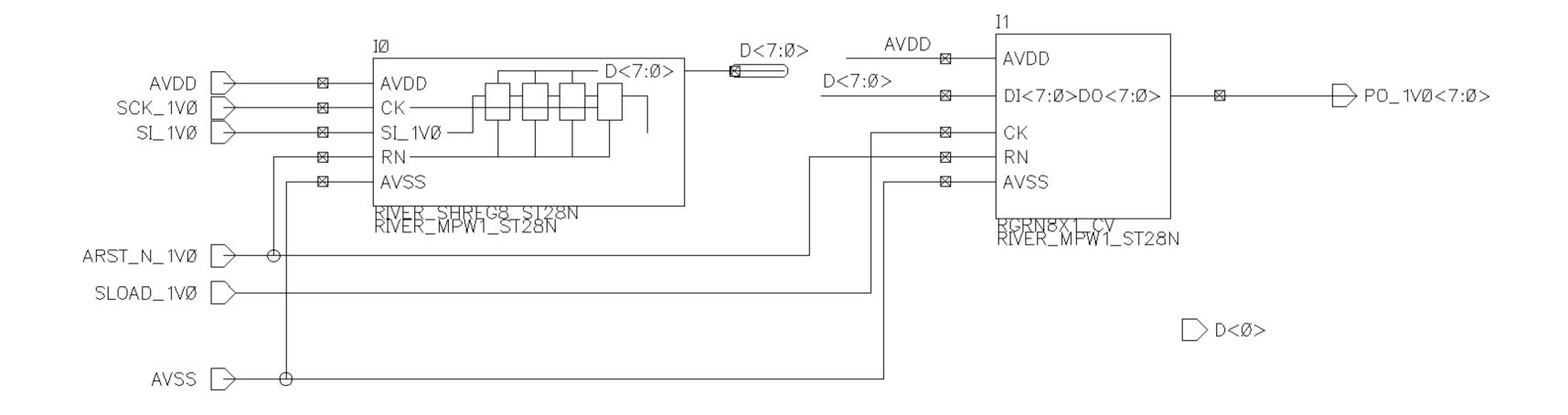
Shift register with parallell load



RIVE	ER_SREGLD8_ST28N
Designer	cawu
Created	Oct 9 10:24:04 2014
Modified	Oct 9 11:37:12 2Ø14
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