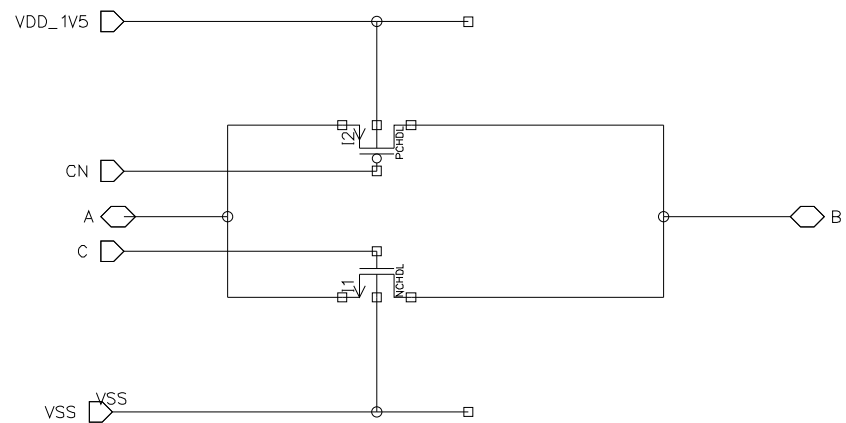
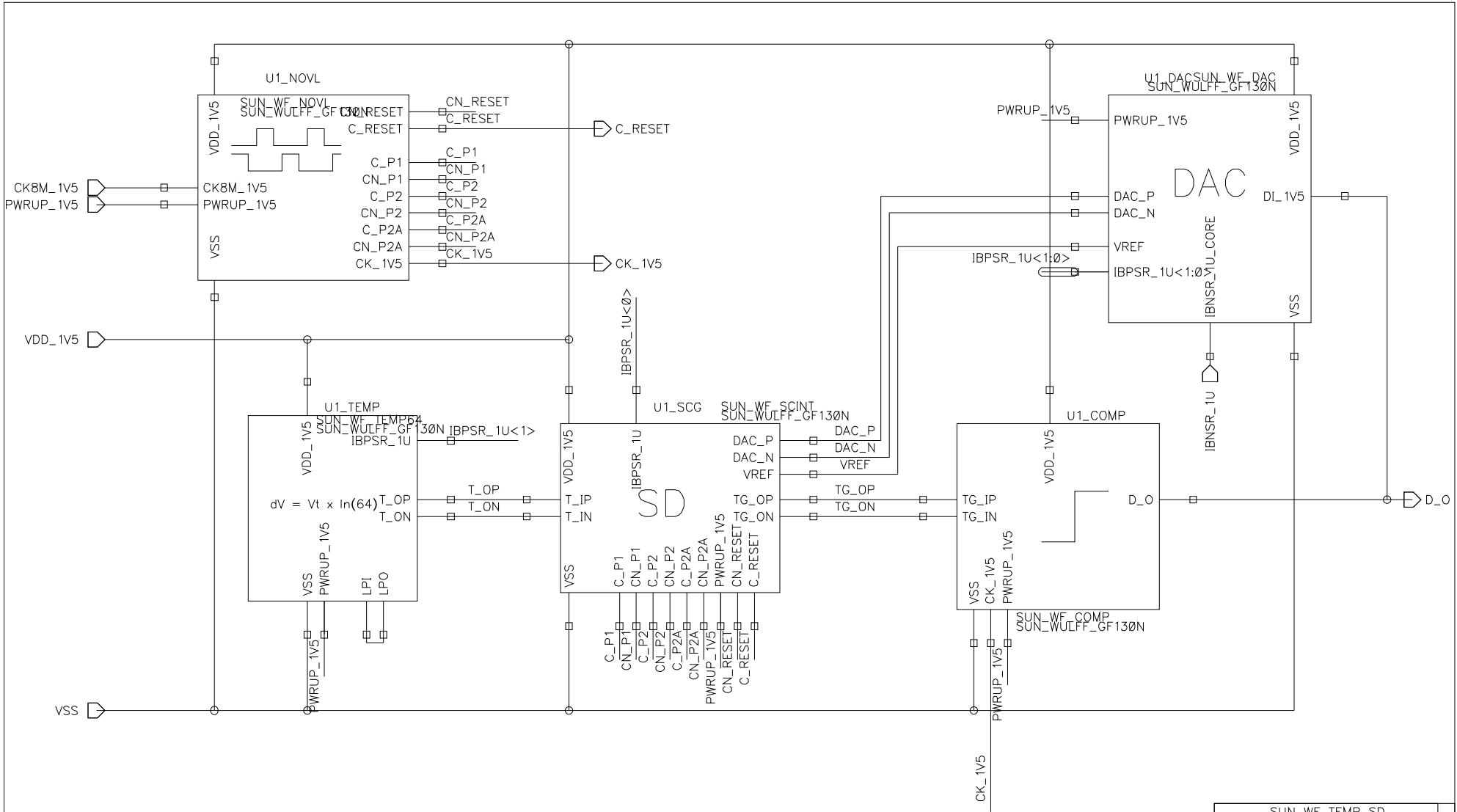


SUN_WULFF	
Designer	wulff
Created	Jan 10 14:26:02 2022
Modified	Feb 23 10:13:38 2022
copyright NTNU	

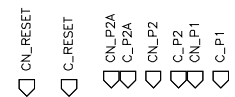


SUN_WF_TG	
Designer	wulff
Created	Feb 5 20:32:47 2022
Modified	Feb 23 20:28:18 2022
copyright NTNU	

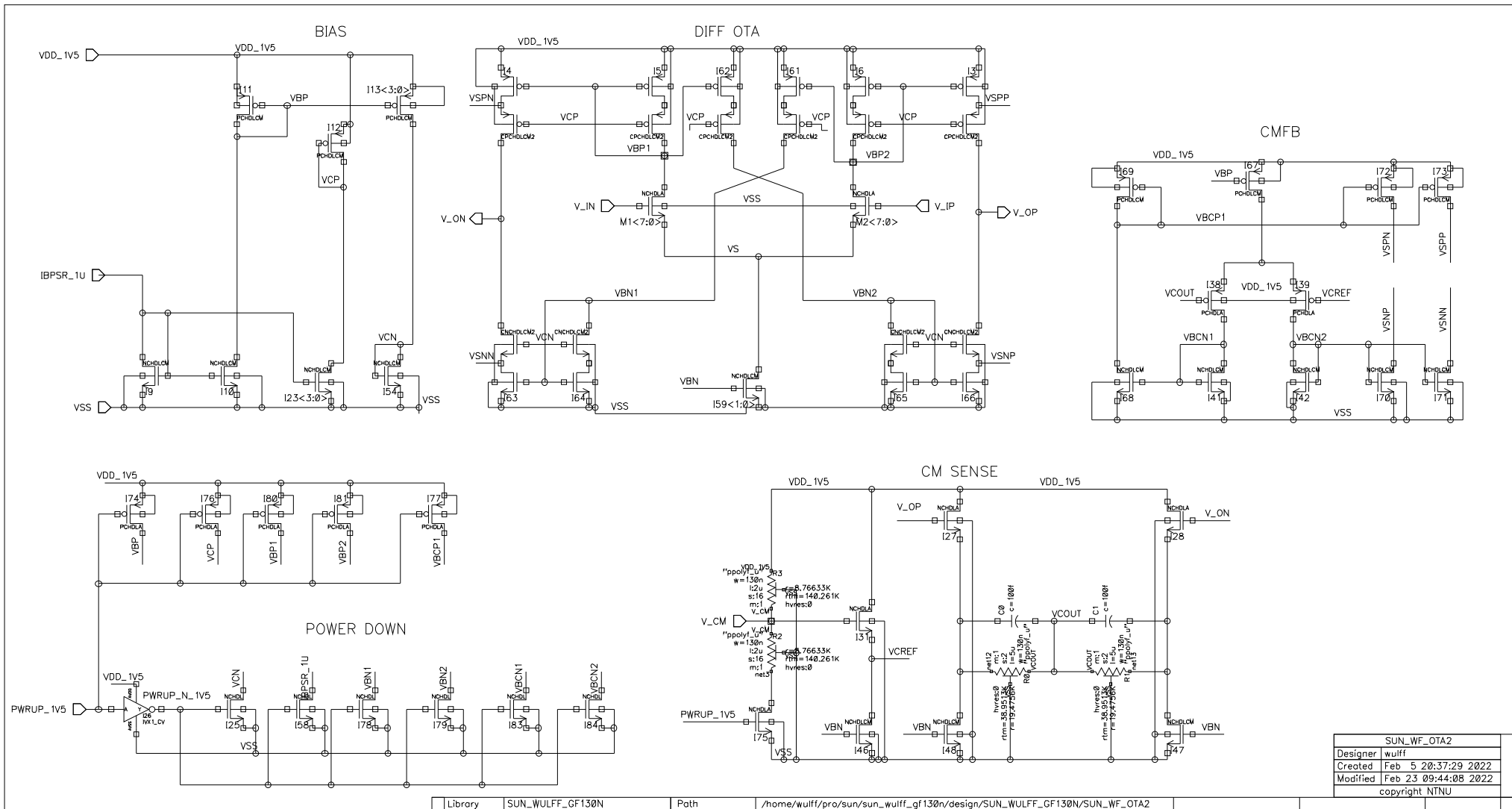
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SUN_WF_TEMP_SD	
Designer	wulff
Created	Feb 7 14:10:05 2022
Modified	Feb 23 20:26:11 2022
copyright NTNU	

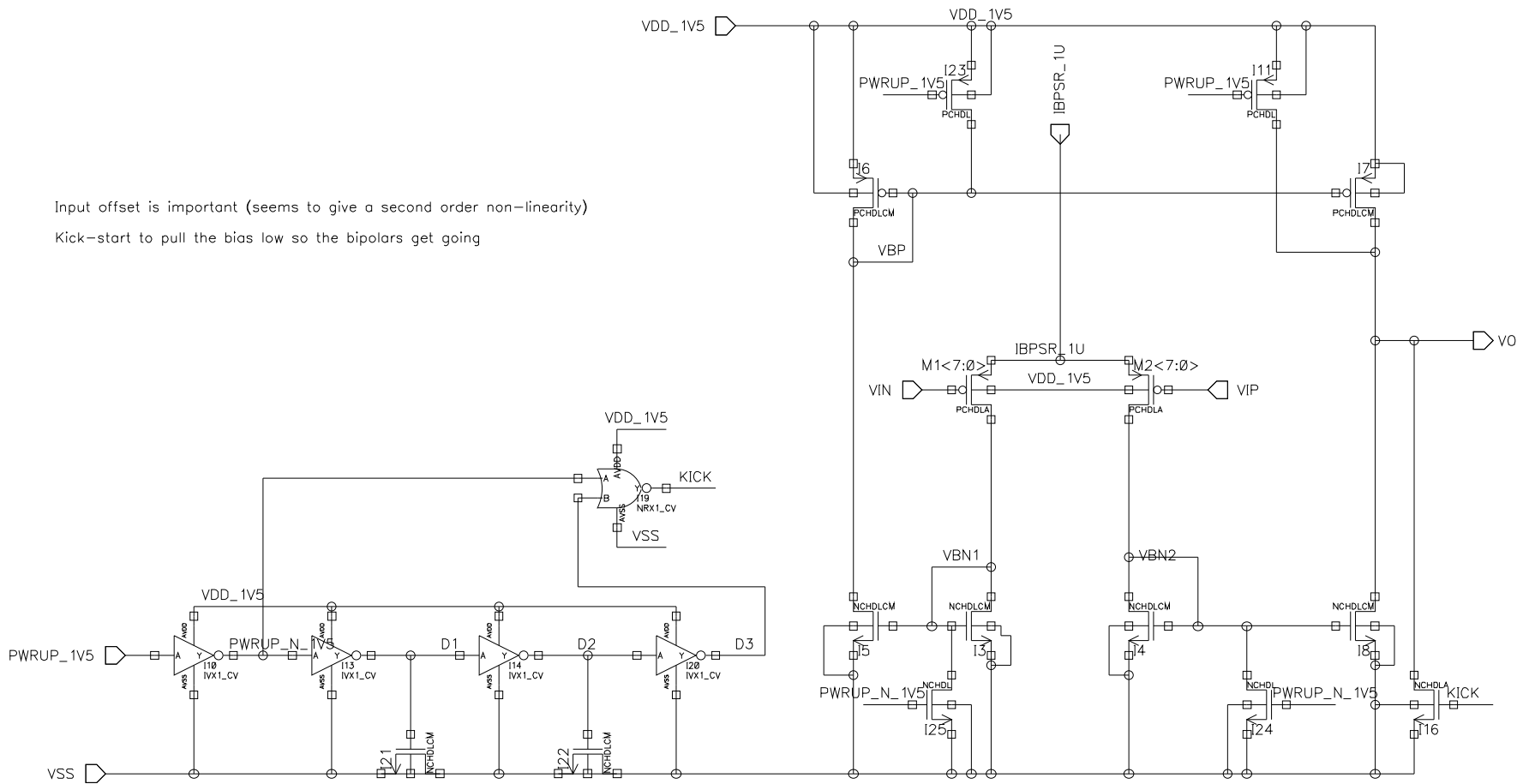


SUN_WF_SDH	
Designer	wulff
Created	Feb 13 17:29:45 2022
Modified	Feb 23 20:28:23 2022
copyright NTNU	



SUN_WUFF_OTA2
Designer wulff
Created Feb 5 20:37:29 2022
Modified Feb 23 09:44:08 2022
copyright NTNU

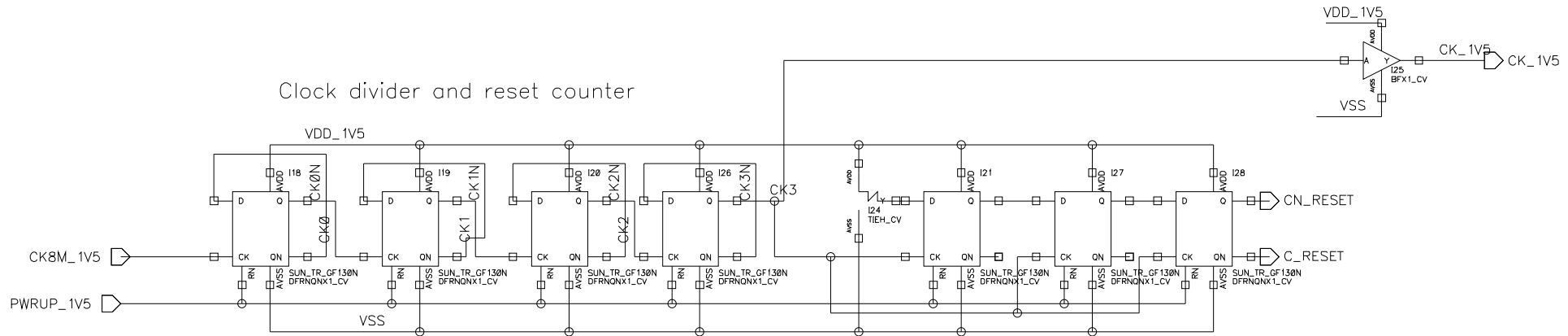
Input offset is important (seems to give a second order non-linearity)
 Kick-start to pull the bias low so the bipolars get going



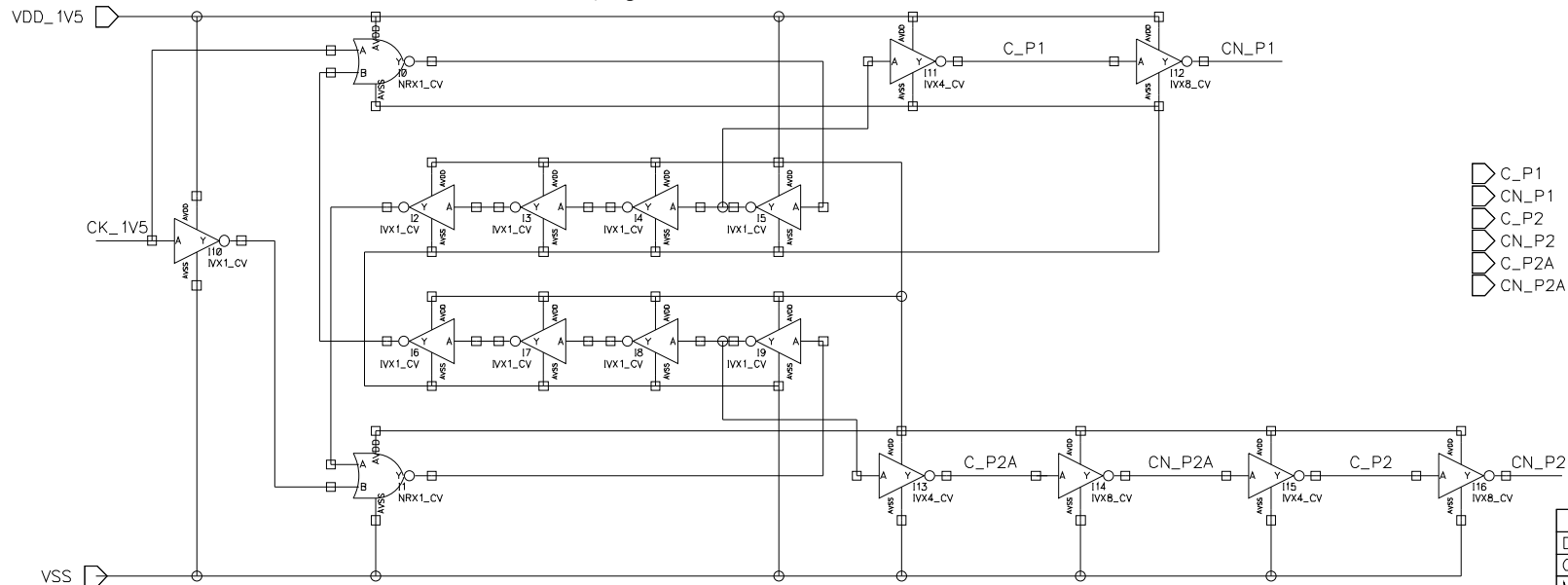
SUN_WF_OTA1	
Designer	wulff
Created	Feb 5 19:59:28 2022
Modified	Feb 23 09:19:21 2022
copyright NTNU	

Library	SUN_WULFF_GF130N	Path	/home/wulff/pro/sun/sun_wulff_gf130n/design/SUN_WULFF_GF130N/SUN_WF_OTA1		
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Clock divider and reset counter





Non overlap generator

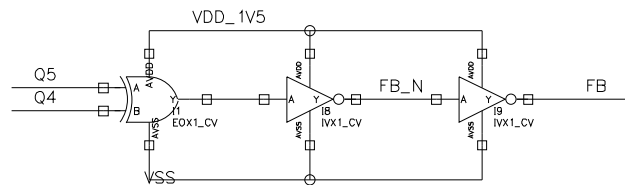


SUN_WF_NOVL	
Designer	wulff
Created	Feb 5 21:11:46 2022
Modified	Feb 23 20:25:10 2022
copyright NTNU	

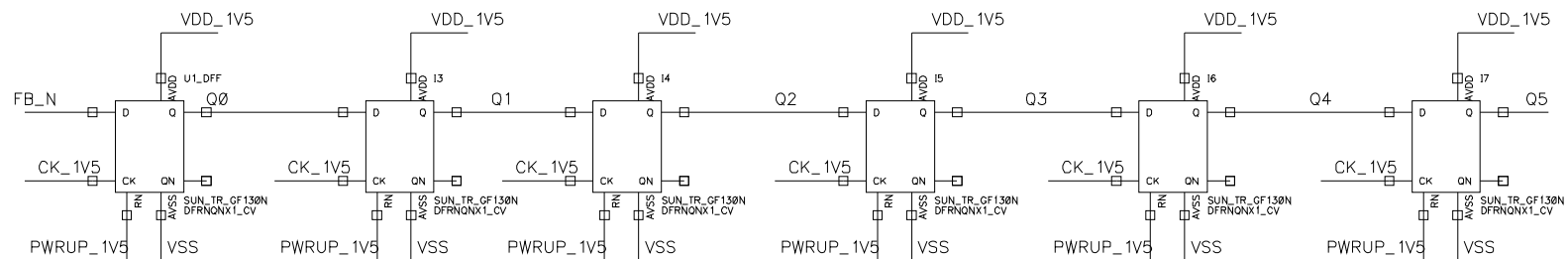
VDD_1V5 

CK_1V5 

PWRUP_1V5 



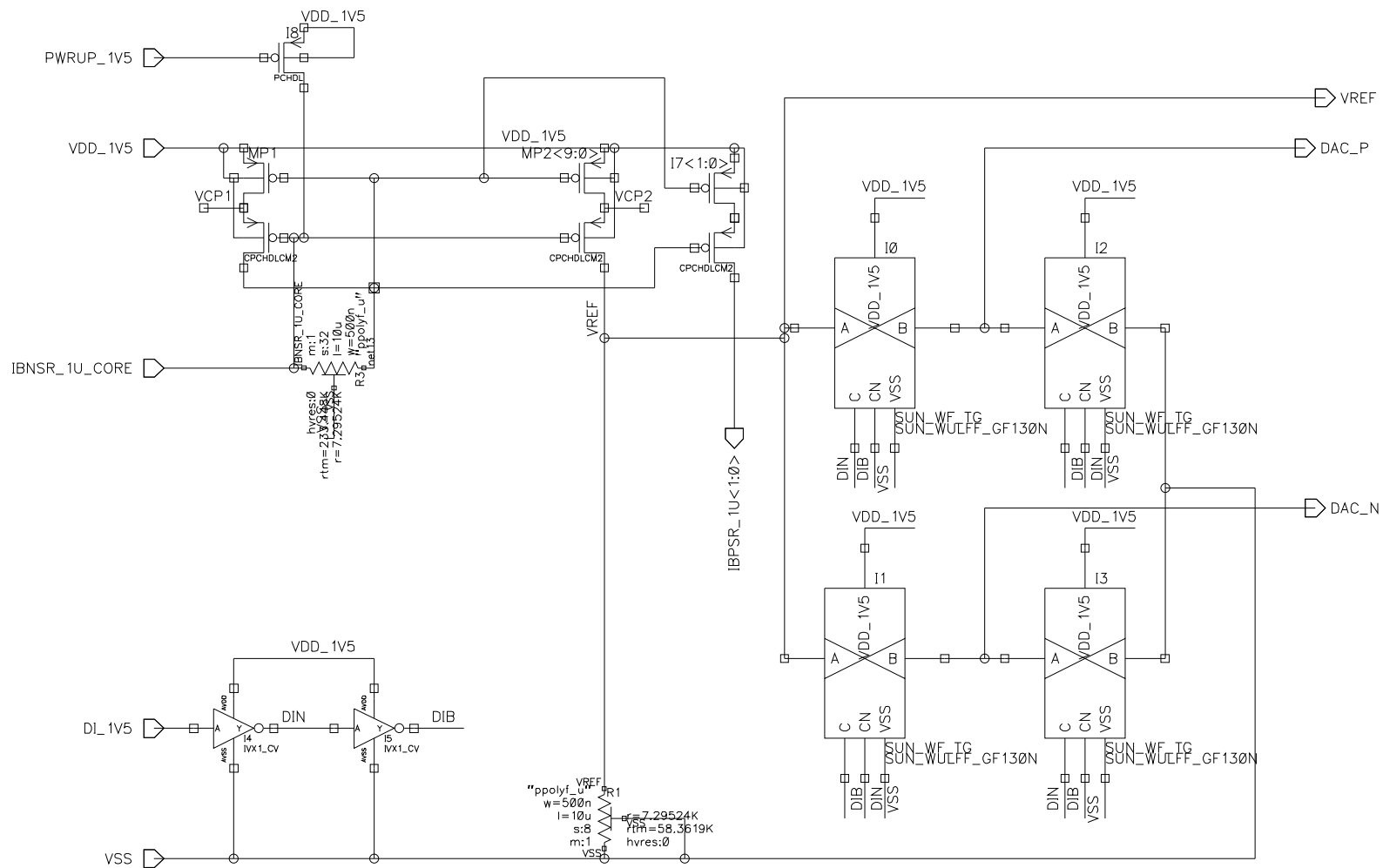
 FB



VSS 

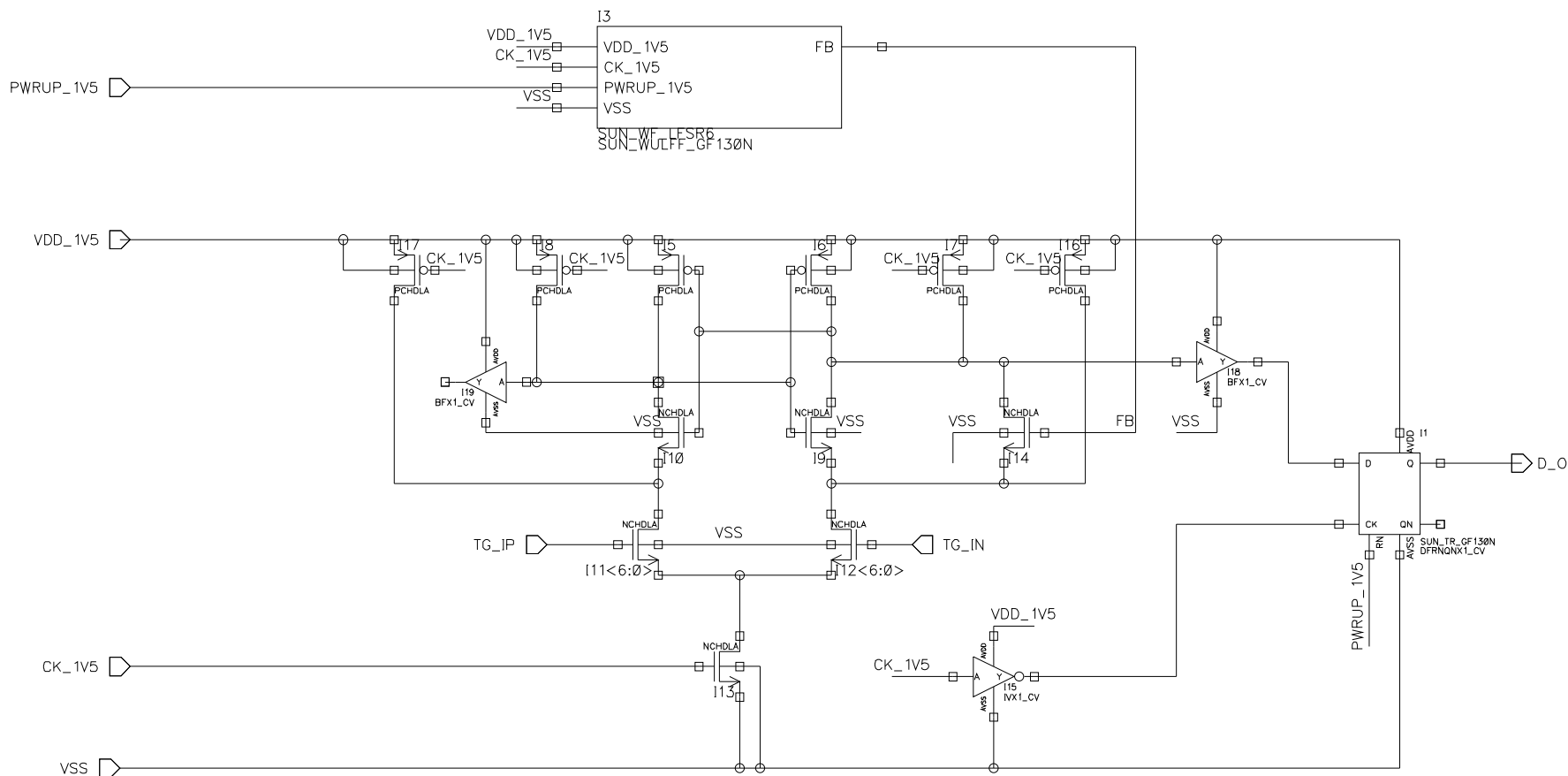
SUN_WF_LFSR6	
Designer	wulff
Created	Feb 16 20:21:01 2022
Modified	Feb 23 20:25:44 2022
copyright NTNU	

Library	SUN_WULFF_GF130N	Path	/home/wulff/pro/sun/sun_wulff_gf130n/design/SUN_WULFF_GF130N/SUN_WF_LFSR6						
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SUN_WF_DAC	
Designer	wulff
Created	Feb 7 14:19:27 2022
Modified	Feb 23 20:25:56 2022
copyright NTNU	

Library	SUN_WULFF_GF130N	Path	/home/wulff/pro/sun/sun_wulff_gf130n/design/SUN_WULFF_GF130N/SUN_WF_DAC		
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SUN_WF_COMP	
Designer	wulff
Created	Feb 7 14:12:44 2022
Modified	Feb 23 09:44:07 2022
copyright NTNU	