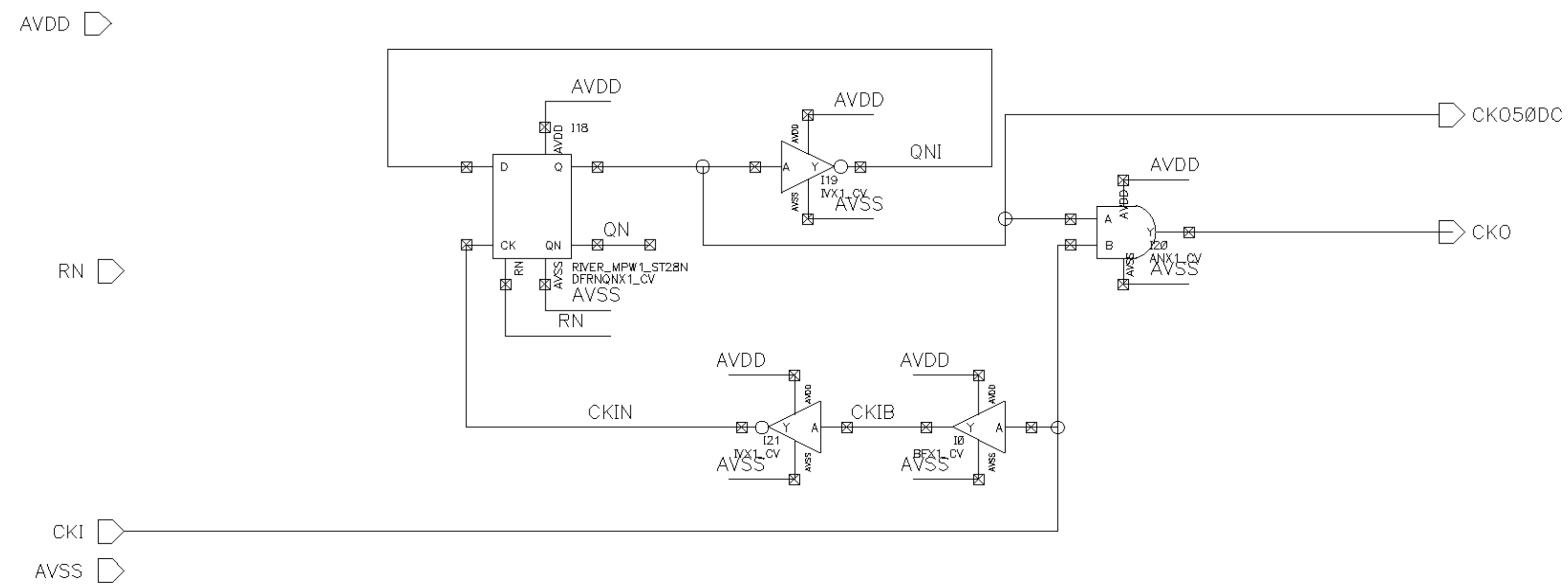


Clock gate divide by two



RIVER_CKDIV2_ST28N	
Designer	
Created	Aug 29 14:07:46 2014
Modified	Oct 24 11:51:17 2014
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Library	RIVER_MPW1_ST28N	Path	/home/wulff/pro/icarus_mpw2/design/RIVER_MPW1_ST28N/RIVER_CKDNV2_ST28N
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