VDD_1V5

U1_TEMP

VDD_1V5

$dV = Vt \times \ln(32)$ T_OP
T_ON

T_OP
T_ON

VSS   TREQ_1V5

VSS

U1_SCG   SUN_WF_SCG
SUN_WULFF_GF13ØN

VDD_1V5

T_IP
T_IN

×10

TG_OP
TG_ON

TG_OP
TG_ON

C_P1   CN_P1   C_P2   CN_P2   C_P2A   CN_P2A   TREQ_1V5

VSS

C_1V5<5>
C_1V5<4>
C_1V5<3>
C_1V5<2>
C_1V5<1>
C_1V5<0>

VSS

TREQ_1V5

C_1V5<5:Ø>

	SUN_WF_TEMP_GAIN	
Designer		
Created	Feb 5 21:Ø2:46 2Ø22	
Modified	Feb 5 22:24:3Ø 2Ø22	
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