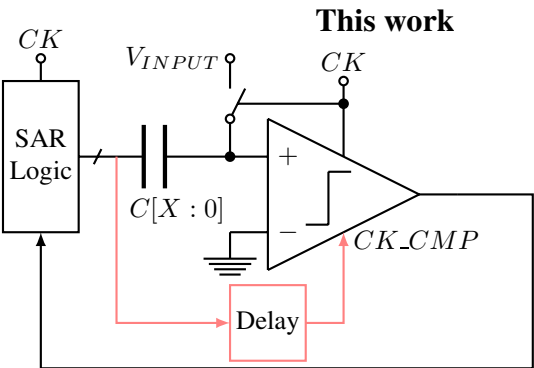


(a) Clock generation separate from CDAC



(b) Clock generation including CDAC