

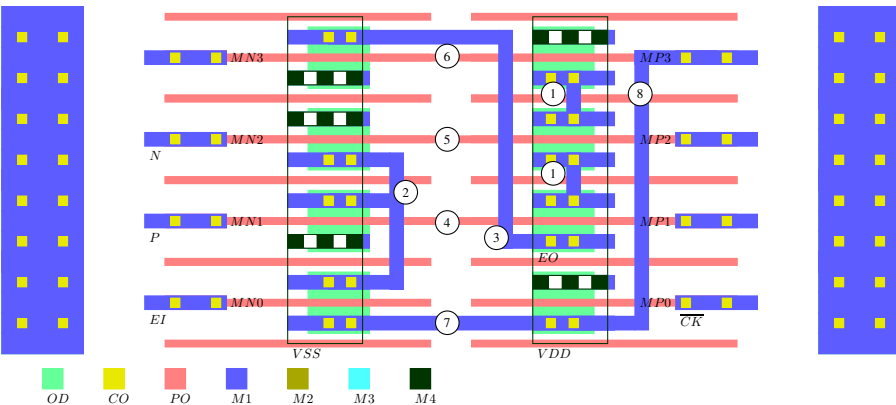
(a)

```
.SUBCKT SAREMX1_CV_P N EI EO CK_N AVDD AVSS
MN0 N3 EI A AVSS NCHDL
MN1 N3 P AVSS AVSS NCHDL
MN2 AVSS N N3 AVSS NCHDL
MN3 EO A AVSS AVSS NCHDL
MP0 AVDD CK_N A AVSS PCHDL
MP1 N2 P EO AVSS PCHDL
MP2 N1 N N2 AVSS PCHDL
MP3 AVDD A N1 AVSS PCHDL
.ENDS
```

(b)

```
{
  "name": "SAREMX1_CV",
  "class": "Layout:LayoutDigitalCell",
  "addConnectivityRoutes": [
    ["M1", "N1|N2", "| |", ""], 1
    ["M1", "N3", "- |", ""], 2
    ["M1", "EO", "-- |", "onTopR"] 3
  ],
  "addDirectedRoutes": [
    ["PO", "P", "MN1:G-MP1:G"], 4
    ["PO", "N", "MN2:G-MP2:G"], 5
    ["PO", "A", "MN3:G-MP3:G"], 6
    ["M1", "A", "MN0:S-MP0:S"], 7
    ["M1", "A", "MP0:S-|--MP3:G"] 8
  ]
}
```

(c)



(d)

OD CO PO M1 M2 M3 M4