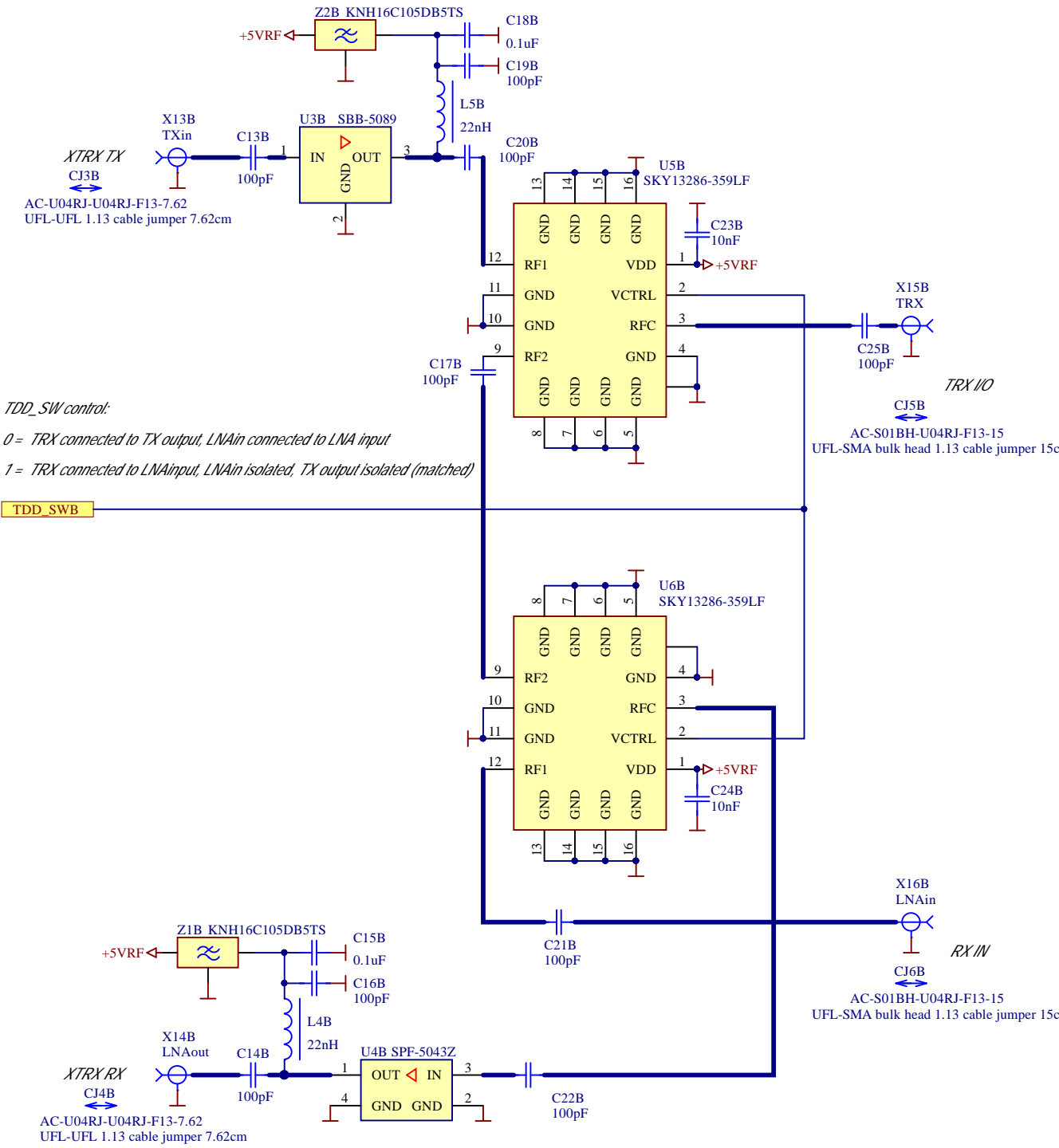


Title MiniPCle XTRX to PCIe x4 Carrier Board			
Size A4	Number RF part v13.SchDoc	Revision 1 Fully eqpt	
Date:	16.04.2019	Fairwaves Inc	Sheet 2 of 2
File:	C:\Git\...\RF part v13.SchDoc	Drawn By: Andrey Sviyazov	



TDD_SW control:
0 = TRX connected to TX output, LNAin connected to LNA input
1 = TRX connected to LNAinput, LNAin isolated, TX output isolated (matched)