

Mapping HALO Exchange onto Toruses and Stuff

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Abstract—Abstract != Introduction.

Many High Performance Computing(HPC) applications involve application domain decompositions that involve nearest neighbour communication. Halo exchange is a nearest neighbour exchange pattern. In this paper we examine the impact of the different placements of task within the network topology and develop models for the network performance of mapping strategies.

[TODO]

I. INTRODUCTION

Halo exchange is a common communication pattern in parallel codes, where each process is assigned an application subdomain and must periodically communicate with other processors that have neighboring subdomains to update information about the state of the boundary between subdomains. A common special case is when a multi-dimensional cartesian space is decomposed into subdomains of equal size. For example, in the three-dimensional case, a 8x8x8 cube might be decomposed into 256 2x1x1 cubes for execution on 256 processors.

This paper explores the problem of mapping such multi-dimensional cartesian halo exchange communications onto parallel computers with hypercube or torus networks. A typical computation job on a leadership class machine would utilize several hundreds to thousands of cores, as a result there are a large number of possible mappings to the physical hardware that could be chosen. We seek to understand what aspects of the mappings have an impact on performance and how to quantify them so that the impact these mappings have on performance can be modelled. Mapping strategies are a very cheap optimisation which requires no code changes to the application and as our empirical studies show, these mapping strategies have significantly different network performance characteristics. There are no studies to the best of our knowledge that attempt to model the impact of mapping performance on HPC systems.

1. An analytical model that describes the performance on Halo exchange with different mappings 2. A nearly worst and optimal mapping. The rest of the paper is organised as follows :

The rest of the paper is organised as follows: Section 2, provides background information on the HPC system topologies and BlueGene/Q in particular. Section 3, describes mapping strategies. Section 4, develops the analytical model and Section 5, details the experiment design for empirical validation of the model.

II. HIGH-PERFORMANCE COMPUTER NETWORKS

The state of the art in High Performance Computing(HPC) infrastructure, demands high-performance networks to support the movement of data between the nodes as well as to-and-from disk-arrays. HPC systems are increasingly architected with high radix interconnects such as hypercubes and N-dimensional tori. Parallel applications have a wide range of task placements options to exploit the network topology of these HPC systems. These networks have evolved to several different network topologies in order to support different requirements, and data movement patterns. For HPC applications which involve fine-grained communication, high-radix networks provide low latency, smaller diameter, and large bandwidth as multiple links along the multiple dimensions supported.

We are using Message Passing Interface (MPI) to implement the messaging and synchronization aspects of the HALO exchange code, and hence the performance observed from running the application would be influenced by the behavior of MPI due to its various protocols on the BlueGene/Q. There are four protocols supported by the MPI implementation used BlueGene/Q. The protocol utilized by MPI is determined by the size of the message that is being sent. The data sizes at which the switch to different protocol occurs is configurable, but for our experiments we are using the defaults on BlueGene/Q.

Protocol	Min Data Limit	Max Data Limit	Routing
Immediate	0	112	Direct
Short	113	496	Direct
Eager	497	2048	Direct
Rendezvous	2048	unlimited	Adaptive

We're going to cite Swift/T [2] and include an illustration (see Figure 1).

III. HIGH-PERFORMANCE COMPUTER NETWORKS

A. Blue Gene/Q 5D torus

RedBook [1]

B. Cray Gemini 3D torus

IV. MODELS FOR NETWORK COMMUNICATION

On an HPC system such as BlueGene/Q or Cray XE6, each node has multiple duplex links to its neighbors. If the application on every node attempts to exchange messages with

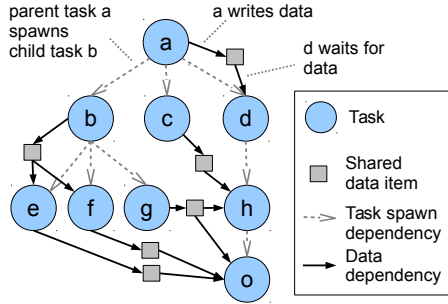


Fig. 1: This is a figure.

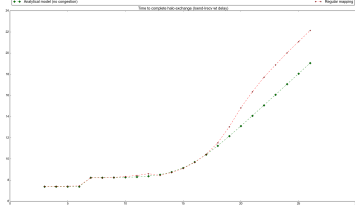


Fig. 2: This is a figure.

every neighbor, we can assume that every link on the network will see similar traffic. Thus we, consider a single link and it's bandwidth to determine t_b the time required to send a Byte along the link. Assume that there are N_{procs} number of identical processes all of which will attempt to utilize the same links. To capture the differences between different mappings we use a simple program to calculate the average distance between neighbors, N_{steps} . The current MPI implementation on leadership class systems like Mira (Bluegene/Q) utilizes shared memory for intranode communication. When a neighbor is present within the same node, the link is weighted as zero, and every network link or hop is weighted as one. Since it is possible to place neighboring tasks from the application domain on the same node there are mappings possible which minimize N_{steps} below one. There are constant costs involved in startup, acquiring a buffer etc, and t_c is an experimentally calibrated constant that is a catch-all for the various constant costs of network communication using MPI. N is the message size in bytes that are exchanged between neighbors.

A simple analytical model determines the time to completion, T of a complete halo exchange operation, from the variables defined above as follows:

$$T = t_c + (N_{steps} * N_{procs} * N * t_b) \quad (1)$$

Since we have a complex set of operations on several hundreds of nodes, there is a cost for synchronisation. We use MPI barriers to synchronise all tasks before and after measurements

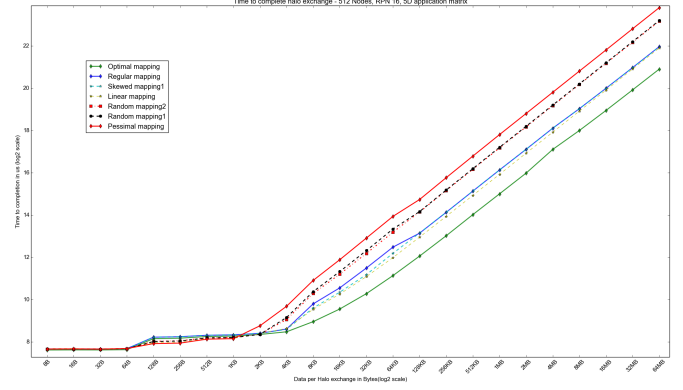


Fig. 3: 3D halo exchange on 512 nodes with 16 RPN

V. EXPERIMENTAL DESIGN

VI. RESULTS

VII. CONCLUSION

REFERENCES

- [1] M. Gilge. *IBM System Blue Gene Solution Blue Gene/Q Application Development, Second Edition*. IBM Redbooks, 2013.
- [2] J. M. Wozniak, T. G. Armstrong, M. Wilde, D. S. Katz, E. Lusk, and I. T. Foster. Swift/T: Large-scale application composition via distributed-memory data flow processing. In *Proc. CCGrid '13*.