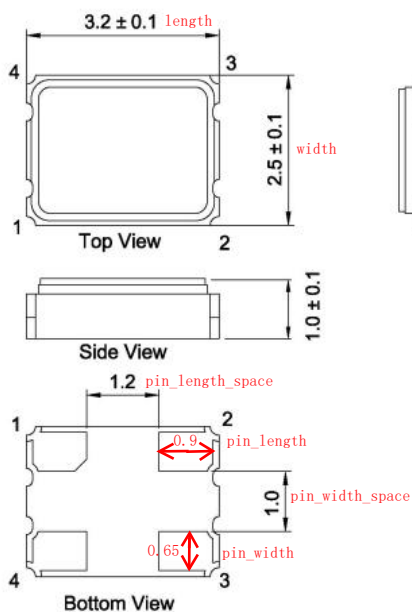




LUCKI CM ELECTRONICS Co.,Ltd

● OUTLINE DIMENSIONS (unit: mm)



Pin	Connection
1	E/D
2	GND
3	Output
4	V _{DD}

- Note:
- 0.01uF bypass capacitor should be placed between V_{DD} (pin 4) and GND (pin 2) to minimize power supply line noise;
 - Line shouldn't be layed under the oscillator in the PCB to minimize signal interference.
 - The Load we advise is only 15pF (that means drive only 1 CMOS/TTL gate).
 - unlabeled tolerance: $\pm 0.1\text{mm}$

● MARKING

