1. SPECIFICATIONS

100	NTROL SECTION	[한번째 1949년에 - 40대 12 - 15	<patch panel=""></patch>				
	Keyboard Voltage controlled		1. Keyboard	 Keyboard control voltage output (exponential) (0∼ +8V) 			
	oscillator 1	octaves, + cent, - cent) • Wave form (∧ ,		Keyboard trigger output (+5V → GND)			
		~ []), white noise) (4 modes)		•VCO-1 + VCO-2 control			
3	V.C.O.2	 Pulse width adjust 1 : 1 ~ 1 : ∞ Scale (16', 8', 4', 2') (6 octaves, 		voltage input (linear response)			
3.	1.0.0.2	+ cent, - cent)		(0~+8V)			
		Wave form (VCO-2 control voltage input (linear response) (0 ~ 8V) 			
		modulator) (4 modes)	2. VCO	•VCO-1 + VCO-2 external			
		Pitch (±1 OCTAVES)	2. 100	frequency control input			
	V.C.O. master	 Master tune (±100 cent) 		(OCT/V) (+5V -5V)			
	control	Portamento (max. 00 sec)	3. VCF	 External signal input 			
		Frequency modulation intensity		(3Vp-pmax.)			
		by MG/T. EXT (±5V)		 External HP filter cutoff 			
		 Frequency modulation intensity by EG1/EXT (+5V) 		frequency control input			
	V.C.O. mixer	• V.C.O1 level		(2OCT/V) (-5V~+5V)			
Э.	V.O.O. IIIIXGI	• V.C.O2 level		• External LP filter cutoff			
6	Voltage controlled	Cutoff frequency		frequency control input			
	high pass filter	(50Hz~15,000Hz)	4. VCO + VCF	(2OCT/V) (−5V ~ +5V) • Total external modulation input			
		Peak (flat~self OSC)	4. 100 T 10F	(T. ext) (−5~ +5V)			
		 Cutoff frequency modulation in- 	5. VCA	• External initial gain control input			
		tensity by MG/T.EXT	5,000,000,000	(0~+5V)			
		$(-5V \sim +5V)$	6. EG	•EG 1 envelope signal normal			
		Cutoff frequency modulation in-		output (-5V == 0V)			
		tensity by EG2/EXT		 EG 1 envelope signal reverse 			
7	Vallage controlled	(-5V ~ +5V) • Cutoff fraguesia		output (+ 5V 0V)			
1.	low pass filter	Cutoff frequency (50Hz~15,000Hz)		•EG 1 + EG 2 trigger input			
	low pass litter	• Peak (flat~self OSC)		(TLGND)			
		Cutoff frequency modulation in-		• EG 1 trigger input (¬LGND) • EG 2 equeloge signal reverse			
		tensity by MG/T.EXT		 ◆EG 2 envelope signal reverse output (
		(-5V ~ +5V)	7. MG	Triangle output (\\ ~ \ ~ \ ~ \)			
		 Cutoff frequency modulation in- 	6 113	(5Vp-p4x0V)			
		tensity by EG2/EXT		• Rectangle output (☐~☐~			
		$(-5V \sim +5V)$		டா)			
8.	Envelope	Delay time (10 sec)		(UTC5V)			
	generator 1	Attack time (10 sec)	8. Noise generator	Pink noise output (5Vp-p ±20)			
^	Farmers	• Release time (10 sec)		• White noise output (5Vp-p ±20)			
9.	Envelope	Hold time (20 sec) Attack time (10 sec)	9. Sample and hold	Clock trigger input (¬LGND)			
	generator 2	Decay time (10 sec)		 Sample signal input (5Vp-p 			
		• Sustain level (0~5V)		max.)			
		Release time (10 sec)	The second secon	•S/H output (5Vp-p max.)			
10.	Modulation	 Wave form (\ ~ ∧ ~ ✓ . 	Modulation VCA	• Control voltage input (0~+5V)			
	generator	口~口~口)		• Signal input (-5V~+5V)			
		Frequency (1:1 ~ 1:80)	11. Manual controller	 Signal output (−5V ~ +5V) Control wheel output 			
11.	Manual controller	 Control wheel (center click) 	11. Manual Controller	(-5V ← 0V → +5V)			
		(0.1Hz ~ 20Hz)		Momentary switch output			
		Momentary switch		(Tegno)			
••	0.0.1.1	GND	10 011				
	P. Switch and volume	*Volume	12. Signal out	 Signal output (2Vp-p output in- pedance 3.5kΩ) 			
13.	Indicator	 LED (KBD trigger, MG rate) 	Head phones	 Head phones output ((8Ω) 120m watts 5.6) 			
			Power consumptio	n+ 10 watts			
			15. Dimensions	• 569(W) x 309(D) x 249(H) mm			
			16. Weight	• 7.7 kgs			
EXTERNAL SIGNAL			17. Accessories	Patch cord, connection cord (25 cm x 2 2 m x 1)			
1.	Control section	Input signal level (0dB max.)	18. Options	(35 cm x 2, 3 m x 1) Stand, hard case, foot pedal			
		 Low cut frequency (50 ~ 2,500Hz) 	io. Options	(MS-01)			
		• High cut frequency		Junction box (MS-02)			
		(100 ~ 5,000Hz)		and the second section of the section			
		CV adjust					
		Threshold level					
2.	Input and output	 Signal In (auto pad system) 					
	THE CONTRACTOR OF THE PERSON AND A	(1.0 ~+ 14.0V)					
		Amplifier Out					
		Band pass filtered Out					

Band pass filtered Out

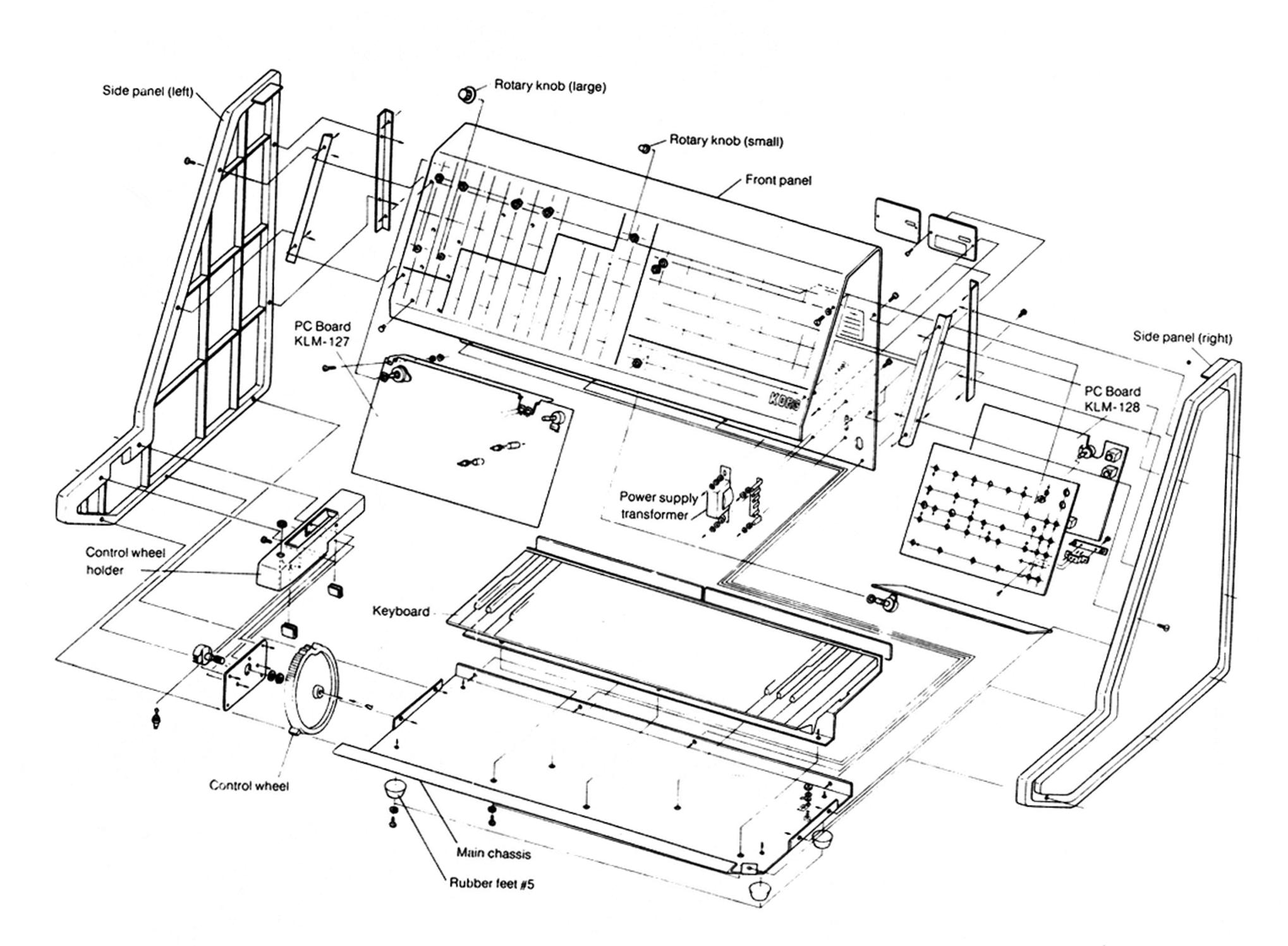
•ENV Out (0 ~ +5V) •Trig Out (+5V ¬L_{GND})

Peak indicatorTrigger indicator

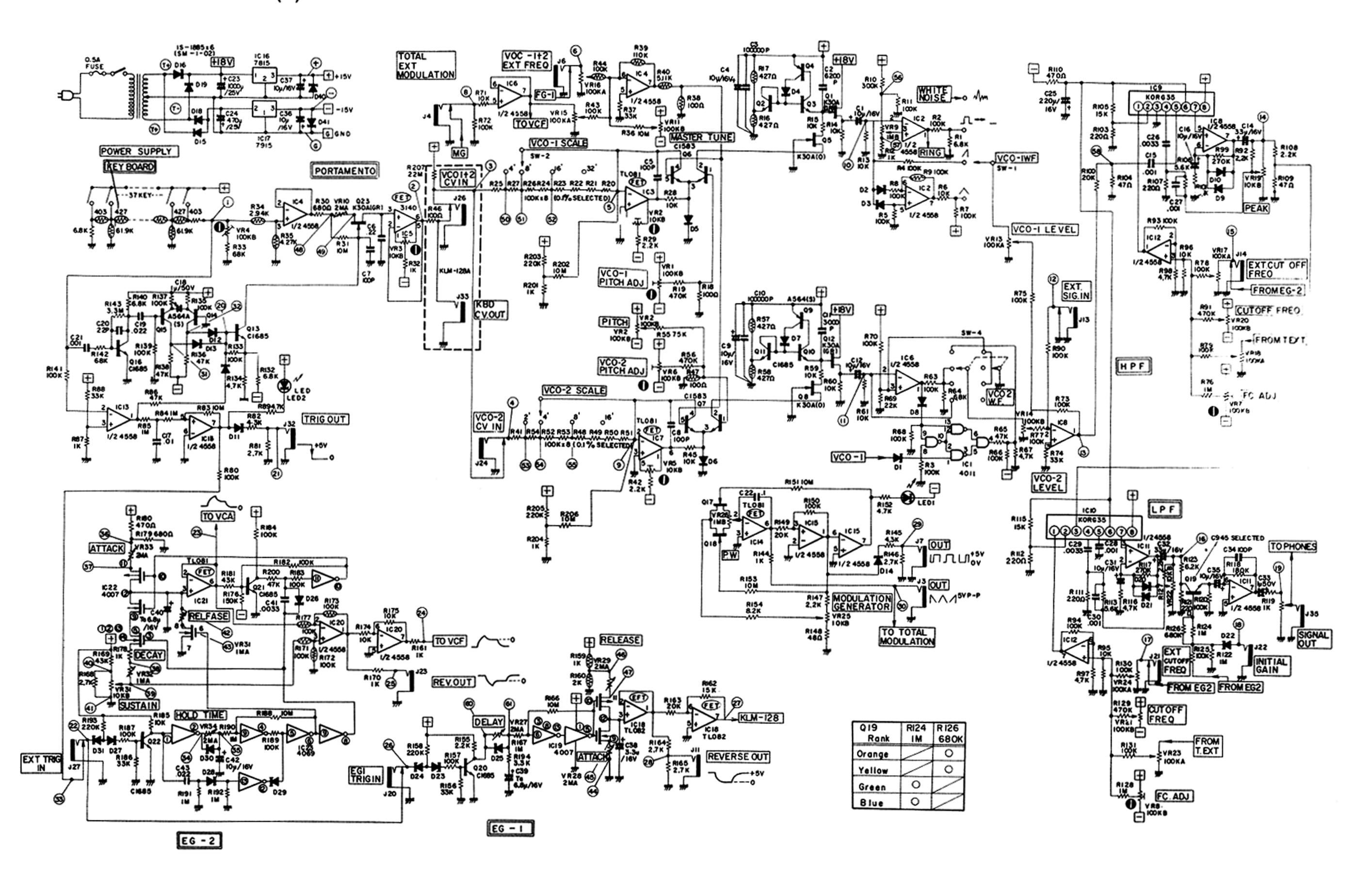
3. Indicator (LED)

• CV Out (F ∞ V) (0 ~ +8.4V)

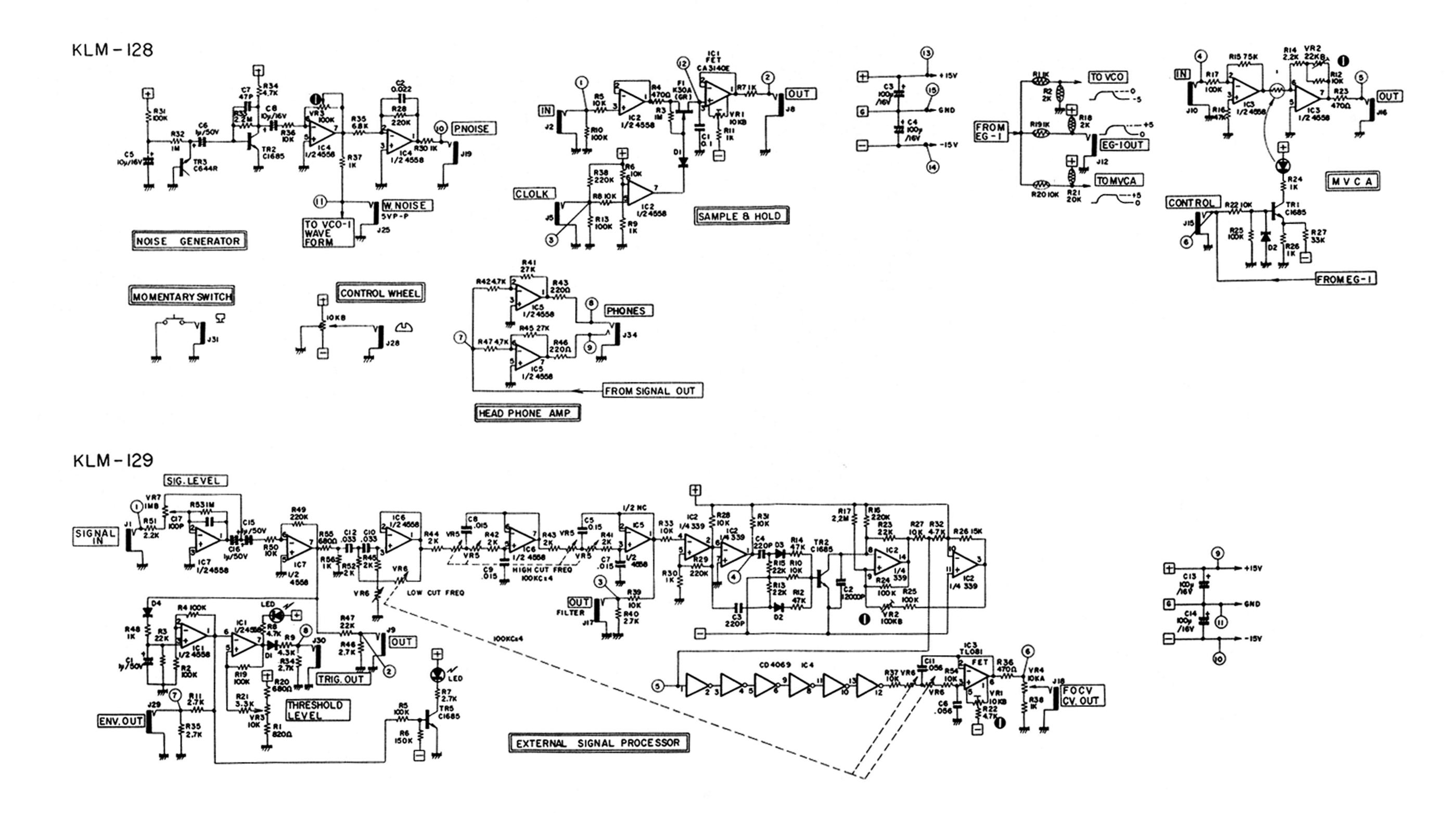
2. STRUCTURAL DIAGRAM



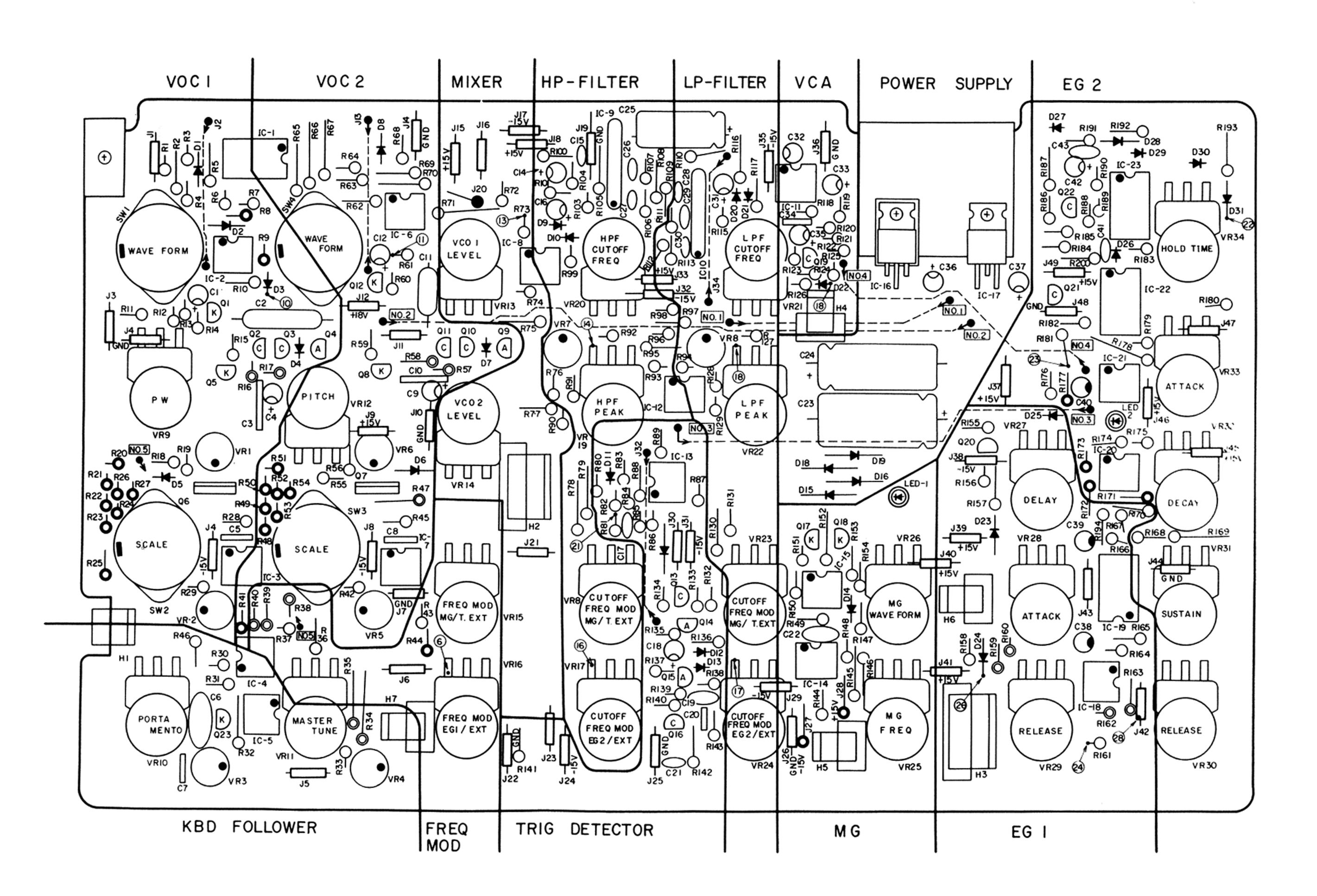
3. CIRCUIT DIAGRAM (1)



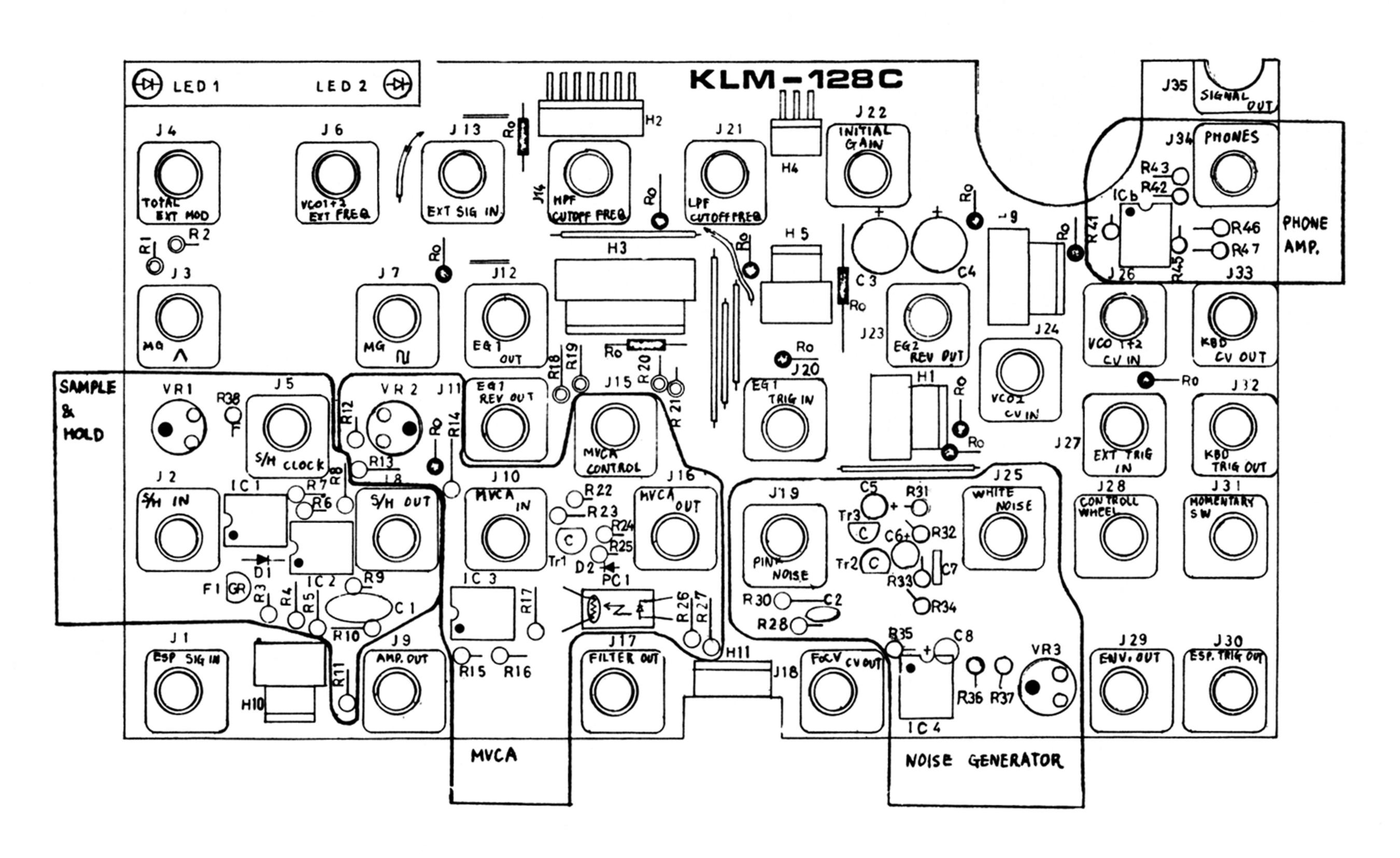
CIRCUIT DIAGRAM (2)



4. FRONT VIEW OF PRINTED CIRCUIT BOARD KLM-127



FRONT VIEW OF PRINTED CIRCUIT BOARD KLM-128C



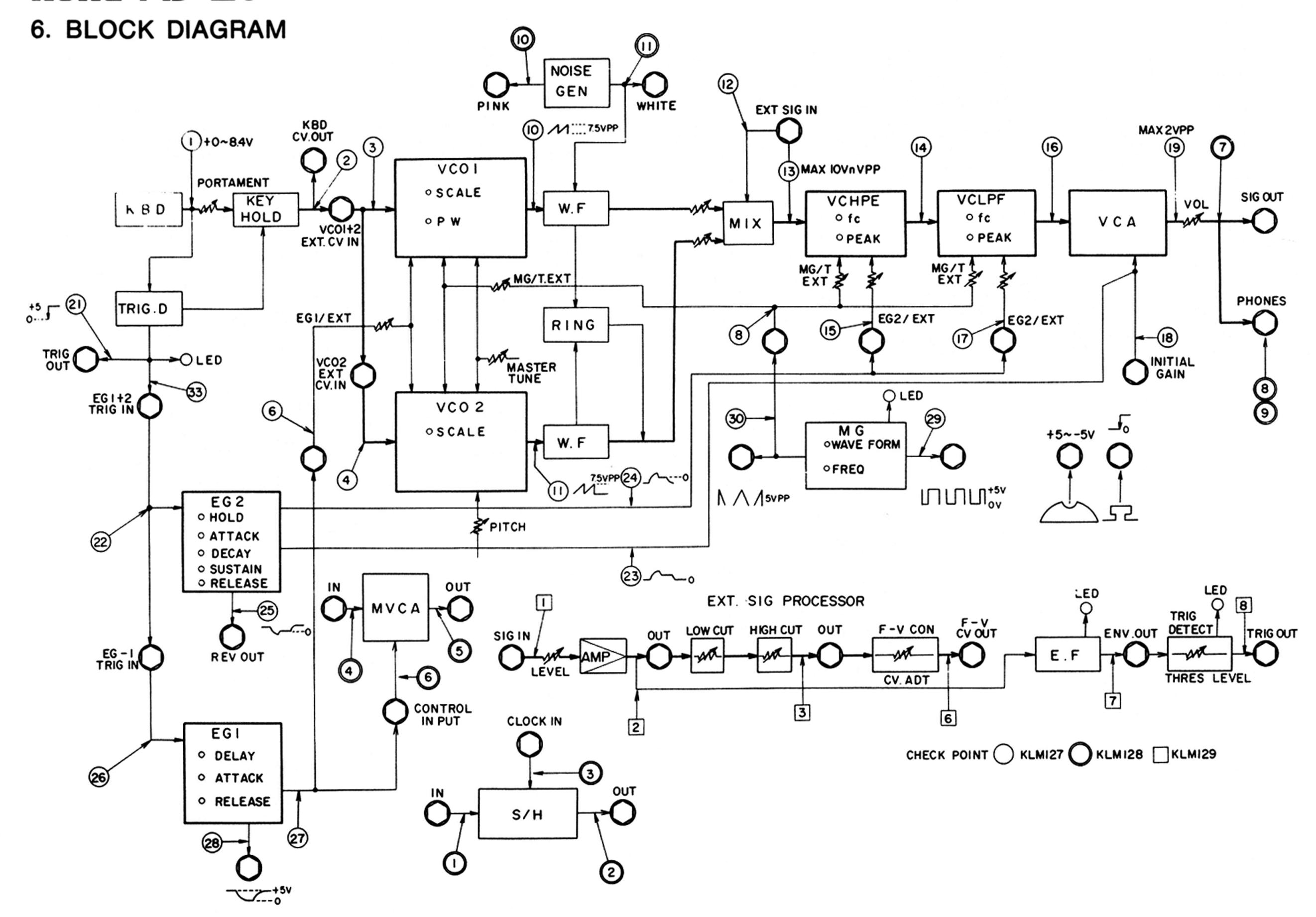
5. PARTS LIST

(Mechanical parts not listed)

16V-220_µF x 1

● CARBON RESIS	STO	DRS	●POLYSTYRENE CAPAC	CITC	DRS	●ROTARY VAR	≀IAE	BLE	
not listed			50V-3000pF x 1			RESISTORS			
			50V-6200pF	x	1	10KB	x	4	
● METAL FILM RESIS	STC	DRS	•			100KA	х	8	
1/4W 1% 100Ω	x	2	●POLYPROPF	YLF	ENE	100KA	x	4	
1/4W 1% 403Ω	x	18	CAPAC	CITC	DRS	1MB	x	2	
1/4W 1% 427Ω	x	22	200V-0.22µF	x	1	2MA	x	6	
1/4W 1% 1kΩ	x	3				1MA	X.	2	
	x	3	●TRANSI	STC	DRS	Printed 10KA	x	1	
1/4W 1% 2.94kΩ	x	1	2SA-564(S)	x	4	Printed 10KB	x	1	
1/4W 1% 4.27kΩ	x	1	2SC-945(L)K	x	1	Printed 1MB	x	1	
1/4W 1% 10kΩ	x	1	(special selected)			Printed 4-ganged 100KC	x	1	
1/4W 1% 15kΩ	x	1	2SC-1583G	x	2	24φ 10KB	x	1	
1/4W 1% 20kΩ	x	2	2SC-1685S	x	13	Center click-stop 10KB	x	1	
1/4W 1% 61.9kΩ	x	17	2SC-644R	x	1				
1/4W 1% 100kΩ	x	23				●ROTARY S	WIT	СН	
1/4W 1% 110kΩ	x	1			FET	SRM-1034 1-15mm	x	4	
1/4W 1% 5.11kΩ	x	1	2SK-30(O)	x	4				
			2SK-30(GR)	x	4	●KEYE	30 <i>A</i>	RD	
●SOLID RESIS	STC	DRS				ESK-431	37	key	
1/4W 10% 10MΩ x 7		● DIODES		DES					
			1S-1555	x	33	●TERMINAL LUG E	3OA	RD	
●MYLAR CAPAC	H	DRS				2L4P	x	1	
not listed		●LED							
			GD4-203RD	x	4	●PUSH S	WIT	CH	
●STYROL CAPAC	CITC	DRS				MS-102	х	1	
50V-12000pF	50V-12000pF x 1 ●PHOTOCOUPLER		LER						
		HTV-P873-G35-201B	×	1	● CONNEC	сто	RS		
● CERAMIC CAPAC	CITC	ORS				D 3b	х	6	
50V-56pF	X	1			● IC	4P	х	2	
50V-22pF	X	1	μPC-4558C	X	17	5P	х	2	
50V-100pF	X		081	×	5	7P	х	1	
50V-220pF	х	2.	TL-(071)			L_8P	х	2	
50V-47pF	X	1	(3140)			∱ ³Р	х	3	
25V-100000pF	X	2	082	X	1	Ų 5P	х	1	
			TL-(072)			Female Con	nec	tors	
●TANTALUM CAPAC	CITC	ORS	(3140)				х	10	
16V-3.3μF		1	MC-14007		2	4P	х	2	
16V-6.8μF	X	2	MC-14069B		2	5P	X	3	
			μPD4011C		1	7P		2	
●ELECTROLYTIC CAPAC			μPC339C		1	8P	х	2	
16V-10μF			μPC14315		1				
16V-33μF			μA79M15		1				
16V-100μF			KORG35	X	2				
50V-1μF x 6									
25V-470µF x 1		●SEMI-FIXED RES							
25V-10000μF	X	1	SR19R(10kB) SR19R(100kB)		6				
1 16 17 - 13 13 1 1	~	- 1	. SEIVELIEUR						

SR19R(10kB) x 6 SR19R(100kB) x 7



7. ADJUSTMENT PROCEDURE

7-1 Power supply check

- Positive ripple.
 Should be no more than 2mVp-p.
 Set oscilloscope vertical gain at 10mV/cm and check that power supply ripple is 2mV or less.
- Negative ripple.
 Same as positive, should be no more than 2mVp-p.

7-2. Pitch adjustment

1. VCO-1.

Perform adjustment with synthesizer controls at "normal setting" (Scale = 8, Waveform = $\Gamma \sqcup$, Master Tune, Pitch, and all other knobs at "0"). See figure 1.

- a. Play C-4 (high C) on the keyboard and adjust the high semi-fixed screw until you obtain the correct tuning as indicated by WT-10A (connected to the SIG OUT jack).
- b. Play key C-1 and adjust the low semifixed screw.
- Repeat steps a and b as many times as necessary until both are tuned to the correct pitch.
- d. Check the tuning of C-1, C-2, C-3, and C-4 on the WT-10A meter to make sure pitch deviation is within ±2 cents for each.

- e. Change the scale to 32', 16', 8', and 4' and check the tuning of all four C keys to make sure that the pitch deviation of each is within ± 10 cents.
- 2. VCO-2.

Set the VCO-1 level at "0" and the VCO-2 level at "10". Then follow the same procedure as for VCO-1, by adjusting the high (s) and low (4) semi-fixed screws.

7-3. KBD CV adjustment

Use a 4-1/2 digital voltmeter to measure the KBD CV OUT signal.

- a. Measure output voltage first when you play key C-4, then when you play key C-3. The output voltage for C-3 should be exactly half that for C-4. Adjust the KBD CV high semi-fixed screw as necessary so that C-3 produces half the voltage of C-4.
- b. Measure C-2 and then C-1 in the same way. Adjust the KBD CV low semi-fixed screw as necessary so that C-2 produces exactly half the voltage of C-4.
- c. Repeat steps a and b as many times as necessary until the output voltage of each of C-1, C-2, C-3, and C-4 is exactly half that of the next.

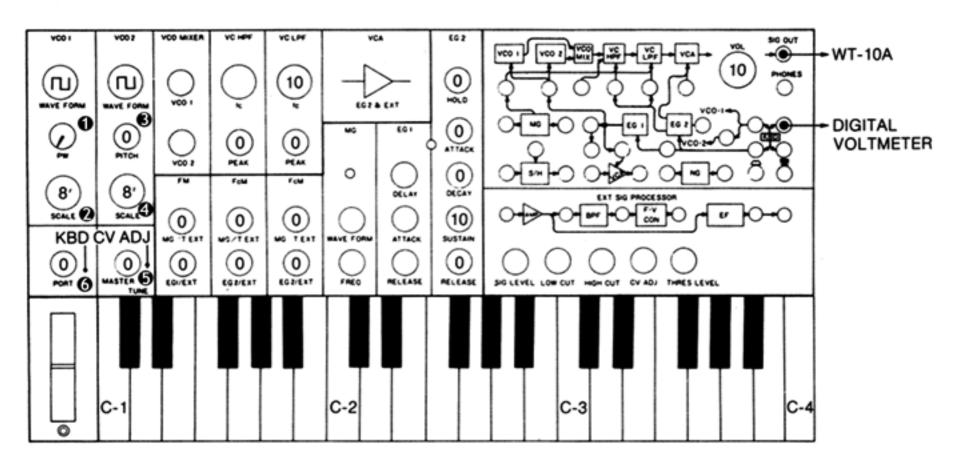


Fig. 1

7-4. VCF Fc adjustment

Connect a frequency counter to the PHONES jack (since a high output level is needed for measurement). Set VCO-1 and VCO-2 level at "0".

1. VC HPF

Refer to the settings shown in figure 2. Set the LPF PEAK knob at "0", and the HPF PEAK knob

at "10". Then adjust the **1** semi-fixed screw as necessary so that the HPF oscillation frequency is 500Hz.

2. VC LPF

Set HPF PEAK at "0", and LPF PEAK at "10". Then adjust the ② semi-fixed screw in the same way as you did for the HPF.

