## © G. Forrest Cook 1994 cook@stout.atd.ucar.edu One Channel's Variable Gain Stage 100pF 1.0uF 6 Non Polar 5.0uF Non Polar 2N3904 2N3904 \* Input 10K 10K Aud Input Level Duplicate this circuit 10K for the second channel \* The transistors should be a matched pair, i.e. with the gain control at full negative Gain and no input signal, the collectors Control should be at the same voltage, +/-Input epsilon where epsilon < 0.1 Volt. To the 10K resistor on the second channel The power supply is +/- 15 VDC control voltage amp 0.01uF and inverter power supply filter .6K Right Gain Control 0-10V 0.1uF 0 Panner Left Gain Control Control Voltage Input 0.1uF Trimmer Left Channel Zero Adjust

**Voltage Controlled Panner**