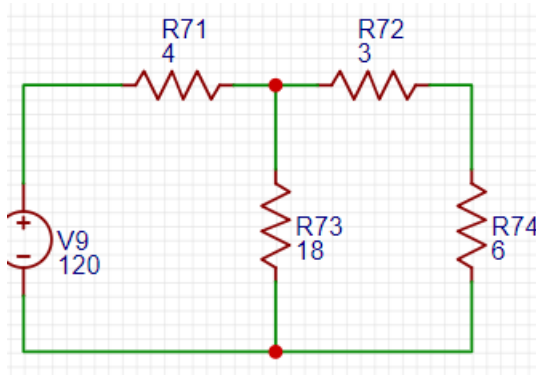


### 3.1

3.1 a) Show that the solution of the circuit in Fig. 3.9 (see Example 3.1) satisfies Kirchhoff's current law at junctions x and y.

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b) Show that the solution of the circuit in Fig. 3.9 satisfies Kirchhoff's voltage law around every closed loop.



Handwritten calculations and circuit diagrams for Kirchhoff's Current Law (KCL) and Kirchhoff's Voltage Law (KVL) verification.

**KCL at Junction x:**

Currents entering junction x:  $i_1 = 4$  A,  $i_2 = 8$  A.

Currents leaving junction x:  $i_3 = 12$  A.

Power dissipation calculations:

- Power in  $4\Omega$  resistor:  $P_1 = i_1^2 R = 4^2 \times 4 = 64$  W
- Power in  $18\Omega$  resistor:  $P_2 = i_2^2 R = 8^2 \times 18 = 1152$  W
- Power in  $3\Omega$  resistor:  $P_3 = i_3^2 R = 12^2 \times 3 = 432$  W
- Power in  $6\Omega$  resistor:  $P_4 = i_4^2 R = 8^2 \times 6 = 384$  W

Total power dissipation:  $P_{total} = 64 + 1152 + 432 + 384 = 2032$  W

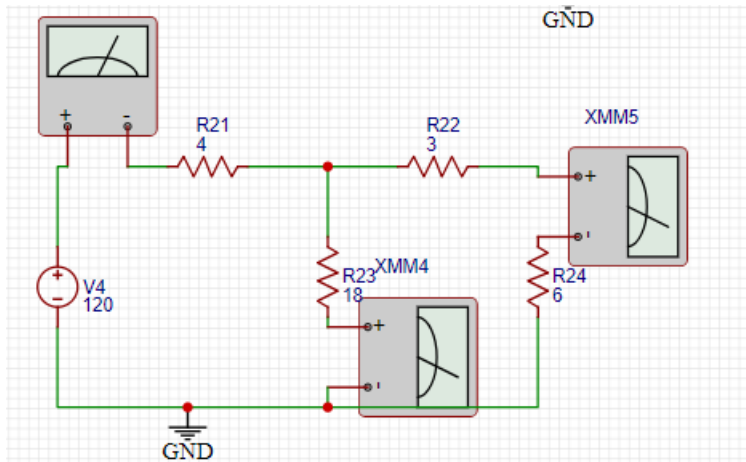
**KVL around the loop:**

Sum of voltages around the loop:  $V_{source} - V_{R71} - V_{R73} - V_{R72} - V_{R74} = 120 - 4 \times 4 - 18 \times 8 - 3 \times 12 - 6 \times 8 = 120 - 16 - 144 - 36 - 48 = -120$  V

The sum of voltages around the loop is zero, satisfying KVL.

### 3.2

- 3.2 a) Find the power dissipated in each resistor in the circuit shown in Fig. 3.9.  
 b) Find the power delivered by the 120 V source.  
 c) Show that the power delivered equals the power dissipated.



3.2

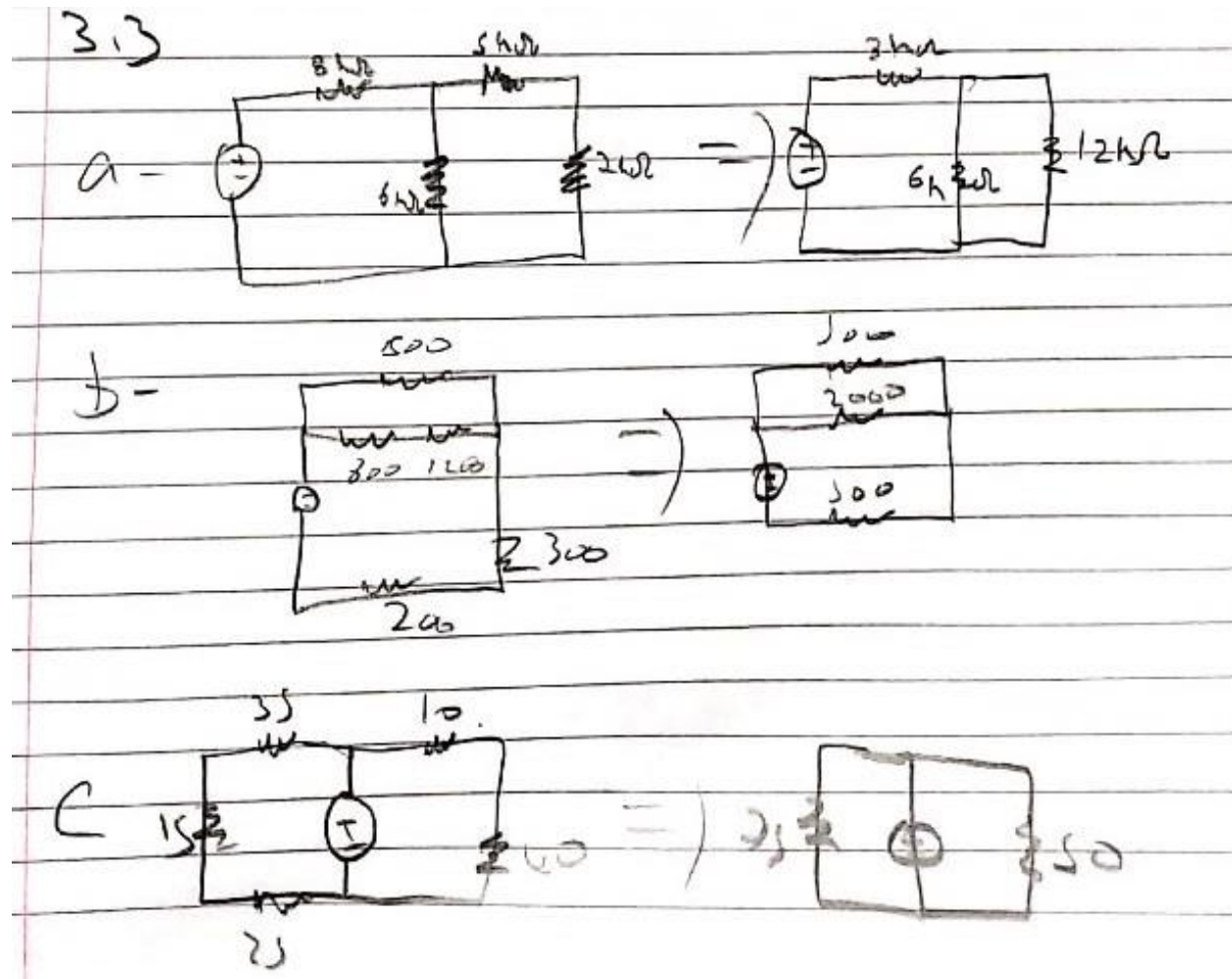
(a)  $P_{4\Omega} = I^2 R = 12^2 \times 4 = 576 \text{ watt}$   $P_{18\Omega} = 288 \text{ watt}$   
 $P_{3\Omega} = 192 \text{ watt}$   $P_{6\Omega} = 384 \text{ watt}$

(b)  $P_{120} = 120i = 120 \times 12 = 1440 \text{ watt}$

(c)  $P_{diss} = 576 + 288 + 192 + 384 = 1440 \text{ watt}$  Correct

3.3

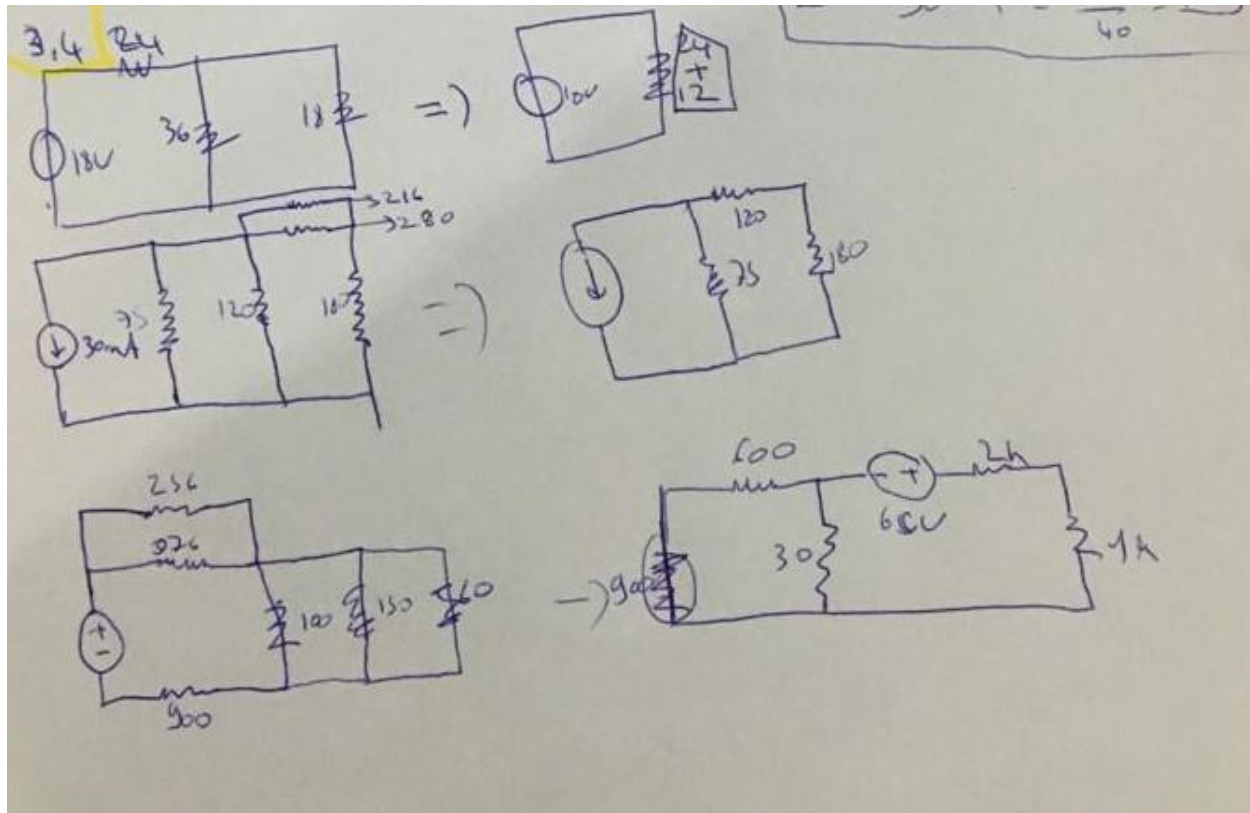
- 3.3 For each of the circuits shown in Fig. P3.3,  
 a) identify the resistors connected in series,  
 b) simplify the circuit by replacing the series-connected resistors with equivalent resistors.



3.4

3.4 For each of the circuits shown in Fig. P3.4,

- identify the resistors connected in parallel,
- simplify the circuit by replacing the parallel-connected resistors with equivalent resistors.



3.5

- 3.5 For each of the circuits shown in Fig. P3.3,  
 a) find the equivalent resistance seen by the source,  
 b) find the power developed by the source.

3,5

$$R_{eq} = [(7000 + 5000) \parallel 6000] + 800 = 12,000 \parallel 6000 + 8000$$

$$= 4000 + 8000 = 12 \text{ k}\Omega$$

$$R_{eq} = [500 \parallel (800 + 1200)] + 300 + 200 = (500 \parallel 2000) + 300 + 200$$

$$= 400 + 300 + 200 = 900 \Omega$$

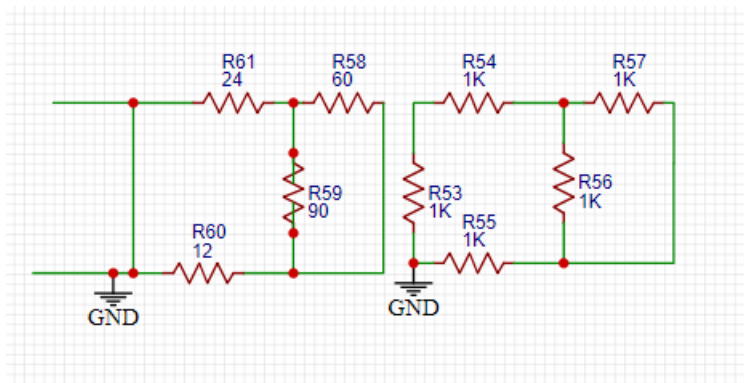
$$R_{eq} = (35 + 15 + 25) \parallel (10 + 40) = 75 \parallel 50 = 30 \Omega$$

$$R_{eq} = ([ (70 + 80) \parallel 100 ] + 50 + 90) \parallel 300 = [ (150 \parallel 100) + 50 + 90 ] \parallel 300$$

$$= (60 + 50 + 90) \parallel 300 = 200 \parallel 300 = 120 \Omega$$

3.8

Find the equivalent resistance  $R_{ab}$  for each of the circuits in Fig. P3.8.



3.8

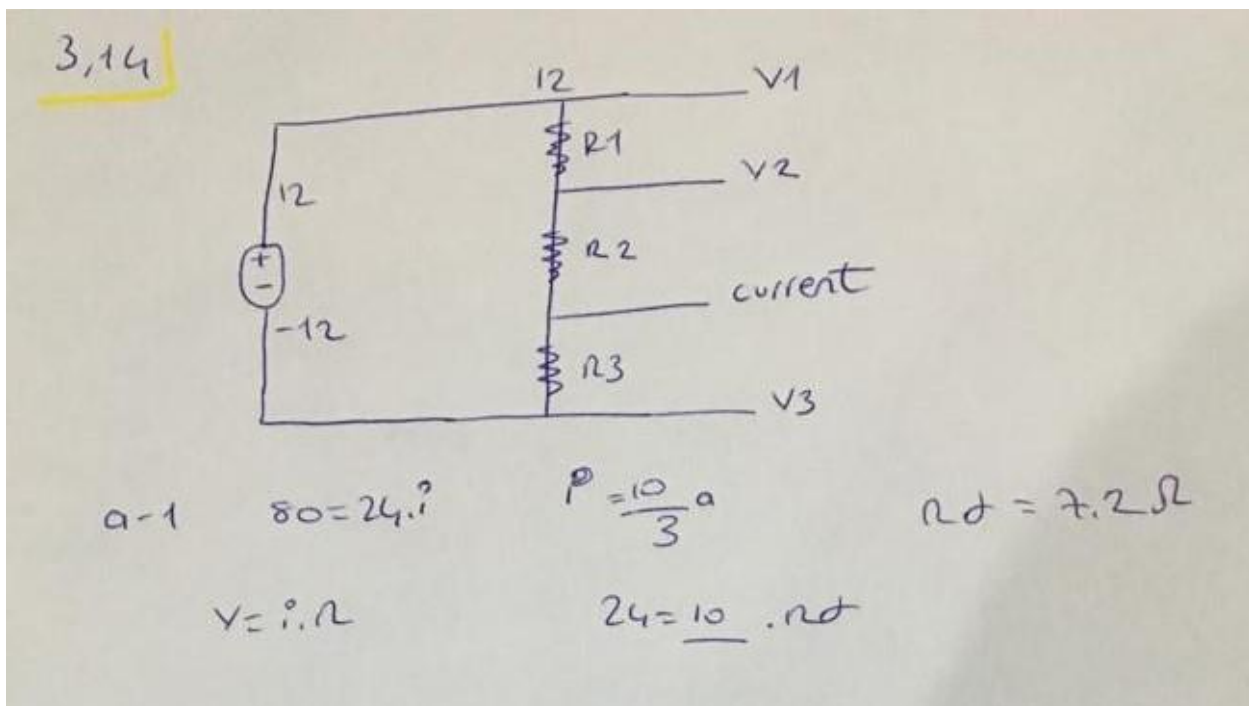
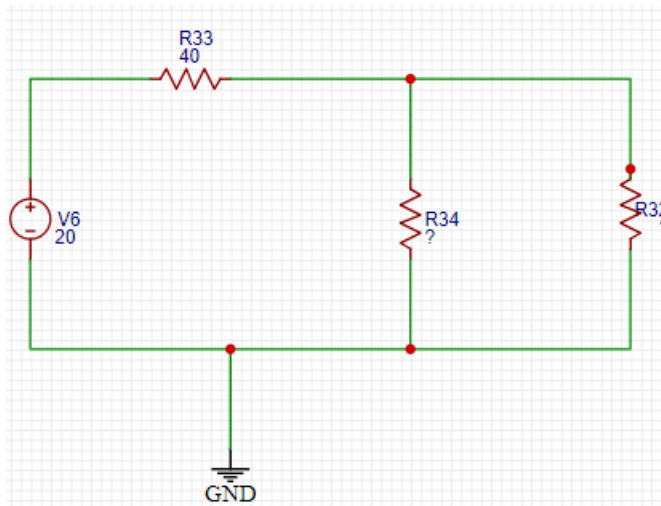
(a)  $R_{ab} = 24 + 90 \parallel 60 + 12 = 24 + 36 + 12 = 72 \Omega$

(b)  $R_{ab} = 12 \parallel 8 + 5.2 = 10.4 \Omega$

(c)  $R_{ab} = 1200 \parallel 720 \parallel 300 \Rightarrow 288 \Omega$

3.13

**3.13** In the voltage-divider circuit shown in Fig. P3.13, the no-load value of  $v_o$  is 4 V. When the load resistance  $R_L$  is attached across the terminals a and b,  $v_o$  drops to 3 V. Find  $R_L$ .

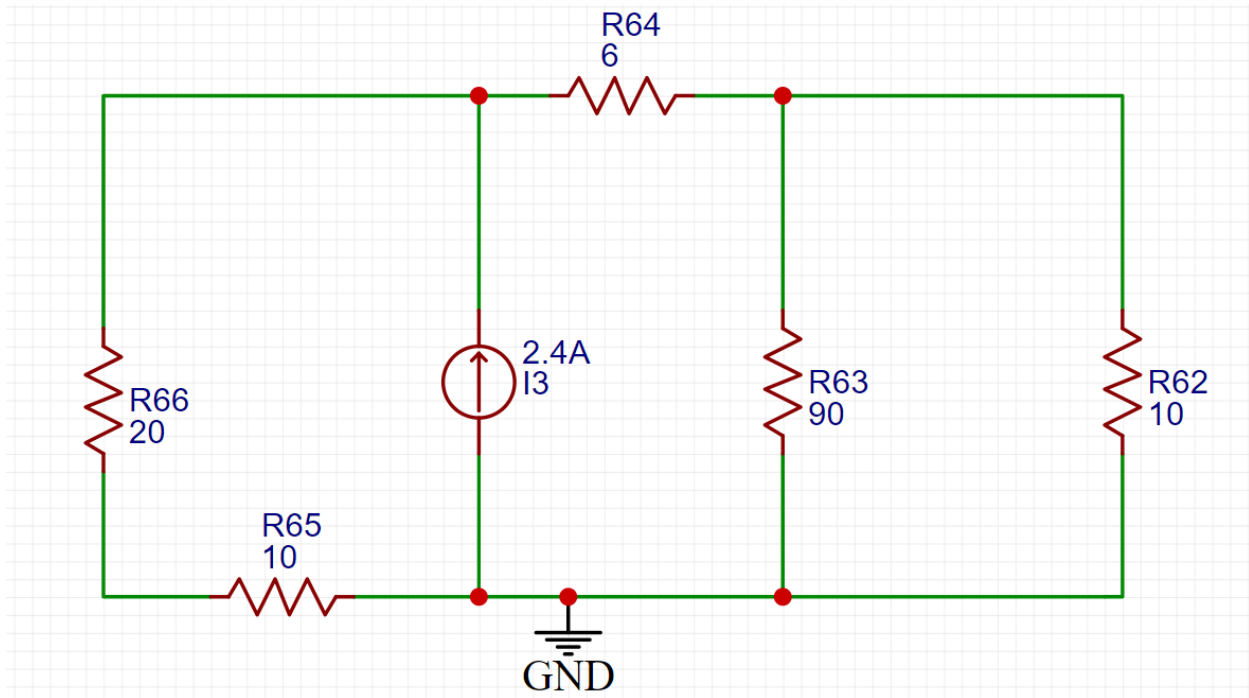


3.17

3.17 For the current divider circuit in Fig. P3.17 calculate

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- $i_o$  and  $v_o$ .
- the power dissipated in the  $6\ \Omega$  resistor.
- the power developed by the current source.



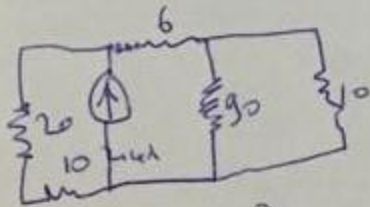


3.17

$$R_{eq} = 30 \parallel (12 + 90 \parallel 10) = 30 \parallel 15 = 10 \Omega$$

$$V_{2,4} = 10 \cdot 2,4 = 24 \quad / \quad U_0 = U_{2ar} = \frac{20}{30} 24 = 16V$$

$$V_{2ar} = \frac{90 \parallel 10}{6 + 90 \parallel 10} (24) = \frac{9}{15} 24 = 14,4V$$



$$a - i_{10} = \frac{14,4}{40} = 0,16A$$

$$(b) - \frac{(24 - 14,4)^2}{6} = 15,36 \text{ Watt}$$

$$(c) (-2,4) \cdot 24 = -57,6 \text{ Watt}$$

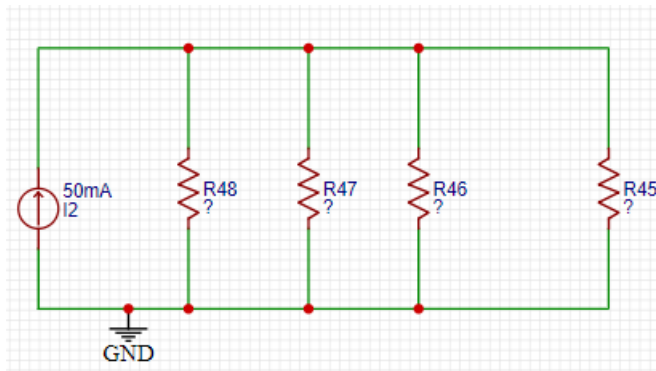
3.18

**3.18** Specify the resistors in the current divider circuit in Fig. P3.18 to meet the following design criteria:

DESIGN  
PROBLEM

$$i_g = 50 \text{ mA}; v_g = 25 \text{ V}; i_1 = 0.6i_2;$$

$$i_3 = 2i_2; \text{ and } i_4 = 4i_1.$$



3.18

$$0.05 = i_1 + i_2 + i_3 + i_4 = 0.6i_2 + i_2 + 2i_2 + 4i_1 = 0.6i_2 + i_2 + 2i_2 + 4(0.6i_2)$$

$$+ 4(0.6i_2) = 6i_2$$

$$i_2 = 0.05 / 6 = 0.00833 = 8.33 \text{ mA}$$

$$i_1 = 0.6i_2 = 0.6(0.00833) = 0.005 = 5 \text{ mA}$$

$$i_3 = 2i_2 = 2(0.00833) = 0.01667 = 16.67 \text{ mA}$$

$$i_4 = 4i_1 = 4(0.005) = 0.02 = 20 \text{ mA}$$

$$R_2 = 25 / i_2 = 25 / 0.00833 = 3000 = 3 \text{ k}\Omega$$

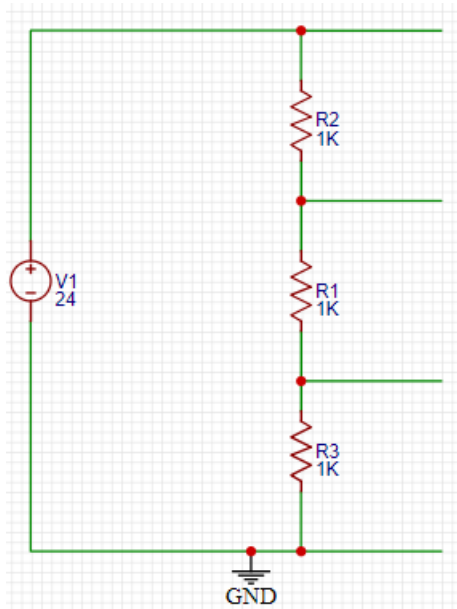
$$R_3 = 25 / i_3 = 25 / 0.01667 = 1500 = 1.5 \text{ k}\Omega$$

$$R_4 = 25 / i_4 = 25 / 0.02 = 1250 = 1.25 \text{ k}\Omega$$

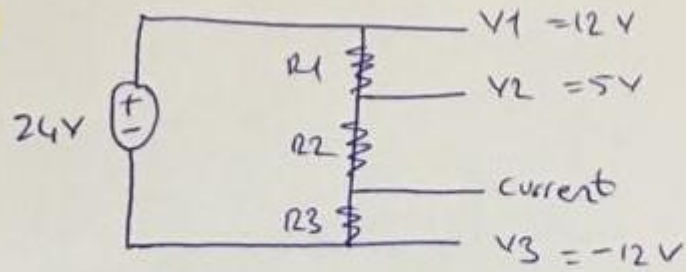
## 3.19

There is often a need to produce more than one voltage using a voltage divider. For example, the memory components of many personal computers require voltages of  $-12\text{ V}$ ,  $5\text{ V}$ , and  $+12\text{ V}$ , all with respect to a common reference terminal. Select the values of  $R_1$ ,  $R_2$ , and  $R_3$  in the circuit in Fig. P3.19 to meet the following design requirements:

- The total power supplied to the divider circuit by the  $24\text{ V}$  source is  $80\text{ W}$  when the divider is unloaded.
- The three voltages, all measured with respect to the common reference terminal, are  $v_1 = 12\text{ V}$ ,  $v_2 = 5\text{ V}$ , and  $v_3 = -12\text{ V}$ .



3.19



$$W = 80$$

$$W = V \cdot i$$

$$80 = 24 \cdot i$$

$$i = \frac{10}{3} A$$

$$R_1 = \frac{12 - 5}{\frac{10}{3}} = 2.1 \Omega$$

$$V = i \cdot R$$

$$\frac{V}{i} = R$$

$$R_2 = \frac{5}{\frac{10}{3}} = 1.5 \Omega$$

$$2.1 \Omega$$

$$1.5 \Omega$$

$$3.4 \Omega$$

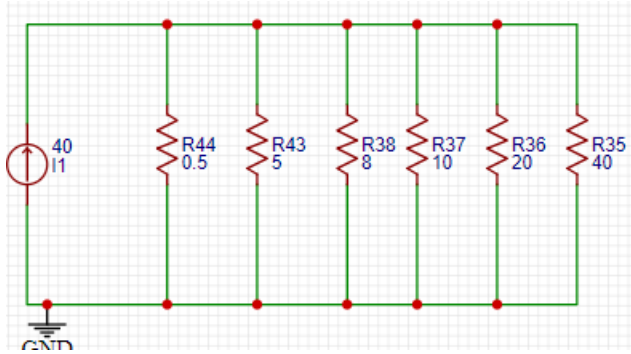
$$R_3 = \frac{-(-12)}{\frac{10}{3}} = 0.6 \Omega$$

## 3.22

a) Show that the current in the  $k$ th branch of the circuit in Fig. P3.22(a) is equal to the source current  $i_g$  times the conductance of the  $k$ th branch divided by the sum of the conductances, that is,

$$i_k = \frac{i_g G_k}{G_1 + G_2 + G_3 + \cdots + G_k + \cdots + G_N}$$

b) Use the result derived in (a) to calculate the current in the  $5\ \Omega$  resistor in the circuit in Fig. P3.22(b).



3,22

$$i_g = V_0 G_1 + V_0 G_2 + \dots + V_0 G_N = V_0 (G_1 + G_2 + \dots + G_N)$$

$$V_0 = \frac{i_g}{(G_1 + G_2 + \dots + G_N)}$$

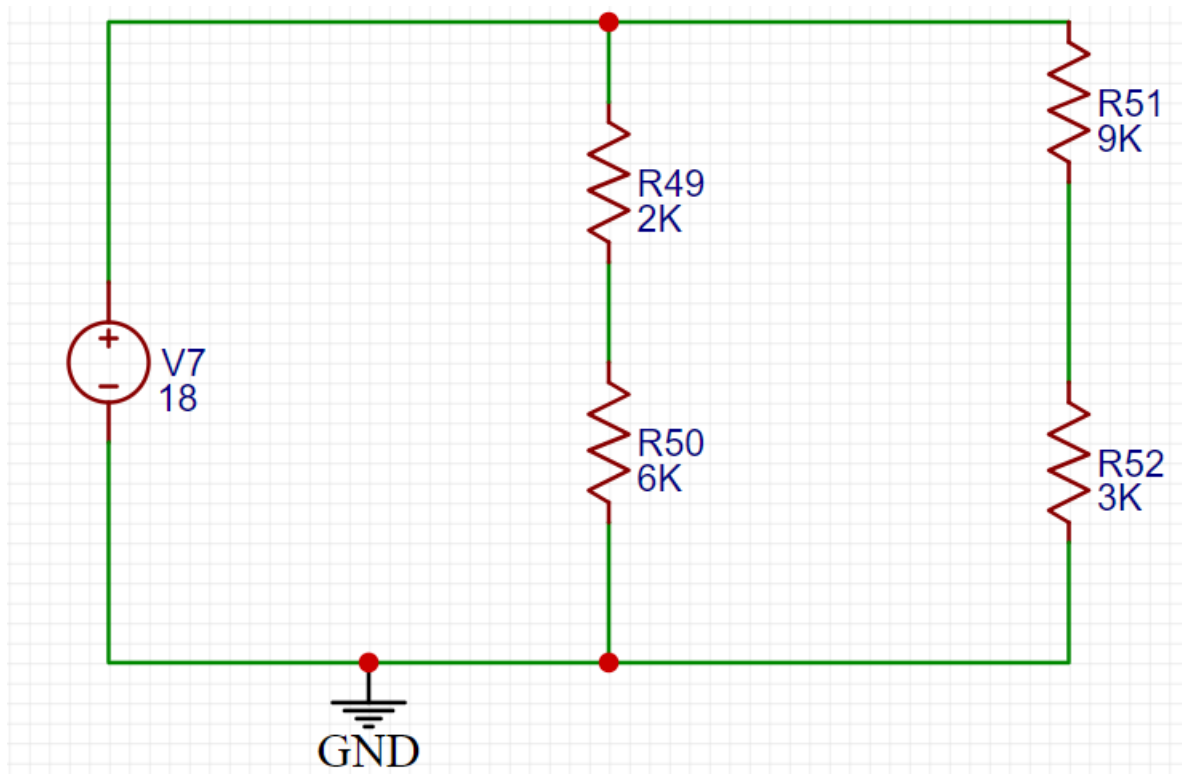
$$i_k = V_0 G_k$$

$$i_k = \frac{i_g G_k}{[G_1 + G_2 + \dots + G_N]}$$

$$i_5 = \frac{40 (0,2)}{2 + 0,2 + 0,125 + 0,1 + 0,05 + 0,025} = 3,2\text{ A}$$

3.28

- a) Find the voltage  $v_x$  in the circuit in Fig. P3.28 using voltage and/or current division.
- b) Replace the 18 V source with a general voltage source equal to  $V_s$ . Assume  $V_s$  is positive at the upper terminal. Find  $v_x$  as a function of  $V_s$ .



3.28

$$(a) \quad V_{oh} = \frac{6}{9} \cdot 18 = \boxed{12.0V}$$

$$V_{zh} = \frac{3}{12} \cdot 18 = \boxed{4.5V}$$

$$\boxed{V_x = V_{oh} - V_{zh} = 9V}$$

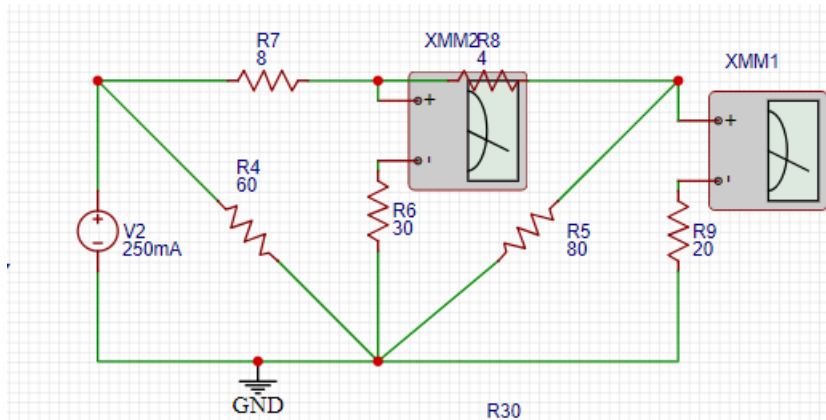
$$(b) \quad V_{oh} = \frac{6}{8} V_s = \cancel{1.5} \cdot \frac{3}{4} V_s$$

$$V_{zh} = \frac{1}{4} V_s$$

$$\boxed{V_x = 0.5 V_s}$$

3.32

For the circuit in Fig. P3.32, calculate  $i_1$  and  $i_2$  using current division.



3.32

$$R_{eq} = (20 \parallel 80 + 4) \parallel 30 + 8 = 20$$

$$i_0 = \frac{60 \parallel 20}{R_{eq}} (0.25) = 187.5 \text{ mA}$$

$$i_1 = \frac{30 \parallel (4 + 80 \parallel 20)}{30} \quad i_8 = 0.025 = 25 \text{ mA}$$

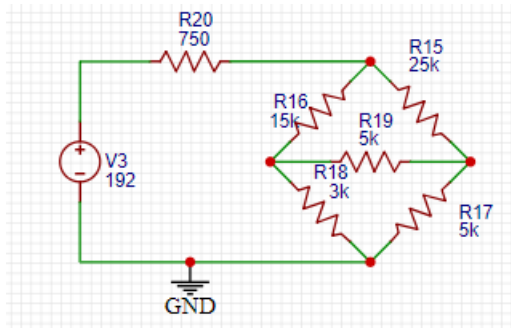
$$i_{4\Omega} = \frac{30 \parallel (4 + 80 \parallel 20)}{4 + 80 \parallel 20} \quad i_8 = 0.1125 = 112.5 \text{ mA}$$

$$i_2 = \frac{80 \parallel 20}{20} (i_{4\Omega}) = 0.09 = 90 \text{ mA}$$



3.52

Find the power dissipated in the  $3\text{ k}\Omega$  resistor in the circuit in Fig. P3.52.



3,52  $R_{eq} = 750 + (15,000 + 3,000) \parallel (25,000 + 5,000) = 750 + 11,250 = 12\text{ k}\Omega$

Source current  $= 192 / 12,000 = 16\text{ mA}$

The current will move on the  $15\text{ k}\Omega$  and  $3\text{ k}\Omega$

$$i_{3k} = \frac{11,250}{18,000} (0,016) = 10\text{ mA}$$

$$P_{3k} = 3000 (0,01)^2 = 0,3\text{ W}$$

3.53

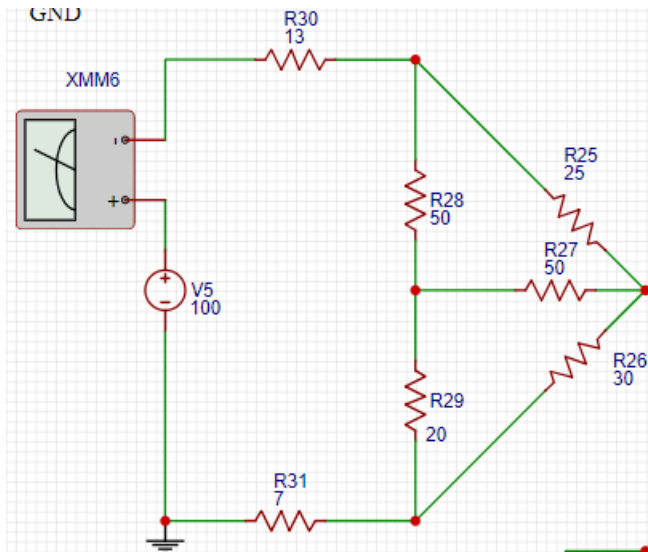
3.53 Find the detector current  $i_d$  in the unbalanced bridge in Fig. P3.53 if the voltage drop across the detector is negligible.

3.53

$6\text{ k}\Omega \parallel 30\text{ k}\Omega = 5\text{ k}\Omega$   
 $12\text{ k}\Omega \parallel 20\text{ k}\Omega = 7.5\text{ k}\Omega$   
 $i_s = \frac{75}{12,500} = 6\text{ mA}$   
 $v_1 = 0,006(5000) = 30\text{ V}$   
 $v_2 = 0,006(7500) = 45\text{ V}$   
 $i_1 = \frac{30}{6000} = 5\text{ mA}$   
 $i_d = i_1 = i_2 = 1,25\text{ mA}$

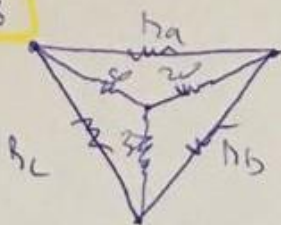
3.58

- 3.58 a) Find the equivalent resistance  $R_{ab}$  in the circuit in Fig. P3.58 by using a Y-to- $\Delta$  transformation involving resistors  $R_2$ ,  $R_3$ , and  $R_5$ .
- b) Repeat (a) using a  $\Delta$ -to-Y transformation involving resistors  $R_3$ ,  $R_4$ , and  $R_5$ .
- c) Give two additional  $\Delta$ -to-Y or Y-to- $\Delta$  transformations that could be used to find  $R_{ab}$ .



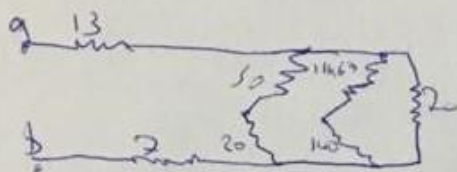
3.58

(a)



$$R_A = 2530 + 2550 + 30 \cdot 50 \quad \frac{3500}{30} = 116.67 \Omega$$

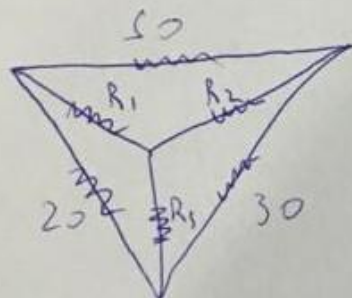
$$R_b = 70 \Omega \quad R_c = 140 \Omega$$



$$70 \parallel (50 \parallel 116.67) + 20 \parallel 160 = 30$$

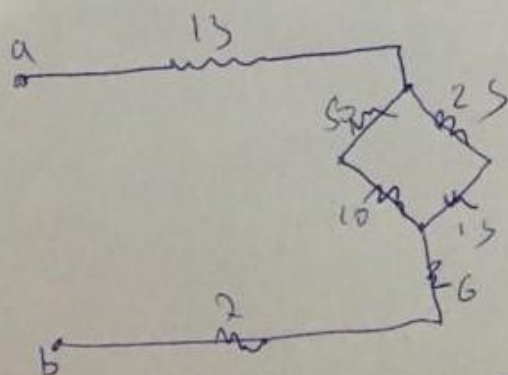
$$R_{ab} = 13 + 30 + 7 = 50$$

(b)



$$R_1 = \frac{50 \cdot 20}{50 + 20 + 30} = 10 \quad R_2 = \frac{50 \cdot 30}{50 + 20 + 30} = 15$$

$$R_3 = \frac{20 \cdot 30}{50 + 20 + 30} = 6 \Omega$$



$$(50 + 10) \parallel ((25 + 15) + 6) = 30$$

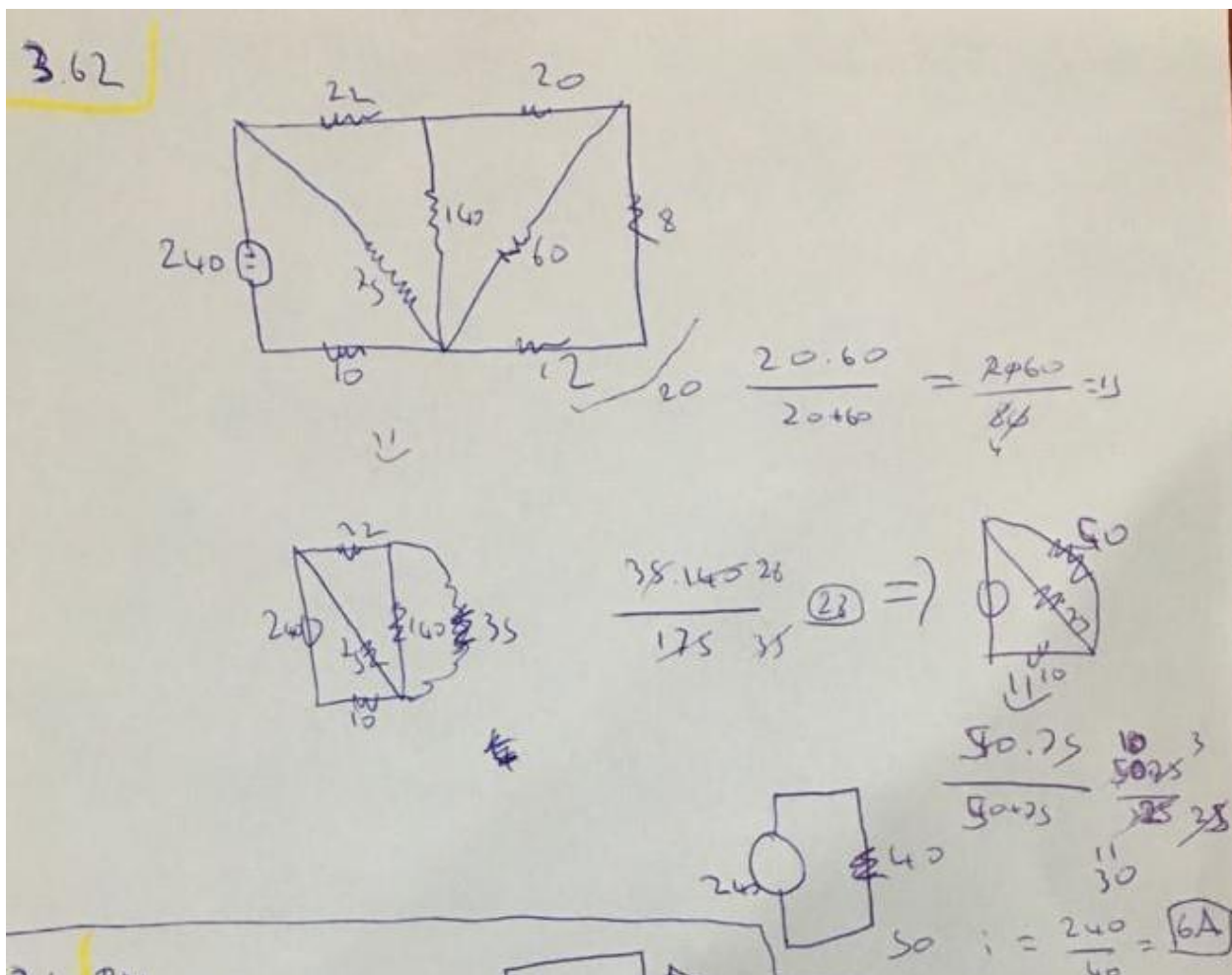
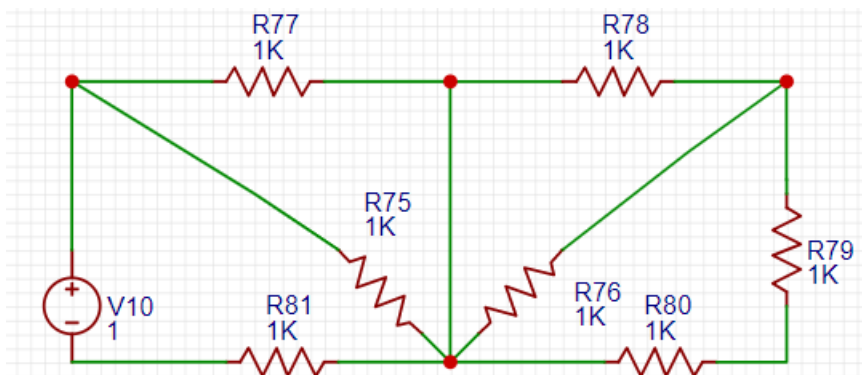
$$R_{ab} = 13 + 30 + 7 = 50$$

$$(c) \quad R_1 - R_2 - R_3 = R_1 - R_3 - R_4$$

3.62

Find  $i_o$  and the power dissipated in the  $140\ \Omega$  resistor in the circuit in Fig. P3.62.

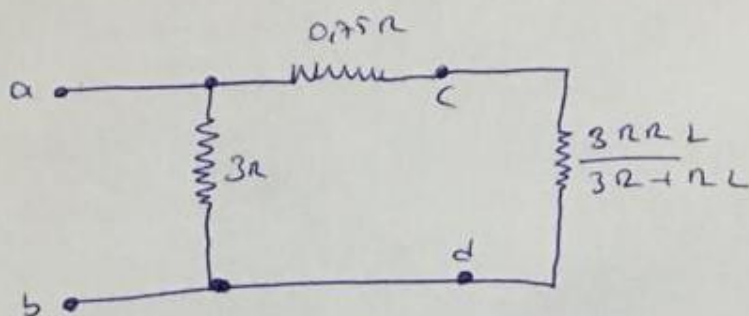
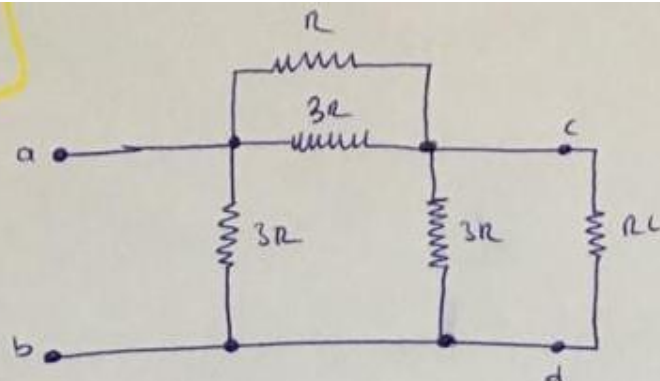
For the circuit shown in Fig. P3.63, find (a)  $i_1$ , (b)  $v$ , (c)  $i_2$ , and (d) the power supplied by the voltage source.



3.67

- a) The fixed-attenuator pad shown in Fig. P3.67 is called a *bridged tee*. Use a Y-to- $\Delta$  transformation to show that  $R_{ab} = R_L$  if  $R = R_L$ .
- b) Show that when  $R = R_L$ , the voltage ratio  $v_o/v_i$  equals 0.50.

3.67

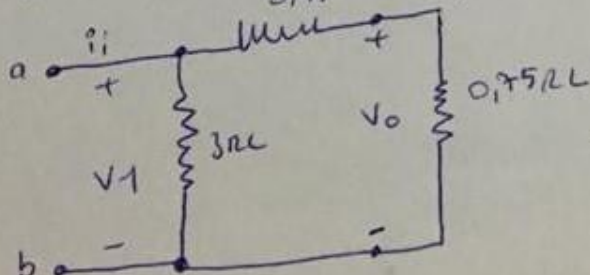


$$0.75R + \frac{3RRL}{3R+RL} = \frac{2.25R^2 + 3.75RRL}{3R+RL}$$

$$R_{ab} = \frac{3R \left( \frac{2.25R^2 + 3.75RRL}{3R+RL} \right)}{3R \left( \frac{2.25R^2 + 3.75RRL}{3R+RL} \right)} = \frac{3R(3R+5RL)}{15R+9RL}$$

If  $R = RL$ , we have  $R_{ab} = \frac{3RL(8RL)}{24RL} = RL$

When  $R = RL$ , the circuit reduces to



$$i_0 = \frac{i_1(3RL)}{4.5RL} = \frac{1}{1.5} i_1$$

$$\rightarrow \frac{V_0}{V_1} = 0.5$$

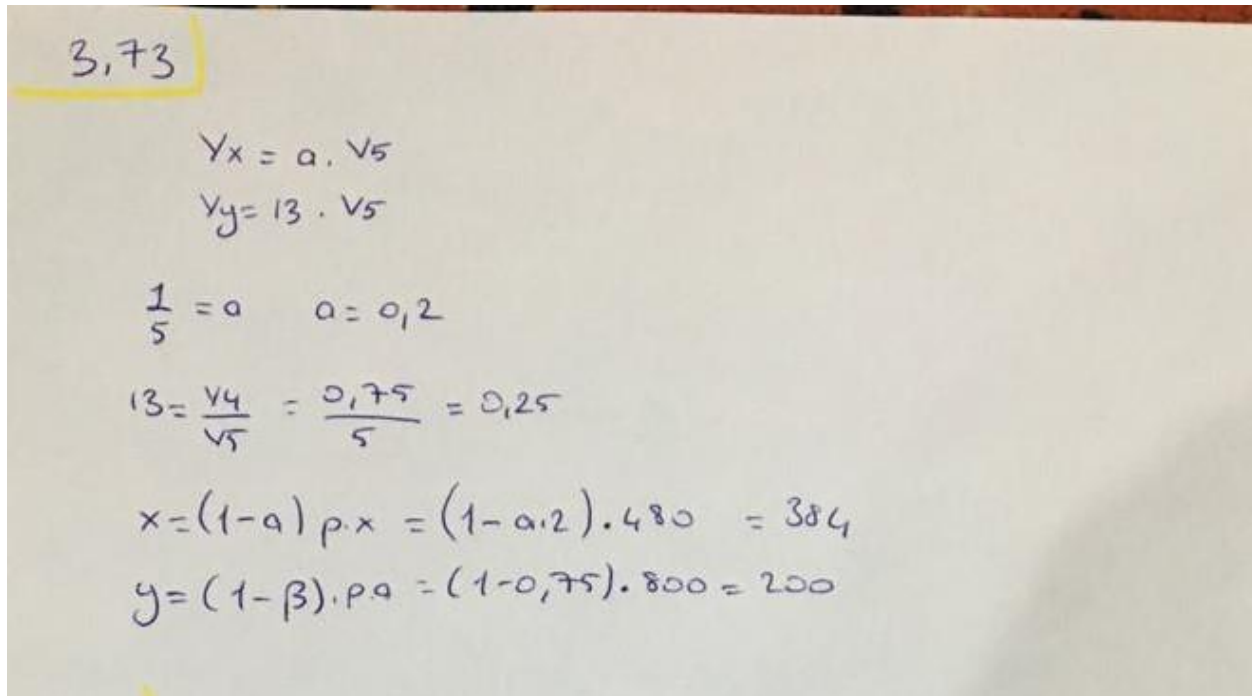


3.73

A resistive touch screen has 5 V applied to the grid in the x-direction and in the y-direction. The screen has 480 pixels in the x-direction and 800 pixels in

the y-direction. When the screen is touched, the voltage in the x-grid is 1 V and the voltage in the y-grid is 3.75 V.)

- a) Calculate the values of  $\alpha$  and  $\beta$ .
- a) Calculate the x- and y-coordinates of the pixel at the point where the screen was touched.



Handwritten solution for problem 3.73:

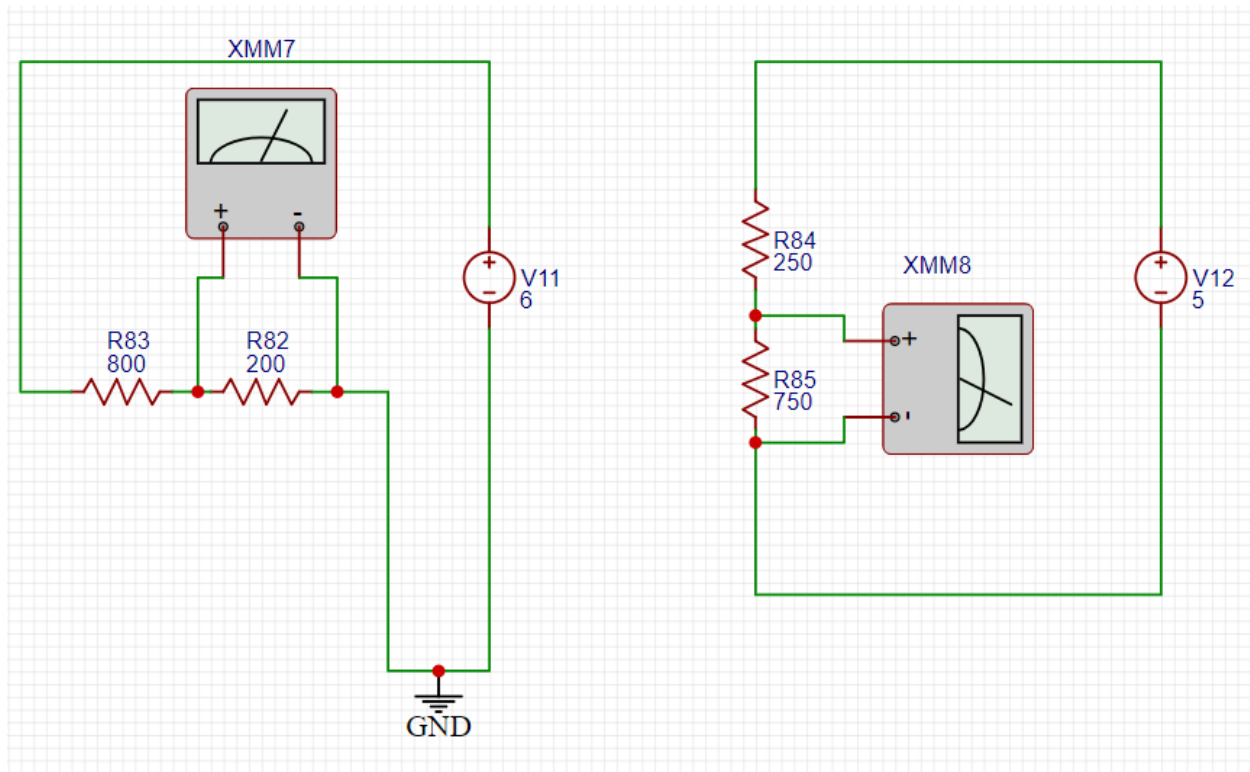
3.73

$$V_x = \alpha \cdot V_5$$
$$V_y = \beta \cdot V_5$$
$$\frac{1}{5} = \alpha \quad \alpha = 0,2$$
$$\beta = \frac{V_y}{V_5} = \frac{3,75}{5} = 0,75$$
$$x = (1 - \alpha) \cdot p_x = (1 - 0,2) \cdot 480 = 384$$
$$y = (1 - \beta) \cdot p_y = (1 - 0,75) \cdot 800 = 200$$



3.74

A resistive touch screen has 640 pixels in the  $x$ -direction and 1024 pixels in the  $y$ -direction. The resistive grid has 8 V applied in both the  $x$ - and  $y$ -directions. The pixel coordinates at the touch point are (480, 192). Calculate the voltages  $V_x$  and  $V_y$ .



3,74

$$x = (1-a) p_x$$

$$640(1-a) = 480$$

$$1-a = \frac{2}{4} \quad a = \frac{1}{4}$$

$$y = (1-\beta) p_y$$

$$v_y = 8 \cdot 0,8125$$

$$v_y = 6,5 \text{ V}$$

$$640,1024$$

$$480,142$$

$$v_{x1} = v_{s1} \quad v_{x2} = 8 \cdot \frac{1}{4} = 2 \text{ V}$$

$$192 = (1-\beta) \cdot 1024$$

$$260 = (1-\beta) \cdot 2^{10}$$

$$\frac{3}{16} = 1-\beta$$

$$\beta = \frac{13}{16} = 0,8125$$