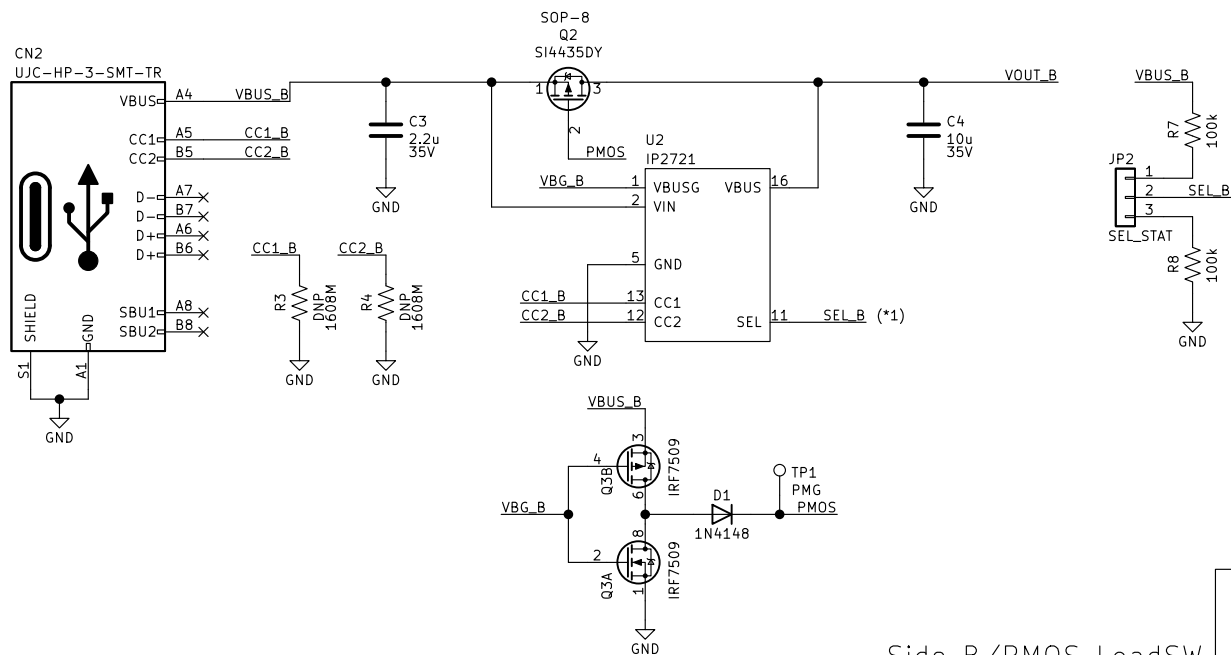


Side A/NMOS LoadSW

## Breakout



\*1: SEL-pin status:  
2 to 1: High(VBUS=20V or 12V)  
2 to 3 Low(VBUS=5V)  
DNP: Float(VBUS=15V or 9V)

Note

Side B/PMOS LoadSW

Sheet: /

File: PDBBreakout.sch

**Title: USB PD Controller IP2721 Breakout Schematic**

Size: A4

Date:

Size: A	Date:
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