

Figure 17. Power-Up Sequence

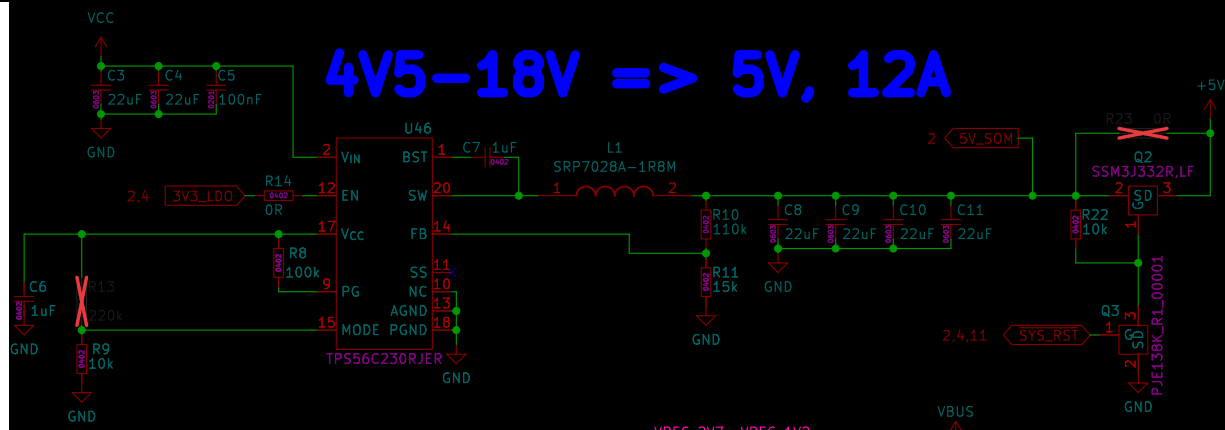
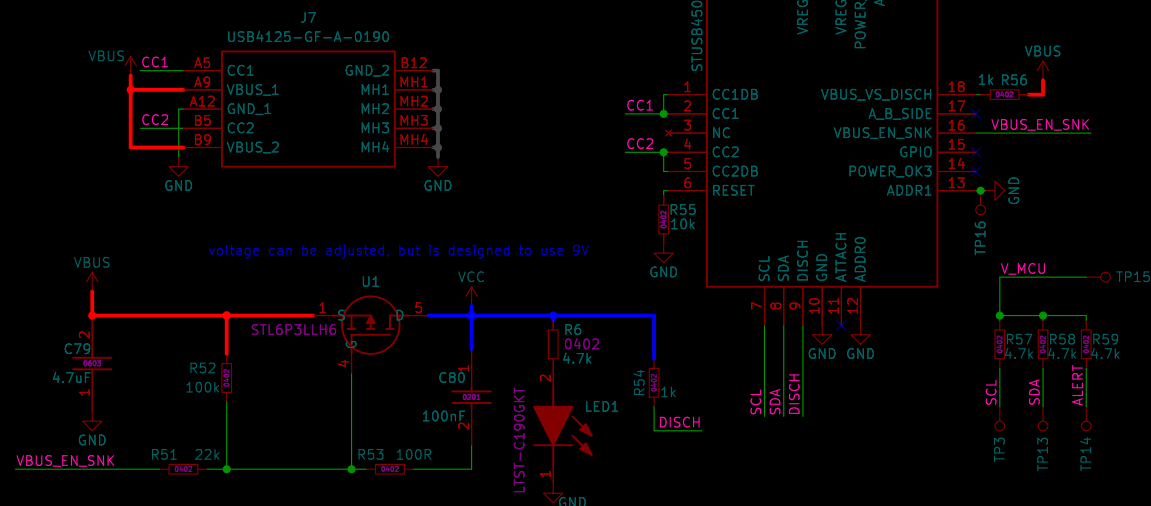
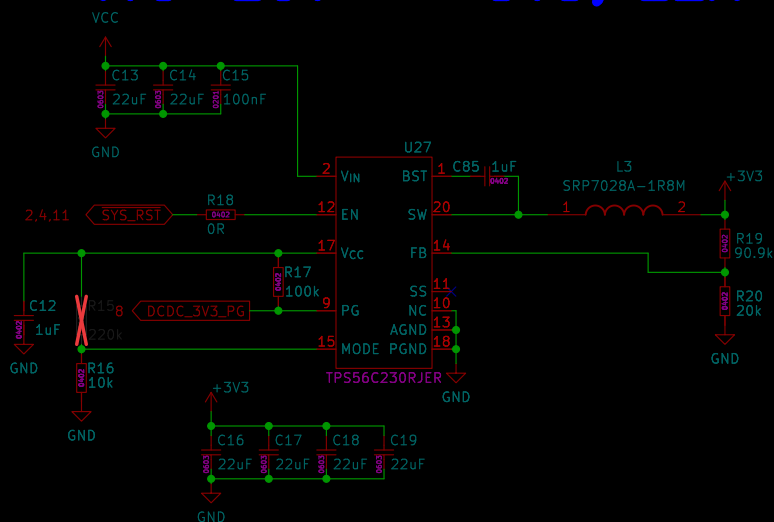
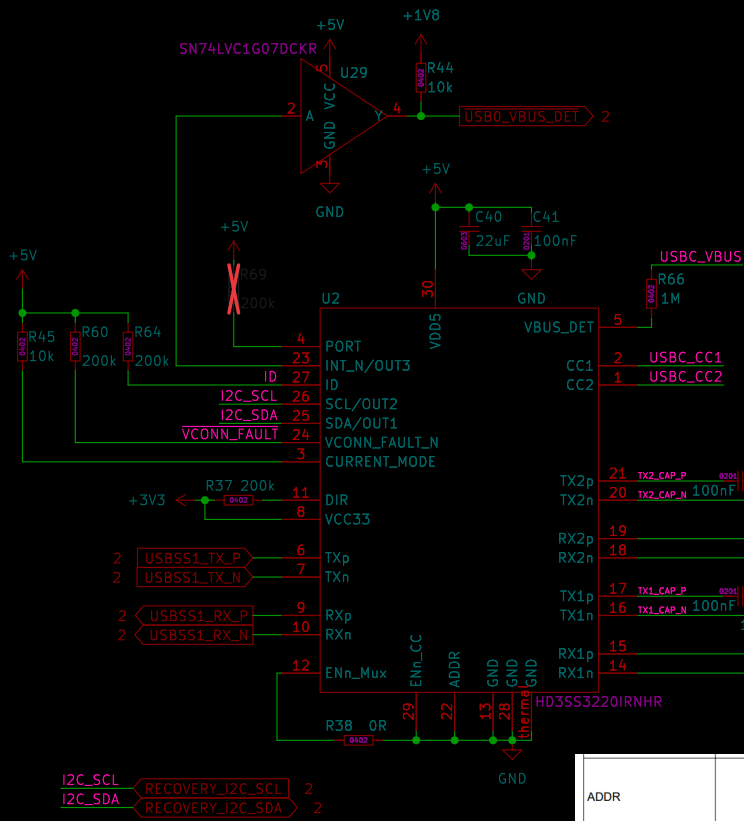


Table 1. MODE Pin Resistor Settings

Voltage on MODE	Recommended Resistor		LIGHT LOAD OPERATION
	R _{M_H} (kΩ)	R _{M_L} (kΩ)	
(0-10%)*VCC	330	15	Eco-mode
(10%-20%)*VCC	180	33	FCCM

4V5-18V => 3V3, 12A





ADDR	22	I	Tri-level input pin to indicate I ² C address or GPIO mode: H (connect to VDD5) - I ² C is enabled and I2C 7-bit address is 0x67. NC - GPIO mode (I2C is disabled) L (connect to GND) - I ² C is enabled and I2C 7-bit address is 0x47. ADDR pin should be pulled up to VDD5 if high configuration is desired
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