PIC16(L)F145X

FIGURE 3: 20-PIN PDIP, SOIC, SSOP DIAGRAM FOR PIC16(L)F1459

PDIP, SOIC, SSOP

PIC16(L)F1459

Pin	Left		Right	Pin
1	VDD		Vss	20
2	RA5		RA0/D+/ICSPDAT[1]	19
3	RA4 (AN3)		RA1/D-/ICSPCLK[1]	18
4	MCLR/VPP/RA3		VUSB3V3	17
5	RC5		RC0/ICSPDAT/AN4	16
6	RC4		RC1/ICSPCLK/AN5	15
7	RC3 (AN7)		RC2/AN6	14
8	RC6 (AN8/SS)		RB4/SDI/AN10 AN	13
9	RC7 (AN7/SDO)		RB5/AN11	12
10	RB7		RB6/SCK	11

Handwritten notes: "VUSB3V3 Output Power?", "AN = Analogue Input"
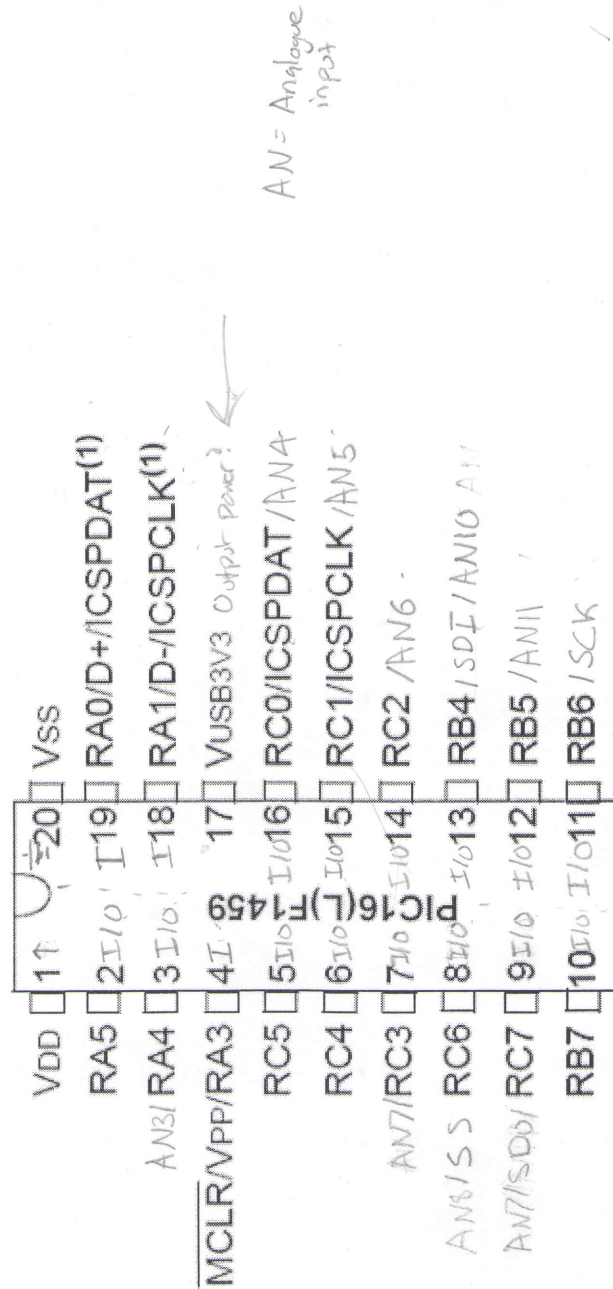
Note 1: LVP support for PIC18(L)F1XK50 legacy designs.

Note 2: See Table 3 for location of all peripheral functions.