TATICHERLA

MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS-C1

REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	CHK.
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: COVER SHEET

REF.SIG.PLAN.No. IPU.2107B

n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00001 dqy 'khVSHEETS 00228 CONT'D ON: 00002

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00001_R00.DWG

(MICROLOK-II WARM STANDBY)

SL.NO.	DESCRIPTION	SHEET NO.	REVISION
01	COVER SHEET	00001	00
02	INDEX	00002	00
03	SPARE	00003	
04	SYSTEM INITIALIZATION LOGIC	00004-00005	00
05	SERIAL LINK ENABLE DETAIN LOGIC	00006-00007	00
06	COMMUNICATION LINK STATUS CHECK LOGIC	00007	00
07	SYSTEM RESET LOGIC	00008	00
08	PANEL - PC CHANGEOVER LOGIC	00008-00010	00
09	REMINDER COLLAR LOGIC - BLOCK & UNBLOCK	00010-00011	00
10	REMINDER COLLAR LOGIC - SIGNAL BUTTON	00011-00018	00
11	REMINDER COLLAR LOGIC - ROUTE BUTTON	00018-00023	00
12	REMINDER COLLAR LOGIC - POINT,CRANK HANDLE & LEVEL CROSSING BUTTONS	00023-00026	00
13	ALL_BLK ENABLE LOGIC DURING PC FAILURE	00027-00028	00
14	ROUTE DISABLE LOGIC	00028-00033	00
15	SIGNAL BUTTON LOGIC - GNR	00034-00046	00
16	ROUTE BUTTON LOGIC - UNR	00047-00051	00
17	POINT , CRANKHANDLE & LC GATE BUTTON LOGIC	00051-00053	00
18	COMMON BUTTON LOGIC	00053-00057	00
19	SIGNAL NR LOGIC	00057-00059	00
20	RLR LOGIC	00060	00
21	ROUTE REQUEST LOGIC	00061-00075	00
22	POINT LOGIC	00076-00099	00
23	UCR LOGIC	00100-00107	00
24	TSR LOGIC	00107-00109	00
25	ASR LOGIC	00110-00135	00
26	COUNTER LOGIC	00136-00136	00
27	OVERLAP LOCK & RELEASE LOGIC	00136-00139	00
28	CRANK HANDLE & LC GATE CONTROL LOGIC	00141-00143	00
29	SIGNAL CONTROL LOGIC	00143-00156	00
30	BLOCK LOGIC	00156-00159	00
31	FCOR LOGIC	00160-00161	00
32	CRANK HANDLE & LEVEL CROSSING INDICATION LOGIC	00161-00164	00
33	ROUTE INITIATION INDICATION LOGIC	00164-00166	00
34	POINT INDICATION LOGIC	00167-00170	00
35	SIGNAL INDICATION LOGIC	00170-00176	00
36	TRACK INDICATION LOGIC	00176-00195	00
37	OVERLAP INDICATION LOGIC	00196-00197	00
38	ROUTE CANCELLATION INDICATION LOGIC	00198-00201	00
39	CALLING ON BUZZER LOGIC	00202	00
40	BUTTON HELD BUZZER & INDICATION LOGIC	00202-00203	00
41	SIGNAL FAILURE INDICATION & BUZZER LOGIC	00204	00
42	POINT FAILURE INDICATION & BUZZER LOGIC	00204-00205	00
43	DC-DC CONVERTER FAILURE BUZZER & INDICATION LOGIC	00205	00
44	POWER FAILURE BUZZER & INDICATION LOGIC	00206	00
45	REDIRECTIONAL LOGIC TO DATA LOGGER	00205-00228	00

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TITLE: INDEX

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00002 | dqy 'khVSHEETS 00228 | CONT'D ON: 00004

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

rkfVpøykZ
TATICHERLA

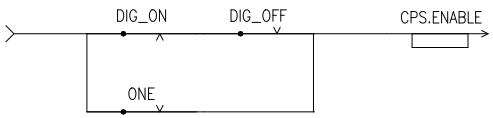
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00002_R00.DWG

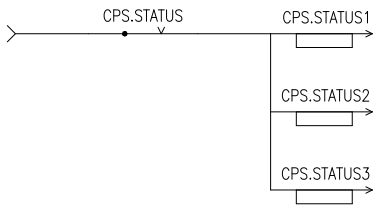
(MICROLOK-II WARM STANDBY)

ASSIGN ~CHECKADDRESS TO CHECKADDRESS;

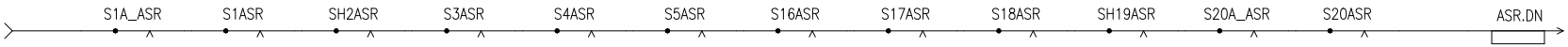
ASSIGN ~DIG_ON * DIG_OFF + ONE TO CPS.ENABLE;



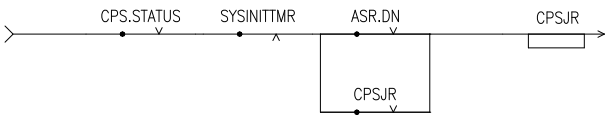
ASSIGN CPS.STATUS TO
CPS.STATUS1,CPS.STATUS2,CPS.STATUS3;



ASSIGN ~S1A_ASR * ~S1ASR * ~SH2ASR * ~S3ASR * ~S4ASR * ~S5ASR * ~S16ASR * ~S17ASR * ~S18ASR * ~SH19ASR * ~S20A_ASR * ~S20ASR TO ASR.DN;

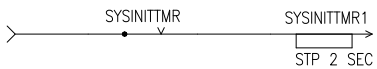


ASSIGN CPS.STATUS * ~SYSINITTMR * (ASR.DN + CPSJR) TO CPSJR;

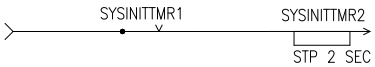


ASSIGN CPS.STATUS TO SYSINITTMR;

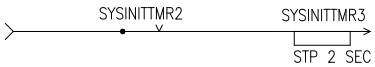
ASSIGN SYSINITTMR TO SYSINITTMR1;



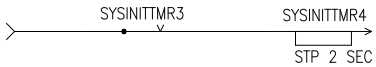
ASSIGN SYSINITTMR1 TO SYSINITTMR2;



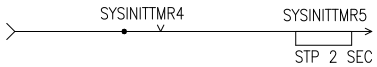
ASSIGN SYSINITTMR2 TO SYSINITTMR3;



ASSIGN SYSINITTMR3 TO SYSINITTMR4;



ASSIGN SYSINITTMR4 TO SYSINITTMR5;



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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khVSHEET 00004 | dqy 'khVSHEETS 00228 | CONT'D ON: 00005

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

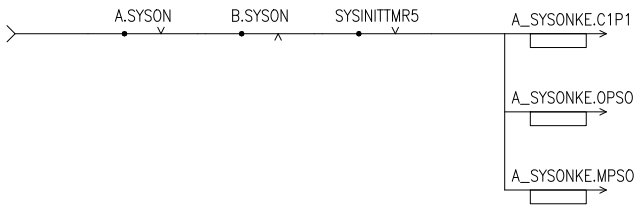
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

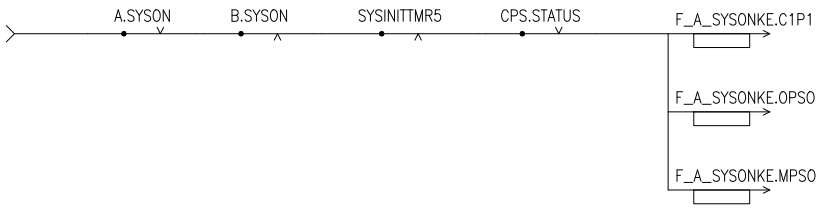
CAD FILE: 512_TAC_C1_00004_R00.DWG

(MICROLOK-II WARM STANDBY)

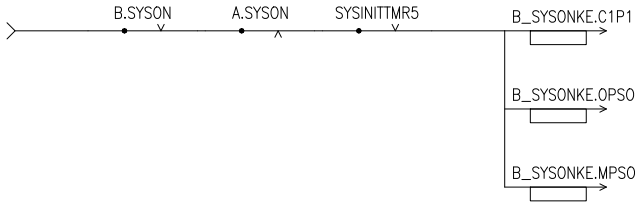
ASSIGN A.SYSON * ~B.SYSON * SYSINITTMR5 TO
A_SYSONKE.C1P1,A_SYSONKE.OPSO,A_SYSONKE.MPSO;



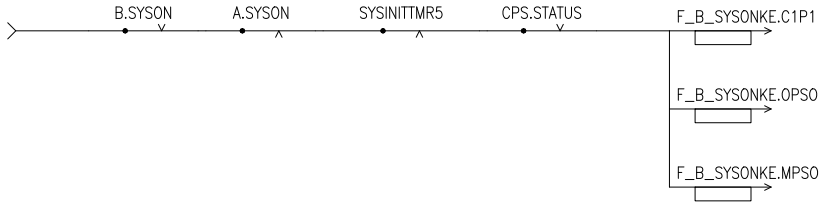
ASSIGN A.SYSON * ~B.SYSON * ~SYSINITTMR5 * CPS.STATUS TO
F_A_SYSONKE.C1P1,F_A_SYSONKE.OPSO,F_A_SYSONKE.MPSO;



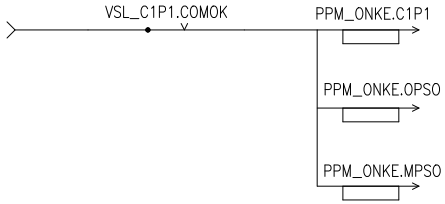
ASSIGN B.SYSON * ~A.SYSON * SYSINITTMR5 TO
B_SYSONKE.C1P1,B_SYSONKE.OPSO,B_SYSONKE.MPSO;



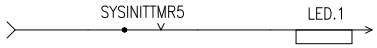
ASSIGN B.SYSON * ~A.SYSON * ~SYSINITTMR5 * CPS.STATUS TO
F_B_SYSONKE.C1P1,F_B_SYSONKE.OPSO,F_B_SYSONKE.MPSO;



ASSIGN VSL_C1P1.COMOK TO
PPM_ONKE.C1P1,PPM_ONKE.OPSO,PPM_ONKE.MPSO;



NV.ASSIGN SYSINITTMR5 TO LED.1;



NV.ASSIGN VSL_C1P1.COMOK TO LED.4;

NV.ASSIGN VSL_C1OP.COMOK TO LED.5;

NV.ASSIGN VSL_C1MP.COMOK TO LED.6;

NV.ASSIGN NVSL_C1DL.COMOK TO LED.8;

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CHK.

APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00005 | dqy 'khVSHEETS 00228 | CONT'D ON: 00006

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

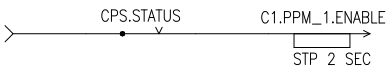
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

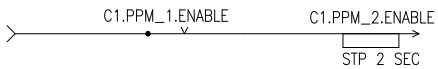
CAD FILE: 512_TAC_C1_00005_R00.DWG

(MICROLOK-II WARM STANDBY)

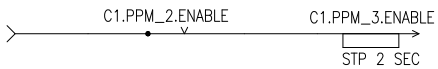
ASSIGN CPS.STATUS TO C1.PPM_1.ENABLE;



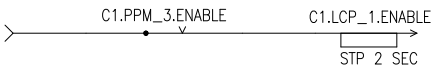
ASSIGN C1.PPM_1.ENABLE TO C1.PPM_2.ENABLE;



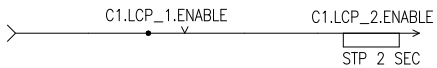
ASSIGN C1.PPM_2.ENABLE TO C1.PPM_3.ENABLE;



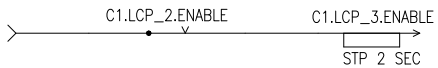
ASSIGN C1.PPM_3.ENABLE TO C1.LCP_1.ENABLE;



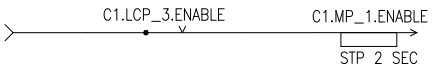
ASSIGN C1.LCP_1.ENABLE TO C1.LCP_2.ENABLE;



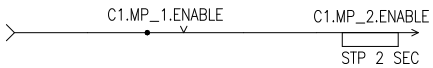
ASSIGN C1.LCP_2.ENABLE TO C1.LCP_3.ENABLE;



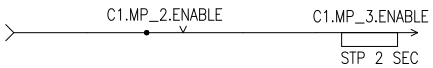
ASSIGN C1.LCP_3.ENABLE TO C1.MP_1.ENABLE;



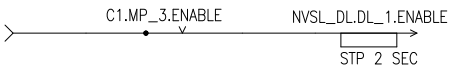
ASSIGN C1.MP_1.ENABLE TO C1.MP_2.ENABLE;



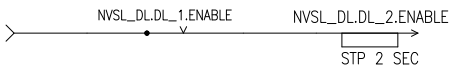
ASSIGN C1.MP_2.ENABLE TO C1.MP_3.ENABLE;



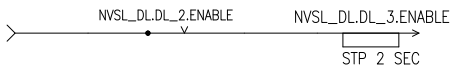
ASSIGN C1.MP_3.ENABLE TO NVSL_DL.DL_1.ENABLE;



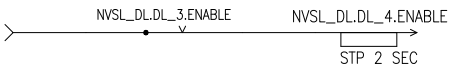
ASSIGN NVSL_DL.DL_1.ENABLE TO NVSL_DL.DL_2.ENABLE;



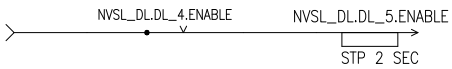
ASSIGN NVSL_DL.DL_2.ENABLE TO NVSL_DL.DL_3.ENABLE;



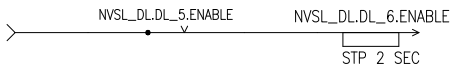
ASSIGN NVSL_DL.DL_3.ENABLE TO NVSL_DL.DL_4.ENABLE;



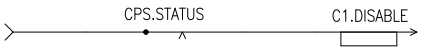
ASSIGN NVSL_DL.DL_4.ENABLE TO NVSL_DL.DL_5.ENABLE;



ASSIGN NVSL_DL.DL_5.ENABLE TO NVSL_DL.DL_6.ENABLE;



ASSIGN ~CPS.STATUS TO C1.DISABLE;



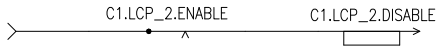
ASSIGN ~C1.PPM_1.ENABLE TO C1.PPM_1.DISABLE;

ASSIGN ~C1.PPM_2.ENABLE TO C1.PPM_2.DISABLE;

ASSIGN ~C1.PPM_3.ENABLE TO C1.PPM_3.DISABLE;

ASSIGN ~C1.LCP_1.ENABLE TO C1.LCP_1.DISABLE;

ASSIGN ~C1.LCP_2.ENABLE TO C1.LCP_2.DISABLE;



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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	a fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON: 00007
00006	00228	

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00006_R00.DWG

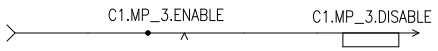
(MICROLOK-II WARM STANDBY)

ASSIGN ~C1.LCP_3.ENABLE TO C1.LCP_3.DISABLE;

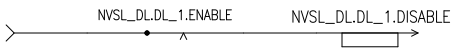
ASSIGN ~C1.MP_1.ENABLE TO C1.MP_1.DISABLE;

ASSIGN ~C1.MP_2.ENABLE TO C1.MP_2.DISABLE;

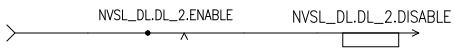
ASSIGN ~C1.MP_3.ENABLE TO C1.MP_3.DISABLE;



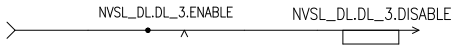
ASSIGN ~NVSL_DL.DL_1.ENABLE TO NVSL_DL.DL_1.DISABLE;



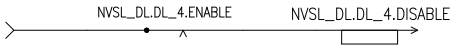
ASSIGN ~NVSL_DL.DL_2.ENABLE TO NVSL_DL.DL_2.DISABLE;



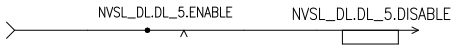
ASSIGN ~NVSL_DL.DL_3.ENABLE TO NVSL_DL.DL_3.DISABLE;



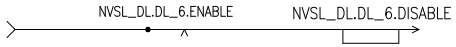
ASSIGN ~NVSL_DL.DL_4.ENABLE TO NVSL_DL.DL_4.DISABLE;



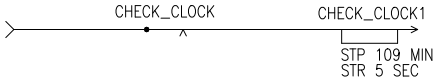
ASSIGN ~NVSL_DL.DL_5.ENABLE TO NVSL_DL.DL_5.DISABLE;



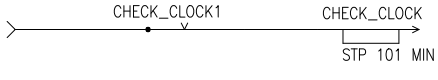
ASSIGN ~NVSL_DL.DL_6.ENABLE TO NVSL_DL.DL_6.DISABLE;



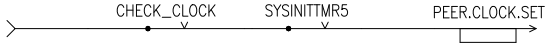
ASSIGN ~CHECK_CLOCK TO CHECK_CLOCK1;



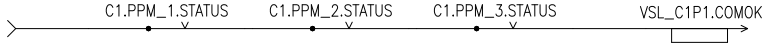
ASSIGN CHECK_CLOCK1 TO CHECK_CLOCK;



ASSIGN CHECK_CLOCK * SYSINITTMR5 TO PEER.CLOCK.SET;

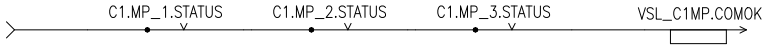


ASSIGN C1.PPM_1.STATUS * C1.PPM_2.STATUS * C1.PPM_3.STATUS TO VSL_C1P1.COMOK;

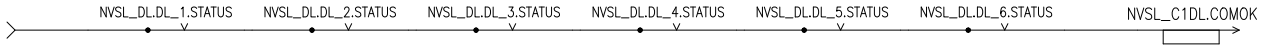


ASSIGN C1.LCP_1.STATUS * C1.LCP_2.STATUS * C1.LCP_3.STATUS TO VSL_C1OP.COMOK;

ASSIGN C1.MP_1.STATUS * C1.MP_2.STATUS * C1.MP_3.STATUS TO VSL_C1MP.COMOK;



ASSIGN NVSL_DL.DL_1.STATUS * NVSL_DL.DL_2.STATUS * NVSL_DL.DL_3.STATUS * NVSL_DL.DL_4.STATUS * NVSL_DL.DL_5.STATUS * NVSL_DL.DL_6.STATUS TO NVSL_C1DL.COMOK;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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JE-1	
SE/SSE	
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CHK.

APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khVSHEET 00007 | dqy 'khVSHEETS 00228 | CONT'D ON: 00008

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

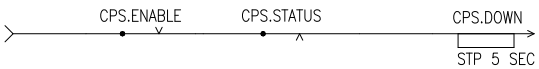
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00007_R00.DWG

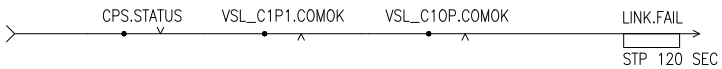
(MICROLOK-II WARM STANDBY)

ASSIGN CPS.ENABLE * ~CPS.STATUS TO CPS.DOWN;

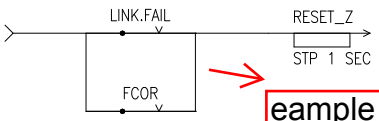


ASSIGN CPS.DOWN TO CPS.RESET;

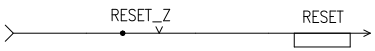
ASSIGN CPS.STATUS * ~VSL_C1P1.COMOK * ~VSL_C10P.COMOK TO LINK.FAIL;



ASSIGN LINK.FAIL + FCOR TO RESET_Z;

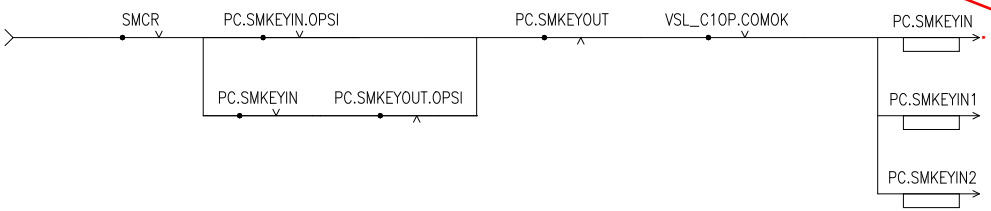


ASSIGN RESET_Z TO RESET;



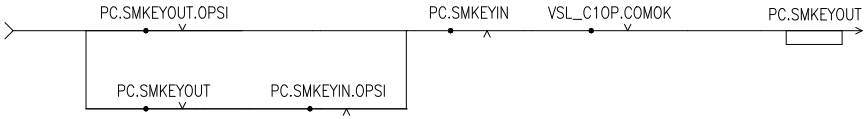
eample No.3

ASSIGN SMCR * (PC.SMKEYIN.OPSI + PC.SMKEYIN * ~PC.SMKEYOUT.OPSI) * ~PC.SMKEYOUT * VSL_C10P.COMOK TO PC.SMKEYIN,PC.SMKEYIN1,PC.SMKEYIN2;

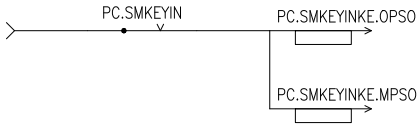


EXample NO.1

ASSIGN (PC.SMKEYOUT.OPSI + PC.SMKEYOUT * ~PC.SMKEYIN.OPSI) * ~PC.SMKEYIN * VSL_C10P.COMOK TO PC.SMKEYOUT;



ASSIGN PC.SMKEYIN TO PC.SMKEYINKE.OPSO,PC.SMKEYINKE.MPSO;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	

CHK.

APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khVSHEET 00008 | dqy 'khVSHEETS 00228 | CONT'D ON: 00009

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

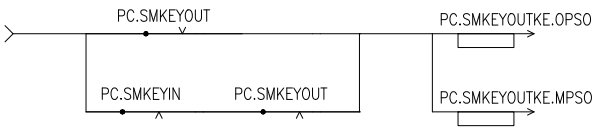
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

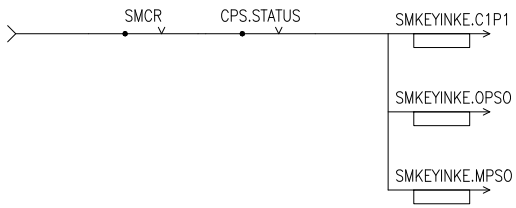
CAD FILE: 512_TAC_C1_00008_R00.DWG

(MICROLOK-II WARM STANDBY)

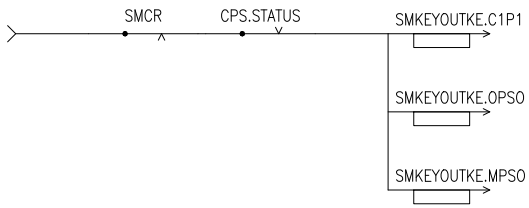
ASSIGN (PC.SMKEYOUT + ~PC.SMKEYIN * ~PC.SMKEYOUT) TO
PC.SMKEYOUTKE.OPSO,PC.SMKEYOUTKE.MPSO;



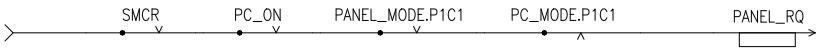
ASSIGN SMCR * CPS.STATUS TO
SMKEYINKE.C1P1,SMKEYINKE.OPSO,SMKEYINKE.MPSO;



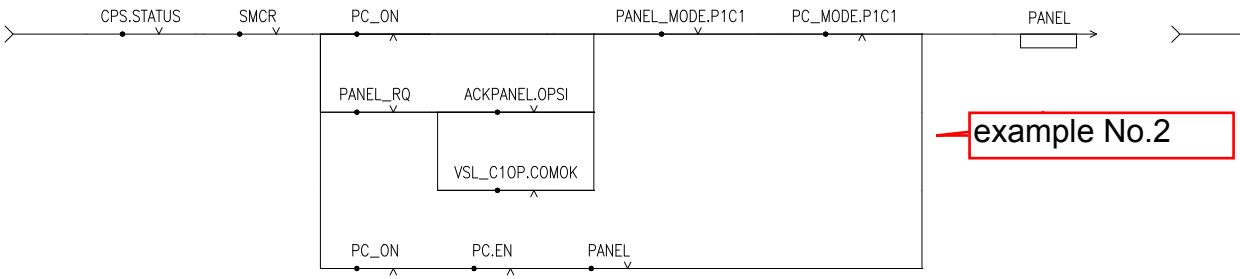
ASSIGN ~SMCR * CPS.STATUS TO
SMKEYOUTKE.C1P1,SMKEYOUTKE.OPSO,SMKEYOUTKE.MPSO;



ASSIGN SMCR * PC_ON * PANEL_MODE.P1C1 * ~PC_MODE.P1C1 TO PANEL_RQ;

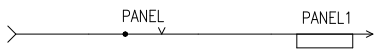


ASSIGN CPS.STATUS * SMCR * ((~PC_ON + PANEL_RQ * (ACKPANEL.OPSI + ~VSL_C1OP.COMOK)) * PANEL_MODE.P1C1 * ~PC_MODE.P1C1 + ~PC_ON
* ~PC.EN * PANEL) TO PANEL;

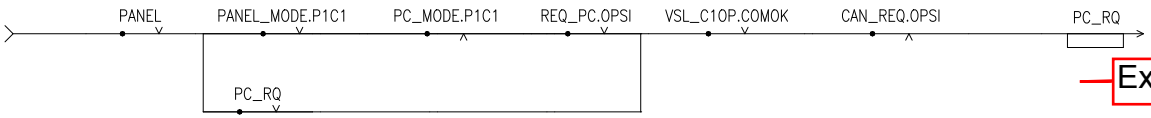


example No.2

ASSIGN PANEL TO PANEL1;



ASSIGN PANEL * (PANEL_MODE.P1C1 * ~PC_MODE.P1C1 * REQ_PC.OPSI + PC_RQ) * VSL_C1OP.COMOK * ~CAN_REQ.OPSI TO PC_RQ;



Example No.4

REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B | 'khVSHEET 00009 | dqy 'khVSHEETS 00228 | CONT'D ON: 00010

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

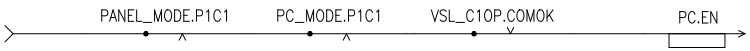
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

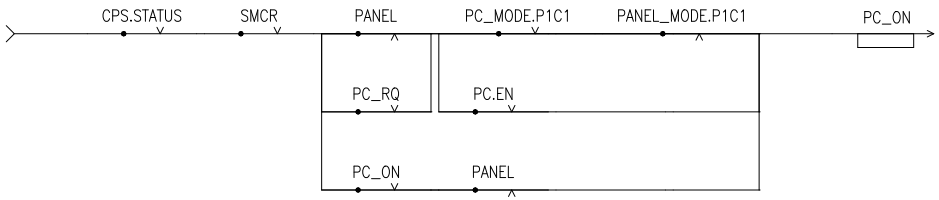
(MICROLOK-II WARM STANDBY)

CAD FILE: 512_TAC_C1_00009_R00.DWG

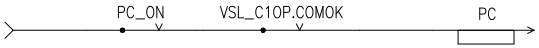
ASSIGN ~PANEL_MODE.P1C1 * ~PC_MODE.P1C1 * VSL_C1OP.COMOK TO PC.EN;



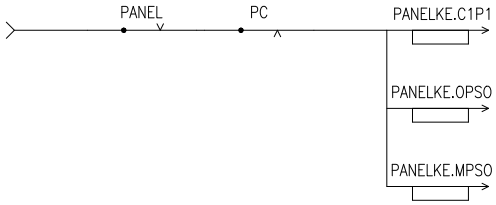
ASSIGN CPS.STATUS * SMCR * ((~PANEL + PC_RQ) * (PC_MODE.P1C1 * ~PANEL_MODE.P1C1 + PC.EN) + PC_ON * ~PANEL) TO PC_ON;



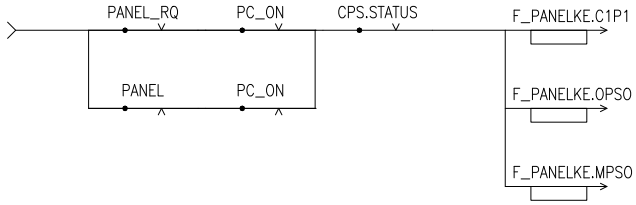
ASSIGN PC_ON * VSL_C1OP.COMOK TO PC;



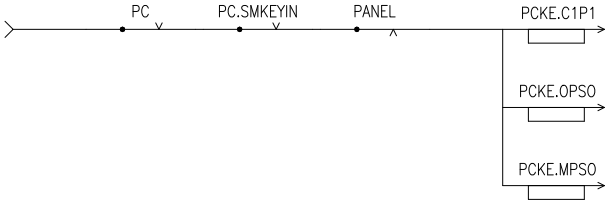
ASSIGN PANEL * ~PC TO PANELKE.C1P1,PANELKE.OPSO,PANELKE.MPSO;



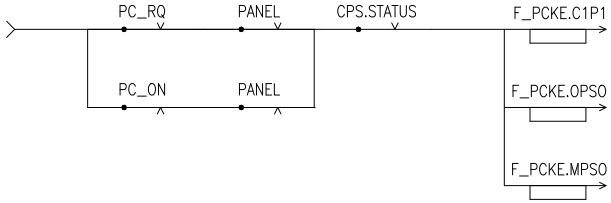
ASSIGN (PANEL_RQ * PC_ON + ~PANEL * ~PC_ON) * CPS.STATUS TO F_PANELKE.C1P1,F_PANELKE.OPSO,F_PANELKE.MPSO;



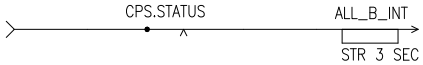
ASSIGN PC * PC.SMKEYIN * ~PANEL TO PCKE.C1P1,PCKE.OPSO,PCKE.MPSO;



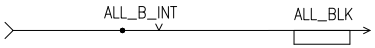
ASSIGN (PC_RQ * PANEL + ~PC_ON * ~PANEL) * CPS.STATUS TO F_PCKE.C1P1,F_PCKE.OPSO,F_PCKE.MPSO;



ASSIGN ~ CPS.STATUS TO ALL_B_INT;



ASSIGN ALL_B_INT TO ALL_BLK;



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Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

	REVISION
CHK.	JE-1
	SE/SSE
	ASTE
	SSTE
APP.	f CSTE

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00010	00228	00011

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

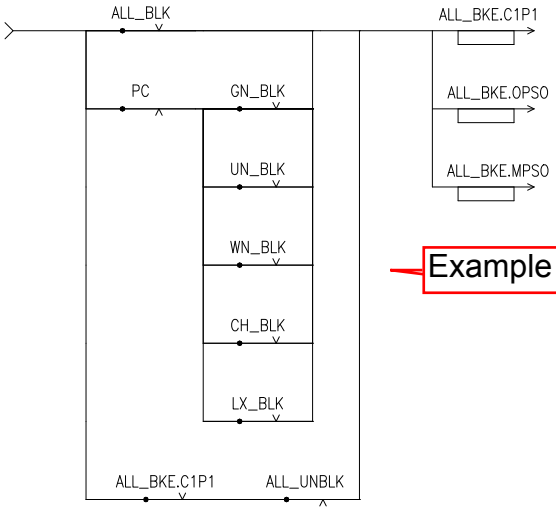
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00010_R00.DWG

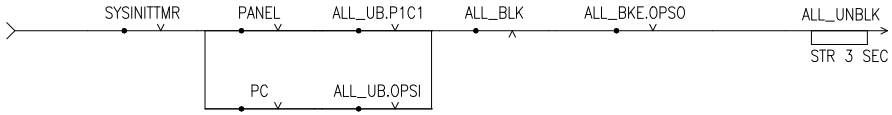
(MICROLOK-II WARM STANDBY)

ASSIGN ALL_BLK + ~PC * (GN_BLK + UN_BLK + WN_BLK + CH_BLK + LX_BLK) + ALL_BKE.C1P1 * ~ALL_UNBLK TO ALL_BKE.C1P1,ALL_BKE.OPSO,ALL_BKE.MPSO;

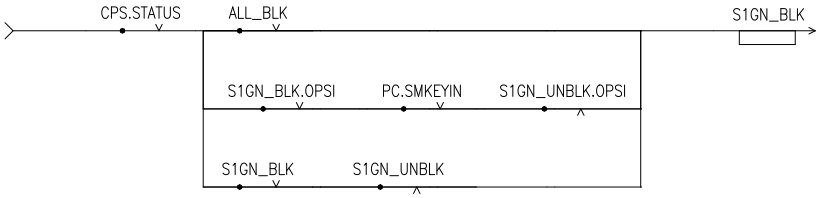


Example No.5

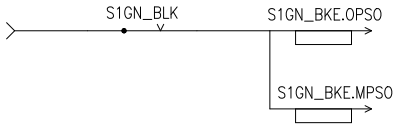
ASSIGN SYSINITTMR * (PANEL * ALL_UB.P1C1 + PC * ALL_UB.OPSI) * ~ALL_BLK * ALL_BKE.OPSO TO ALL_UNBLK;



ASSIGN CPS.STATUS * (ALL_BLK + S1GN_BLK.OPSI * PC.SMKEYIN * ~S1GN_UNBLK.OPSI + S1GN_BLK * ~S1GN_UNBLK) TO S1GN_BLK;



ASSIGN S1GN_BLK TO S1GN_BKE.OPSO,S1GN_BKE.MPSO;



example No.6

REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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f CSTE	

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APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khVSHEET 00011	dqy 'khVSHEETS 00228	CONT'D ON: 00012

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

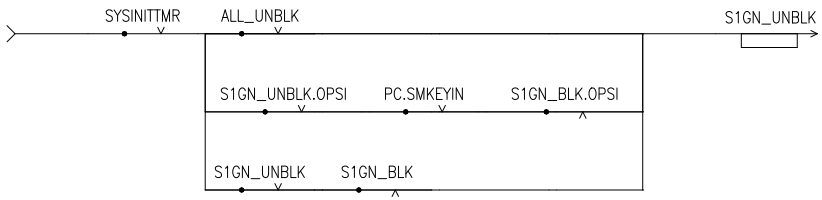
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

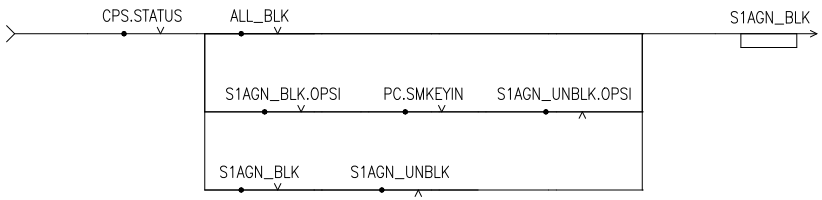
CAD FILE: 512_TAC_C1_00011_R00.DWG

(MICROLOK-II WARM STANDBY)

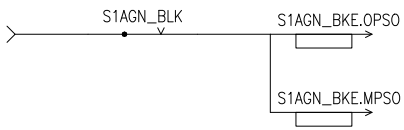
ASSIGN SYSINITTMR * (ALL_UNBLK + S1GN_UNBLK.OPSI * PC.SMKEYIN * ~S1GN_BLK.OPSI + S1GN_UNBLK * ~S1GN_BLK) TO S1GN_UNBLK;



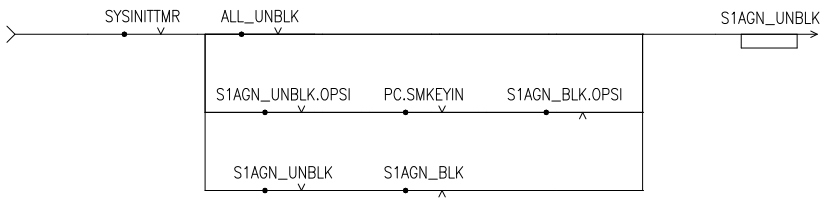
ASSIGN CPS.STATUS * (ALL_BLK + S1AGN_BLK.OPSI * PC.SMKEYIN * ~S1AGN_UNBLK.OPSI + S1AGN_BLK * ~S1AGN_UNBLK) TO S1AGN_BLK;



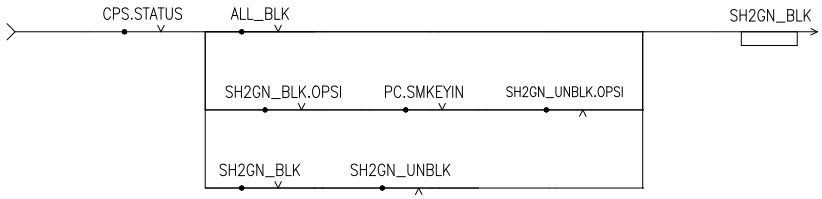
ASSIGN S1AGN_BLK TO S1AGN_BKE.OPSO,S1AGN_BKE.MPSO;



ASSIGN SYSINITTMR * (ALL_UNBLK + S1AGN_UNBLK.OPSI * PC.SMKEYIN * ~S1AGN_BLK.OPSI + S1AGN_UNBLK * ~S1AGN_BLK) TO S1AGN_UNBLK;



ASSIGN CPS.STATUS * (ALL_BLK + SH2GN_BLK.OPSI * PC.SMKEYIN * ~SH2GN_UNBLK.OPSI + SH2GN_BLK * ~SH2GN_UNBLK) TO SH2GN_BLK;



ASSIGN SH2GN_BLK TO SH2GN_BKE.OPSO,SH2GN_BKE.MPSO;

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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET 00012	dqy 'khVSHEETS 00228	CONT'D ON: 00013

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

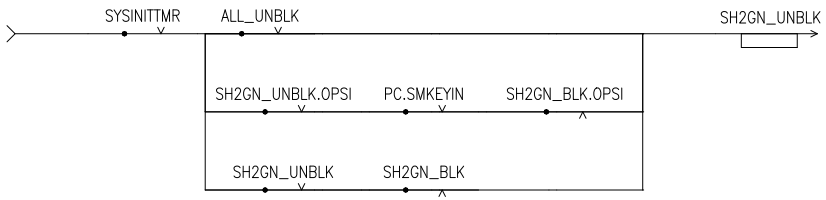
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

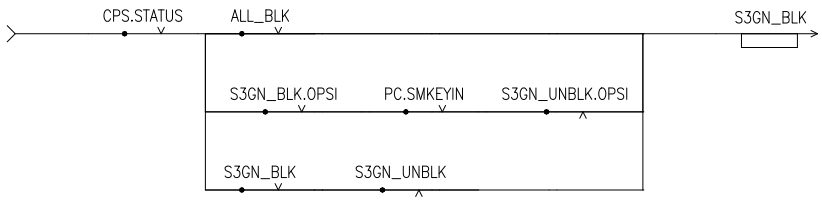
CAD FILE: 512_TAC_C1_00012_R00.DWG

(MICROLOK-II WARM STANDBY)

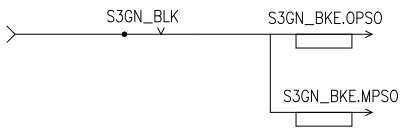
ASSIGN SYSINITTMR * (ALL_UNBLK + SH2GN_UNBLK.OPSI * PC.SMKEYIN * ~SH2GN_BLK.OPSI + SH2GN_UNBLK * ~SH2GN_BLK) TO SH2GN_UNBLK;



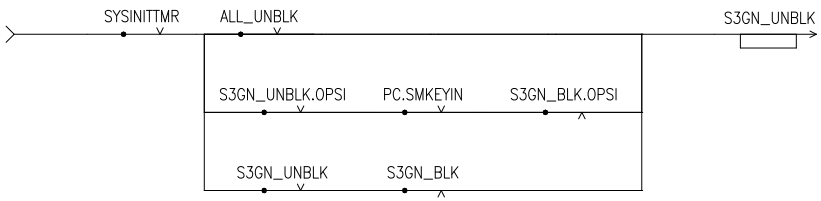
ASSIGN CPS.STATUS * (ALL_BLK + S3GN_BLK.OPSI * PC.SMKEYIN * ~S3GN_UNBLK.OPSI + S3GN_BLK * ~S3GN_UNBLK) TO S3GN_BLK;



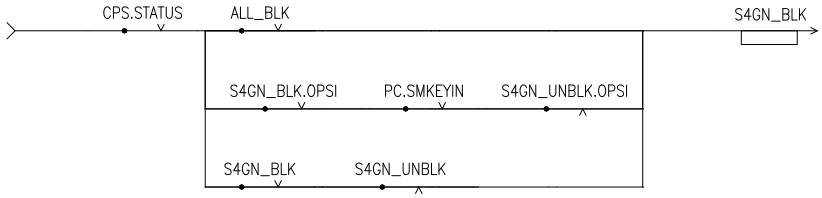
ASSIGN S3GN_BLK TO S3GN_BKE.OPSO,S3GN_BKE.MPSO;



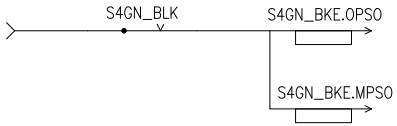
ASSIGN SYSINITTMR * (ALL_UNBLK + S3GN_UNBLK.OPSI * PC.SMKEYIN * ~S3GN_BLK.OPSI + S3GN_UNBLK * ~S3GN_BLK) TO S3GN_UNBLK;



ASSIGN CPS.STATUS * (ALL_BLK + S4GN_BLK.OPSI * PC.SMKEYIN * ~S4GN_UNBLK.OPSI + S4GN_BLK * ~S4GN_UNBLK) TO S4GN_BLK;



ASSIGN S4GN_BLK TO S4GN_BKE.OPSO,S4GN_BKE.MPSO;



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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00013 | dqy 'khVSHEETS 00228 | CONT'D ON: 00014

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

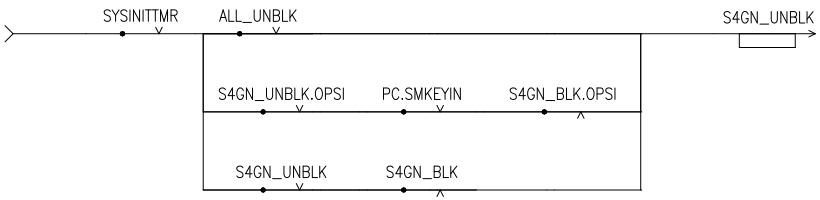
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

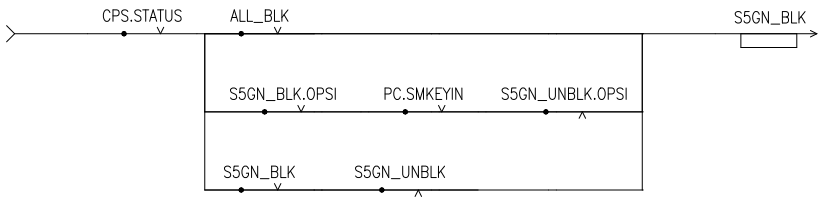
CAD FILE: 512_TAC_C1_00013_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN SYSINITTMR * (ALL_UNBLK + S4GN_UNBLK.OPSI * PC.SMKEYIN * ~S4GN_BLK.OPSI + S4GN_UNBLK * ~S4GN_BLK) TO S4GN_UNBLK;

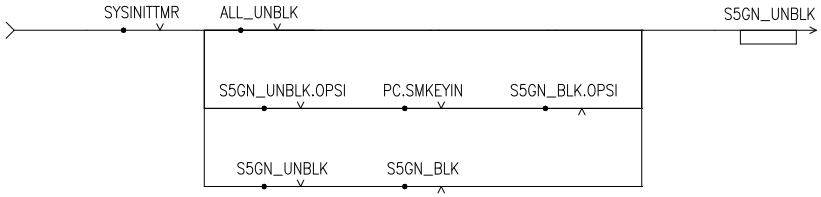


ASSIGN CPS.STATUS * (ALL_BLK + S5GN_BLK.OPSI * PC.SMKEYIN * ~S5GN_UNBLK.OPSI + S5GN_BLK * ~S5GN_UNBLK) TO S5GN_BLK;

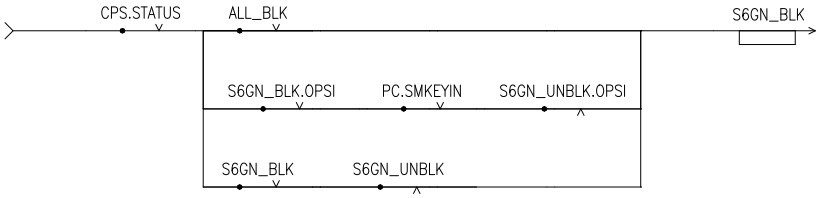


ASSIGN S5GN_BLK TO S5GN_BKE.OPSO,S5GN_BKE.MPSO;

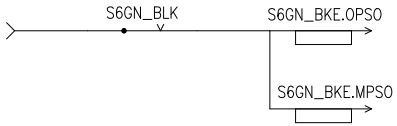
ASSIGN SYSINITTMR * (ALL_UNBLK + S5GN_UNBLK.OPSI * PC.SMKEYIN * ~S5GN_BLK.OPSI + S5GN_UNBLK * ~S5GN_BLK) TO S5GN_UNBLK;



ASSIGN CPS.STATUS * (ALL_BLK + S6GN_BLK.OPSI * PC.SMKEYIN * ~S6GN_UNBLK.OPSI + S6GN_BLK * ~S6GN_UNBLK) TO S6GN_BLK;



ASSIGN S6GN_BLK TO S6GN_BKE.OPSO,S6GN_BKE.MPSO;



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APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	a fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00014	00228	00015

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

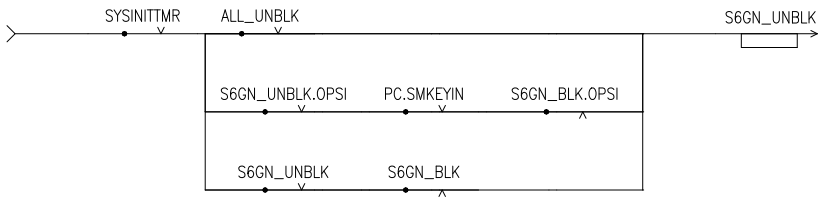
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

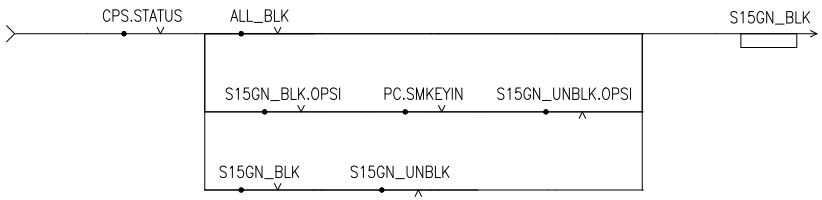
CAD FILE: 512_TAC_C1_00014_R00.DWG

(MICROLOK-II WARM STANDBY)

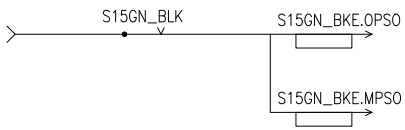
ASSIGN SYSINITTMR * (ALL_UNBLK + S6GN_UNBLK.OPSI * PC.SMKEYIN * ~S6GN_BLK.OPSI + S6GN_UNBLK * ~S6GN_BLK) TO S6GN_UNBLK;



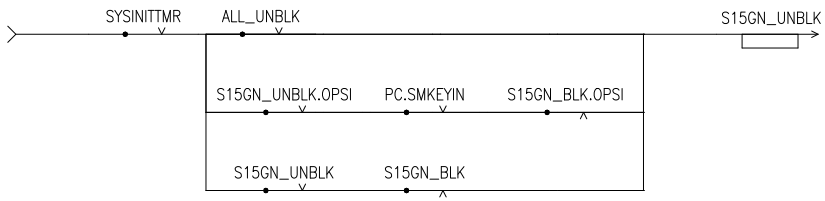
ASSIGN CPS.STATUS * (ALL_BLK + S15GN_BLK.OPSI * PC.SMKEYIN * ~S15GN_UNBLK.OPSI + S15GN_BLK * ~S15GN_UNBLK) TO S15GN_BLK;



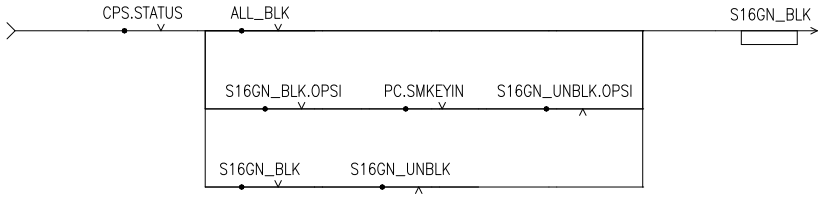
ASSIGN S15GN_BLK TO S15GN_BKE.OPSO,S15GN_BKE.MPSO;



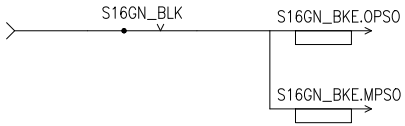
ASSIGN SYSINITTMR * (ALL_UNBLK + S15GN_UNBLK.OPSI * PC.SMKEYIN * ~S15GN_BLK.OPSI + S15GN_UNBLK * ~S15GN_BLK) TO S15GN_UNBLK;



ASSIGN CPS.STATUS * (ALL_BLK + S16GN_BLK.OPSI * PC.SMKEYIN * ~S16GN_UNBLK.OPSI + S16GN_BLK * ~S16GN_UNBLK) TO S16GN_BLK;



ASSIGN S16GN_BLK TO S16GN_BKE.OPSO,S16GN_BKE.MPSO;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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JE-1	
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CHK.

APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00015 | dqy 'khVSHEETS 00228 | CONT'D ON: 00016

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

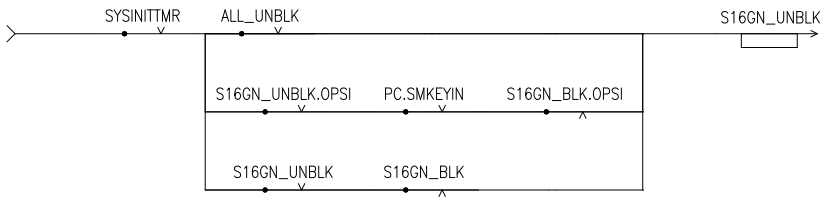
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

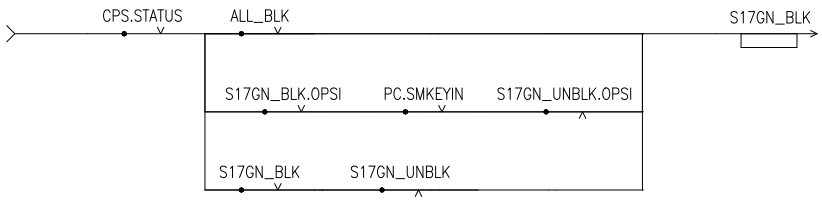
CAD FILE: 512_TAC_C1_00015_R00.DWG

(MICROLOK-II WARM STANDBY)

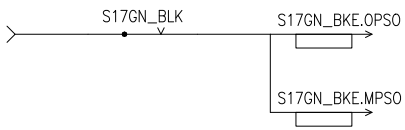
ASSIGN SYSINITTMR * (ALL_UNBLK + S16GN_UNBLK.OPSI * PC.SMKEYIN * ~S16GN_BLK.OPSI + S16GN_UNBLK * ~S16GN_BLK) TO S16GN_UNBLK;



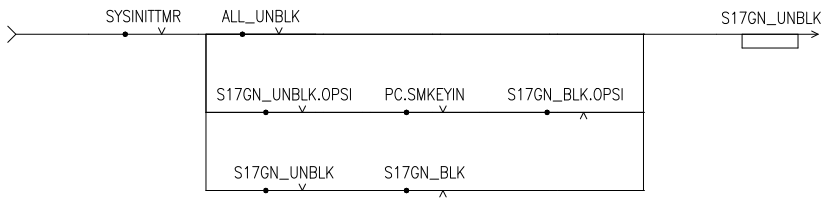
ASSIGN CPS.STATUS * (ALL_BLK + S17GN_BLK.OPSI * PC.SMKEYIN * ~S17GN_UNBLK.OPSI + S17GN_BLK * ~S17GN_UNBLK) TO S17GN_BLK;



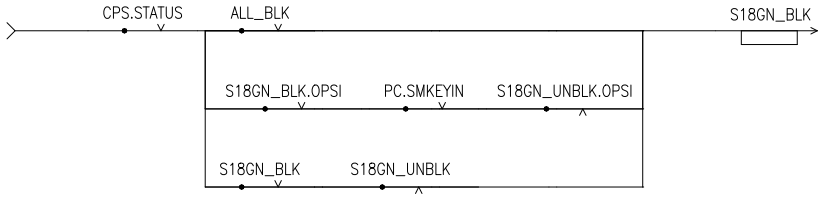
ASSIGN S17GN_BLK TO S17GN_BKE.OPSO,S17GN_BKE.MPSO;



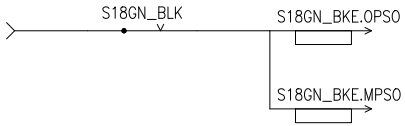
ASSIGN SYSINITTMR * (ALL_UNBLK + S17GN_UNBLK.OPSI * PC.SMKEYIN * ~S17GN_BLK.OPSI + S17GN_UNBLK * ~S17GN_BLK) TO S17GN_UNBLK;



ASSIGN CPS.STATUS * (ALL_BLK + S18GN_BLK.OPSI * PC.SMKEYIN * ~S18GN_UNBLK.OPSI + S18GN_BLK * ~S18GN_UNBLK) TO S18GN_BLK;



ASSIGN S18GN_BLK TO S18GN_BKE.OPSO,S18GN_BKE.MPSO;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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SE/SSE	
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CHK.

APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00016 | dqy 'khVSHEETS 00228 | CONT'D ON: 00017

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

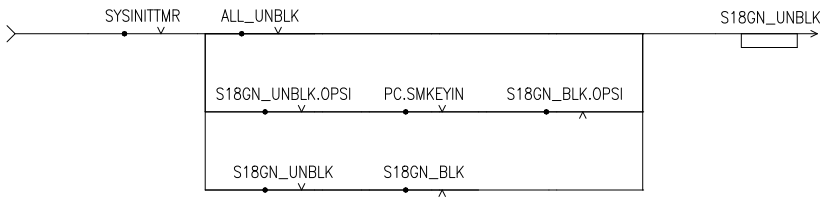
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

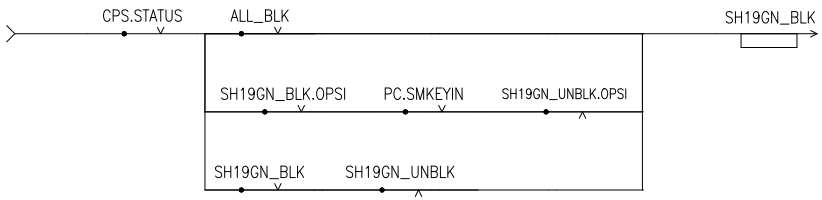
CAD FILE: 512_TAC_C1_00016_R00.DWG

(MICROLOK-II WARM STANDBY)

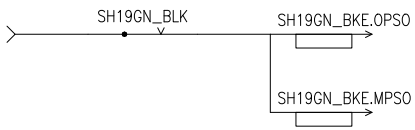
ASSIGN SYSINITTMR * (ALL_UNBLK + S18GN_UNBLK.OPSI * PC.SMKEYIN * ~S18GN_BLK.OPSI + S18GN_UNBLK * ~S18GN_BLK) TO S18GN_UNBLK;



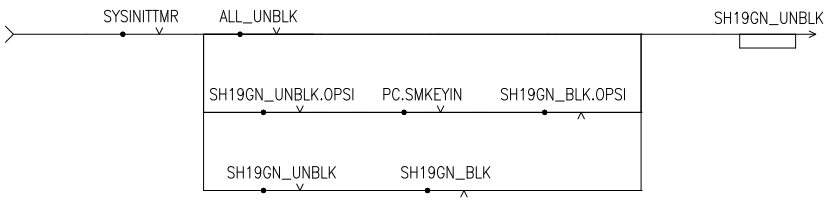
ASSIGN CPS.STATUS * (ALL_BLK + SH19GN_BLK.OPSI * PC.SMKEYIN * ~SH19GN_UNBLK.OPSI + SH19GN_BLK * ~SH19GN_UNBLK) TO SH19GN_BLK;



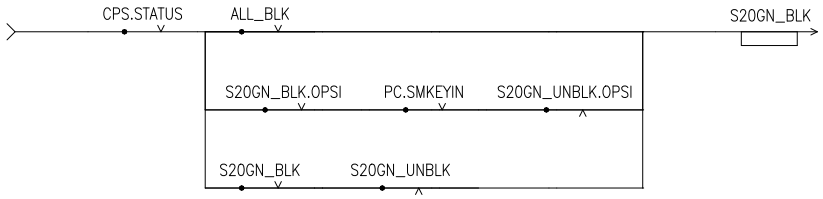
ASSIGN SH19GN_BLK TO SH19GN_BKE.OPSO,SH19GN_BKE.MPSO;



ASSIGN SYSINITTMR * (ALL_UNBLK + SH19GN_UNBLK.OPSI * PC.SMKEYIN * ~SH19GN_BLK.OPSI + SH19GN_UNBLK * ~SH19GN_BLK) TO SH19GN_UNBLK;



ASSIGN CPS.STATUS * (ALL_BLK + S20GN_BLK.OPSI * PC.SMKEYIN * ~S20GN_UNBLK.OPSI + S20GN_BLK * ~S20GN_UNBLK) TO S20GN_BLK;



ASSIGN S20GN_BLK TO S20GN_BKE.OPSO,S20GN_BKE.MPSO;

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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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CHK.

APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00017 | dqy 'khVSHEETS 00228 | CONT'D ON: 00018

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

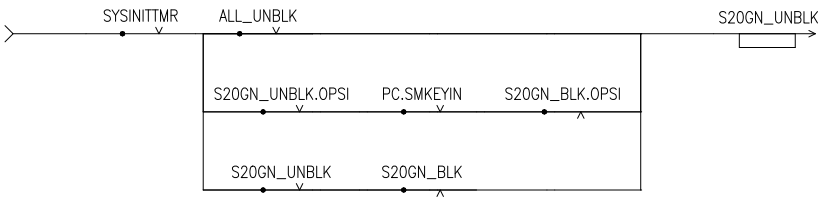
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

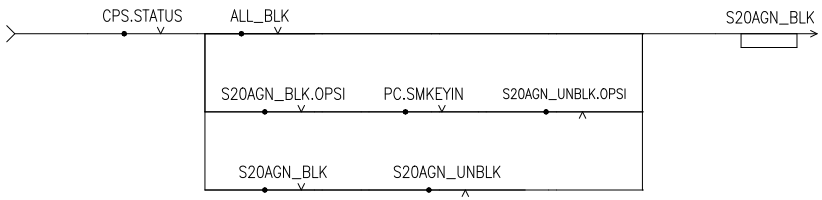
CAD FILE: 512_TAC_C1_00017_R00.DWG

(MICROLOK-II WARM STANDBY)

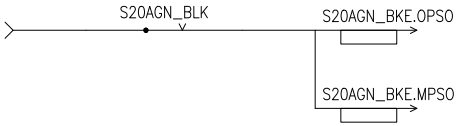
ASSIGN SYSINITTMR * (ALL_UNBLK + S20GN_UNBLK.OPSI * PC.SMKEYIN * ~S20GN_BLK.OPSI + S20GN_UNBLK * ~S20GN_BLK) TO S20GN_UNBLK;



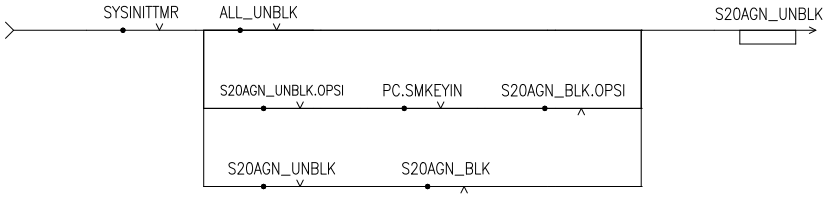
ASSIGN CPS.STATUS * (ALL_BLK + S20AGN_BLK.OPSI * PC.SMKEYIN * ~S20AGN_UNBLK.OPSI + S20AGN_BLK * ~S20AGN_UNBLK) TO S20AGN_BLK;



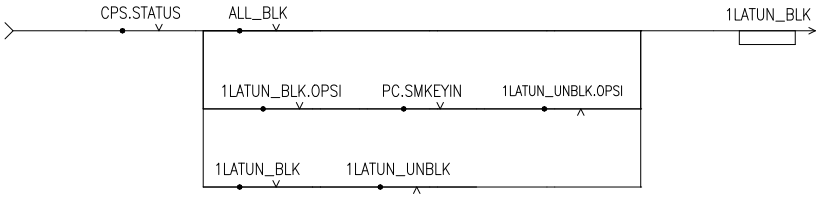
ASSIGN S20AGN_BLK TO S20AGN_BKE.OPSO,S20AGN_BKE.MPSO;



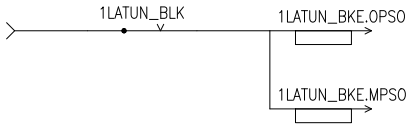
ASSIGN SYSINITTMR * (ALL_UNBLK + S20AGN_UNBLK.OPSI * PC.SMKEYIN * ~S20AGN_BLK.OPSI + S20AGN_UNBLK * ~S20AGN_BLK) TO S20AGN_UNBLK;



ASSIGN CPS.STATUS * (ALL_BLK + 1LATUN_BLK.OPSI * PC.SMKEYIN * ~1LATUN_UNBLK.OPSI + 1LATUN_BLK * ~1LATUN_UNBLK) TO 1LATUN_BLK;



ASSIGN 1LATUN_BLK TO 1LATUN_BKE.OPSO,1LATUN_BKE.MPSO;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET 00018	dqy 'khVSHEETS 00228	CONT'D ON: 00019

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

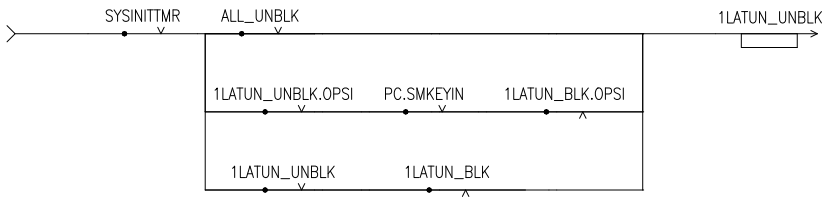
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

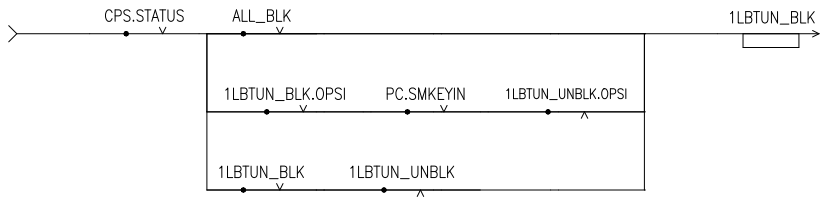
CAD FILE: 512_TAC_C1_00018_R00.DWG

(MICROLOK-II WARM STANDBY)

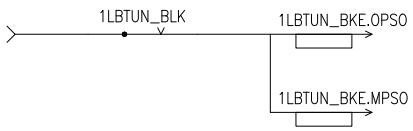
ASSIGN SYSINITTMR * (ALL_UNBLK + 1LATUN_UNBLK.OPSI * PC.SMKEYIN * ~1LATUN_BLK.OPSI + 1LATUN_UNBLK * ~1LATUN_BLK) TO 1LATUN_UNBLK;



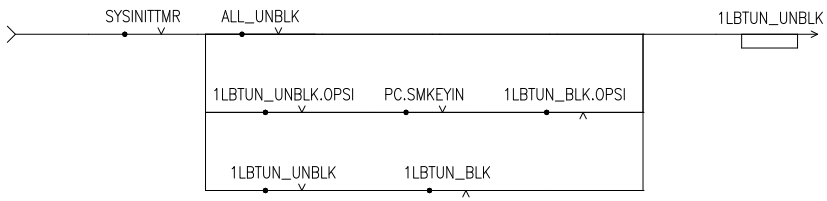
ASSIGN CPS.STATUS * (ALL_BLK + 1LBTUN_BLK.OPSI * PC.SMKEYIN * ~1LBTUN_UNBLK.OPSI + 1LBTUN_BLK * ~1LBTUN_UNBLK) TO 1LBTUN_BLK;



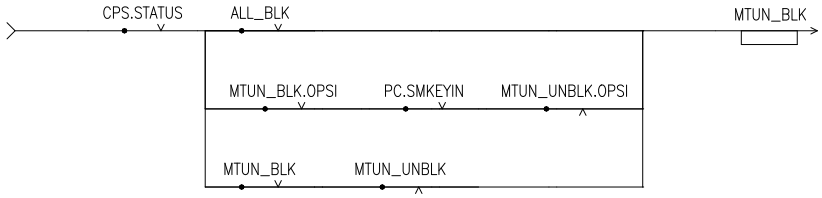
ASSIGN 1LBTUN_BLK TO 1LBTUN_BKE.OPSO,1LBTUN_BKE.MPSO;



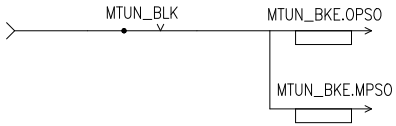
ASSIGN SYSINITTMR * (ALL_UNBLK + 1LBTUN_UNBLK.OPSI * PC.SMKEYIN * ~1LBTUN_BLK.OPSI + 1LBTUN_UNBLK * ~1LBTUN_BLK) TO 1LBTUN_UNBLK;



ASSIGN CPS.STATUS * (ALL_BLK + MTUN_BLK.OPSI * PC.SMKEYIN * ~MTUN_UNBLK.OPSI + MTUN_BLK * ~MTUN_UNBLK) TO MTUN_BLK;



ASSIGN MTUN_BLK TO MTUN_BKE.OPSO,MTUN_BKE.MPSO;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00019 | dqy 'khVSHEETS 00228 | CONT'D ON: 00020

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

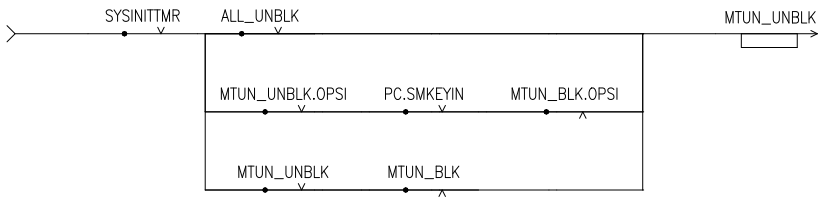
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

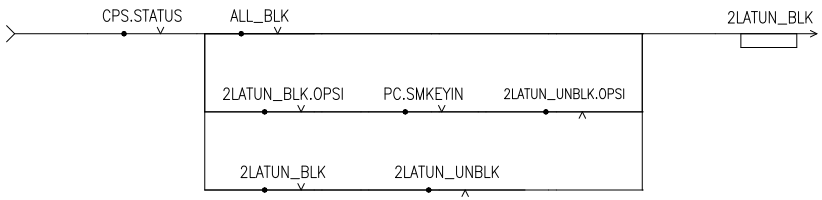
CAD FILE: 512_TAC_C1_00019_R00.DWG

(MICROLOK-II WARM STANDBY)

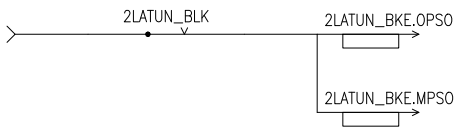
ASSIGN SYSINITTMR * (ALL_UNBLK + MTUN_UNBLK.OPSI * PC.SMKEYIN * ~MTUN_BLK.OPSI + MTUN_UNBLK * ~MTUN_BLK) TO MTUN_UNBLK;



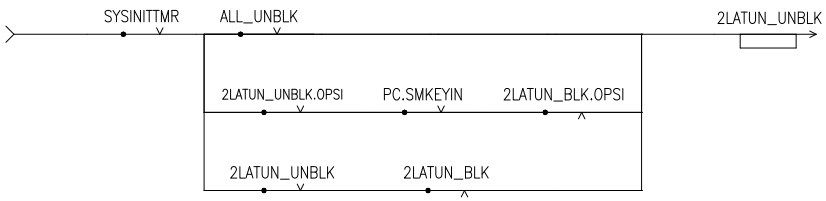
ASSIGN CPS.STATUS * (ALL_BLK + 2LATUN_BLK.OPSI * PC.SMKEYIN * ~2LATUN_UNBLK.OPSI + 2LATUN_BLK * ~2LATUN_UNBLK) TO 2LATUN_BLK;



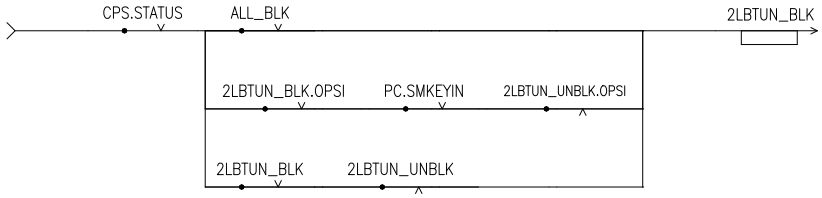
ASSIGN 2LATUN_BLK TO 2LATUN_BKE.OPSO,2LATUN_BKE.MPSO;



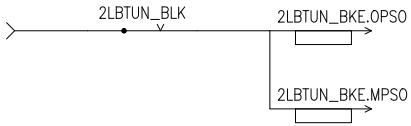
ASSIGN SYSINITTMR * (ALL_UNBLK + 2LATUN_UNBLK.OPSI * PC.SMKEYIN * ~2LATUN_BLK.OPSI + 2LATUN_UNBLK * ~2LATUN_BLK) TO 2LATUN_UNBLK;



ASSIGN CPS.STATUS * (ALL_BLK + 2LBTUN_BLK.OPSI * PC.SMKEYIN * ~2LBTUN_UNBLK.OPSI + 2LBTUN_BLK * ~2LBTUN_UNBLK) TO 2LBTUN_BLK;



ASSIGN 2LBTUN_BLK TO 2LBTUN_BKE.OPSO,2LBTUN_BKE.MPSO;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
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APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00020	00228	00021

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

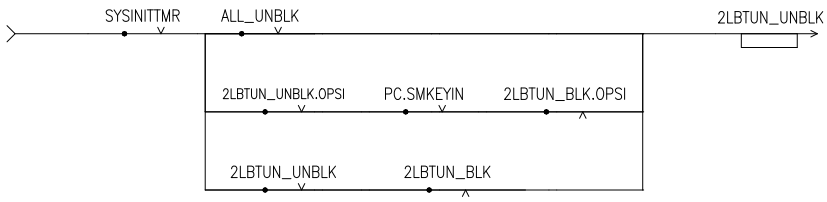
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

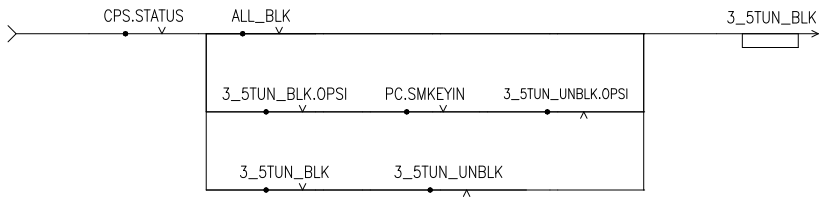
CAD FILE: 512_TAC_C1_00020_R00.DWG

(MICROLOK-II WARM STANDBY)

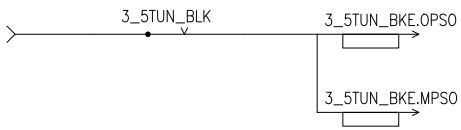
ASSIGN SYSINITTMR * (ALL_UNBLK + 2LBTUN_UNBLK.OPSI * PC.SMKEYIN * ~2LBTUN_BLK.OPSI + 2LBTUN_UNBLK * ~2LBTUN_BLK) TO 2LBTUN_UNBLK;



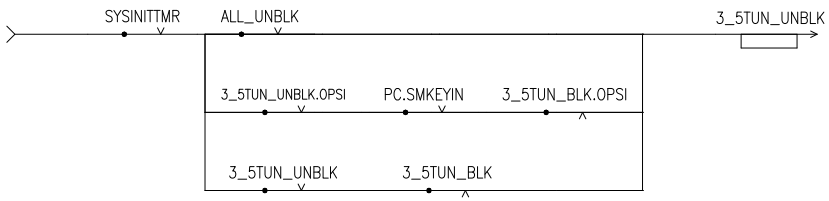
ASSIGN CPS.STATUS * (ALL_BLK + 3_5TUN_BLK.OPSI * PC.SMKEYIN * ~3_5TUN_UNBLK.OPSI + 3_5TUN_BLK * ~3_5TUN_UNBLK) TO 3_5TUN_BLK;



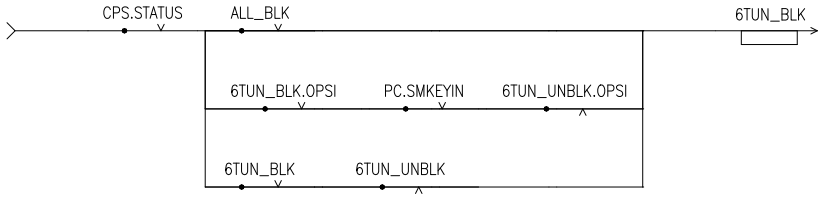
ASSIGN 3_5TUN_BLK TO 3_5TUN_BKE.OPSO,3_5TUN_BKE.MPSO;



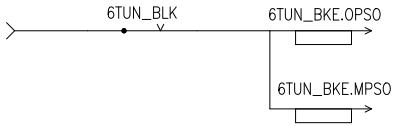
ASSIGN SYSINITTMR * (ALL_UNBLK + 3_5TUN_UNBLK.OPSI * PC.SMKEYIN * ~3_5TUN_BLK.OPSI + 3_5TUN_UNBLK * ~3_5TUN_BLK) TO 3_5TUN_UNBLK;



ASSIGN CPS.STATUS * (ALL_BLK + 6TUN_BLK.OPSI * PC.SMKEYIN * ~6TUN_UNBLK.OPSI + 6TUN_BLK * ~6TUN_UNBLK) TO 6TUN_BLK;



ASSIGN 6TUN_BLK TO 6TUN_BKE.OPSO,6TUN_BKE.MPSO;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross, 100 Feet Road, J.P.Nagar, 6th Phase, Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khVSHEET 00021 | dqy 'khVSHEETS 00228 | CONT'D ON: 00022

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

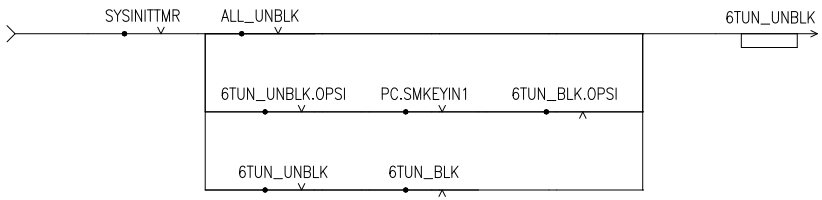
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

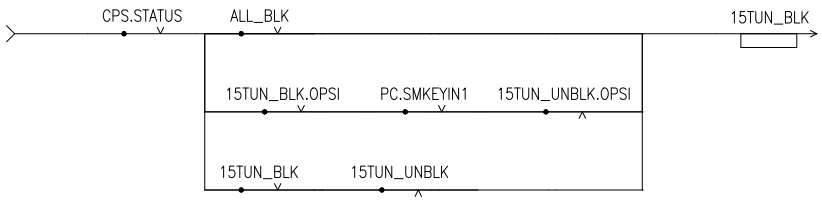
CAD FILE: 512_TAC_C1_00021_R00.DWG

(MICROLOK-II WARM STANDBY)

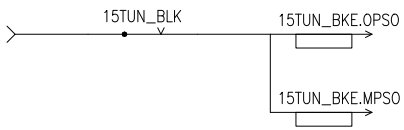
ASSIGN SYSINITTMR * (ALL_UNBLK + 6TUN_UNBLK.OPSI * PC.SMKEYIN1 * ~6TUN_BLK.OPSI + 6TUN_UNBLK * ~6TUN_BLK) TO 6TUN_UNBLK;



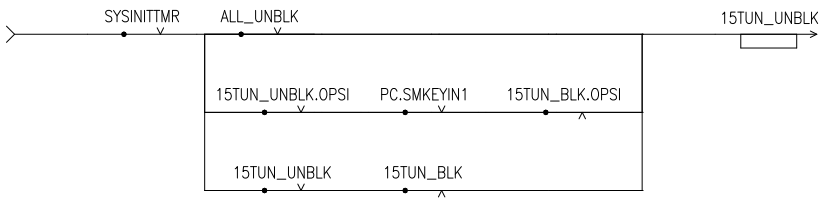
ASSIGN CPS.STATUS * (ALL_BLK + 15TUN_BLK.OPSI * PC.SMKEYIN1 * ~15TUN_UNBLK.OPSI + 15TUN_BLK * ~15TUN_UNBLK) TO 15TUN_BLK;



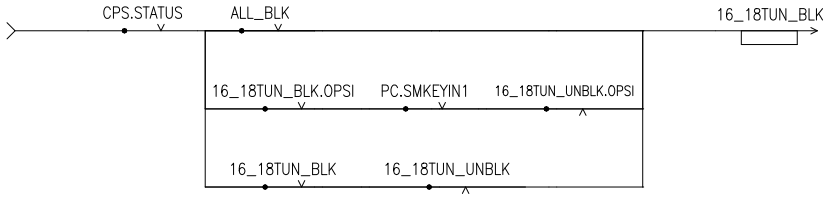
ASSIGN 15TUN_BLK TO 15TUN_BKE.OPSO,15TUN_BKE.MPSO;



ASSIGN SYSINITTMR * (ALL_UNBLK + 15TUN_UNBLK.OPSI * PC.SMKEYIN1 * ~15TUN_BLK.OPSI + 15TUN_UNBLK * ~15TUN_BLK) TO 15TUN_UNBLK;



ASSIGN CPS.STATUS * (ALL_BLK + 16_18TUN_BLK.OPSI * PC.SMKEYIN1 * ~16_18TUN_UNBLK.OPSI + 16_18TUN_BLK * ~16_18TUN_UNBLK) TO 16_18TUN_BLK;



ASSIGN 16_18TUN_BLK TO 16_18TUN_BKE.OPSO,16_18TUN_BKE.MPSO;

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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khVSHEET 00022	dqy 'khVSHEETS 00228	CONT'D ON: 00023

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

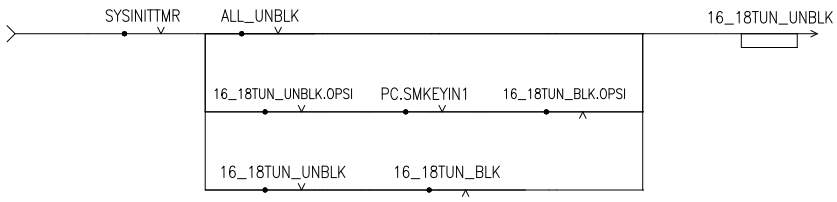
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

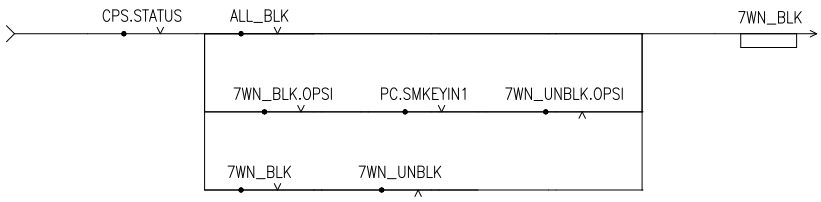
CAD FILE: 512_TAC_C1_00022_R00.DWG

(MICROLOK-II WARM STANDBY)

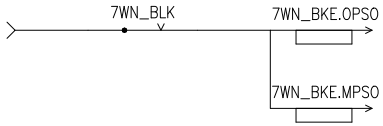
ASSIGN SYSINITTMR * (ALL_UNBLK + 16_18TUN_UNBLK.OPSI * PC.SMKEYIN1 * ~16_18TUN_BLK.OPSI + 16_18TUN_UNBLK * ~16_18TUN_BLK) TO 16_18TUN_UNBLK;



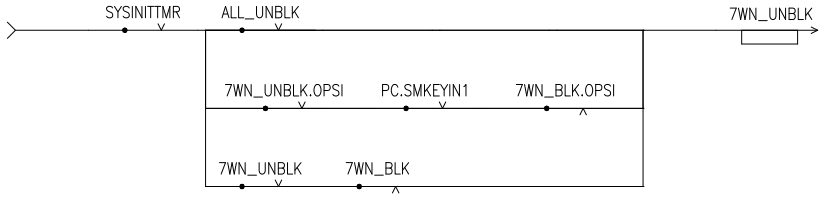
ASSIGN CPS.STATUS * (ALL_BLK + 7WN_BLK.OPSI * PC.SMKEYIN1 * ~7WN_UNBLK.OPSI + 7WN_BLK * ~7WN_UNBLK) TO 7WN_BLK;



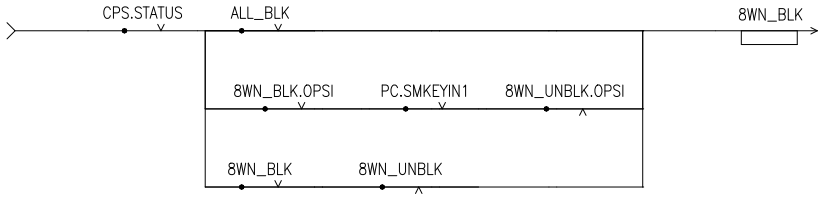
ASSIGN 7WN_BLK TO 7WN_BKE.OPSO,7WN_BKE.MPSO;



ASSIGN SYSINITTMR * (ALL_UNBLK + 7WN_UNBLK.OPSI * PC.SMKEYIN1 * ~7WN_BLK.OPSI + 7WN_UNBLK * ~7WN_BLK) TO 7WN_UNBLK;



ASSIGN CPS.STATUS * (ALL_BLK + 8WN_BLK.OPSI * PC.SMKEYIN1 * ~8WN_UNBLK.OPSI + 8WN_BLK * ~8WN_UNBLK) TO 8WN_BLK;



ASSIGN 8WN_BLK TO 8WN_BKE.OPSO,8WN_BKE.MPSO;

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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khVSHEET 00023 | dqy 'khVSHEETS 00228 | CONT'D ON: 00024

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

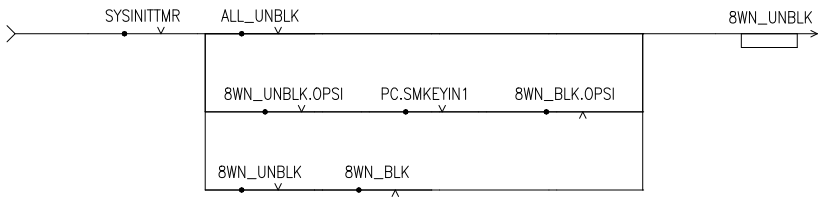
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

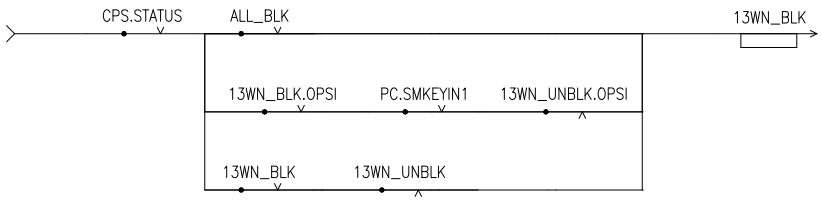
(MICROLOK-II WARM STANDBY)

CAD FILE: 512_TAC_C1_00023_R00.DWG

ASSIGN SYSINITTMR * (ALL_UNBLK + 8WN_UNBLK.OPSI * PC.SMKEYIN1 * ~8WN_BLK.OPSI + 8WN_UNBLK * ~8WN_BLK) TO 8WN_UNBLK;

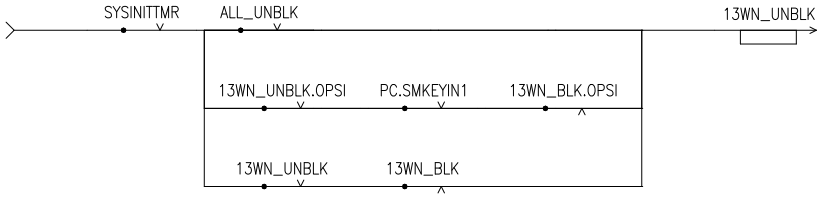


ASSIGN CPS.STATUS * (ALL_BLK + 13WN_BLK.OPSI * PC.SMKEYIN1 * ~13WN_UNBLK.OPSI + 13WN_BLK * ~13WN_UNBLK) TO 13WN_BLK;

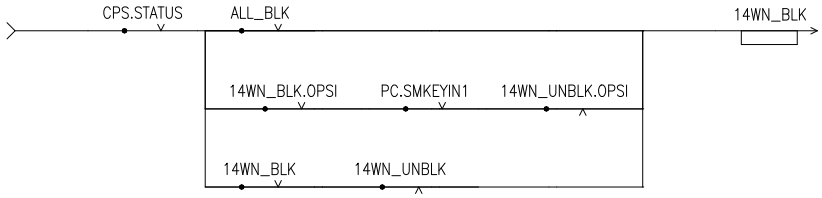


ASSIGN 13WN_BLK TO 13WN_BKE.OPSO,13WN_BKE.MPSO;

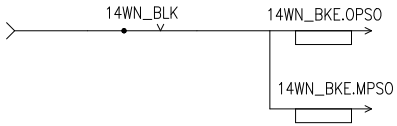
ASSIGN SYSINITTMR * (ALL_UNBLK + 13WN_UNBLK.OPSI * PC.SMKEYIN1 * ~13WN_BLK.OPSI + 13WN_UNBLK * ~13WN_BLK) TO 13WN_UNBLK;



ASSIGN CPS.STATUS * (ALL_BLK + 14WN_BLK.OPSI * PC.SMKEYIN1 * ~14WN_UNBLK.OPSI + 14WN_BLK * ~14WN_UNBLK) TO 14WN_BLK;



ASSIGN 14WN_BLK TO 14WN_BKE.OPSO,14WN_BKE.MPSO;



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#610, "NARAYANA", 1st Floor, 15th Cross,
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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO a fc B

'khVSHEET 00024 | dqy 'khVSHEETS 00228 | CONT'D ON: 00025

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

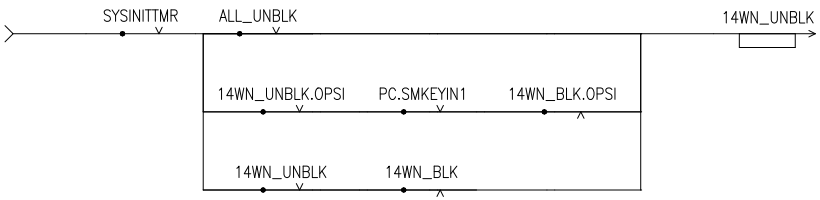
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

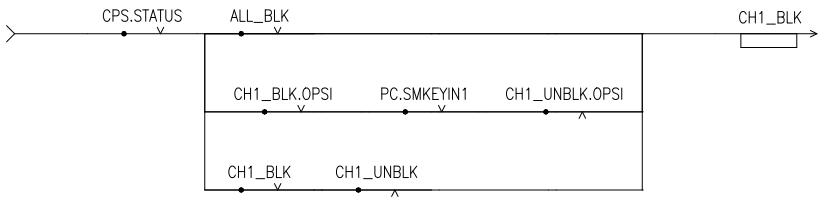
CAD FILE: 512_TAC_C1_00024_R00.DWG

(MICROLOK-II WARM STANDBY)

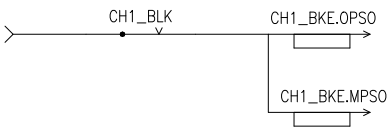
ASSIGN SYSINITTMR * (ALL_UNBLK + 14WN_UNBLK.OPSI * PC.SMKEYIN1 * ~14WN_BLK.OPSI + 14WN_UNBLK * ~14WN_BLK) TO 14WN_UNBLK;



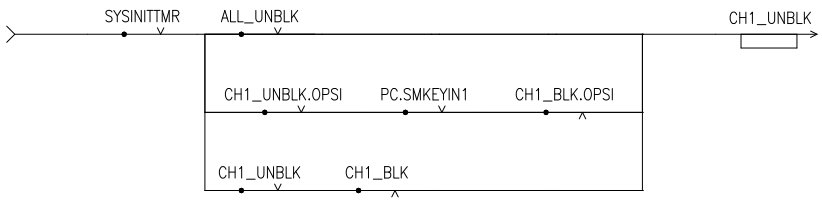
ASSIGN CPS.STATUS * (ALL_BLK + CH1_BLK.OPSI * PC.SMKEYIN1 * ~CH1_UNBLK.OPSI + CH1_BLK * ~CH1_UNBLK) TO CH1_BLK;



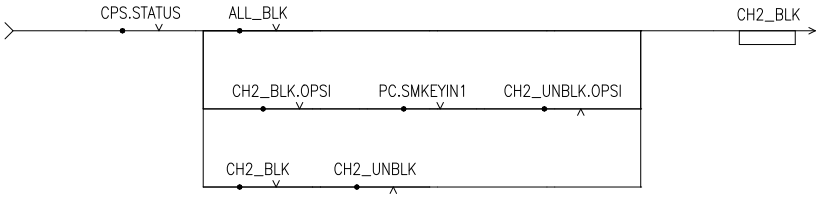
ASSIGN CH1_BLK TO CH1_BKE.OPSO,CH1_BKE.MPSO;



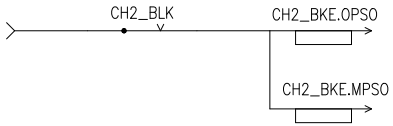
ASSIGN SYSINITTMR * (ALL_UNBLK + CH1_UNBLK.OPSI * PC.SMKEYIN1 * ~CH1_BLK.OPSI + CH1_UNBLK * ~CH1_BLK) TO CH1_UNBLK;



ASSIGN CPS.STATUS * (ALL_BLK + CH2_BLK.OPSI * PC.SMKEYIN1 * ~CH2_UNBLK.OPSI + CH2_BLK * ~CH2_UNBLK) TO CH2_BLK;



ASSIGN CH2_BLK TO CH2_BKE.OPSO,CH2_BKE.MPSO;



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APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khVSHEET 00025	dqy 'khVSHEETS 00228	CONT'D ON: 00026

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

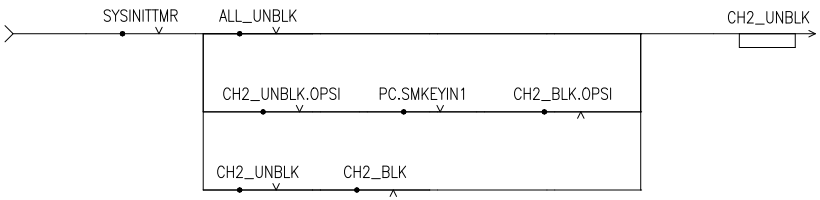
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

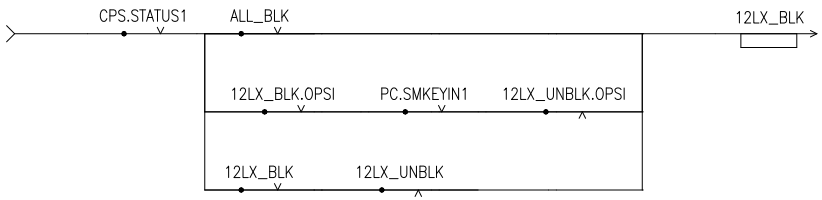
CAD FILE: 512_TAC_C1_00025_R00.DWG

(MICROLOK-II WARM STANDBY)

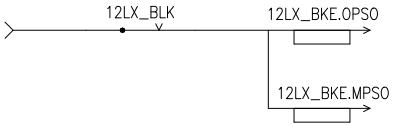
ASSIGN SYSINITTMR * (ALL_UNBLK + CH2_UNBLK.OPSI * PC.SMKEYIN1 * ~CH2_BLK.OPSI + CH2_UNBLK * ~CH2_BLK) TO CH2_UNBLK;



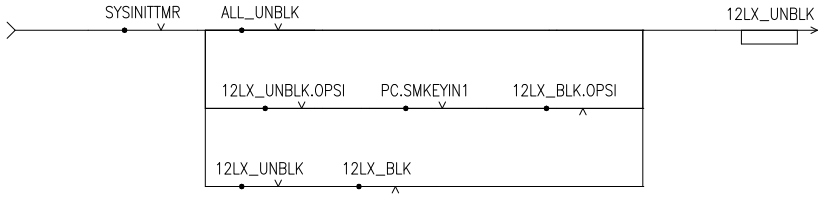
ASSIGN CPS.STATUS1 * (ALL_BLK + 12LX_BLK.OPSI * PC.SMKEYIN1 * ~12LX_UNBLK.OPSI + 12LX_BLK * ~12LX_UNBLK) TO 12LX_BLK;



ASSIGN 12LX_BLK TO 12LX_BKE.OPSO,12LX_BKE.MPSO;



ASSIGN SYSINITTMR * (ALL_UNBLK + 12LX_UNBLK.OPSI * PC.SMKEYIN1 * ~12LX_BLK.OPSI + 12LX_UNBLK * ~12LX_BLK) TO 12LX_UNBLK;



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APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khVSHEET 00026 | dqy 'khVSHEETS 00228 | CONT'D ON: 00027

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SIGNAL & TELECOMMUNICATIONS

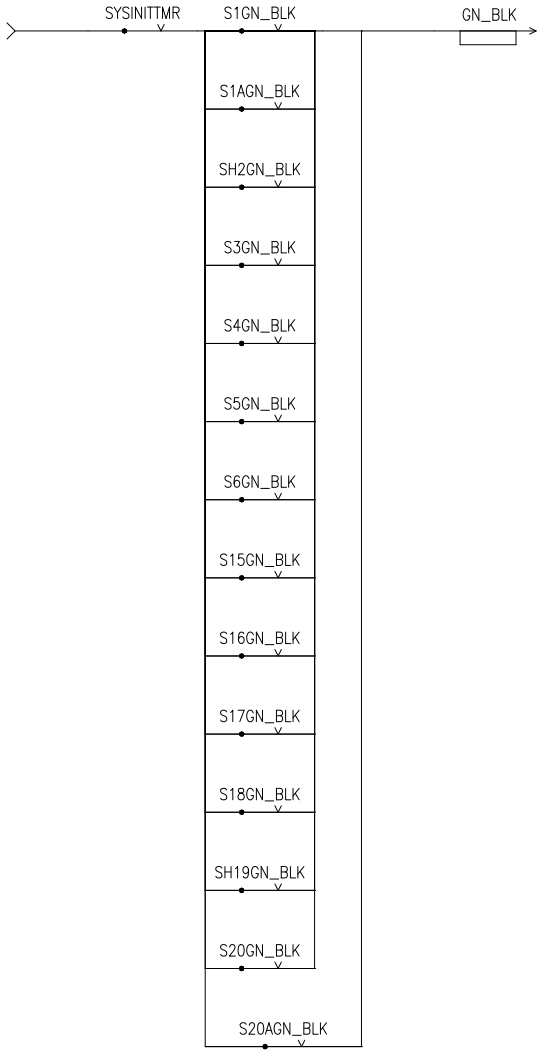
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

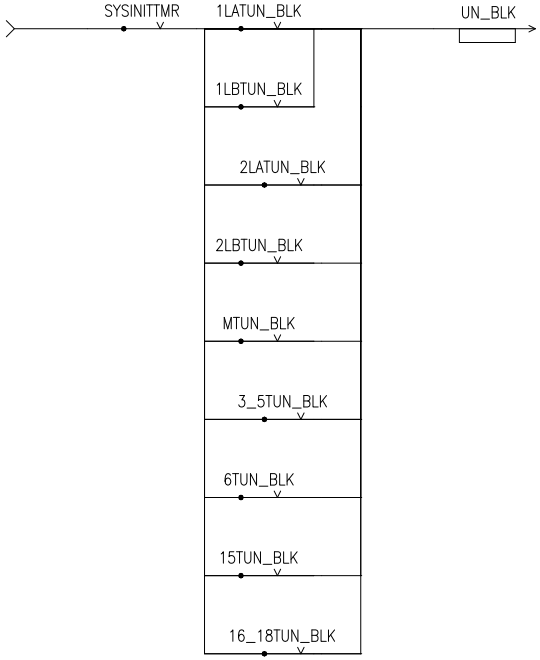
CAD FILE: 512_TAC_C1_00026_R00.DWG

(MICROLOK-II WARM STANDBY)

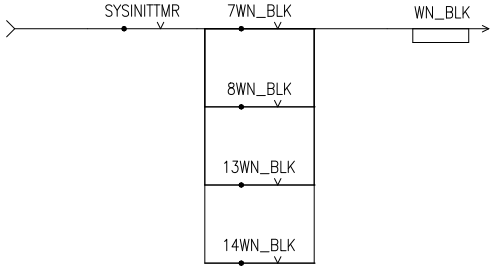
ASSIGN SYSINITTMR * (S1GN_BLK + S1AGN_BLK + SH2GN_BLK + S3GN_BLK + S4GN_BLK + S5GN_BLK + S6GN_BLK + S15GN_BLK + S16GN_BLK + S17GN_BLK + S18GN_BLK + SH19GN_BLK + S20GN_BLK + S20AGN_BLK) TO GN_BLK;



ASSIGN SYSINITTMR * (1LATUN_BLK + 1LBTUN_BLK + 2LATUN_BLK + 2LBTUN_BLK + MTUN_BLK + 3_5TUN_BLK + 6TUN_BLK + 15TUN_BLK + 16_18TUN_BLK) TO UN_BLK;



ASSIGN SYSINITTMR * (7WN_BLK + 8WN_BLK + 13WN_BLK + 14WN_BLK) TO WN_BLK;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross, 100 Feet Road, J.P.Nagar, 6th Phase, Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO a fc B | 'khV SHEET 00027 | dqy 'khVSHEETS 00228 | CONT'D ON: 00028

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

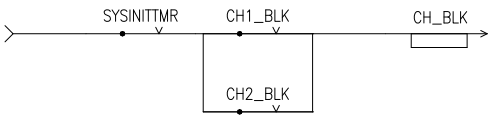
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

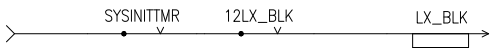
CAD FILE: 512_TAC_C1_00027_R00.DWG

(MICROLOK-II WARM STANDBY)

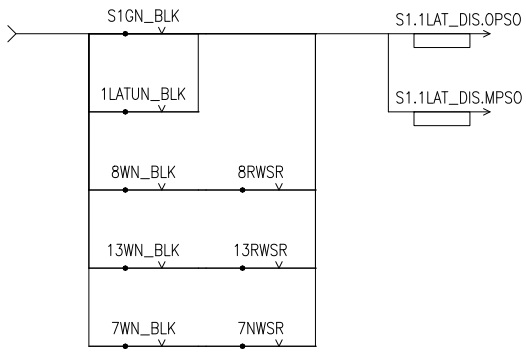
ASSIGN SYSINITTMR * (CH1_BLK + CH2_BLK) TO CH_BLK;



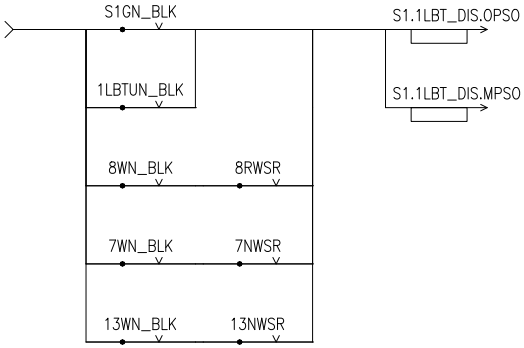
ASSIGN SYSINITTMR * 12LX_BLK TO LX_BLK;



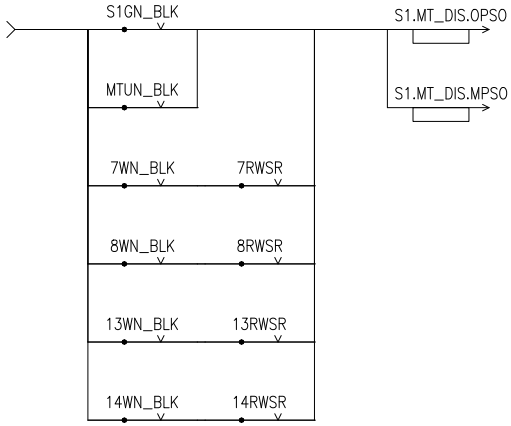
ASSIGN (S1GN_BLK + 1LATUN_BLK + 8WN_BLK * 8RWSR + 13WN_BLK * 13RWSR + 7WN_BLK * 7NWSR) TO S1.1LAT_DIS.OPSO,S1.1LAT_DIS.MPSO;



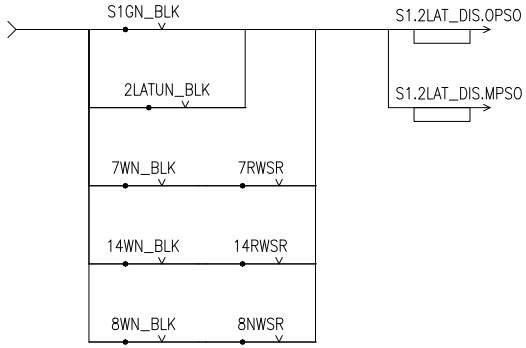
ASSIGN (S1GN_BLK + 1LBTUN_BLK + 8WN_BLK * 8RWSR + 7WN_BLK * 7NWSR + 13WN_BLK * 13NWSR) TO S1.1LBT_DIS.OPSO,S1.1LBT_DIS.MPSO;



ASSIGN (S1GN_BLK + MTUN_BLK + 7WN_BLK * 7RWSR + 8WN_BLK * 8RWSR + 13WN_BLK * 13RWSR + 14WN_BLK * 14RWSR) TO S1.MT_DIS.OPSO,S1.MT_DIS.MPSO;



ASSIGN (S1GN_BLK + 2LATUN_BLK + 7WN_BLK * 7RWSR + 14WN_BLK * 14RWSR + 8WN_BLK * 8NWSR) TO S1.2LAT_DIS.OPSO,S1.2LAT_DIS.MPSO;



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TVM Signalling & Transportation Systems Pvt. Ltd.
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
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SSTE	
f CSTE	APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET 00028	dqy 'khVSHEETS 00228	CONT'D ON: 00029

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

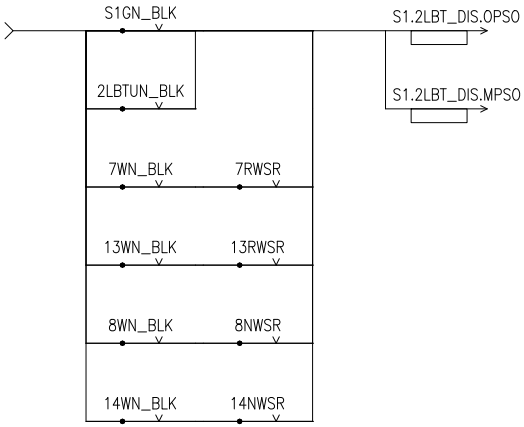
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

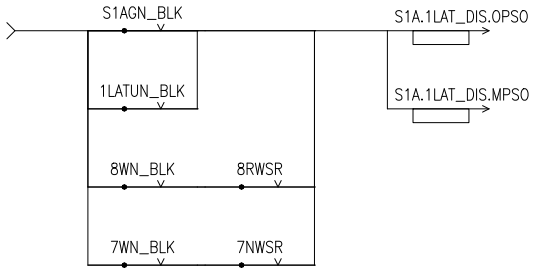
CAD FILE: 512_TAC_C1_00028_R00.DWG

(MICROLOK-II WARM STANDBY)

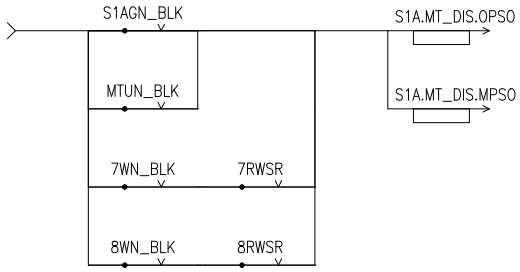
ASSIGN (S1GN_BLK + 2LBTUN_BLK + 7WN_BLK * 7RWSR + 13WN_BLK * 13RWSR + 8WN_BLK * 8NWSR + 14WN_BLK * 14NWSR) TO S1.2LBT_DIS.OPSO,S1.2LBT_DIS.MPSO;



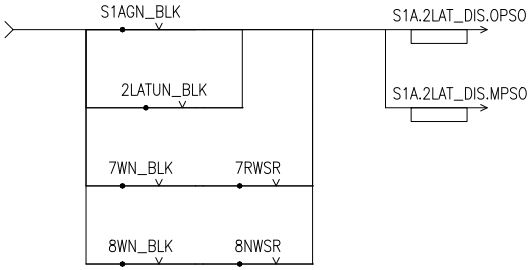
ASSIGN (S1AGN_BLK + 1LATUN_BLK + 8WN_BLK * 8RWSR + 7WN_BLK * 7NWSR) TO S1A.1LAT_DIS.OPSO,S1A.1LAT_DIS.MPSO;



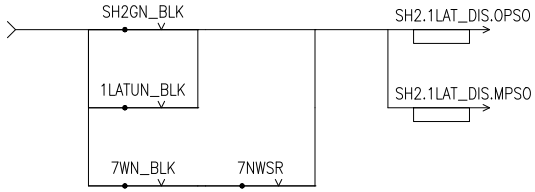
ASSIGN (S1AGN_BLK + MTUN_BLK + 7WN_BLK * 7RWSR + 8WN_BLK * 8RWSR) TO S1A.MT_DIS.OPSO,S1A.MT_DIS.MPSO;



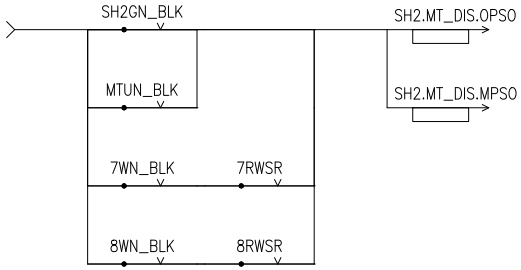
ASSIGN (S1AGN_BLK + 2LATUN_BLK + 7WN_BLK * 7RWSR + 8WN_BLK * 8NWSR) TO S1A.2LAT_DIS.OPSO,S1A.2LAT_DIS.MPSO;



ASSIGN (SH2GN_BLK + 1LATUN_BLK + 7WN_BLK * 7NWSR) TO SH2.1LAT_DIS.OPSO,SH2.1LAT_DIS.MPSO;



ASSIGN (SH2GN_BLK + MTUN_BLK + 7WN_BLK * 7RWSR + 8WN_BLK * 8RWSR) TO SH2.MT_DIS.OPSO,SH2.MT_DIS.MPSO;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
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SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

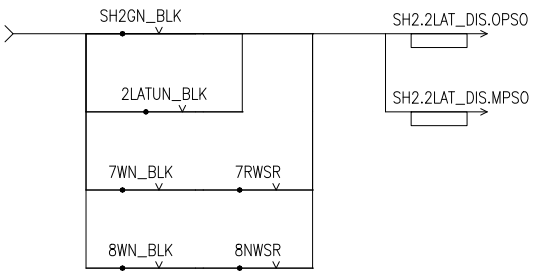
REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B
'khV SHEET 00029 | dqy 'khVSHEETS 00228 | CONT'D ON: 00030

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

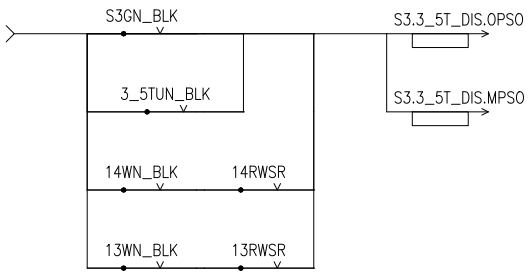
CAD FILE: 512_TAC_C1_00029_R00.DWG

(MICROLOK-II WARM STANDBY)

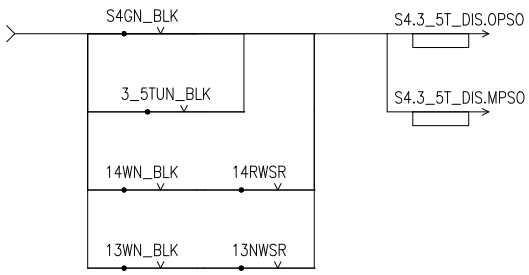
ASSIGN (SH2GN_BLK + 2LATUN_BLK + 7WN_BLK * 7RWSR + 8WN_BLK * 8NWSR) TO SH2.2LAT_DIS.OPS0,SH2.2LAT_DIS.MPS0;



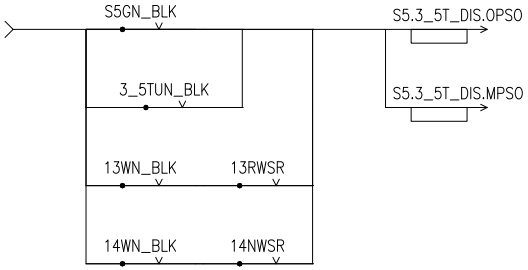
ASSIGN (S3GN_BLK + 3_5TUN_BLK + 14WN_BLK * 14RWSR + 13WN_BLK * 13RWSR) TO S3.3_5T_DIS.OPS0,S3.3_5T_DIS.MPS0;



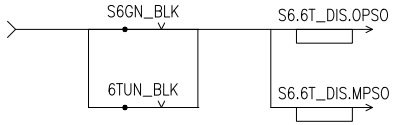
ASSIGN (S4GN_BLK + 3_5TUN_BLK + 14WN_BLK * 14RWSR + 13WN_BLK * 13NWSR) TO S4.3_5T_DIS.OPS0,S4.3_5T_DIS.MPS0;



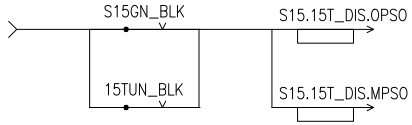
ASSIGN (S5GN_BLK + 3_5TUN_BLK + 13WN_BLK * 13RWSR + 14WN_BLK * 14NWSR) TO S5.3_5T_DIS.OPS0,S5.3_5T_DIS.MPS0;



ASSIGN S6GN_BLK + 6TUN_BLK TO S6.6T_DIS.OPS0,S6.6T_DIS.MPS0;



ASSIGN S15GN_BLK + 15TUN_BLK TO S15.15T_DIS.OPS0,S15.15T_DIS.MPS0;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00030 | dqy 'khVSHEETS 00228 | CONT'D ON: 00031

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

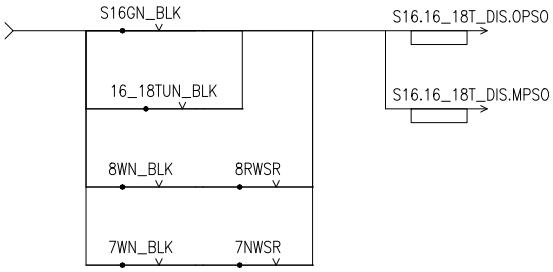
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

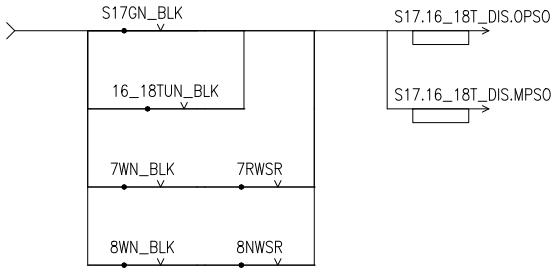
CAD FILE: 512_TAC_C1_00030_R00.DWG

(MICROLOK-II WARM STANDBY)

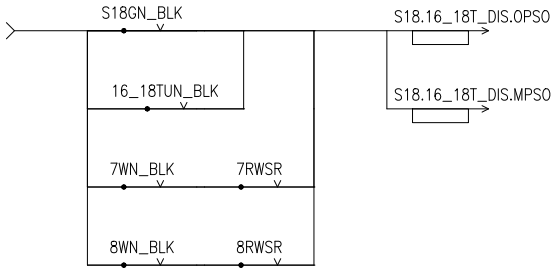
ASSIGN (S16GN_BLK + 16_18TUN_BLK + 8WN_BLK * 8RWSR + 7WN_BLK * 7NWSR) TO S16.16_18T_DIS.OPS0,S16.16_18T_DIS.MPS0;



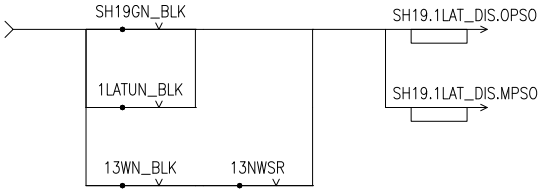
ASSIGN (S17GN_BLK + 16_18TUN_BLK + 7WN_BLK * 7RWSR + 8WN_BLK * 8NWSR) TO S17.16_18T_DIS.OPS0,S17.16_18T_DIS.MPS0;



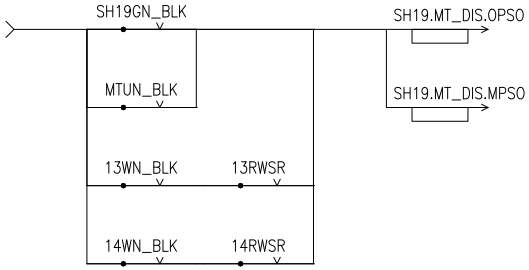
ASSIGN (S18GN_BLK + 16_18TUN_BLK + 7WN_BLK * 7RWSR + 8WN_BLK * 8RWSR) TO S18.16_18T_DIS.OPS0,S18.16_18T_DIS.MPS0;



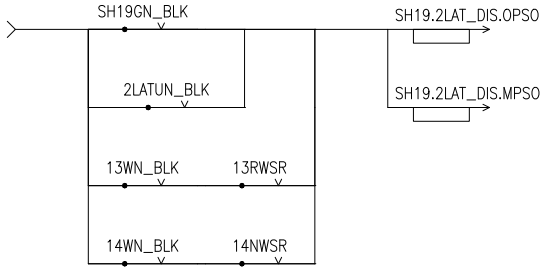
ASSIGN (SH19GN_BLK + 1LATUN_BLK + 13WN_BLK * 13NWSR) TO SH19.1LAT_DIS.OPS0,SH19.1LAT_DIS.MPS0;



ASSIGN (SH19GN_BLK + MTUN_BLK + 13WN_BLK * 13RWSR + 14WN_BLK * 14RWSR) TO SH19.MT_DIS.OPS0,SH19.MT_DIS.MPS0;



ASSIGN (SH19GN_BLK + 2LATUN_BLK + 13WN_BLK * 13RWSR + 14WN_BLK * 14NWSR) TO SH19.2LAT_DIS.OPS0,SH19.2LAT_DIS.MPS0;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
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CHK.

APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00031 | dqy 'khVSHEETS 00228 | CONT'D ON: 00032

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

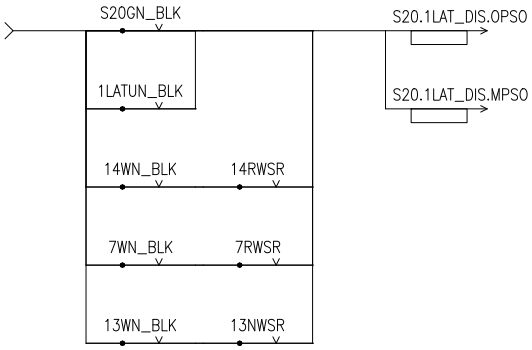
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

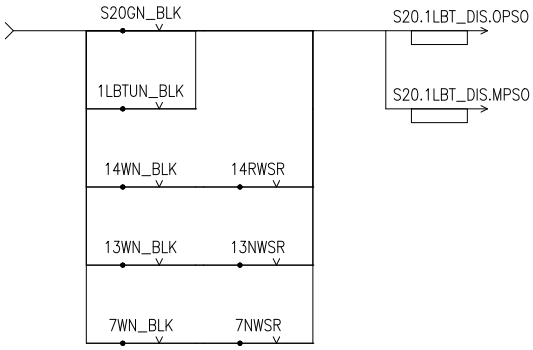
CAD FILE: 512_TAC_C1_00031_R00.DWG

(MICROLOK-II WARM STANDBY)

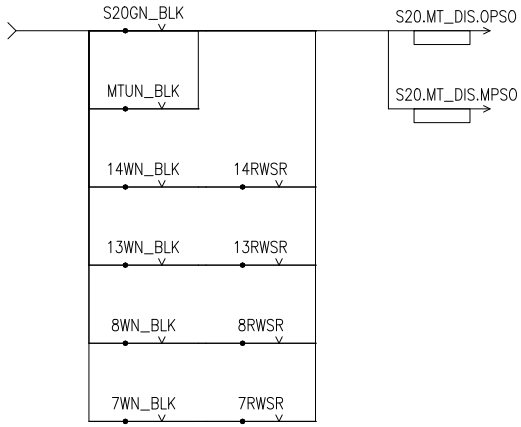
ASSIGN (S20GN_BLK + 1LATUN_BLK + 14WN_BLK * 14RWSR + 7WN_BLK * 7RWSR + 13WN_BLK * 13NWSR) TO S20.1LAT_DIS.OPS0,S20.1LAT_DIS.MPS0;



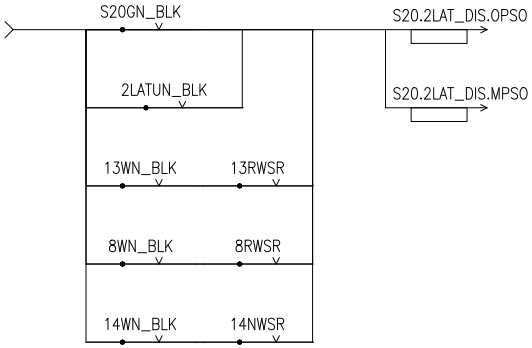
ASSIGN (S20GN_BLK + 1LBTUN_BLK + 14WN_BLK * 14RWSR + 13WN_BLK * 13NWSR + 7WN_BLK * 7NWSR) TO S20.1LBT_DIS.OPS0,S20.1LBT_DIS.MPS0;



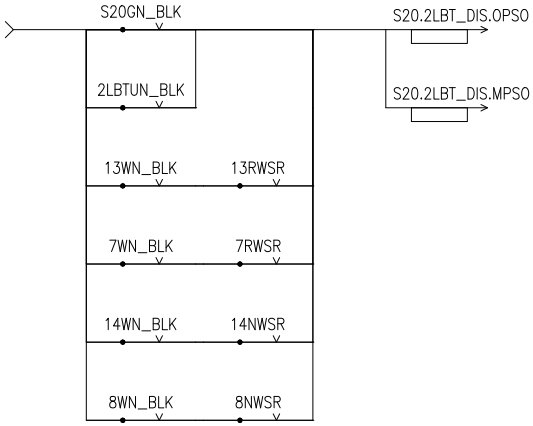
ASSIGN (S20GN_BLK + MTUN_BLK + 14WN_BLK * 14RWSR + 13WN_BLK * 13RWSR + 8WN_BLK * 8RWSR + 7WN_BLK * 7RWSR) TO S20.MT_DIS.OPS0,S20.MT_DIS.MPS0;



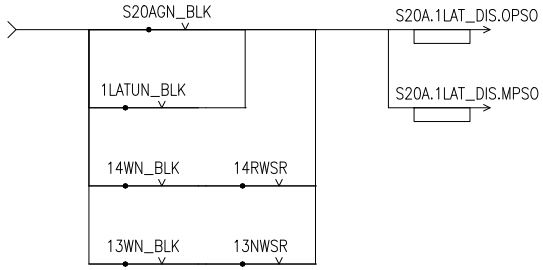
ASSIGN (S20GN_BLK + 2LATUN_BLK + 13WN_BLK * 13RWSR + 8WN_BLK * 8RWSR + 14WN_BLK * 14NWSR) TO S20.2LAT_DIS.OPS0,S20.2LAT_DIS.MPS0;



ASSIGN (S20GN_BLK + 2LBTUN_BLK + 13WN_BLK * 13RWSR + 7WN_BLK * 7RWSR + 14WN_BLK * 14NWSR + 8WN_BLK * 8NWSR) TO S20.2LBT_DIS.OPS0,S20.2LBT_DIS.MPS0;



ASSIGN (S20AGN_BLK + 1LATUN_BLK + 14WN_BLK * 14RWSR + 13WN_BLK * 13NWSR) TO S20A.1LAT_DIS.OPS0,S20A.1LAT_DIS.MPS0;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00032	00228	00033

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

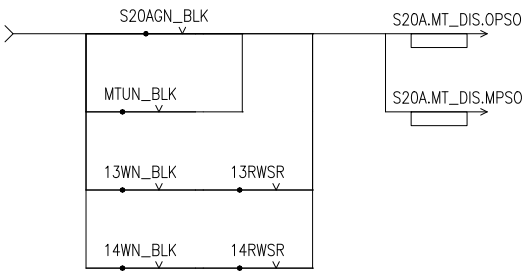
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

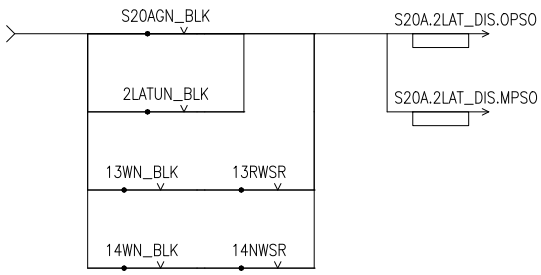
CAD FILE: 512_TAC_C1_00032_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN (S20AGN_BLK + MTUN_BLK + 13WN_BLK * 13RWSR + 14WN_BLK * 14RWSR) TO S20A.MT_DIS.OPSO,S20A.MT_DIS.MPSO;



ASSIGN (S20AGN_BLK + 2LATUN_BLK + 13WN_BLK * 13RWSR + 14WN_BLK * 14NWSR) TO S20A.2LAT_DIS.OPSO,S20A.2LAT_DIS.MPSO;



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00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00033	00228	00034

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

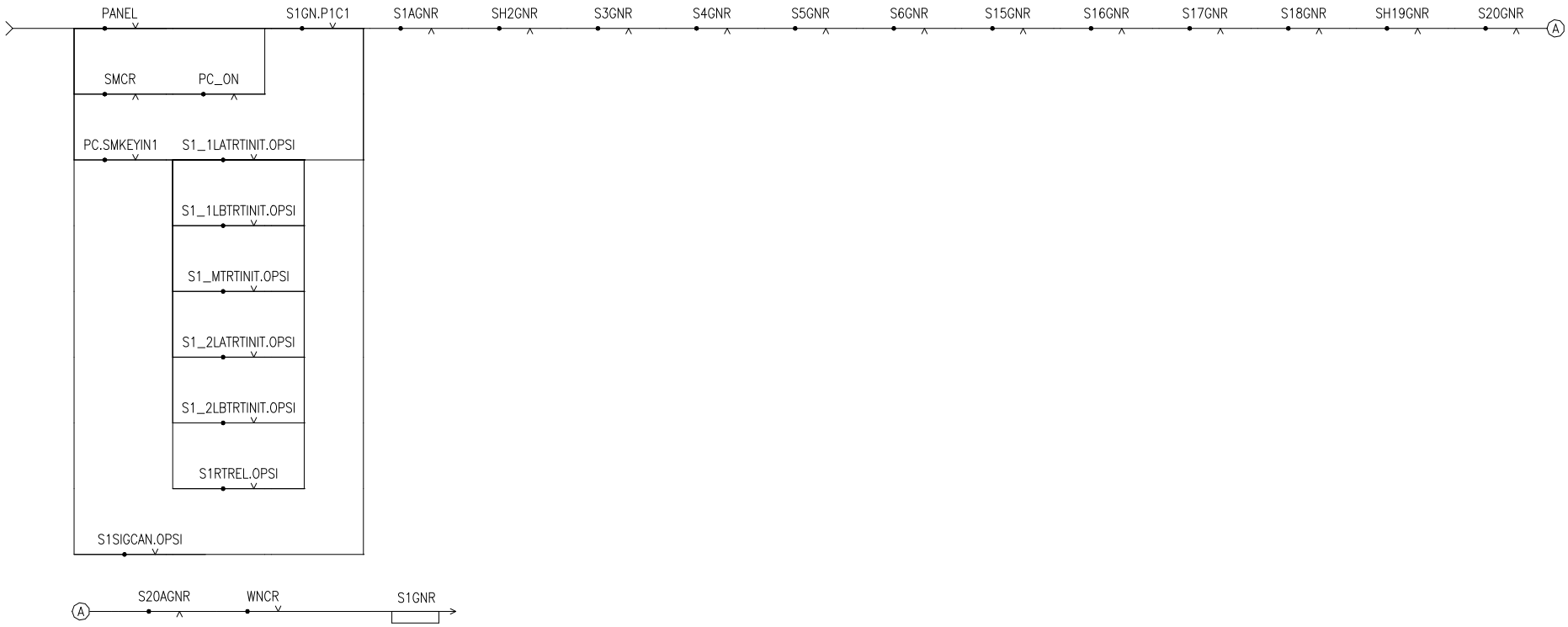
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00033_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ((PANEL + ~SMCR * ~PC_ON) * S1GN.P1C1 + PC.SMKEYIN1 * (S1_1LATRTINIT.OPSI + S1_1LBTRTINIT.OPSI + S1_MTRTINIT.OPSI + S1_2LATRTINIT.OPSI + S1_2LBTRTINIT.OPSI + S1RTREL.OPSI) + S1SIGCAN.OPSI) * ~S1AGNR * ~SH2GNR * ~S3GNR * ~S4GNR * ~S5GNR * ~S6GNR * ~S15GNR * ~S16GNR * ~S17GNR * ~S18GNR * ~SH19GNR * ~S20GNR * ~S20AGNR * WNCR TO S1GNR;



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00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00034 | dqy 'khVSHEETS 00228 | CONT'D ON: 00035

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

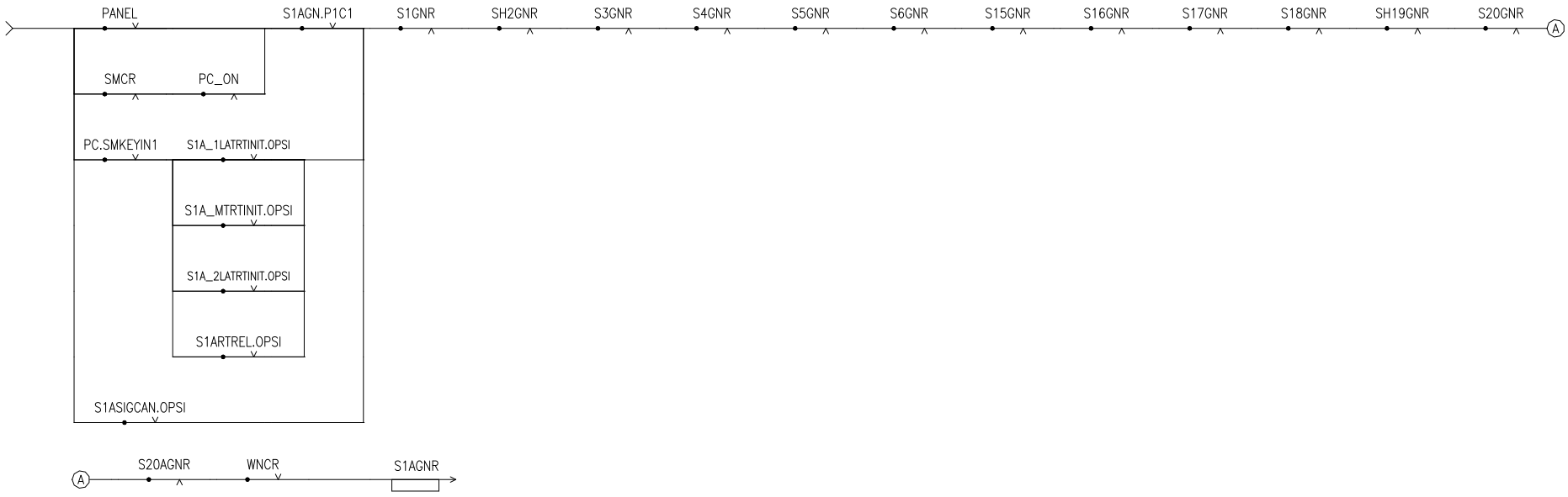
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00034_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ((PANEL + ~SMCR * ~PC_ON) * S1AGN.P1C1 + PC.SMKEYIN1 * (S1A_1LATRTINIT.OPSI + S1A_MTRTINIT.OPSI + S1A_2LATRTINIT.OPSI + S1ARTREL.OPSI) + S1ASIGCAN.OPSI) * ~S1GNR * ~SH2GNR * ~S3GNR * ~S4GNR * ~S5GNR * ~S6GNR * ~S15GNR * ~S16GNR * ~S17GNR * ~S18GNR * ~SH19GNR * ~S20GNR * ~S20AGNR * WNCR TO S1AGNR;



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00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00035 | dqy 'khVSHEETS 00228 | CONT'D ON: 00036

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

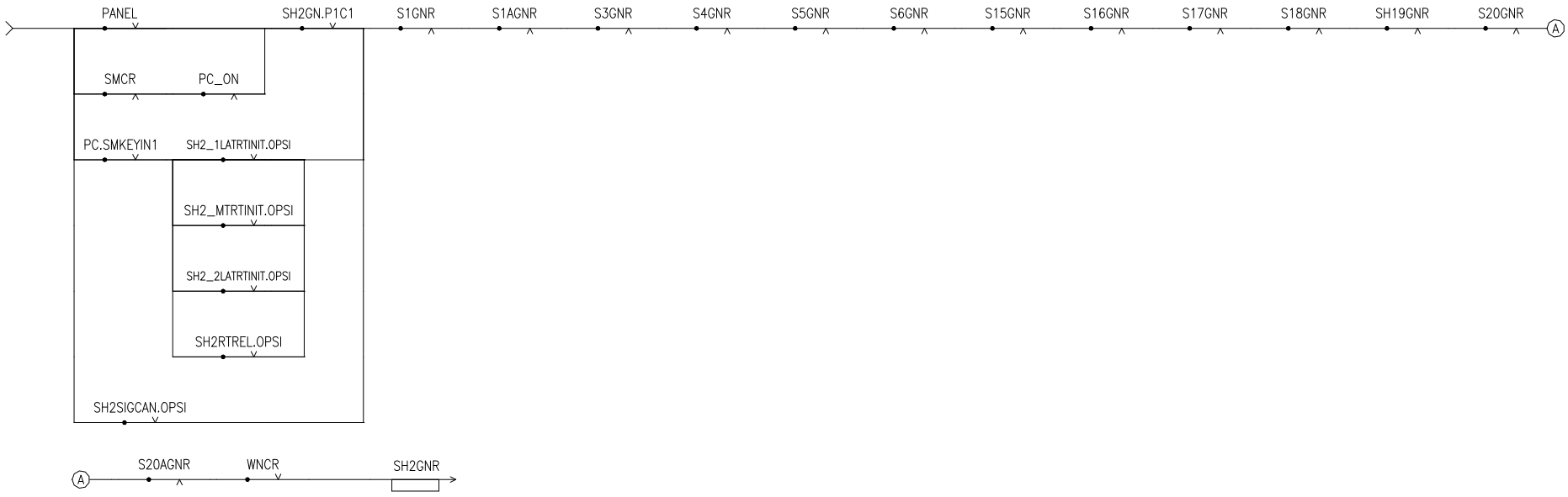
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00035_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ((PANEL + ~SMCR * ~PC_ON) * SH2GN.P1C1 + PC.SMKEYIN1 * (SH2_1LATRTINIT.OPSI + SH2_MTRTINIT.OPSI + SH2_2LATRTINIT.OPSI + SH2RTREL.OPSI) + SH2SIGCAN.OPSI) * ~S1GNR * ~S1AGNR * ~S3GNR * ~S4GNR * ~S5GNR * ~S6GNR * ~S15GNR * ~S16GNR * ~S17GNR * ~S18GNR * ~SH19GNR * ~S20GNR * ~S20AGNR * WNCR TO SH2GNR;



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00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

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E-mail : info@tsts.in

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f CSTE	APP.

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

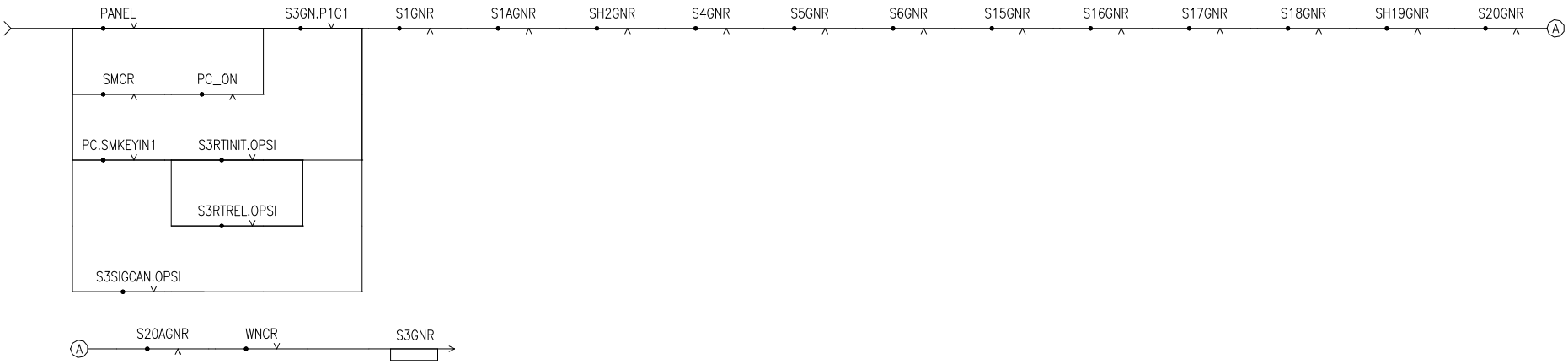
REF.SIG.PLAN.No. IPU.2107B
n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khV SHEET 00036
dqy 'khVSHEETS 00228
CONT'D ON: 00037

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00036_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ((PANEL + ~SMCR * ~PC_ON) * S3GN.P1C1 + PC.SMKEYIN1 * (S3RTINIT.OPSI + S3RTREL.OPSI) + S3SIGCAN.OPSI) * ~S1GNR * ~S1AGNR * ~SH2GNR * ~S4GNR * ~S5GNR * ~S6GNR * ~S15GNR * ~S16GNR * ~S17GNR * ~S18GNR * ~SH19GNR * ~S20GNR * ~S20AGNR * WNCR TO S3GNR;



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00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00037 | dqy 'khVSHEETS 00228 | CONT'D ON: 00038

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

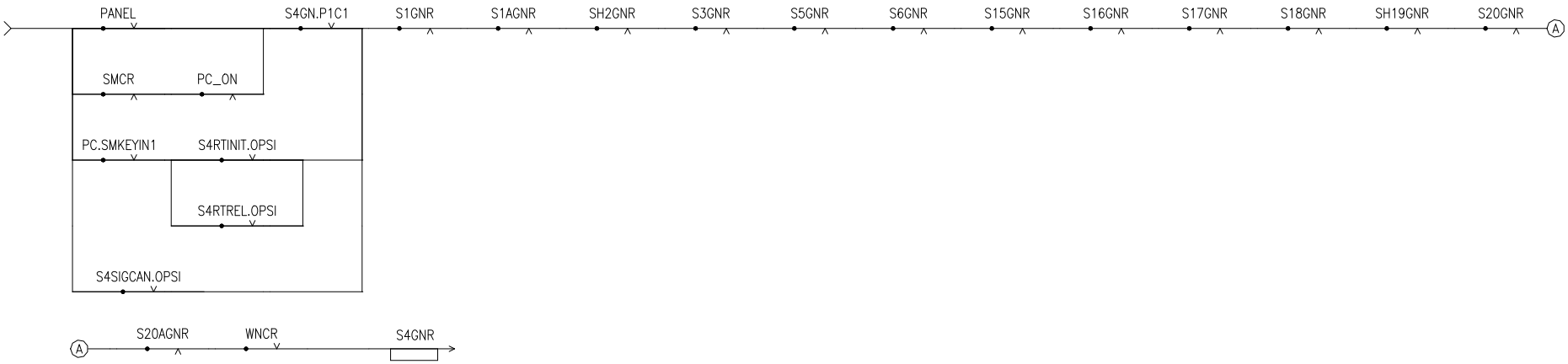
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00037_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ((PANEL + ~SMCR * ~PC_ON) * S4GN.P1C1 + PC.SMKEYIN1 * (S4RTINIT.OPSI + S4RTREL.OPSI) + S4SIGCAN.OPSI) * ~S1GNR * ~S1AGNR * ~SH2GNR * ~S3GNR * ~S5GNR * ~S6GNR * ~S15GNR * ~S16GNR * ~S17GNR * ~S18GNR * ~SH19GNR * ~S20GNR * ~S20AGNR * WNCR TO S4GNR;



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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00038 | dqy 'khVSHEETS 00228 | CONT'D ON: 00039

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00038_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ((PANEL + ~SMCR * ~PC_ON) * S5GN.P1C1 + PC.SMKEYIN1 * (S5RTINIT.OPSI + S5RTREL.OPSI) + S5SIGCAN.OPSI) * ~S1GNR * ~S1AGNR * ~SH2GNR * ~S3GNR * ~S4GNR * ~S6GNR * ~S15GNR * ~S16GNR * ~S17GNR * ~S18GNR * ~SH19GNR * ~S20GNR * ~S20AGNR * WNCR TO S5GNR;

REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	a fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00039	00228	00040

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

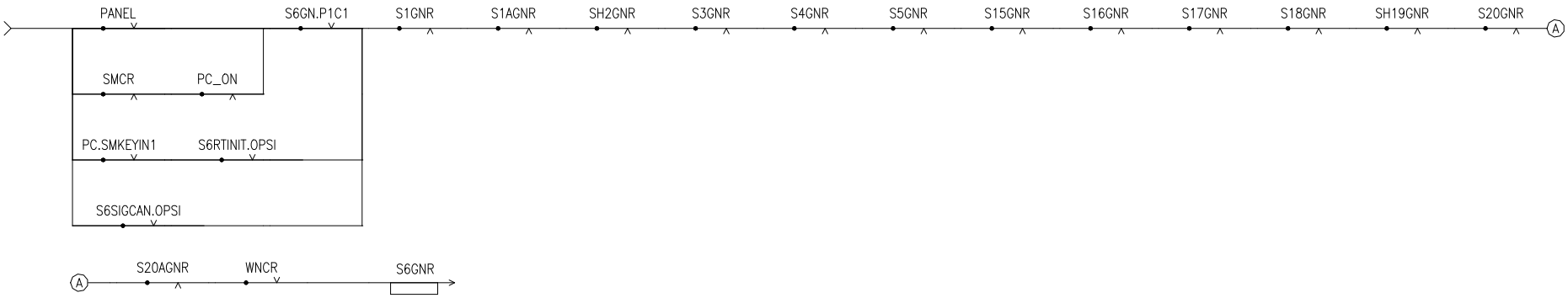
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

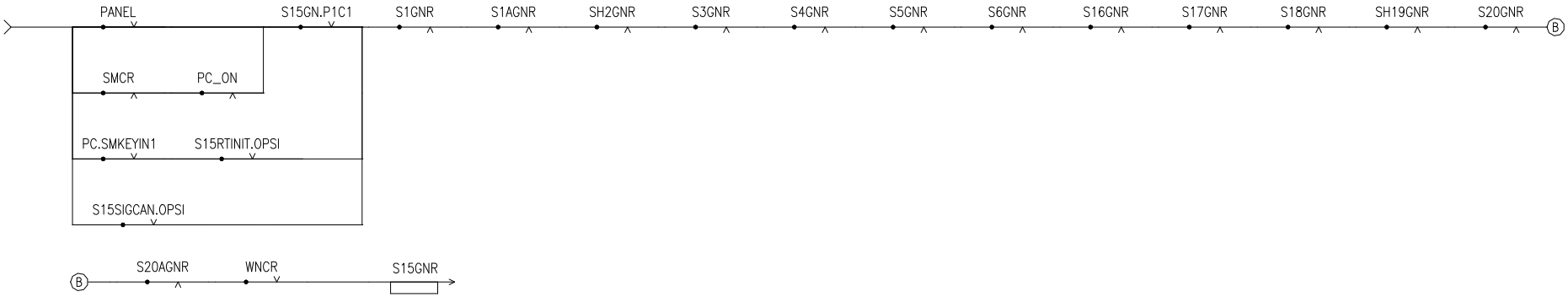
CAD FILE: 512_TAC_C1_00039_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ((PANEL + ~SMCR * ~PC_ON) * S6GN.P1C1 + PC.SMKEYIN1 * S6RTINIT.OPSI + S6SIGCAN.OPSI) * ~S1GNR * ~S1AGNR * ~SH2GNR * ~S3GNR * ~S4GNR * ~S5GNR * ~S15GNR * ~S16GNR * ~S17GNR * ~S18GNR * ~SH19GNR * ~S20GNR * ~S20AGNR * WNCR TO S6GNR;



ASSIGN ((PANEL + ~SMCR * ~PC_ON) * S15GN.P1C1 + PC.SMKEYIN1 * S15RTINIT.OPSI + S15SIGCAN.OPSI) * ~S1GNR * ~S1AGNR * ~SH2GNR * ~S3GNR * ~S4GNR * ~S5GNR * ~S6GNR * ~S16GNR * ~S17GNR * ~S18GNR * ~SH19GNR * ~S20GNR * ~S20AGNR * WNCR TO S15GNR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khV SHEET 00040 | dqy 'khVSHEETS 00228 | CONT'D ON: 00041

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00040_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ((PANEL + ~SMCR * ~PC_ON) * S16GN.P1C1 + PC.SMKEYIN1 * (S16RTINIT.OPSI + S16RTREL.OPSI) + S16SIGCAN.OPSI) * ~S1GNR * ~S1AGNR * ~SH2GNR * ~S3GNR * ~S4GNR * ~S5GNR * ~S6GNR * ~S15GNR * ~S17GNR * ~S18GNR * ~SH19GNR * ~S20GNR * ~S20AGNR * WNCR TO S16GNR;

REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00041 | dqy 'khVSHEETS 00228 | CONT'D ON: 00042

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SIGNAL & TELECOMMUNICATIONS

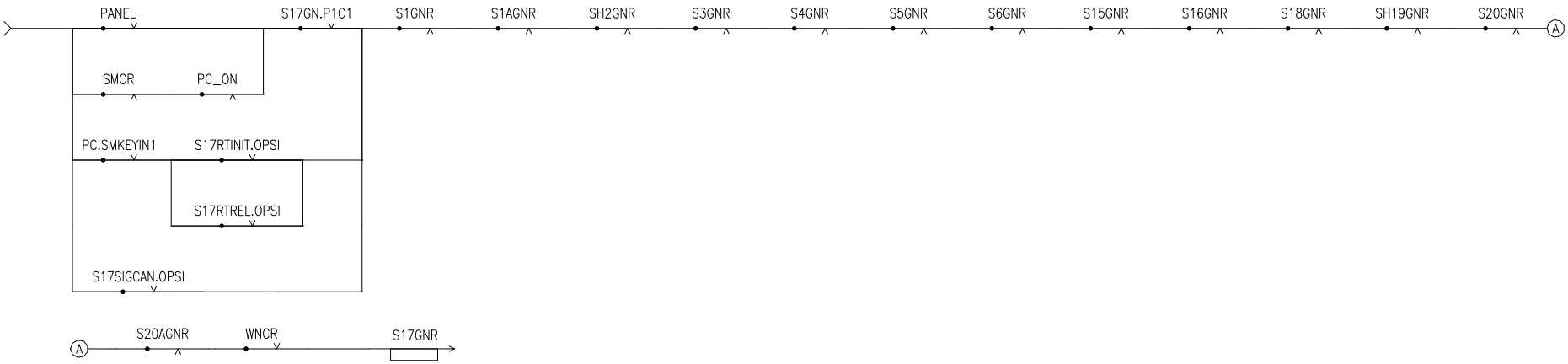
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00041_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ((PANEL + ~SMCR * ~PC_ON) * S17GN.P1C1 + PC.SMKEYIN1 * (S17RTINIT.OPSI + S17RTREL.OPSI) + S17SIGCAN.OPSI) * ~S1GNR * ~S1AGNR * ~SH2GNR * ~S3GNR * ~S4GNR * ~S5GNR * ~S6GNR * ~S15GNR * ~S16GNR * ~S18GNR * ~SH19GNR * ~S20GNR * ~S20AGNR * WNCR TO S17GNR;



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TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B
n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khV SHEET 00042
dqy 'khVSHEETS 00228
CONT'D ON: 00043

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

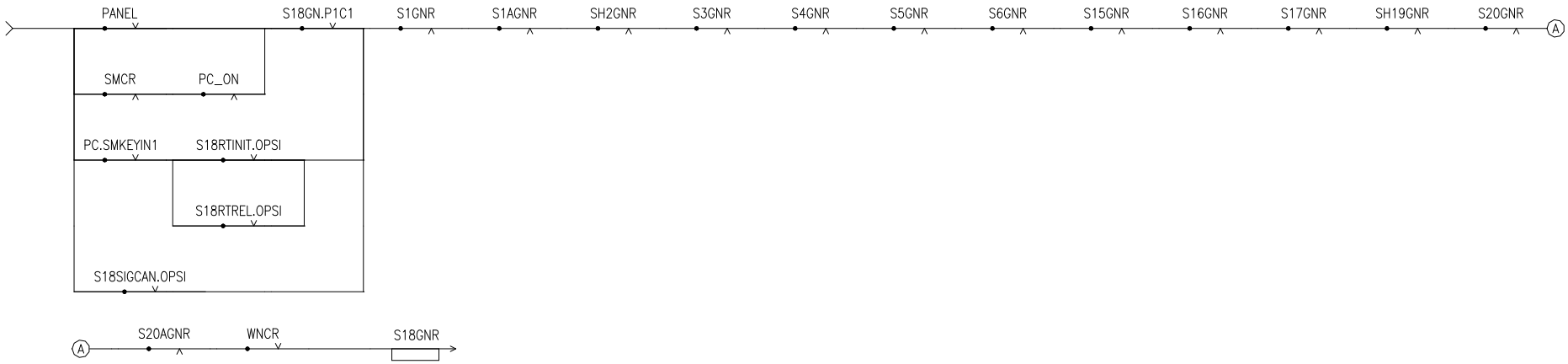
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00042_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ((PANEL + ~SMCR * ~PC_ON) * S18GN.P1C1 + PC.SMKEYIN1 * (S18RTINIT.OPSI + S18RTREL.OPSI) + S18SIGCAN.OPSI) * ~S1GNR * ~S1AGNR * ~SH2GNR * ~S3GNR * ~S4GNR * ~S5GNR * ~S6GNR * ~S15GNR * ~S16GNR * ~S17GNR * ~SH19GNR * ~S20GNR * ~S20AGNR * WNCR TO S18GNR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP:

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

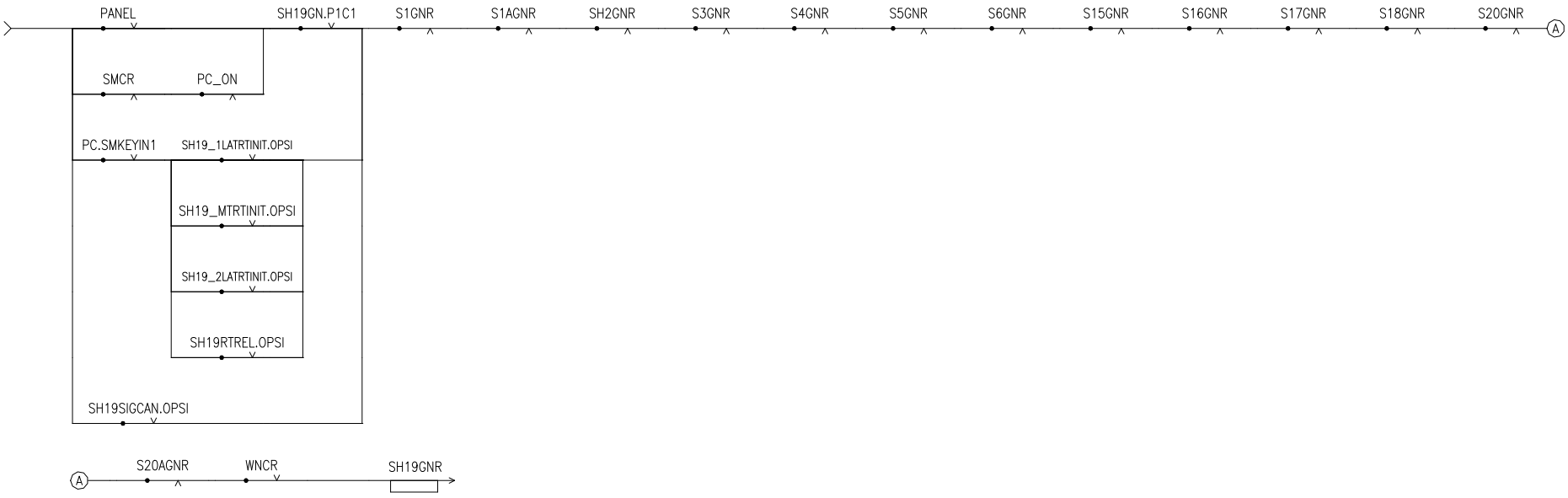
REF.SIG.PLAN.No. IPU.2107B
n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khV SHEET 00043
dqy 'khVSHEETS 00228
CONT'D ON: 00044

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00043_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ((PANEL + ~SMCR * ~PC_ON) * SH19GN.P1C1 + PC.SMKEYIN1 * (SH19_1LATRTINIT.OPSI + SH19_MTRTINIT.OPSI + SH19_2LATRTINIT.OPSI + SH19RTREL.OPSI) + SH19SIGCAN.OPSI) * ~S1GNR * ~S1AGNR * ~SH2GNR * ~S3GNR * ~S4GNR * ~S5GNR * ~S6GNR * ~S15GNR * ~S16GNR * ~S17GNR * ~S18GNR * ~S20GNR * ~S20AGNR * WNCR TO SH19GNR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00044 | dqy 'khVSHEETS 00228 | CONT'D ON: 00045

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

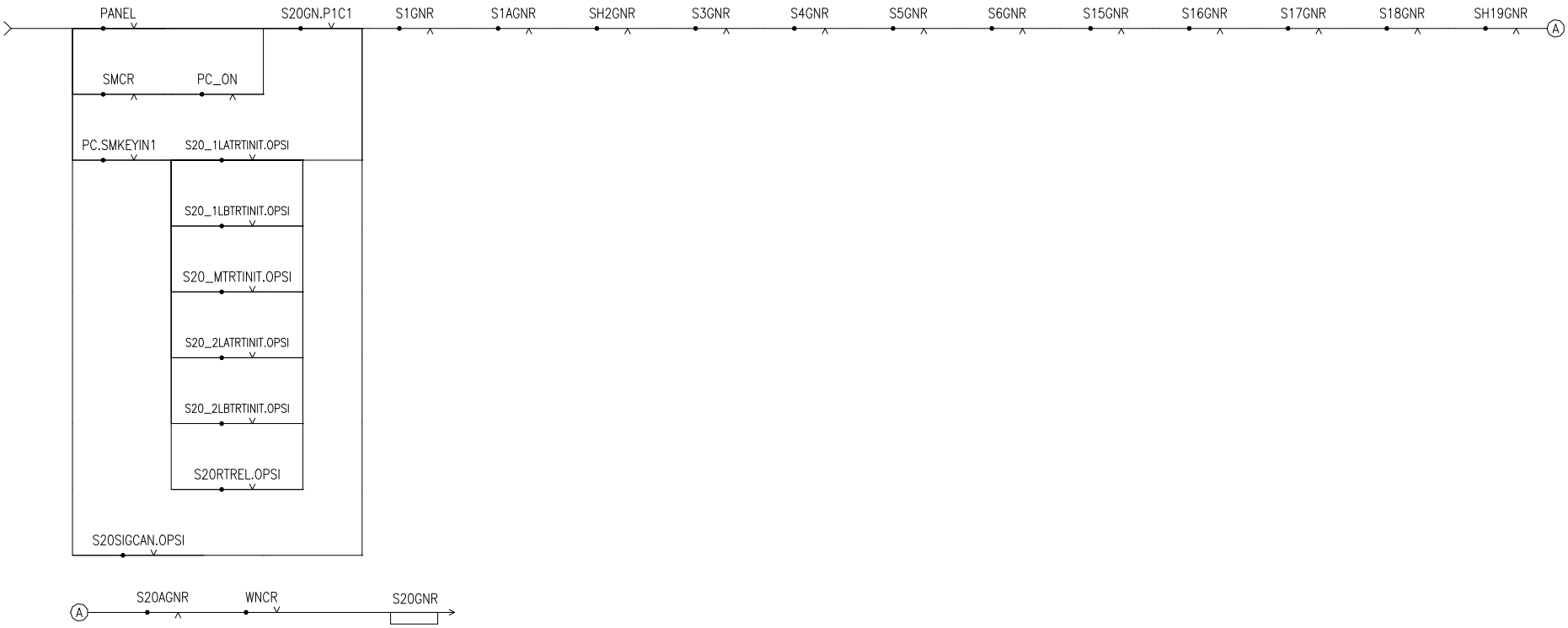
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00044_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ((PANEL + ~SMCR * ~PC_ON) * S20GN.P1C1 + PC.SMKEYIN1 * (S20_1LATRTINIT.OPSI + S20_1LBTRTINIT.OPSI + S20_MTRTINIT.OPSI + S20_2LATRTINIT.OPSI + S20_2LBTRTINIT.OPSI + S20RTREL.OPSI) + S20SIGCAN.OPSI) * ~S1GNR * ~S1AGNR * ~SH2GNR * ~S3GNR * ~S4GNR * ~S5GNR * ~S6GNR * ~S15GNR * ~S16GNR * ~S17GNR * ~S18GNR * ~SH19GNR * ~S20AGNR * WNCR TO S20GNR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00045 | dqy 'khVSHEETS 00228 | CONT'D ON: 00046

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

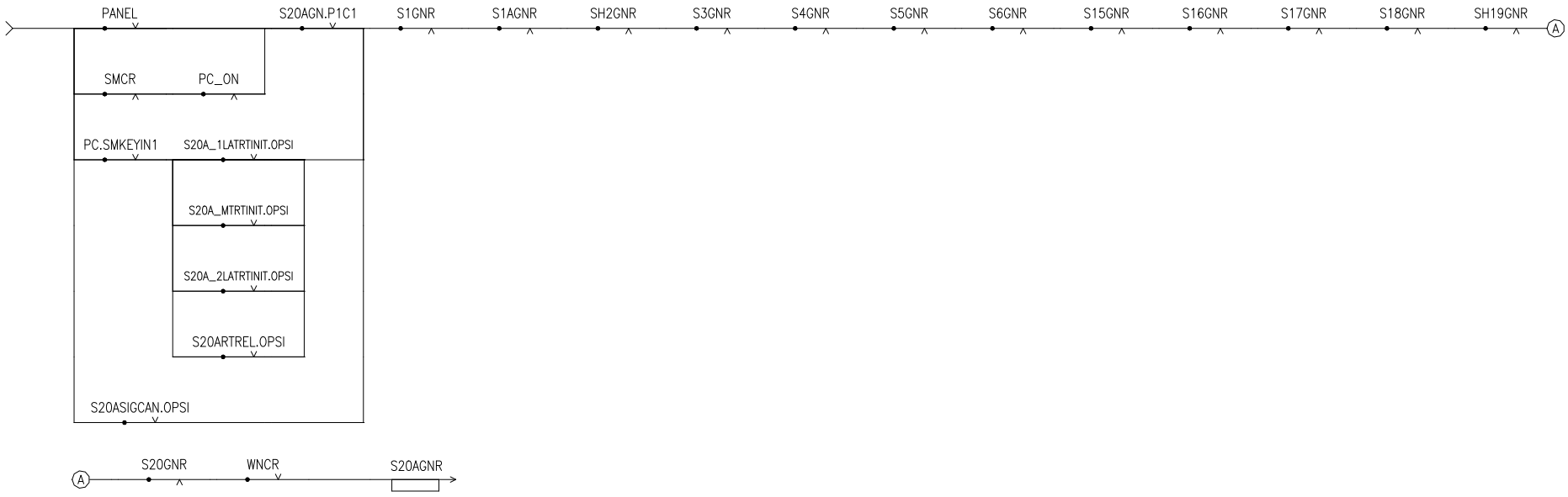
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00045_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ((PANEL + ~SMCR * ~PC_ON) * S20AGN.P1C1 + PC.SMKEYIN1 * (S20A_1LATRTINIT.OPSI + S20A_MTRTINIT.OPSI + S20A_2LATRTINIT.OPSI + S20ARTREL.OPSI) + S20ASIGCAN.OPSI) * ~S1GNR * ~S1AGNR * ~SH2GNR * ~S3GNR * ~S4GNR * ~S5GNR * ~S6GNR * ~S15GNR * ~S16GNR * ~S17GNR * ~S18GNR * ~SH19GNR * ~S20GNR * WNCR TO S20AGNR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00046 | dqy 'khVSHEETS 00228 | CONT'D ON: 00047

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

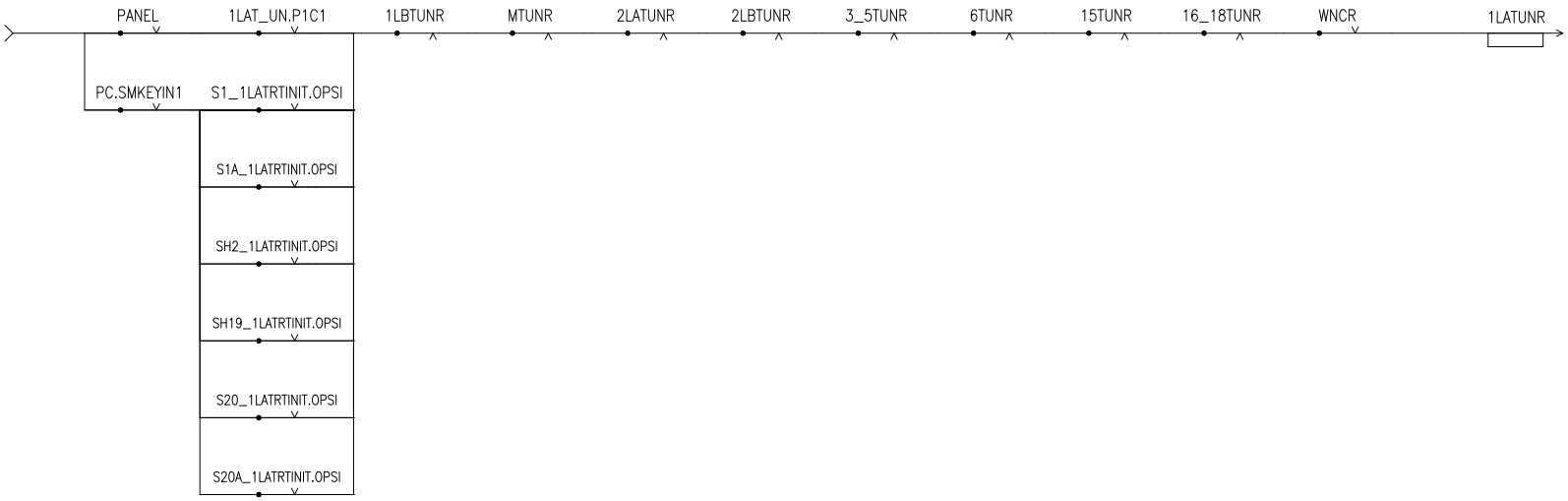
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

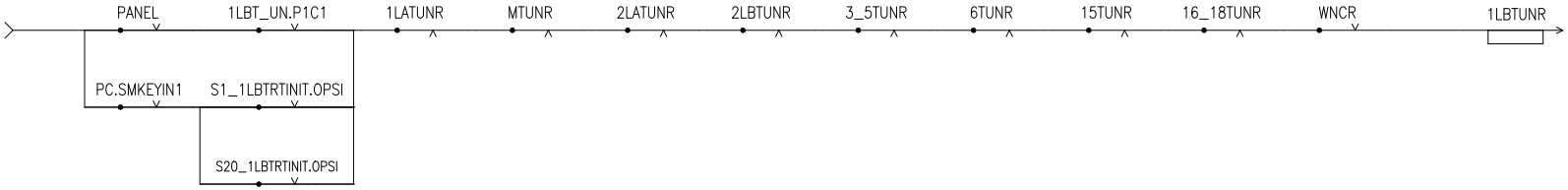
CAD FILE: 512_TAC_C1_00046_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN (PANEL * 1LAT_UN.P1C1 + PC.SMKEYIN1 * (S1_1LATRTINIT.OPSI + S1A_1LATRTINIT.OPSI + SH2_1LATRTINIT.OPSI + SH19_1LATRTINIT.OPSI + S20_1LATRTINIT.OPSI + S20A_1LATRTINIT.OPSI)) * ~1LBTUNR * ~MTUNR * ~2LATUNR * ~2LBTUNR * ~3_5TUNR * ~6TUNR * ~15TUNR * ~16_18TUNR * WNCR TO 1LATUNR;



ASSIGN (PANEL * 1LBT_UN.P1C1 + PC.SMKEYIN1 * (S1_1LBTRTINIT.OPSI + S20_1LBTRTINIT.OPSI)) * ~1LATUNR * ~MTUNR * ~2LATUNR * ~2LBTUNR * ~3_5TUNR * ~6TUNR * ~15TUNR * ~16_18TUNR * WNCR TO 1LBTUNR;



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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
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Bengaluru – 560 078
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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

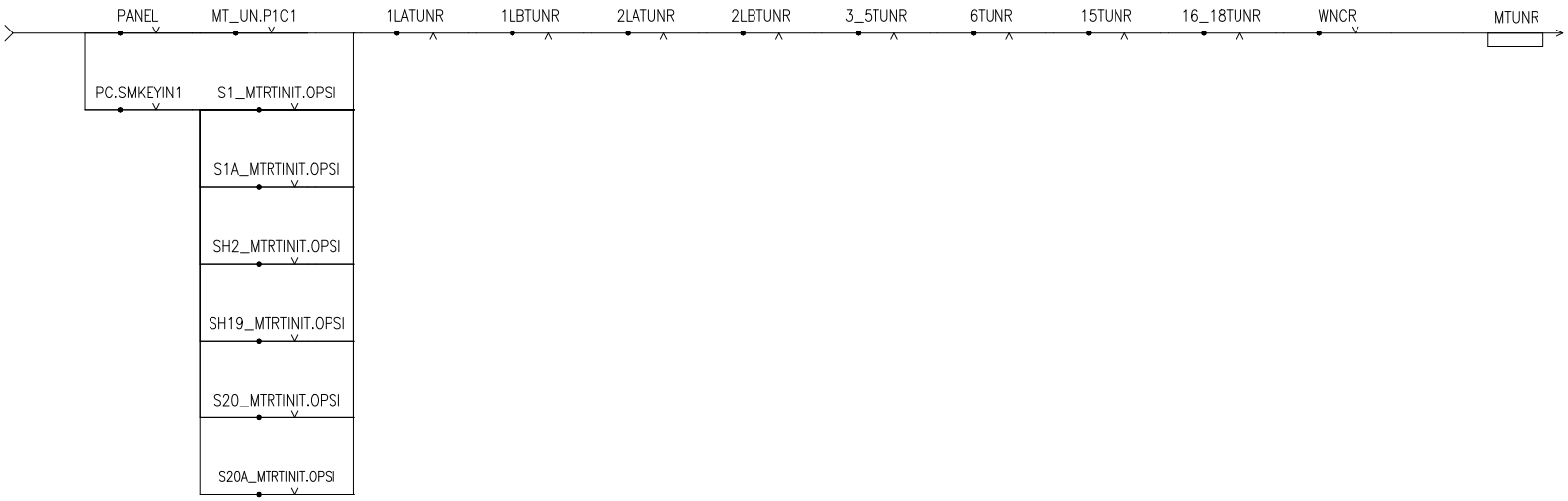
REF.SIG.PLAN.No. IPU.2107B
n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khV SHEET 00047 dqy 'khVSHEETS 00228 CONT'D ON: 00048

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00047_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN (PANEL * MT_UN.P1C1 + PC.SMKEYIN1 * (S1_MTRTINIT.OPSI + S1A_MTRTINIT.OPSI + SH2_MTRTINIT.OPSI + SH19_MTRTINIT.OPSI + S20_MTRTINIT.OPSI + S20A_MTRTINIT.OPSI)) * ~1LATUNR * ~1LBTUNR * ~2LATUNR * ~2LBTUNR * ~3_5TUNR * ~6TUNR * ~15TUNR * ~16_18TUNR * WNCR TO MTUNR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khVSHEET 00048 | dqy 'khVSHEETS 00228 | CONT'D ON: 00049

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

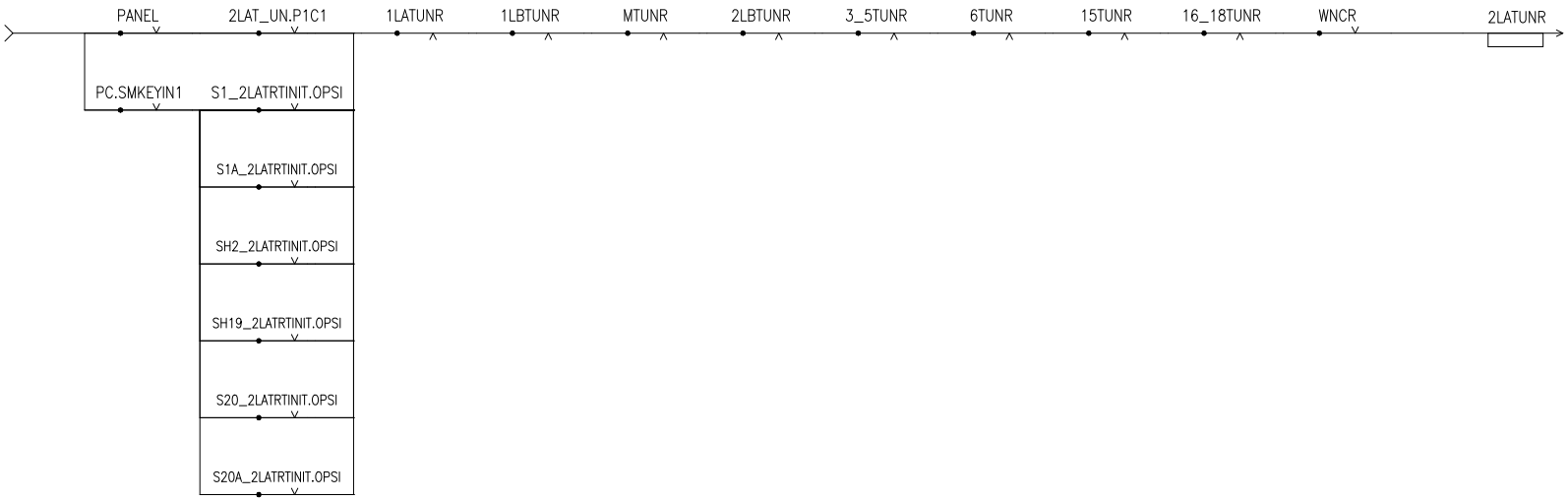
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

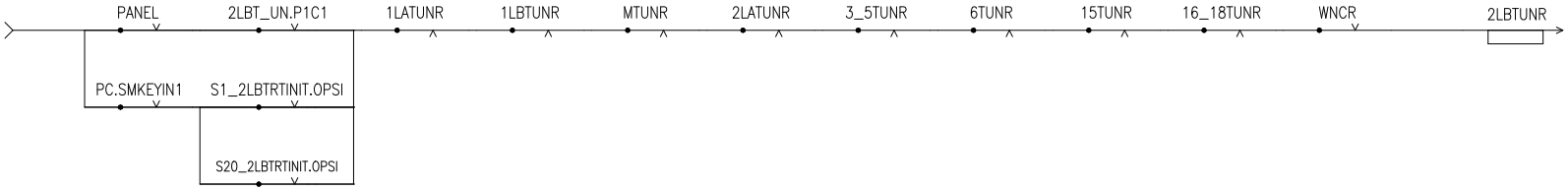
CAD FILE: 512_TAC_C1_00048_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN (PANEL * 2LAT_UN.P1C1 + PC.SMKEYIN1 * (S1_2LATRTINIT.OPSI + S1A_2LATRTINIT.OPSI + SH2_2LATRTINIT.OPSI + SH19_2LATRTINIT.OPSI + S20_2LATRTINIT.OPSI + S20A_2LATRTINIT.OPSI)) * ~1LATUNR * ~1LBTUNR * ~MTUNR * ~2LBTUNR * ~3_5TUNR * ~6TUNR * ~15TUNR * ~16_18TUNR * WNCR TO 2LATUNR;



ASSIGN (PANEL * 2LBT_UN.P1C1 + PC.SMKEYIN1 * (S1_2LBTRTINIT.OPSI + S20_2LBTRTINIT.OPSI)) * ~1LATUNR * ~1LBTUNR * ~MTUNR * ~2LATUNR * ~3_5TUNR * ~6TUNR * ~15TUNR * ~16_18TUNR * WNCR TO 2LBTUNR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00049 | dqy 'khVSHEETS 00228 | CONT'D ON: 00050

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

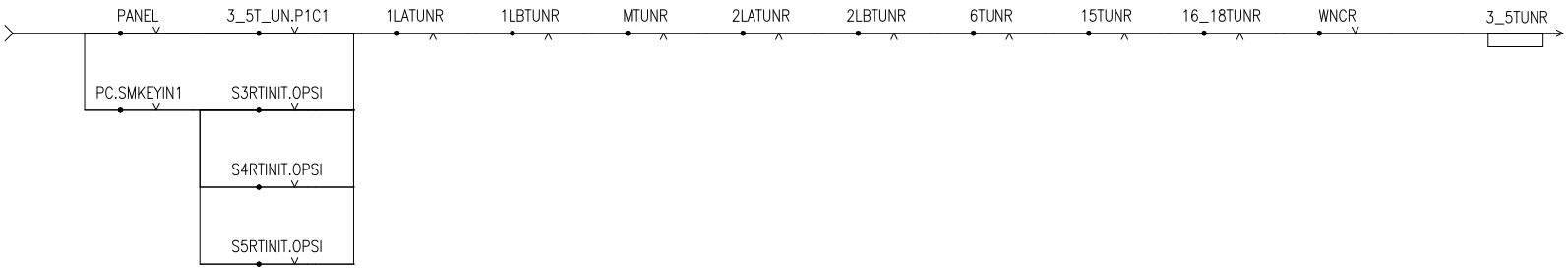
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

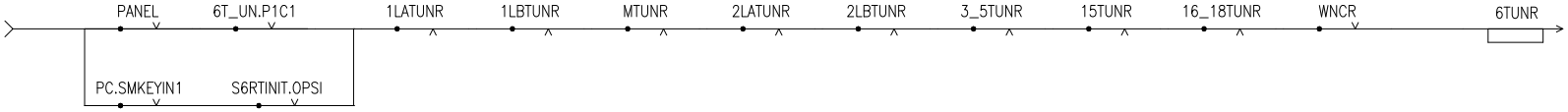
CAD FILE: 512_TAC_C1_00049_R00.DWG

(MICROLOK-II WARM STANDBY)

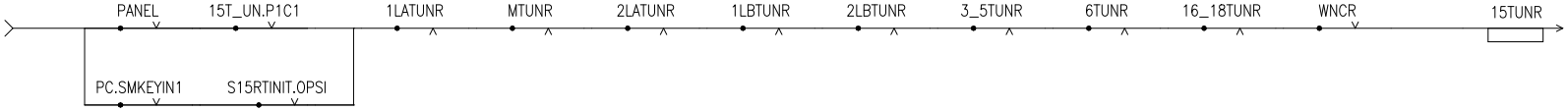
ASSIGN (PANEL * 3_5T_UN.P1C1 + PC.SMKEYIN1 * (S3RTINIT.OPSI + S4RTINIT.OPSI + S5RTINIT.OPSI)) * ~1LATUNR * ~1LBTUNR * ~MTUNR * ~2LATUNR * ~2LBTUNR * ~6TUNR * ~15TUNR * ~16_18TUNR * WNCR TO 3_5TUNR;



ASSIGN (PANEL * 6T_UN.P1C1 + PC.SMKEYIN1 * S6RTINIT.OPSI) * ~1LATUNR * ~1LBTUNR * ~MTUNR * ~2LATUNR * ~2LBTUNR * ~3_5TUNR * ~15TUNR * ~16_18TUNR * WNCR TO 6TUNR;



ASSIGN (PANEL * 15T_UN.P1C1 + PC.SMKEYIN1 * (S15RTINIT.OPSI)) * ~1LATUNR * ~MTUNR * ~2LATUNR * ~1LBTUNR * ~2LBTUNR * ~3_5TUNR * ~6TUNR * ~16_18TUNR * WNCR TO 15TUNR;



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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00050 | dqy 'khVSHEETS 00228 | CONT'D ON: 00051

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SIGNAL & TELECOMMUNICATIONS

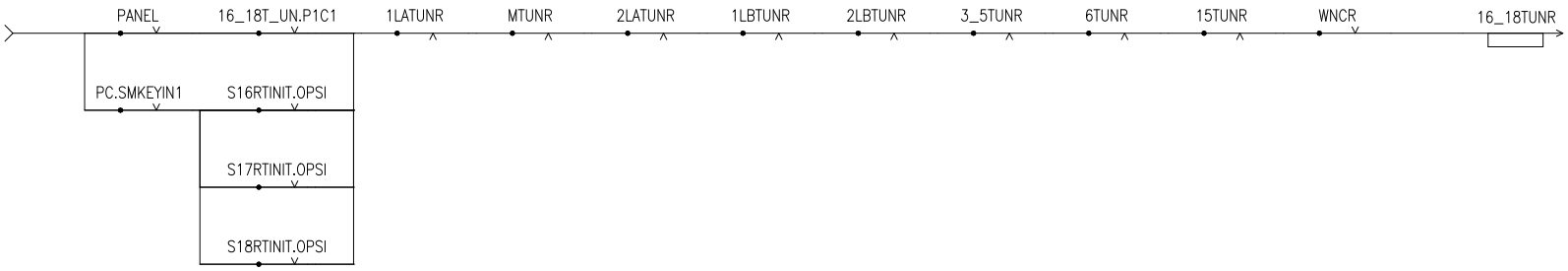
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

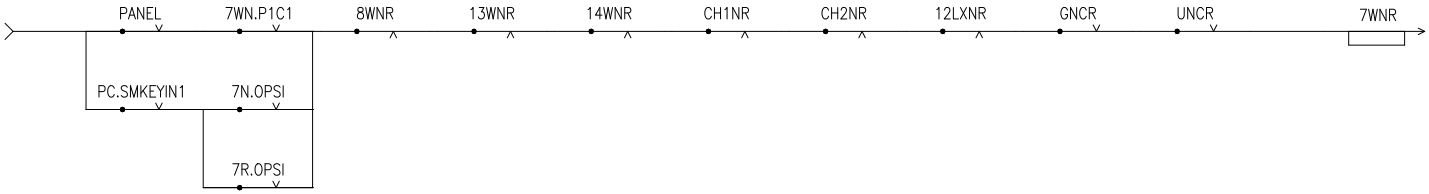
CAD FILE: 512_TAC_C1_00050_R00.DWG

(MICROLOK-II WARM STANDBY)

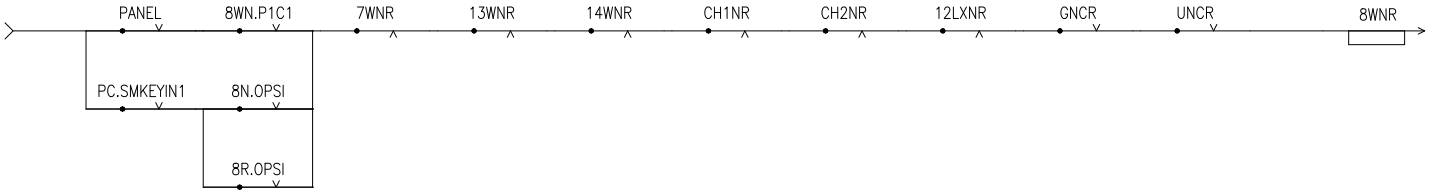
ASSIGN (PANEL * 16_18T_UN.P1C1 + PC.SMKEYIN1 * (S16RTINIT.OPSI + S17RTINIT.OPSI + S18RTINIT.OPSI)) * ~1LATUNR * ~MTUNR * ~2LATUNR * ~1LBTUNR * ~2LBTUNR * ~3_5TUNR * ~6TUNR * ~15TUNR * WNCR
TO 16_18TUNR;



ASSIGN (PANEL * 7WN.P1C1 + PC.SMKEYIN1 * (7N.OPSI + 7R.OPSI)) * ~8WNR * ~13WNR * ~14WNR * ~CH1NR * ~CH2NR * ~12LXNR * GNCR * UNCR TO 7WNR;



ASSIGN (PANEL * 8WN.P1C1 + PC.SMKEYIN1 * (8N.OPSI + 8R.OPSI)) * ~7WNR * ~13WNR * ~14WNR * ~CH1NR * ~CH2NR * ~12LXNR * GNCR * UNCR TO 8WNR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
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TVM Signalling & Transportation Systems Pvt. Ltd.
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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00051 | dqy 'khVSHEETS 00228 | CONT'D ON: 00052

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SIGNAL & TELECOMMUNICATIONS

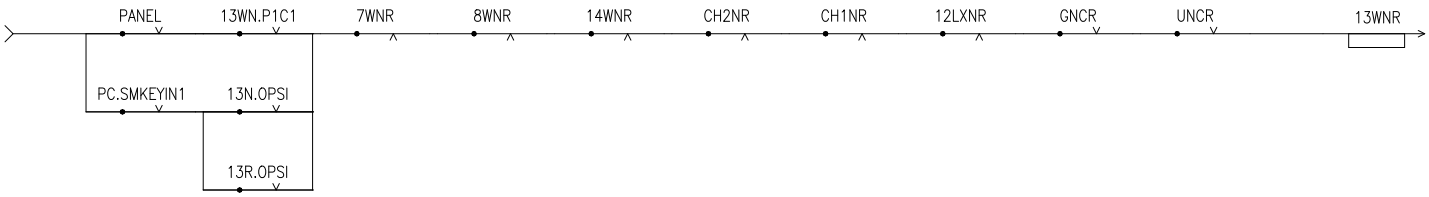
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

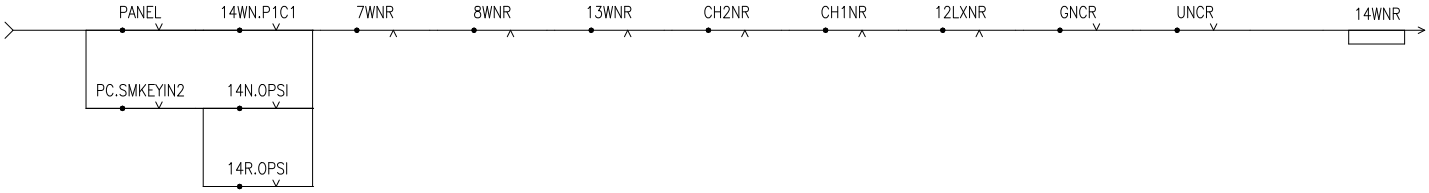
CAD FILE: 512_TAC_C1_00051_R00.DWG

(MICROLOK-II WARM STANDBY)

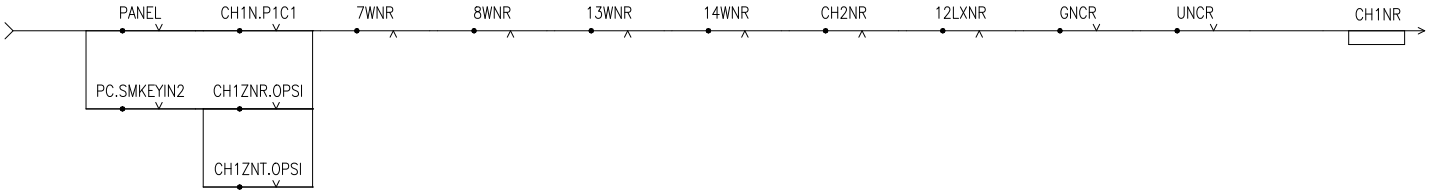
ASSIGN (PANEL * 13WN.P1C1 + PC.SMKEYIN1 * (13N.OPSI + 13R.OPSI)) * ~7WNR * ~8WNR * ~14WNR * ~CH2NR * ~CH1NR * ~12LXNR * GNCR * UNCR TO 13WNR;



ASSIGN (PANEL * 14WN.P1C1 + PC.SMKEYIN2 * (14N.OPSI + 14R.OPSI)) * ~7WNR * ~8WNR * ~13WNR * ~CH2NR * ~CH1NR * ~12LXNR * GNCR * UNCR TO 14WNR;



ASSIGN (PANEL * CH1N.P1C1 + PC.SMKEYIN2 * (CH1ZNR.OPSI + CH1ZNT.OPSI)) * ~7WNR * ~8WNR * ~13WNR * ~14WNR * ~CH2NR * ~12LXNR * GNCR * UNCR TO CH1NR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO a fc B

'khV SHEET 00052 | dqy 'khVSHEETS 00228 | CONT'D ON: 00053

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

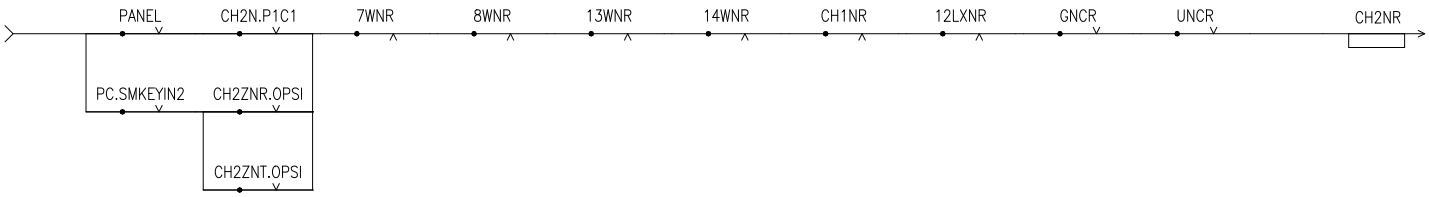
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

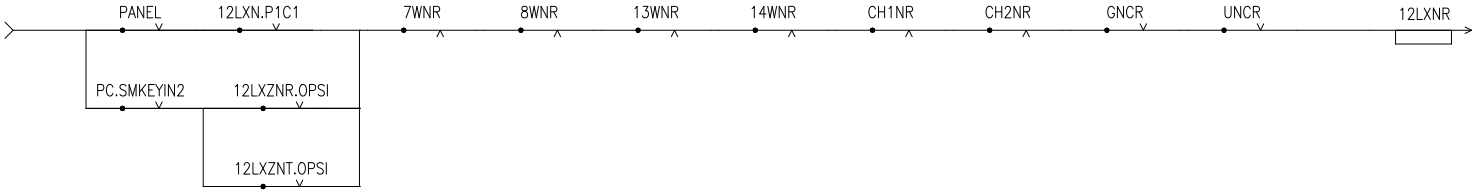
CAD FILE: 512_TAC_C1_00052_R00.DWG

(MICROLOK-II WARM STANDBY)

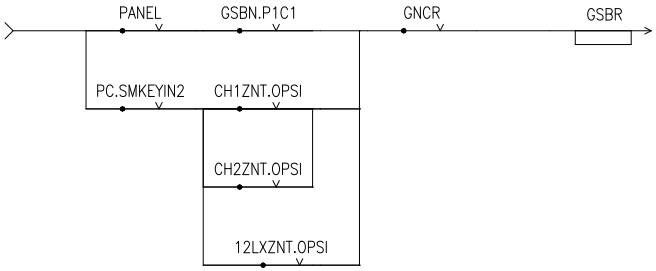
ASSIGN (PANEL * CH2N.P1C1 + PC.SMKEYIN2 * (CH2ZNR.OPSI + CH2ZNT.OPSI)) * ~7WNR * ~8WNR * ~13WNR * ~14WNR * ~CH1NR * ~12LXNR * GNCR * UNCR TO CH2NR;



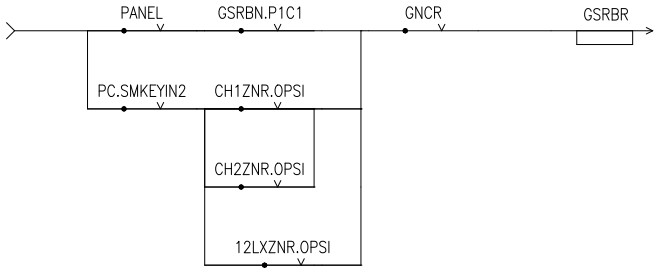
ASSIGN (PANEL * 12LXN.P1C1 + PC.SMKEYIN2 * (12LXZNR.OPSI + 12LXZNT.OPSI)) * ~7WNR * ~8WNR * ~13WNR * ~14WNR * ~CH1NR * ~CH2NR * GNCR * UNCR TO 12LXNR;



ASSIGN (PANEL * GSBN.P1C1 + PC.SMKEYIN2 * (CH1ZNT.OPSI + CH2ZNT.OPSI + 12LXZNT.OPSI)) * GNCR TO GSBR;



ASSIGN (PANEL * GSRBN.P1C1 + PC.SMKEYIN2 * (CH1ZNR.OPSI + CH2ZNR.OPSI + 12LXZNR.OPSI)) * GNCR TO GSRBR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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CHK. APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

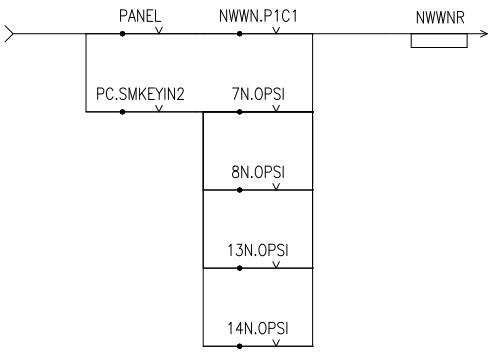
REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khVSHEET 00053 | dqy 'khVSHEETS 00228 | CONT'D ON: 00054

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

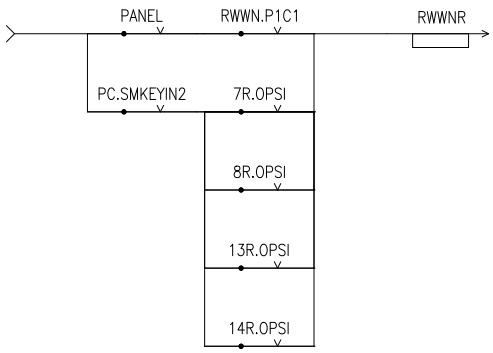
CAD FILE: 512_TAC_C1_00053_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN (PANEL * NWWN.P1C1 + PC.SMKEYIN2 * (7N.OPSI + 8N.OPSI + 13N.OPSI + 14N.OPSI)) TO NWWNR;



ASSIGN (PANEL * RWWN.P1C1 + PC.SMKEYIN2 * (7R.OPSI + 8R.OPSI + 13R.OPSI + 14R.OPSI)) TO RWWNR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
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Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00054	00228	00055

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

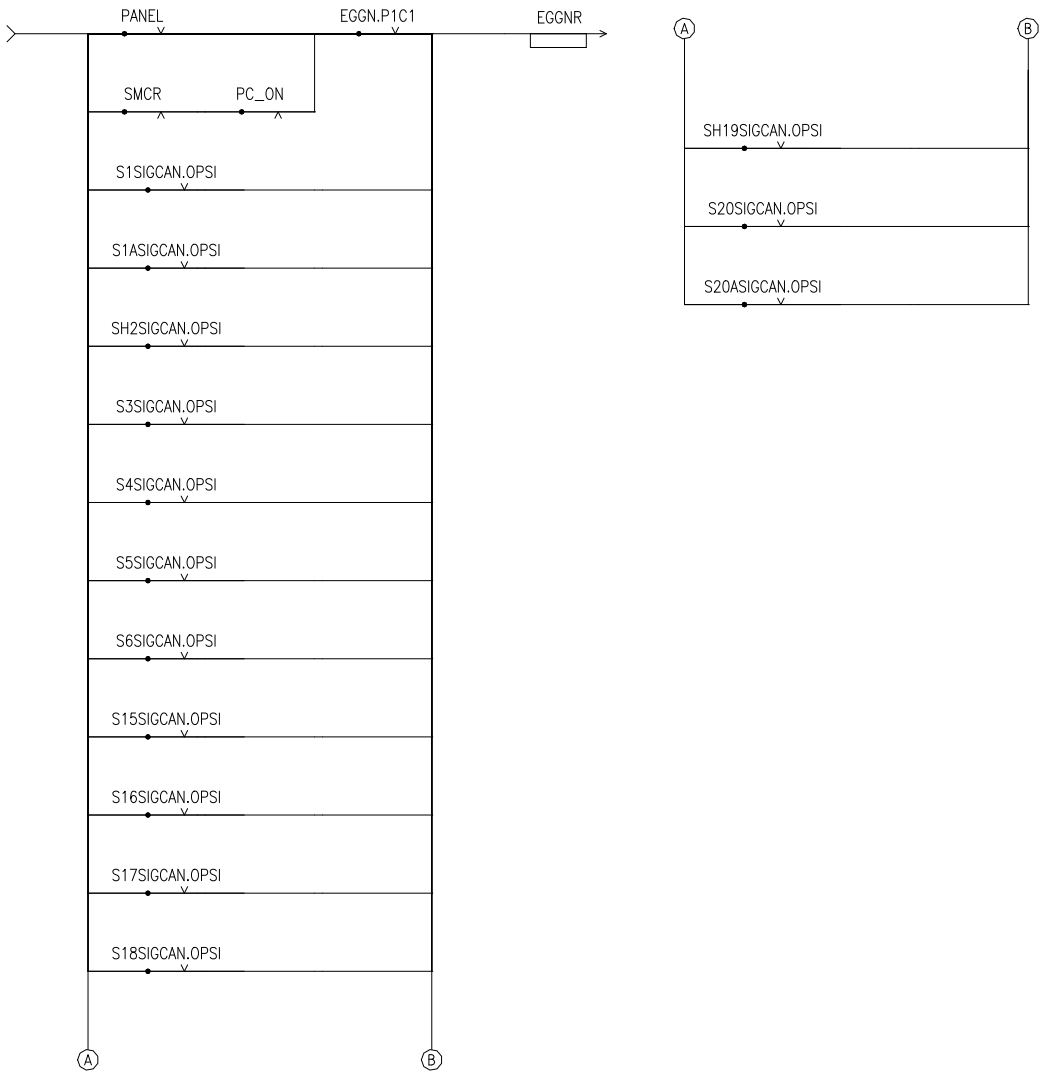
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00054_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ((PANEL + ~SMCR * ~PC_ON) * EGGN.P1C1 + S1SIGCAN.OPSI + S1ASIGCAN.OPSI + SH2SIGCAN.OPSI + S3SIGCAN.OPSI + S4SIGCAN.OPSI + S5SIGCAN.OPSI + S6SIGCAN.OPSI + S15SIGCAN.OPSI + S16SIGCAN.OPSI + S17SIGCAN.OPSI + S18SIGCAN.OPSI + SH19SIGCAN.OPSI + S20SIGCAN.OPSI + S20ASIGCAN.OPSI) TO EGGNR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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CHK.
APP:

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

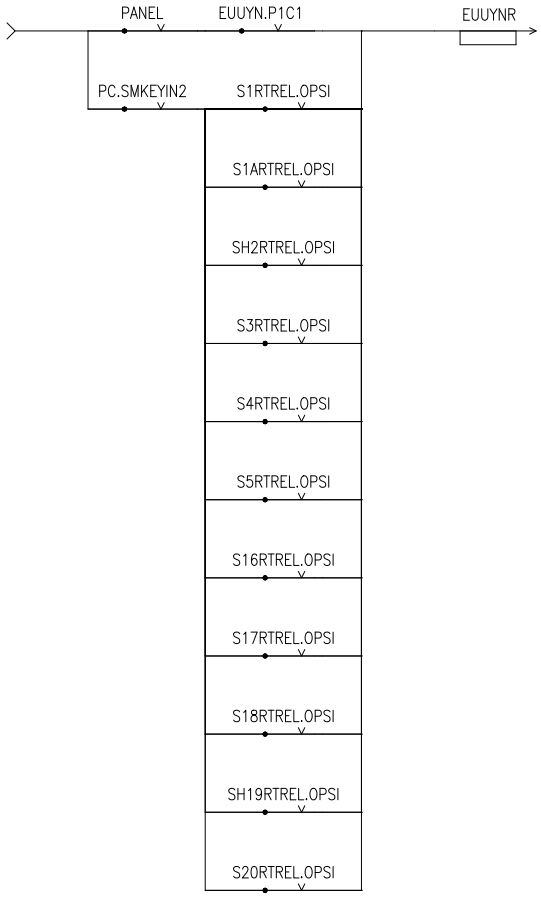
REF.SIG.PLAN.No. IPU.2107B
n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khVSHEET 00055
dqy 'khVSHEETS 00228
CONT'D ON: 00056

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

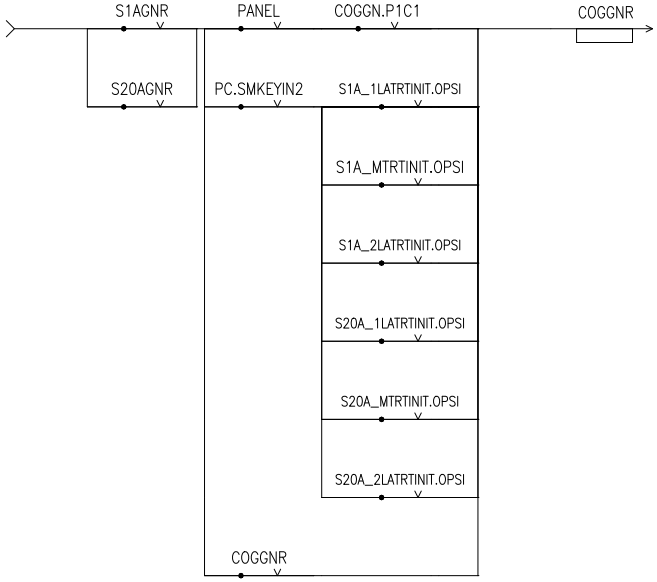
CAD FILE: 512_TAC_C1_00055_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN (PANEL * EUUYN.P1C1 + PC.SMKEYIN2 * (S1RTREL.OPSI + S1ARTREL.OPSI + SH2RTREL.OPSI + S3RTREL.OPSI + S4RTREL.OPSI + S5RTREL.OPSI + S16RTREL.OPSI + S17RTREL.OPSI + S18RTREL.OPSI + SH19RTREL.OPSI + S20RTREL.OPSI)) TO EUUYNR;



ASSIGN (S1AGNR + S20AGNR) * (PANEL * COGGN.P1C1 + PC.SMKEYIN2 * (S1A_1LATRTINIT.OPSI + S1A_MTRTINIT.OPSI + S1A_2LATRTINIT.OPSI + S20A_1LATRTINIT.OPSI + S20A_MTRTINIT.OPSI + S20A_2LATRTINIT.OPSI) + COGGNR) TO COGGNR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO a fc B

'khV SHEET 00056 | dqy 'khVSHEETS 00228 | CONT'D ON: 00057

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

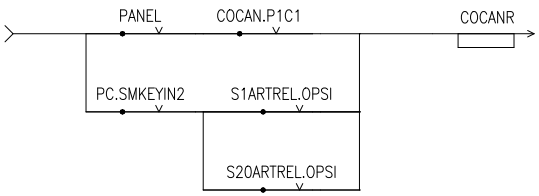
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

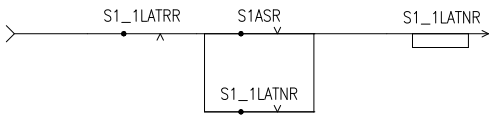
CAD FILE: 512_TAC_C1_00056_R00.DWG

(MICROLOK-II WARM STANDBY)

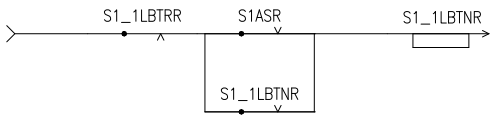
ASSIGN PANEL * COCAN.P1C1 + PC.SMKEYIN2 * (S1ARTREL.OPSI + S20ARTREL.OPSI) TO COCANR;



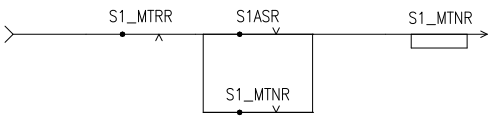
ASSIGN ~S1_1LATRR * (S1ASR + S1_1LATNR) TO S1_1LATNR;



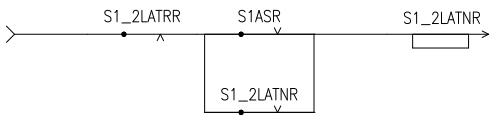
ASSIGN ~S1_1LBTRR * (S1ASR + S1_1LBTNR) TO S1_1LBTNR;



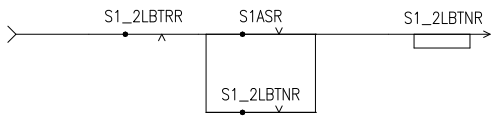
ASSIGN ~S1_MTRR * (S1ASR + S1_MTNR) TO S1_MTNR;



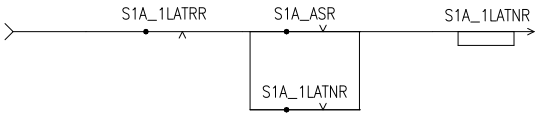
ASSIGN ~S1_2LATRR * (S1ASR + S1_2LATNR) TO S1_2LATNR;



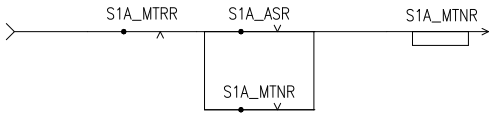
ASSIGN ~S1_2LBTRR * (S1ASR + S1_2LBTNR) TO S1_2LBTNR;



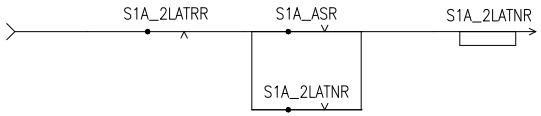
ASSIGN ~S1A_1LATRR * (S1A_ASR + S1A_1LATNR) TO S1A_1LATNR;



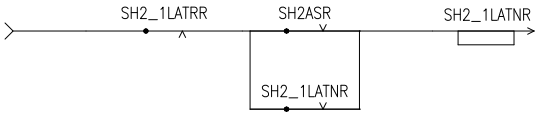
ASSIGN ~S1A_MTRR * (S1A_ASR + S1A_MTNR) TO S1A_MTNR;



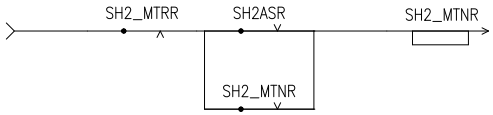
ASSIGN ~S1A_2LATRR * (S1A_ASR + S1A_2LATNR) TO S1A_2LATNR;



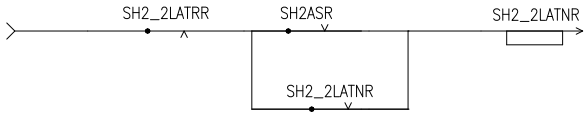
ASSIGN ~SH2_1LATRR * (SH2ASR + SH2_1LATNR) TO SH2_1LATNR;



ASSIGN ~SH2_MTRR * (SH2ASR + SH2_MTNR) TO SH2_MTNR;



ASSIGN ~SH2_2LATRR * (SH2ASR + SH2_2LATNR) TO SH2_2LATNR;



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TVM Signalling & Transportation Systems Pvt. Ltd.
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
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SE/SSE	
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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET 00057	dqy 'khVSHEETS 00228	CONT'D ON: 00058

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

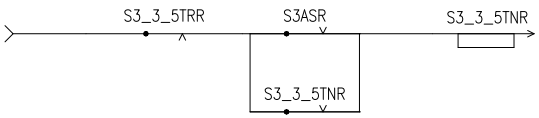
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

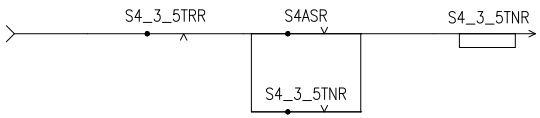
CAD FILE: 512_TAC_C1_00057_R00.DWG

(MICROLOK-II WARM STANDBY)

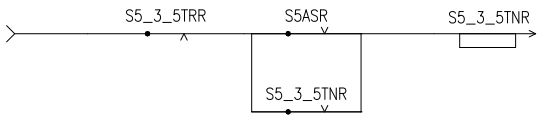
ASSIGN ~S3_3_5TRR * (S3ASR + S3_3_5TNR) TO S3_3_5TNR;



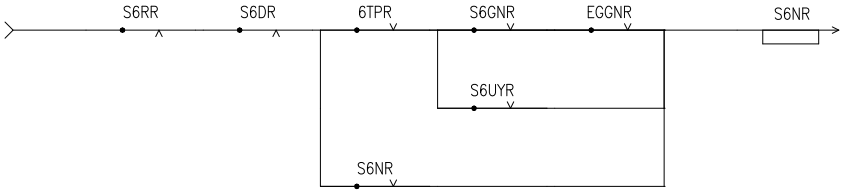
ASSIGN ~S4_3_5TRR * (S4ASR + S4_3_5TNR) TO S4_3_5TNR;



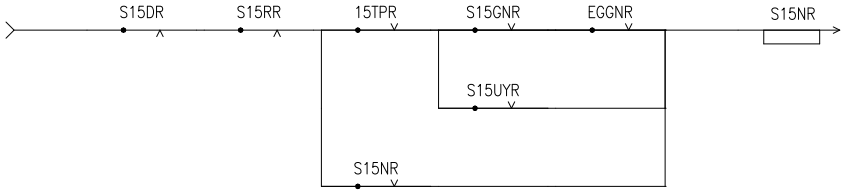
ASSIGN ~S5_3_5TRR * (S5ASR + S5_3_5TNR) TO S5_3_5TNR;



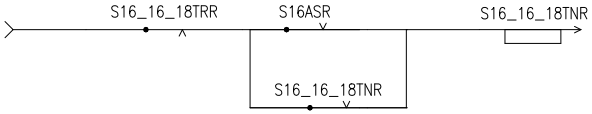
ASSIGN ~S6RR * ~S6DR * (6TPR * (S6GNR * EGGNR + S6UYR) + S6NR) TO S6NR;



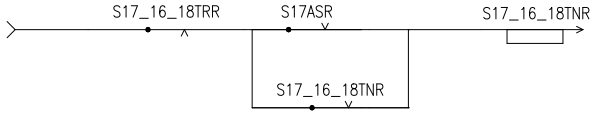
ASSIGN ~S15DR * ~S15RR * (15TPR * (S15GNR * EGGNR + S15UYR) + S15NR) TO S15NR;



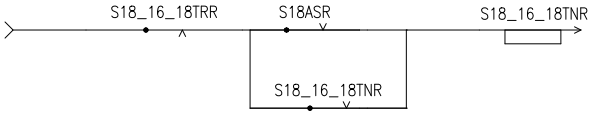
ASSIGN ~S16_16_18TRR * (S16ASR + S16_16_18TNR) TO S16_16_18TNR;



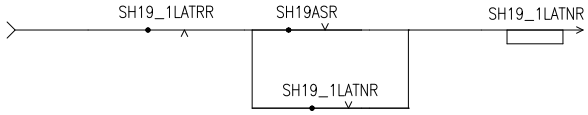
ASSIGN ~S17_16_18TRR * (S17ASR + S17_16_18TNR) TO S17_16_18TNR;



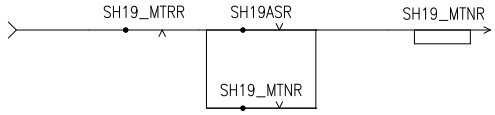
ASSIGN ~S18_16_18TRR * (S18ASR + S18_16_18TNR) TO S18_16_18TNR;



ASSIGN ~SH19_1LATRR * (SH19ASR + SH19_1LATNR) TO SH19_1LATNR;



ASSIGN ~SH19_MTRR * (SH19ASR + SH19_MTNR) TO SH19_MTNR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khVSHEET 00058	dqy 'khVSHEETS 00228	CONT'D ON: 00059

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

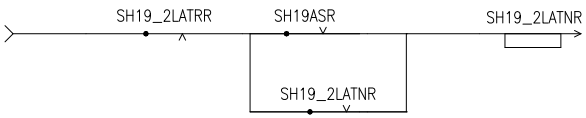
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

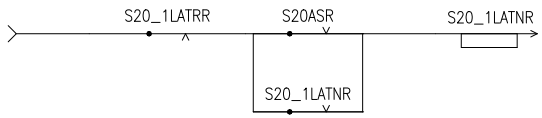
CAD FILE: 512_TAC_C1_00058_R00.DWG

(MICROLOK-II WARM STANDBY)

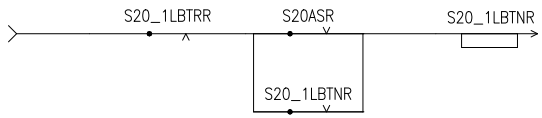
ASSIGN ~SH19_2LATRR * (SH19ASR + SH19_2LATNR) TO SH19_2LATNR;



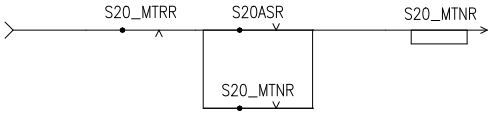
ASSIGN ~S20_1LATRR * (S20ASR + S20_1LATNR) TO S20_1LATNR;



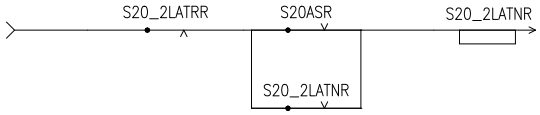
ASSIGN ~S20_1LBTRR * (S20ASR + S20_1LBTNR) TO S20_1LBTNR;



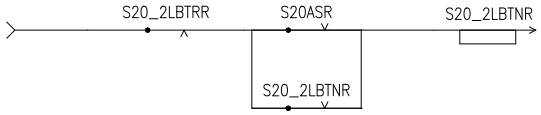
ASSIGN ~S20_MTRR * (S20ASR + S20_MTNR) TO S20_MTNR;



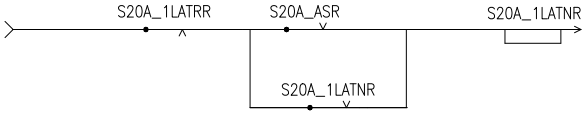
ASSIGN ~S20_2LATRR * (S20ASR + S20_2LATNR) TO S20_2LATNR;



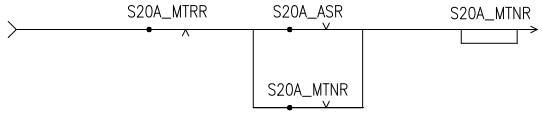
ASSIGN ~S20_2LBTRR * (S20ASR + S20_2LBTNR) TO S20_2LBTNR;



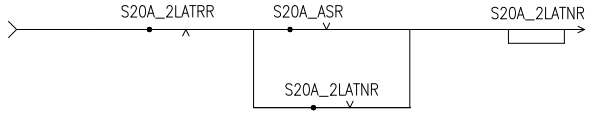
ASSIGN ~S20A_1LATRR * (S20A_ASR + S20A_1LATNR) TO S20A_1LATNR;



ASSIGN ~S20A_MTRR * (S20A_ASR + S20A_MTNR) TO S20A_MTNR;



ASSIGN ~S20A_2LATRR * (S20A_ASR + S20A_2LATNR) TO S20A_2LATNR;



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00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
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Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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SE/SSE	
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APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00059 | dqy 'khVSHEETS 00228 | CONT'D ON: 00060

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

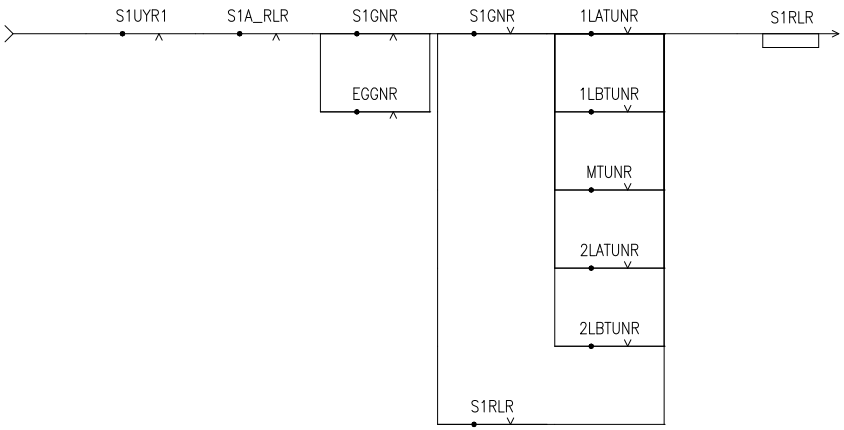
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

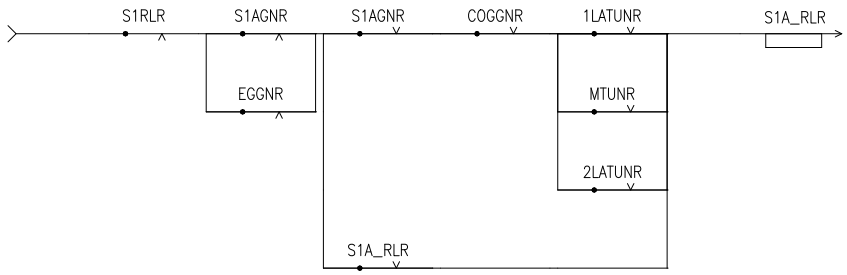
CAD FILE: 512_TAC_C1_00059_R00.DWG

(MICROLOK-II WARM STANDBY)

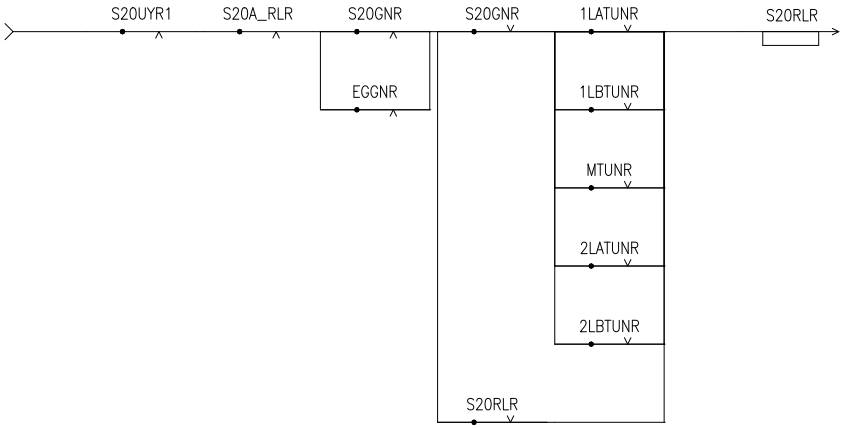
ASSIGN ~S1UYR1 * ~S1A_RLR * (~S1GNR + ~EGGNR) * (S1GNR * (1LATUNR + 1LBTUNR + MTUNR + 2LATUNR + 2LBTUNR) + S1RLR) TO S1RLR;



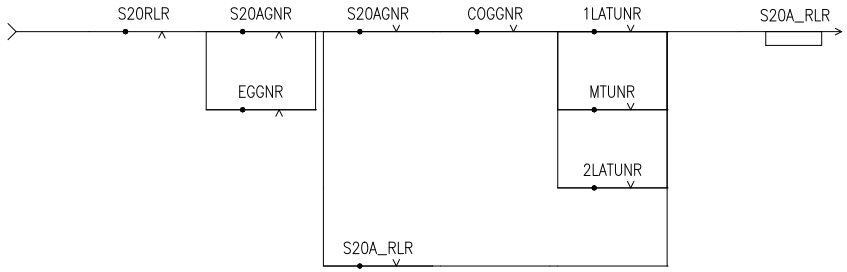
ASSIGN ~S1RLR * (~S1AGNR + ~EGGNR) * (S1AGNR * COGGNR * (1LATUNR + MTUNR + 2LATUNR) + S1A_RLR) TO S1A_RLR;



ASSIGN ~S20UYR1 * ~S20A_RLR * (~S20GNR + ~EGGNR) * (S20GNR * (1LATUNR + 1LBTUNR + MTUNR + 2LATUNR + 2LBTUNR) + S20RLR) TO S20RLR;



ASSIGN ~S20RLR * (~S20AGNR + ~EGGNR) * (S20AGNR * COGGNR * (1LATUNR + MTUNR + 2LATUNR) + S20A_RLR) TO S20A_RLR;



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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

	REVISION
CHK.	JE-1
	SE/SSE
	ASTE
	SSTE
APP.	f CSTE

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00060 | dqy 'khVSHEETS 00228 | CONT'D ON: 00061

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

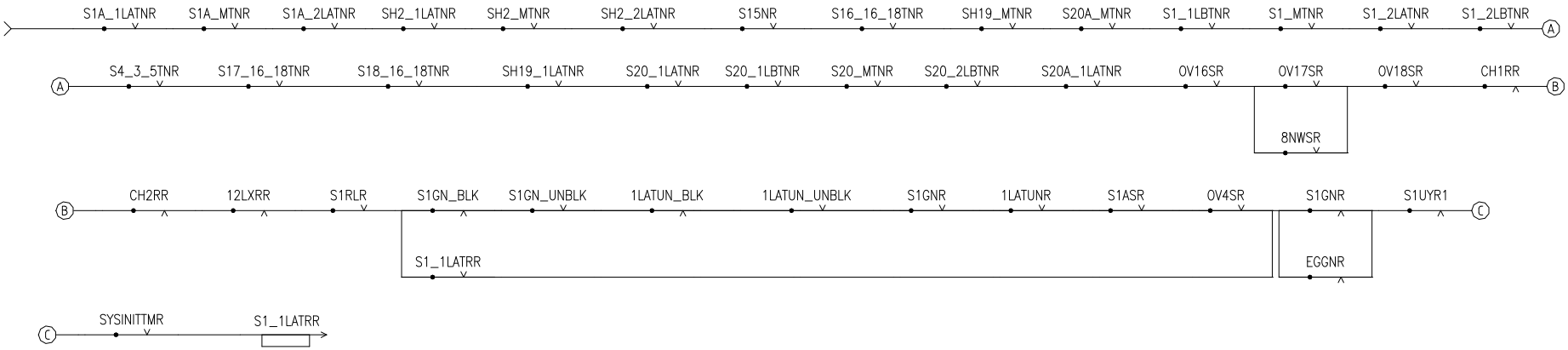
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

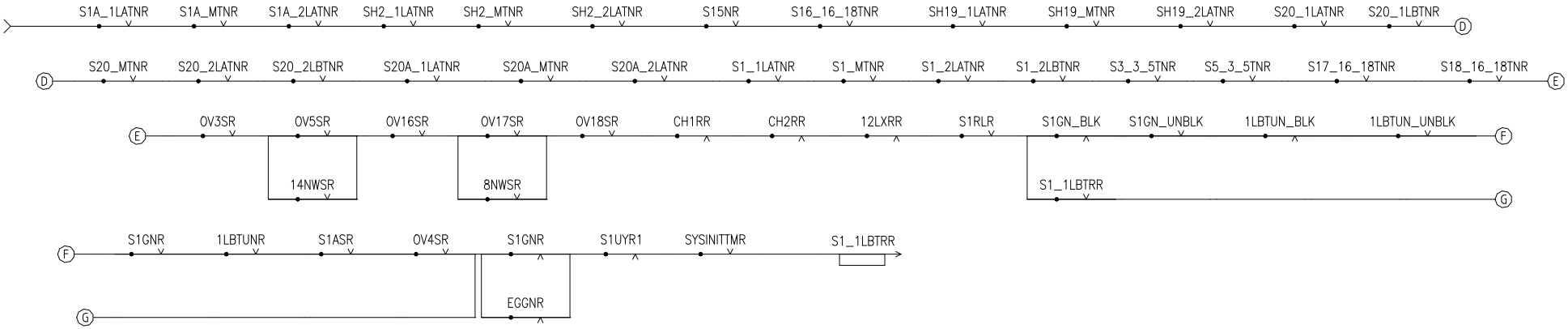
CAD FILE: 512_TAC_C1_00060_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN S1A_1LATNR * S1A_MTNR * S1A_2LATNR * SH2_1LATNR * SH2_MTNR * SH2_2LATNR * S15NR * S16_16_18TNR * SH19_MTNR * S20A_MTNR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S1_2LBTNR * S4_3_5TNR * S17_16_18TNR * S18_16_18TNR * SH19_1LATNR * S20_1LATNR * S20_1LBTNR * S20_MTNR * S20_2LBTNR * S20A_1LATNR * OV16SR * (OV17SR + 8NWSR) * OV18SR * ~CH1RR * ~CH2RR * ~12LXRR * S1RLR * (~S1GN_BLK * S1GN_UNBLK * ~1LATUN_BLK * 1LATUN_UNBLK * S1GNR * 1LATUNR * S1ASR * OV4SR + S1_1LATRR) * (~S1GNR + ~EGGNR) * ~S1UYR1 * SYSINITTMR TO S1_1LATRR;



ASSIGN S1A_1LATNR * S1A_MTNR * S1A_2LATNR * SH2_1LATNR * SH2_MTNR * SH2_2LATNR * S15NR * S16_16_18TNR * SH19_1LATNR * SH19_MTNR * SH19_2LATNR * S20_1LATNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * S20_2LBTNR * S20A_1LATNR * S20A_MTNR * S20A_2LATNR * S1_1LATNR * S1_MTNR * S1_2LATNR * S1_2LBTNR * S3_3_5TNR * S5_3_5TNR * S17_16_18TNR * S18_16_18TNR * OV3SR * (OV5SR + 14NWSR) * OV16SR * (OV17SR + 8NWSR) * OV18SR * ~CH1RR * ~CH2RR * ~12LXRR * S1RLR * (~S1GN_BLK * S1GN_UNBLK * ~1LBTUN_BLK * 1LBTUN_UNBLK * S1GNR * 1LBTUNR * S1ASR * OV4SR + S1_1LBTRR) * (~S1GNR + ~EGGNR) * ~S1UYR1 * SYSINITTMR TO S1_1LBTRR;



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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
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Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
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SSTE	
f CSTE	APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00061 | dqy 'khVSHEETS 00228 | CONT'D ON: 00062

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

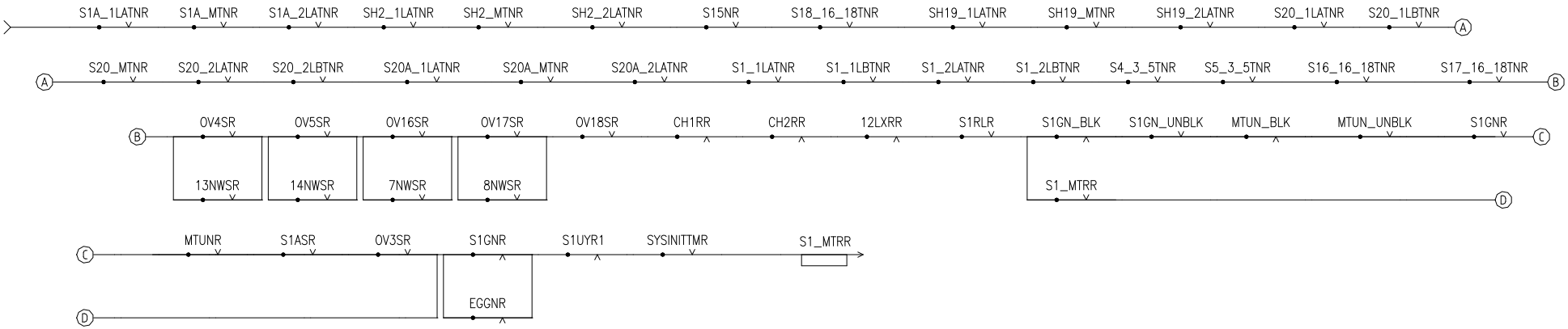
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

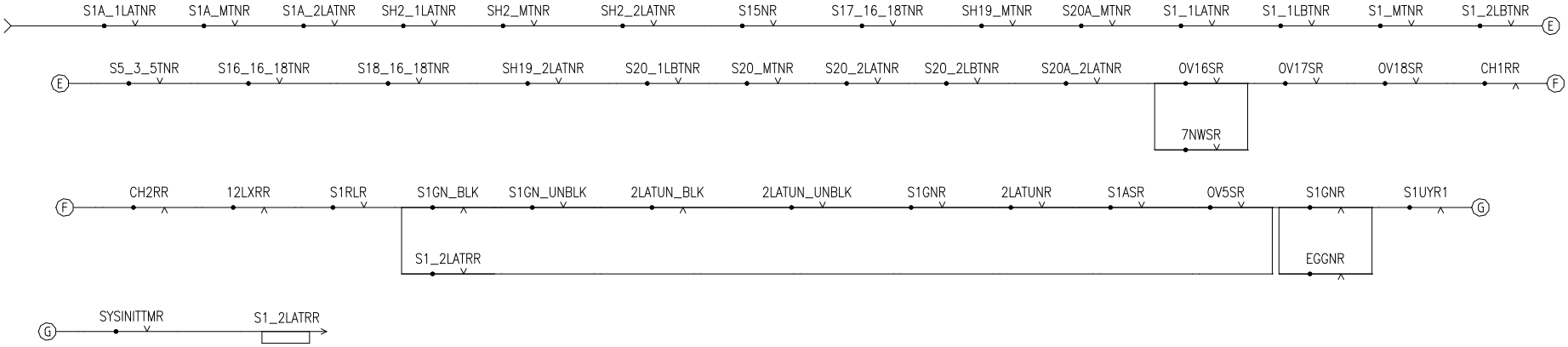
CAD FILE: 512_TAC_C1_00061_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN S1A_1LATNR * S1A_MTNR * S1A_2LATNR * SH2_1LATNR * SH2_MTNR * SH2_2LATNR * S15NR * S18_16_18TNR * SH19_1LATNR * SH19_MTNR * SH19_2LATNR * S20_1LATNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * S20_2LBTNR * S20A_1LATNR * S20A_MTNR * S20A_2LATNR * S1_1LATNR * S1_1LBTNR * S1_2LATNR * S1_2LBTNR * S4_3_5TNR * S5_3_5TNR * S16_16_18TNR * S17_16_18TNR * (OV4SR + 13NWSR) * (OV5SR + 14NWSR) * (OV16SR + 7NWSR) * (OV17SR + 8NWSR) * OV18SR * ~CH1RR * ~CH2RR * ~12LXRR * S1RLR * (~S1GN_BLK * S1GN_UNBLK * ~MTUN_BLK * MTUN_UNBLK * S1GNR * MTUNR * S1ASR * OV3SR + S1_MTRR) * (~S1GNR + ~EGGNR) * ~S1UYR1 * SYSINITTMR TO S1_MTRR;



ASSIGN S1A_1LATNR * S1A_MTNR * S1A_2LATNR * SH2_1LATNR * SH2_MTNR * SH2_2LATNR * S15NR * S17_16_18TNR * SH19_MTNR * S20A_MTNR * S1_1LATNR * S1_1LBTNR * S1_MTNR * S1_2LBTNR * S5_3_5TNR * S16_16_18TNR * S18_16_18TNR * SH19_2LATNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * S20_2LBTNR * S20A_2LATNR * (OV16SR + 7NWSR) * OV17SR * OV18SR * ~CH1RR * ~CH2RR * ~12LXRR * S1RLR * (~S1GN_BLK * S1GN_UNBLK * ~2LATUN_BLK * 2LATUN_UNBLK * S1GNR * 2LATUNR * S1ASR * OV5SR + S1_2LATRR) * (~S1GNR + ~EGGNR) * ~S1UYR1 * SYSINITTMR TO S1_2LATRR;



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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
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f CSTE	APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

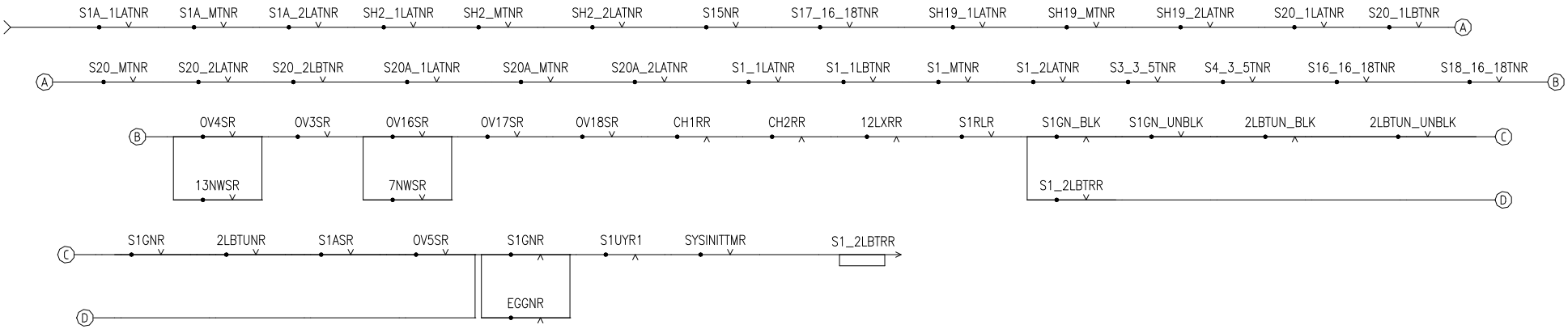
REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khVSHEET 00062	dqy 'khVSHEETS 00228	CONT'D ON: 00063

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

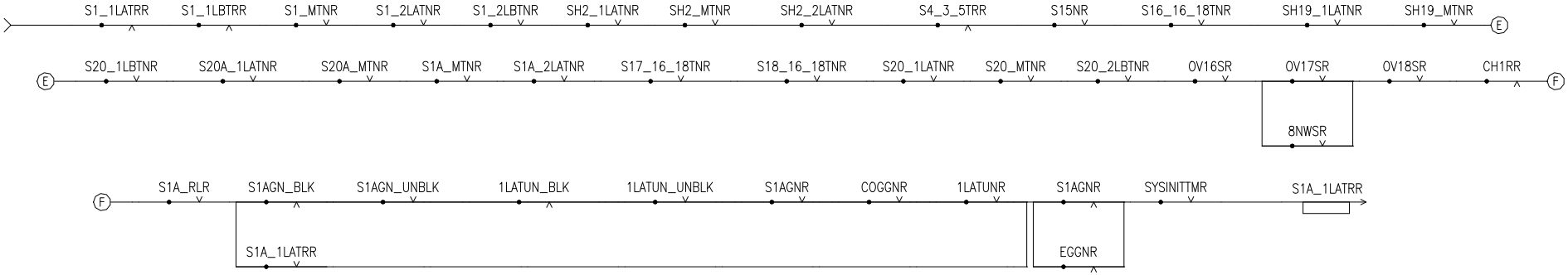
CAD FILE: 512_TAC_C1_00062_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN S1A_1LATNR * S1A_MTNR * S1A_2LATNR * SH2_1LATNR * SH2_MTNR * SH2_2LATNR * S15NR * S17_16_18TNR * SH19_1LATNR * SH19_MTNR * SH19_2LATNR * S20_1LATNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * S20_2LBTNR * S20A_1LATNR * S20A_MTNR * S20A_2LATNR * S1_1LATNR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S3_3_5TNR * S4_3_5TNR * S16_16_18TNR * S18_16_18TNR * (OV4SR + 13NWSR) * OV3SR * (OV16SR + 7NWSR) * OV17SR * OV18SR * ~CH1RR * ~CH2RR * ~12LXRR * S1RLR * (~S1GN_BLK * S1GN_UNBLK * ~2LBTUN_BLK * 2LBTUN_UNBLK * S1GNR * 2LBTUNR * S1ASR * OV5SR + S1_2LBTRR) * (~S1GNR + ~EGGNR) * ~S1UYR1 * SYSINITTMR TO S1_2LBTRR;



ASSIGN ~S1_1LATRR * ~S1_1LBTRR * S1_MTNR * S1_2LATNR * S1_2LBTNR * SH2_1LATNR * SH2_MTNR * SH2_2LATNR * ~S4_3_5TRR * S15NR * S16_16_18TNR * SH19_1LATNR * SH19_MTNR * S20_1LBTNR * S20A_1LATNR * S20A_MTNR * S1A_MTNR * S1A_2LATNR * S17_16_18TNR * S18_16_18TNR * S20_1LATNR * S20_MTNR * S20_2LBTNR * OV16SR * (OV17SR + 8NWSR) * OV18SR * ~CH1RR * S1A_RLR * (~S1AGN_BLK * S1AGN_UNBLK * ~1LATUN_BLK * 1LATUN_UNBLK * S1AGNR * COGGNR * 1LATUNR + S1A_1LATRR) * (~S1AGNR + ~EGGNR) * SYSINITTMR TO S1A_1LATRR;



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00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	

CHK.
APP:

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

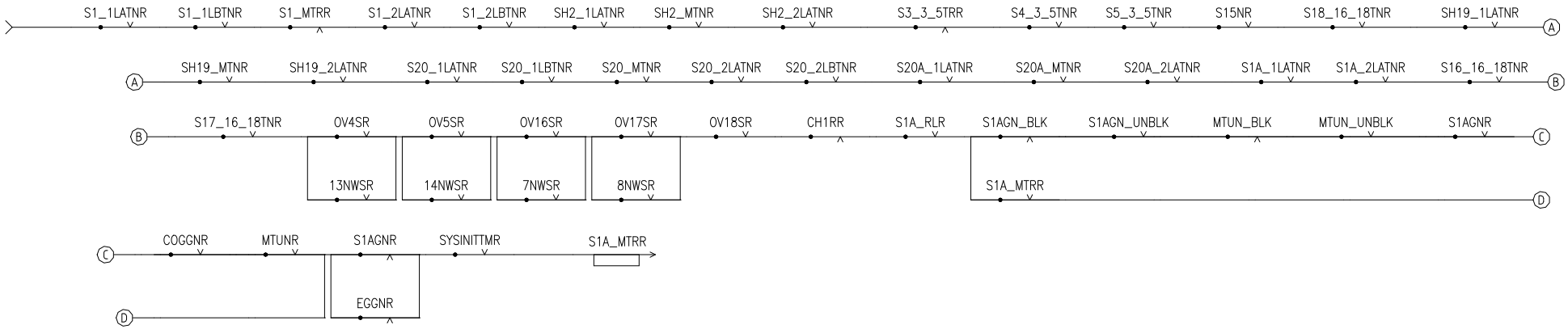
REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00063	00228	00064

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

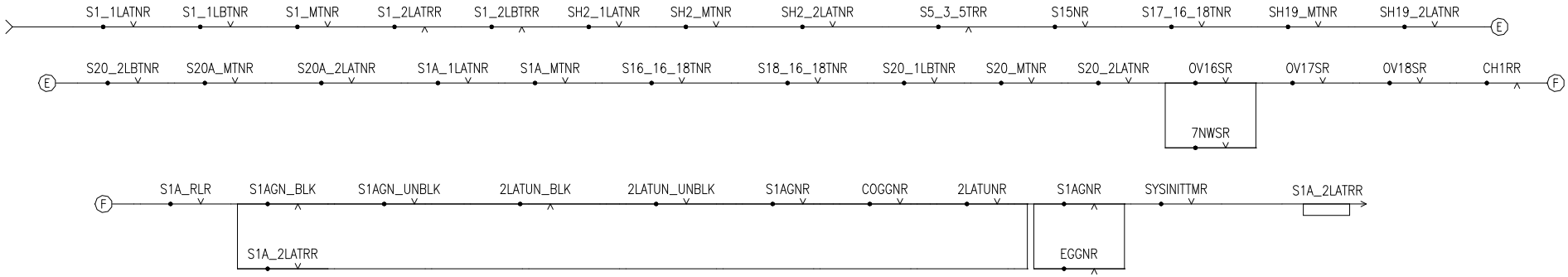
CAD FILE: 512_TAC_C1_00063_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN S1_1LATNR * S1_1LBTNR * ~S1_MTRR * S1_2LATNR * S1_2LBTNR * SH2_1LATNR * SH2_MTNR * SH2_2LATNR * ~S3_3_5TRR * S4_3_5TNR * S5_3_5TNR * S15NR * S18_16_18TNR * SH19_1LATNR * SH19_MTNR * SH19_2LATNR * S20_1LATNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * S20_2LBTNR * S20A_1LATNR * S20A_MTNR * S20A_2LATNR * S1A_1LATNR * S1A_2LATNR * S16_16_18TNR * S17_16_18TNR * (OV4SR + 13NWSR) * (OV5SR + 14NWSR) * (OV16SR + 7NWSR) * (OV17SR + 8NWSR) * OV18SR * ~CH1RR * S1A_RLR * (~S1AGN_BLK * S1AGN_UNBLK * ~MTUN_BLK * MTUN_UNBLK * S1AGNR * COGGNR * MTUNR + S1A_MTRR) * (~S1AGNR + ~EGGNR) * SYSINITTMR TO S1A_MTRR;



ASSIGN S1_1LATNR * S1_1LBTNR * S1_MTNR * ~S1_2LATRR * ~S1_2LBTRR * SH2_1LATNR * SH2_MTNR * SH2_2LATNR * ~S5_3_5TRR * S15NR * S17_16_18TNR * SH19_MTNR * SH19_2LATNR * S20_2LBTNR * S20A_MTNR * S20A_2LATNR * S1A_1LATNR * S1A_MTNR * S16_16_18TNR * S18_16_18TNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * (OV16SR + 7NWSR) * OV17SR * OV18SR * ~CH1RR * S1A_RLR * (~S1AGN_BLK * S1AGN_UNBLK * ~2LATUN_BLK * 2LATUN_UNBLK * S1AGNR * COGGNR * 2LATUNR + S1A_2LATRR) * (~S1AGNR + ~EGGNR) * SYSINITTMR TO S1A_2LATRR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
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Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO a fc B

'khVSHEET 00064 | dqy 'khVSHEETS 00228 | CONT'D ON: 00065

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

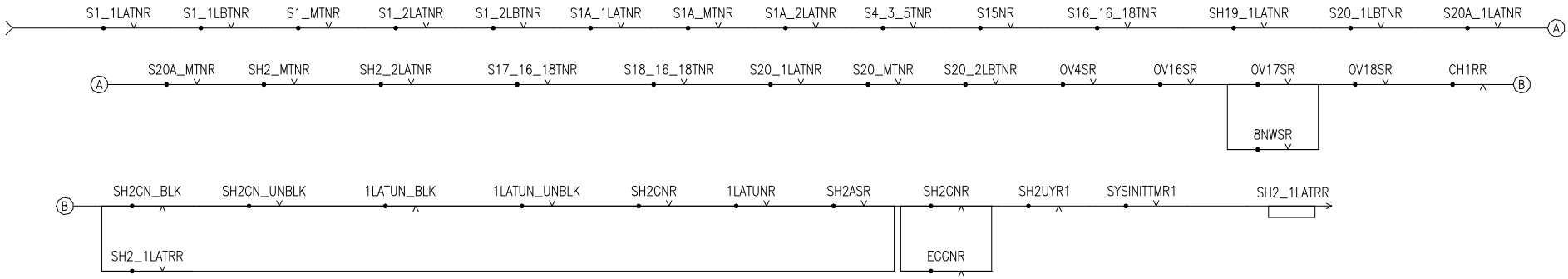
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

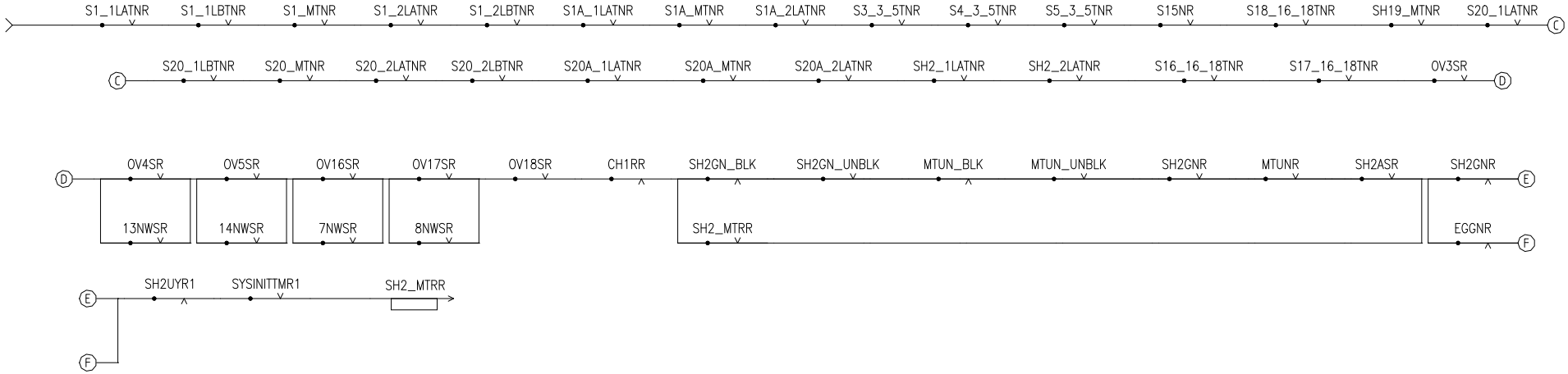
CAD FILE: 512_TAC_C1_00064_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN S1_1LATNR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S1_2LBTNR * S1A_1LATNR * S1A_MTNR * S1A_2LATNR * S4_3_5TNR * S15NR * S16_16_18TNR * SH19_1LATNR * S20_1LBTNR * S20A_1LATNR * S20A_MTNR * SH2_MTNR * SH2_2LATNR * S17_16_18TNR * S18_16_18TNR * S20_1LATNR * S20_MTNR * S20_2LBTNR * 0V4SR * 0V16SR * (0V17SR + 8NWSR) * 0V18SR * ~CH1RR * (~SH2GN_BLK * SH2GN_UNBLK * ~1LATUN_BLK * 1LATUN_UNBLK * SH2GNR * 1LATUNR * SH2ASR + SH2_1LATRR) * (~SH2GNR + ~EGGNR) * ~SH2UYR1 * SYSINITTMR1 TO SH2_1LATRR;



ASSIGN S1_1LATNR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S1_2LBTNR * S1A_1LATNR * S1A_MTNR * S1A_2LATNR * S3_3_5TNR * S4_3_5TNR * S5_3_5TNR * S15NR * S18_16_18TNR * SH19_MTNR * S20_1LATNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * S20_2LBTNR * S20A_1LATNR * S20A_MTNR * S20A_2LATNR * SH2_1LATNR * SH2_2LATNR * S16_16_18TNR * S17_16_18TNR * 0V3SR * (0V4SR + 13NWSR) * (0V5SR + 14NWSR) * (0V16SR + 7NWSR) * (0V17SR + 8NWSR) * 0V18SR * ~CH1RR * (~SH2GN_BLK * SH2GN_UNBLK * ~MTUN_BLK * MTUN_UNBLK * SH2GNR * MTUNR * SH2ASR + SH2_MTRR) * (~SH2GNR + ~EGGNR) * ~SH2UYR1 * SYSINITTMR1 TO SH2_MTRR;



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REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00065 | dqy 'khVSHEETS 00228 | CONT'D ON: 00066

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

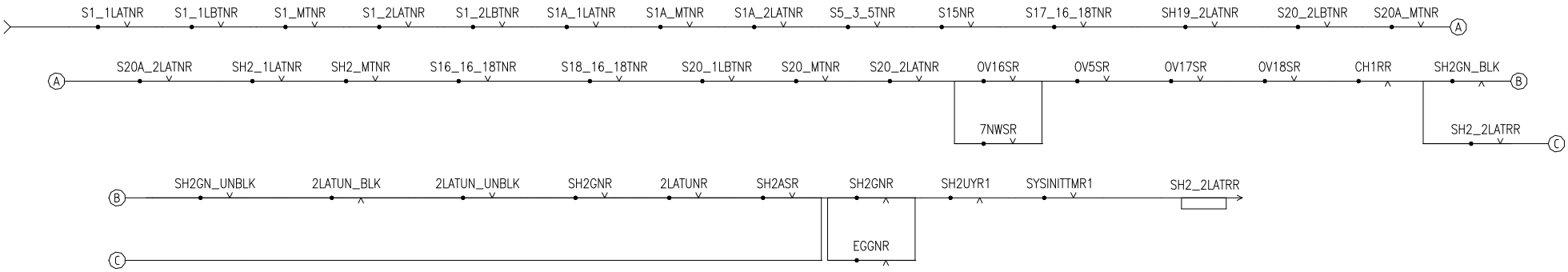
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

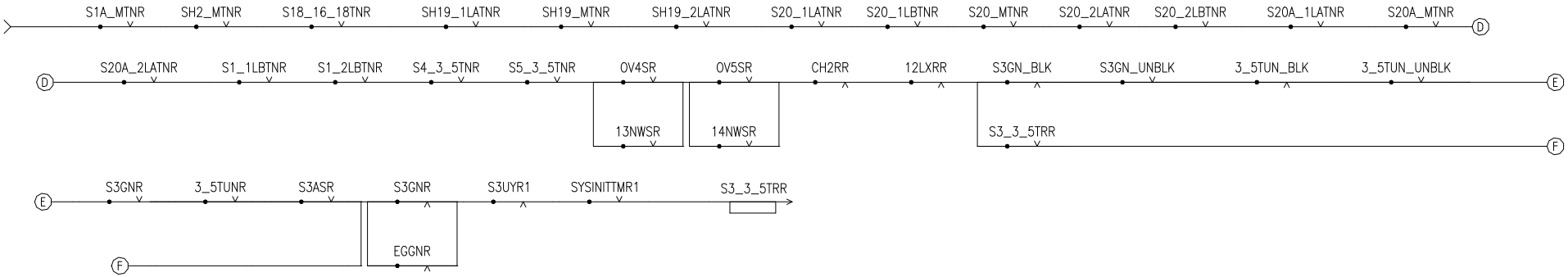
CAD FILE: 512_TAC_C1_00065_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN S1_1LATNR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S1_2LBTNR * S1A_1LATNR * S1A_MTNR * S1A_2LATNR * S5_3_5TNR * S15NR * S17_16_18TNR * SH19_2LATNR * S20_2LBTNR * S20A_MTNR * S20A_2LATNR * SH2_1LATNR * SH2_MTNR * S16_16_18TNR * S18_16_18TNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * (OV16SR + 7NWSR) * OV5SR * OV17SR * OV18SR * ~CH1RR * (~SH2GN_BLK * SH2GN_UNBLK * ~2LATUN_BLK * 2LATUN_UNBLK * SH2GNR * 2LATUNR * SH2ASR + SH2_2LATRR) * (~SH2GNR + ~EGGNR) * ~SH2UYR1 * SYSINITTMR1 TO SH2_2LATRR;



ASSIGN S1A_MTNR * SH2_MTNR * S18_16_18TNR * SH19_1LATNR * SH19_MTNR * SH19_2LATNR * S20_1LATNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * S20_2LBTNR * S20A_1LATNR * S20A_MTNR * S20A_2LATNR * S1_1LBTNR * S1_2LBTNR * S4_3_5TNR * S5_3_5TNR * (OV4SR + 13NWSR) * (OV5SR + 14NWSR) * ~CH2RR * ~12LXRR * (~S3GN_BLK * S3GN_UNBLK * ~3_5TUN_BLK * 3_5TUN_UNBLK * S3GNR * 3_5TUNR * S3ASR + S3_3_5TRR) * (~S3GNR + ~EGGNR) * ~S3UYR1 * SYSINITTMR1 TO S3_3_5TRR;



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REVISION	CHK.
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SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00066 | dqy 'khVSHEETS 00228 | CONT'D ON: 00067

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

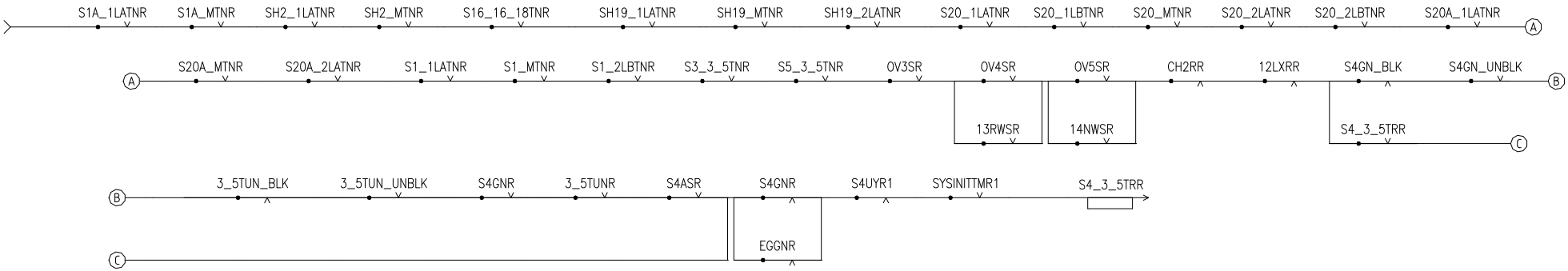
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

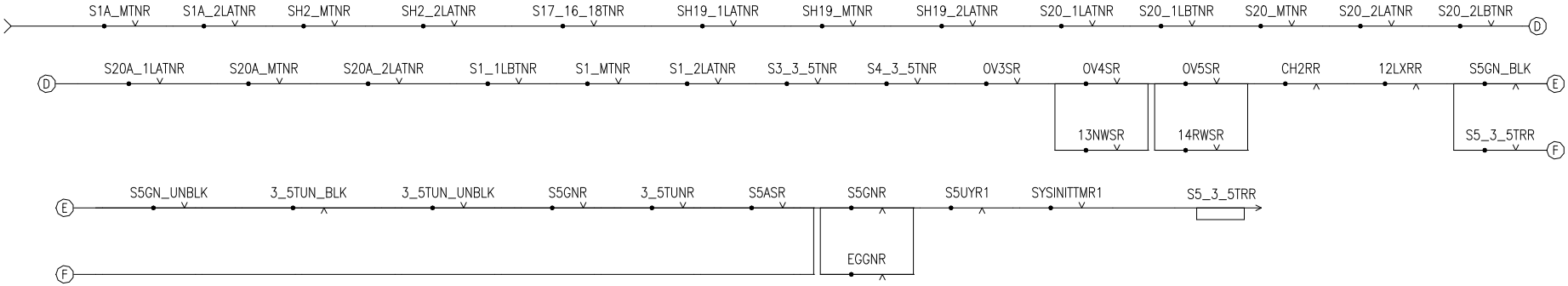
CAD FILE: 512_TAC_C1_00066_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN S1A_1LATNR * S1A_MTNR * SH2_1LATNR * SH2_MTNR * S16_16_18TNR * SH19_1LATNR * SH19_MTNR * SH19_2LATNR * S20_1LATNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * S20_2LBTNR * S20A_1LATNR * S20A_MTNR * S20A_2LATNR * S1_1LATNR * S1_MTNR * S1_2LBTNR * S3_3_5TNR * S5_3_5TNR * OV3SR * (OV4SR + 13RWSR) * (OV5SR + 14NWSR) * ~CH2RR * ~12LXRR * (~S4GN_BLK * S4GN_UNBLK * ~3_5TUN_BLK * 3_5TUN_UNBLK * S4GNR * 3_5TUNR * S4ASR + S4_3_5TRR) * (~S4GNR + ~EGGNR) * ~S4UYR1 * SYSINITTMR1 TO S4_3_5TRR;



ASSIGN S1A_MTNR * S1A_2LATNR * SH2_MTNR * SH2_2LATNR * S17_16_18TNR * SH19_1LATNR * SH19_MTNR * SH19_2LATNR * S20_1LATNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * S20_2LBTNR * S20A_1LATNR * S20A_MTNR * S20A_2LATNR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S3_3_5TNR * S4_3_5TNR * OV3SR * (OV4SR + 13NWSR) * (OV5SR + 14RWSR) * ~CH2RR * ~12LXRR * (~S5GN_BLK * S5GN_UNBLK * ~3_5TUN_BLK * 3_5TUN_UNBLK * S5GNR * 3_5TUNR * S5ASR + S5_3_5TRR) * (~S5GNR + ~EGGNR) * ~S5UYR1 * SYSINITTMR1 TO S5_3_5TRR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
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SE/SSE	
ASTE	
SSTE	
f CSTE	APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00067 | dqy 'khVSHEETS 00228 | CONT'D ON: 00068

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

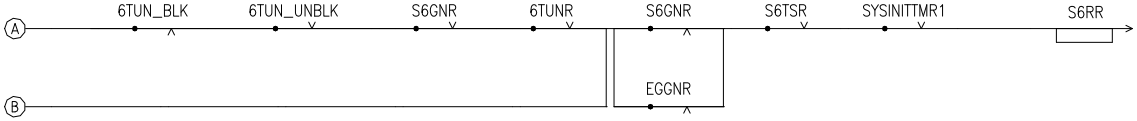
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

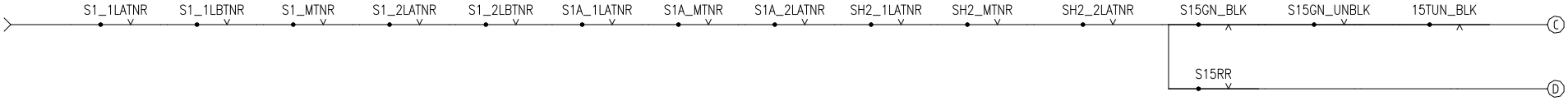
CAD FILE: 512_TAC_C1_00067_R00.DWG

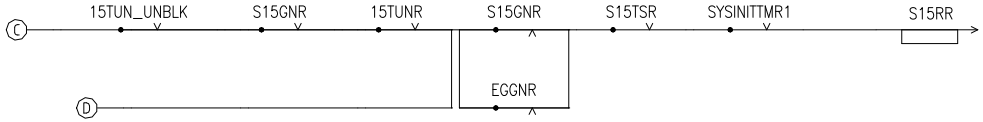
(MICROLOK-II WARM STANDBY)

ASSIGN SH19_1LATNR * SH19_MTNR * SH19_2LATNR * S20_1LATNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * S20_2LBTNR * S20A_1LATNR * S20A_MTNR * S20A_2LATNR * (~S6GN_BLK * S6GN_UNBLK * ~6TUN_BLK * 6TUN_UNBLK * S6GNR * 6TUNR + S6RR) * (~S6GNR + ~EGGNR) * S6TSR * SYSINITTMR1 TO S6RR;



ASSIGN S1_1LATNR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S1_2LBTNR * S1A_1LATNR * S1A_MTNR * S1A_2LATNR * SH2_1LATNR * SH2_MTNR * SH2_2LATNR * (~S15GN_BLK * S15GN_UNBLK * ~15TUN_BLK * 15TUN_UNBLK * S15GNR * 15TUNR + S15RR) * (~S15GNR + ~EGGNR) * S15TSR * SYSINITTMR1 TO S15RR;





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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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JE-1	
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ASTE	
SSTE	
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CHK.
APP:

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

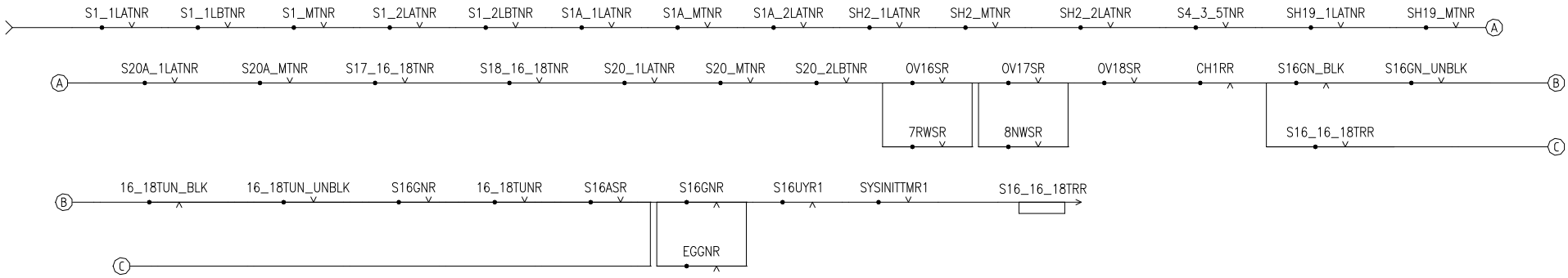
REF.SIG.PLAN.No. IPU.2107B
n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khV SHEET 00068
dqy 'khVSHEETS 00228
CONT'D ON: 00069

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

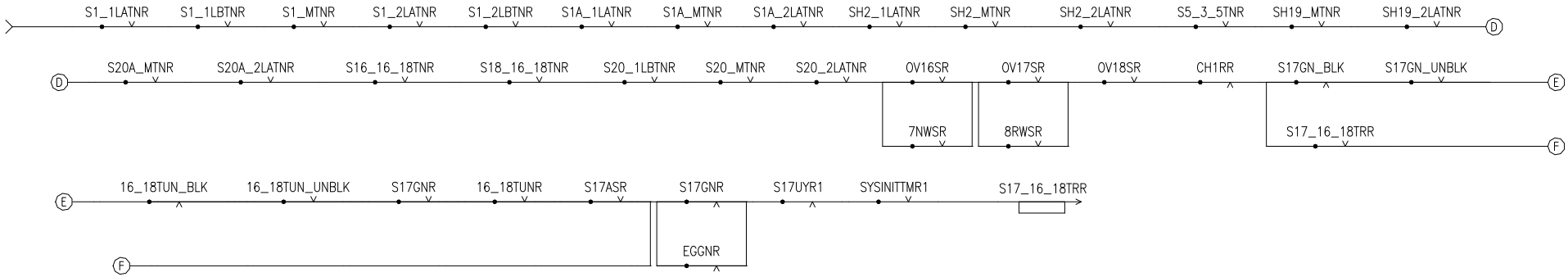
CAD FILE: 512_TAC_C1_00068_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN S1_1LATNR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S1_2LBTNR * S1A_1LATNR * S1A_MTNR * S1A_2LATNR * SH2_1LATNR * SH2_MTNR * SH2_2LATNR * S4_3_5TNR * SH19_1LATNR * SH19_MTNR * S20A_1LATNR * S20A_MTNR * S17_16_18TNR * S18_16_18TNR * S20_1LATNR * S20_MTNR * S20_2LBTNR * (OV16SR + 7RWSR) * (OV17SR + 8NWSR) * OV18SR * ~CH1RR * (~S16GN_BLK * S16GN_UNBLK * ~16_18TUN_BLK * 16_18TUN_UNBLK * S16GNR * 16_18TUNR * S16ASR + S16_16_18TRR) * (~S16GNR + ~EGGNR) * ~S16UYR1 * SYSINITTMR1 TO S16_16_18TRR;



ASSIGN S1_1LATNR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S1_2LBTNR * S1A_1LATNR * S1A_MTNR * S1A_2LATNR * SH2_1LATNR * SH2_MTNR * SH2_2LATNR * S5_3_5TNR * SH19_MTNR * SH19_2LATNR * S20A_MTNR * S20A_2LATNR * S16_16_18TNR * S18_16_18TNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * (OV16SR + 7NWSR) * (OV17SR + 8RWSR) * OV18SR * ~CH1RR * (~S17GN_BLK * S17GN_UNBLK * ~16_18TUN_BLK * 16_18TUN_UNBLK * S17GNR * 16_18TUNR * S17ASR + S17_16_18TRR) * (~S17GNR + ~EGGNR) * ~S17UYR1 * SYSINITTMR1 TO S17_16_18TRR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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SE/SSE	
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

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'khV SHEET 00069 | dqy 'khVSHEETS 00228 | CONT'D ON: 00070

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

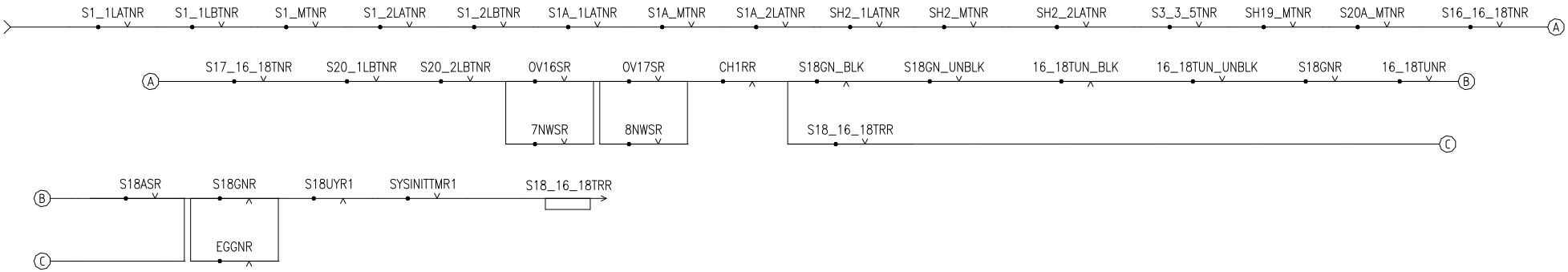
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

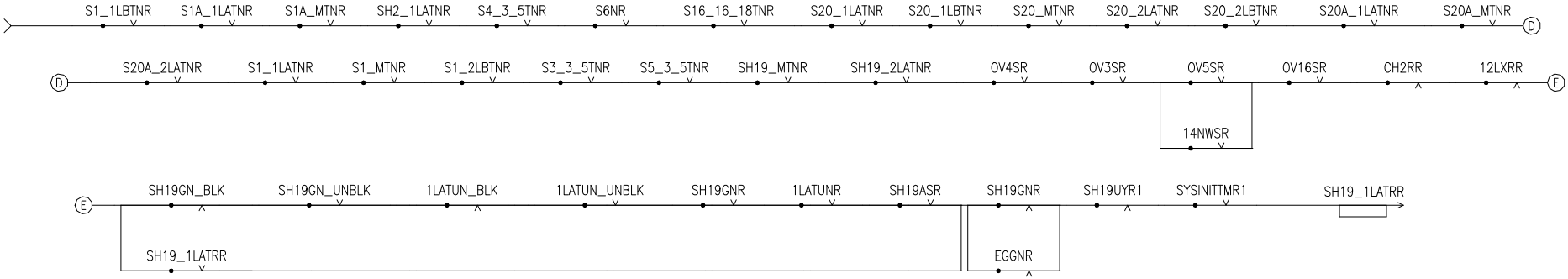
CAD FILE: 512_TAC_C1_00069_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN S1_1LATNR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S1_2LBTNR * S1A_1LATNR * S1A_MTNR * S1A_2LATNR * SH2_1LATNR * SH2_MTNR * SH2_2LATNR * S3_3_5TNR * SH19_MTNR * S20A_MTNR * S16_16_18TNR * S17_16_18TNR * S20_1LBTNR * S20_2LBTNR * (OV16SR + 7NWSR) * (OV17SR + 8NWSR) * ~CH1RR * (~S18GN_BLK * S18GN_UNBLK * ~16_18TUN_BLK * 16_18TUN_UNBLK * S18GNR * 16_18TUNR * S18ASR + S18_16_18TRR) * (~S18GNR + ~EGGNR) * ~S18UYR1 * SYSINITTMR1 TO S18_16_18TRR;



ASSIGN S1_1LBTNR * S1A_1LATNR * S1A_MTNR * SH2_1LATNR * S4_3_5TNR * S6NR * S16_16_18TNR * S20_1LATNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * S20_2LBTNR * S20A_1LATNR * S20A_MTNR * S20A_2LATNR * S1_1LATNR * S1_MTNR * S1_2LBTNR * S3_3_5TNR * S5_3_5TNR * SH19_MTNR * SH19_2LATNR * OV4SR * OV3SR * (OV5SR + 14NWSR) * OV16SR * ~CH2RR * ~12LXRR * (~SH19GN_BLK * SH19GN_UNBLK * ~1LATUN_BLK * 1LATUN_UNBLK * SH19GNR * 1LATUNR * SH19ASR + SH19_1LATRR) * (~SH19GNR + ~EGGNR) * ~SH19UYR1 * SYSINITTMR1 TO SH19_1LATRR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	a fc B
IPU.2107B		
'khV SHEET 00070	dqy 'khVSHEETS 00228	CONT'D ON: 00071

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

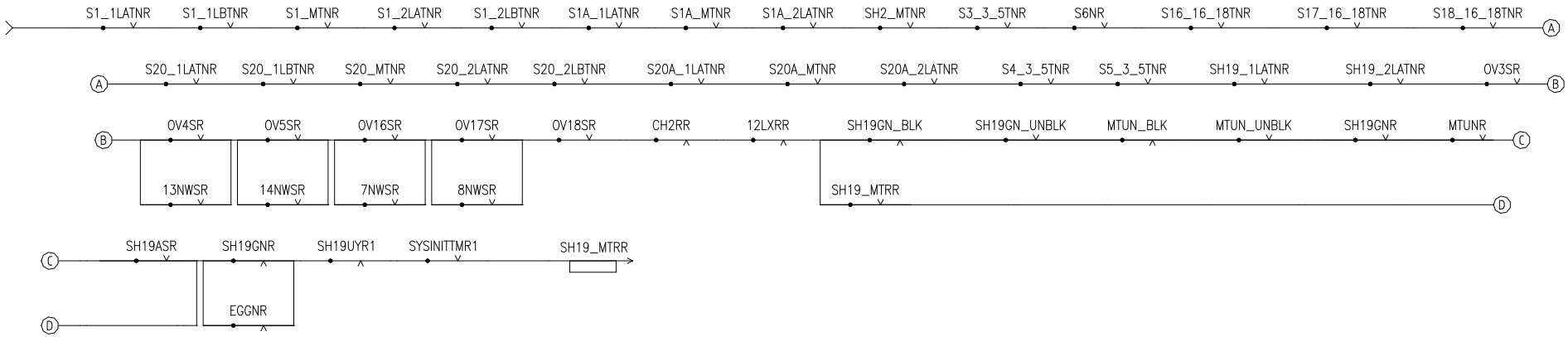
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

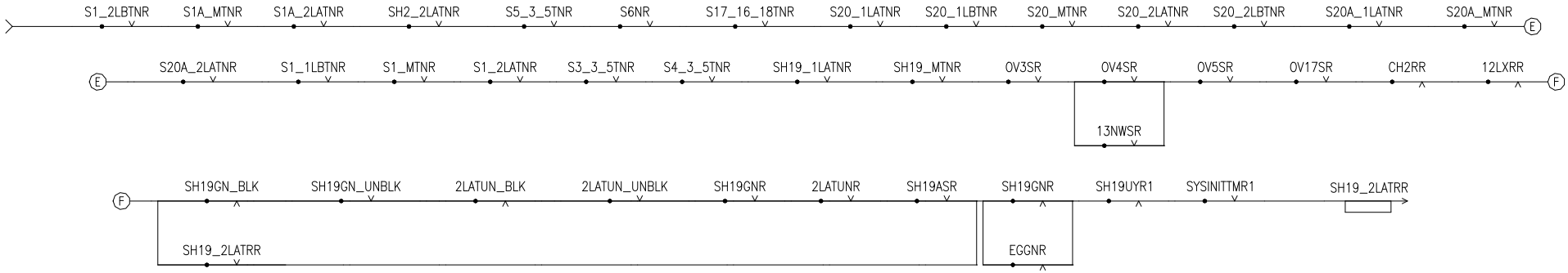
CAD FILE: 512_TAC_C1_00070_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN S1_1LATNR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S1_2LBTNR * S1A_1LATNR * S1A_MTNR * S1A_2LATNR * SH2_MTNR * S3_3_5TNR * S6NR * S16_16_18TNR * S17_16_18TNR * S18_16_18TNR * S20_1LATNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * S20_2LBTNR * S20A_1LATNR * S20A_MTNR * S20A_2LATNR * S4_3_5TNR * S5_3_5TNR * SH19_1LATNR * SH19_2LATNR * OV3SR * (OV4SR + 13NWSR) * (OV5SR + 14NWSR) * (OV16SR + 7NWSR) * (OV17SR + 8NWSR) * OV18SR * ~CH2RR * ~12LXRR * (~SH19GN_BLK * SH19GN_UNBLK * ~MTUN_BLK * MTUN_UNBLK * SH19GNR * MTUNR * SH19ASR + SH19_MTRR) * (~SH19GNR + ~EGGNR) * ~SH19UYR1 * SYSINITTMR1 TO SH19_MTRR;



ASSIGN S1_2LBTNR * S1A_MTNR * S1A_2LATNR * SH2_2LATNR * S5_3_5TNR * S6NR * S17_16_18TNR * S20_1LATNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * S20_2LBTNR * S20A_1LATNR * S20A_MTNR * S20A_2LATNR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S3_3_5TNR * S4_3_5TNR * SH19_1LATNR * SH19_MTNR * OV3SR * (OV4SR + 13NWSR) * OV5SR * OV17SR * ~CH2RR * ~12LXRR * (~SH19GN_BLK * SH19GN_UNBLK * ~2LATUN_BLK * 2LATUN_UNBLK * SH19GNR * 2LATUNR * SH19ASR + SH19_2LATRR) * (~SH19GNR + ~EGGNR) * ~SH19UYR1 * SYSINITTMR1 TO SH19_2LATRR;



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00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khVSHEET 00071	dqy 'khVSHEETS 00228	CONT'D ON: 00072

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

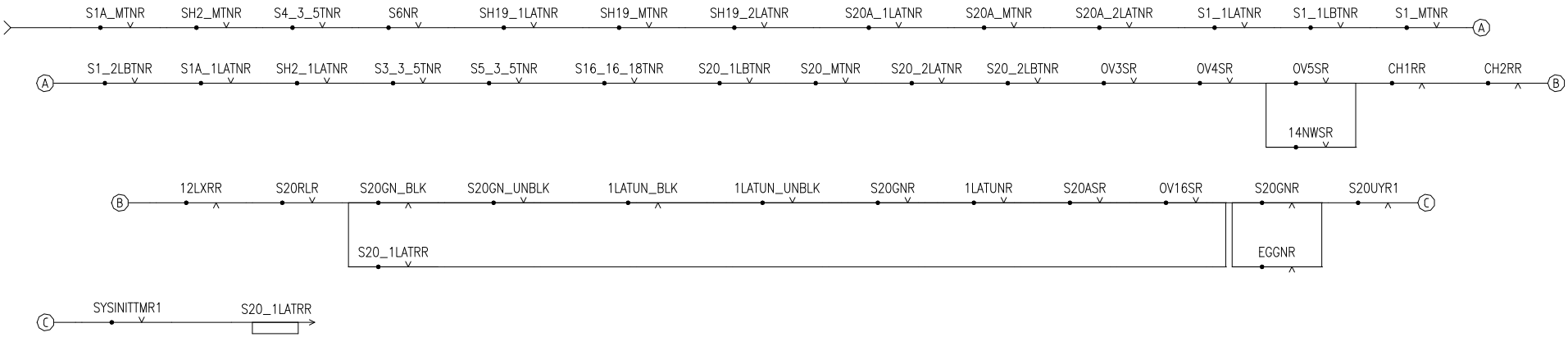
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

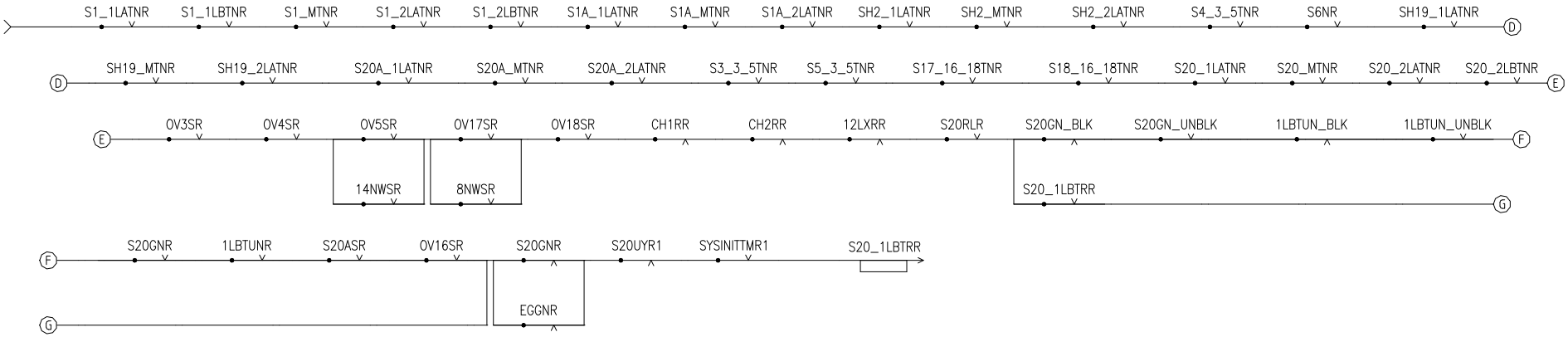
CAD FILE: 512_TAC_C1_00071_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN S1A_MTNR * SH2_MTNR * S4_3_5TNR * S6NR * SH19_1LATNR * SH19_MTNR * SH19_2LATNR * S20A_1LATNR * S20A_MTNR * S20A_2LATNR * S1_1LATNR * S1_1LBTNR * S1_MTNR * S1_2LBTNR * S1A_1LATNR * SH2_1LATNR * S3_3_5TNR * S5_3_5TNR * S16_16_18TNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * S20_2LBTNR * OV3SR * OV4SR * (OV5SR + 14NWSR) * ~CH1RR * ~CH2RR * ~12LXRR * S20RLR * (~S20GN_BLK * S20GN_UNBLK * ~1LATUN_BLK * 1LATUN_UNBLK * S20GNR * 1LATUNR * S20ASR * OV16SR + S20_1LATRR) * (~S20GNR + ~EGGNR) * ~S20UYR1 * SYSINITTMR1 TO S20_1LATRR;



ASSIGN S1_1LATNR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S1_2LBTNR * S1A_1LATNR * S1A_MTNR * S1A_2LATNR * SH2_1LATNR * SH2_MTNR * SH2_2LATNR * S4_3_5TNR * S6NR * SH19_1LATNR * SH19_MTNR * SH19_2LATNR * S20A_1LATNR * S20A_MTNR * S20A_2LATNR * S3_3_5TNR * S5_3_5TNR * S17_16_18TNR * S18_16_18TNR * S20_1LATNR * S20_MTNR * S20_2LATNR * S20_2LBTNR * OV3SR * OV4SR * (OV5SR + 14NWSR) * (OV17SR + 8NWSR) * OV18SR * ~CH1RR * ~CH2RR * ~12LXRR * S20RLR * (~S20GN_BLK * S20GN_UNBLK * ~1LBTUN_BLK * 1LBTUN_UNBLK * S20GNR * 1LBTUNR * S20ASR * OV16SR + S20_1LBTRR) * (~S20GNR + ~EGGNR) * ~S20UYR1 * SYSINITTMR1 TO S20_1LBTRR;



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00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
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Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B | 'khV SHEET 00072 | dqy 'khVSHEETS 00228 | CONT'D ON: 00073

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

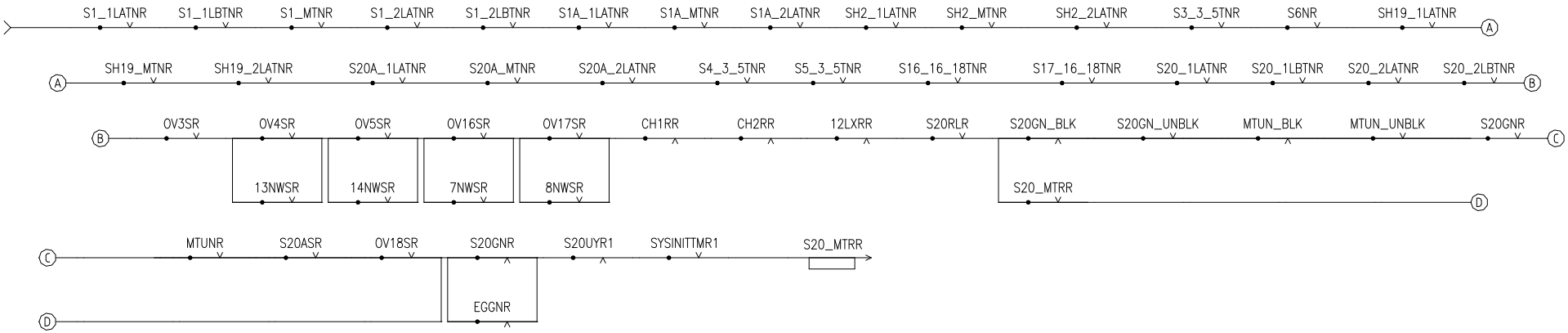
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

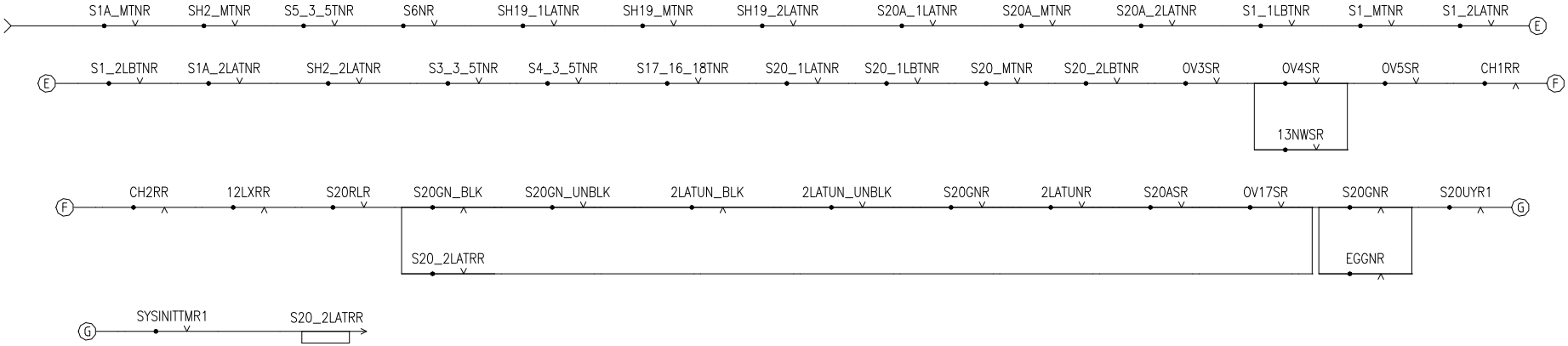
(MICROLOK-II WARM STANDBY)

CAD FILE: 512_TAC_C1_00072_R00.DWG

ASSIGN S1_1LATNR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S1_2LBTNR * S1A_1LATNR * S1A_MTNR * S1A_2LATNR * SH2_1LATNR * SH2_MTNR * SH2_2LATNR * S3_3_5TNR * S6NR * SH19_1LATNR * SH19_MTNR * SH19_2LATNR * S20A_1LATNR * S20A_MTNR * S20A_2LATNR * S4_3_5TNR * S5_3_5TNR * S16_16_18TNR * S17_16_18TNR * S20_1LATNR * S20_1LBTNR * S20_2LATNR * S20_2LBTNR * OV3SR * (OV4SR + 13NWSR) * (OV5SR + 14NWSR) * (OV16SR + 7NWSR) * (OV17SR + 8NWSR) * ~CH1RR * ~CH2RR * ~12LXRR * S20RLR * (~S20GN_BLK * S20GN_UNBLK * ~MTUN_BLK * MTUN_UNBLK * S20GNR * MTUNR * S20ASR * OV18SR + S20_MTRR) * (~S20GNR + ~EGGNR) * ~S20UYR1 * SYSINITTMR1 TO S20_MTRR;



ASSIGN S1A_MTNR * SH2_MTNR * S5_3_5TNR * S6NR * SH19_1LATNR * SH19_MTNR * SH19_2LATNR * S20A_1LATNR * S20A_MTNR * S20A_2LATNR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S1_2LBTNR * S1A_2LATNR * SH2_2LATNR * S3_3_5TNR * S4_3_5TNR * S17_16_18TNR * S20_1LATNR * S20_1LBTNR * S20_MTNR * S20_2LBTNR * OV3SR * (OV4SR + 13NWSR) * OV5SR * ~CH1RR * ~CH2RR * ~12LXRR * S20RLR * (~S20GN_BLK * S20GN_UNBLK * ~2LATUN_BLK * 2LATUN_UNBLK * S20GNR * 2LATUNR * S20ASR * OV17SR + S20_2LATRR) * (~S20GNR + ~EGGNR) * ~S20UYR1 * SYSINITTMR1 TO S20_2LATRR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

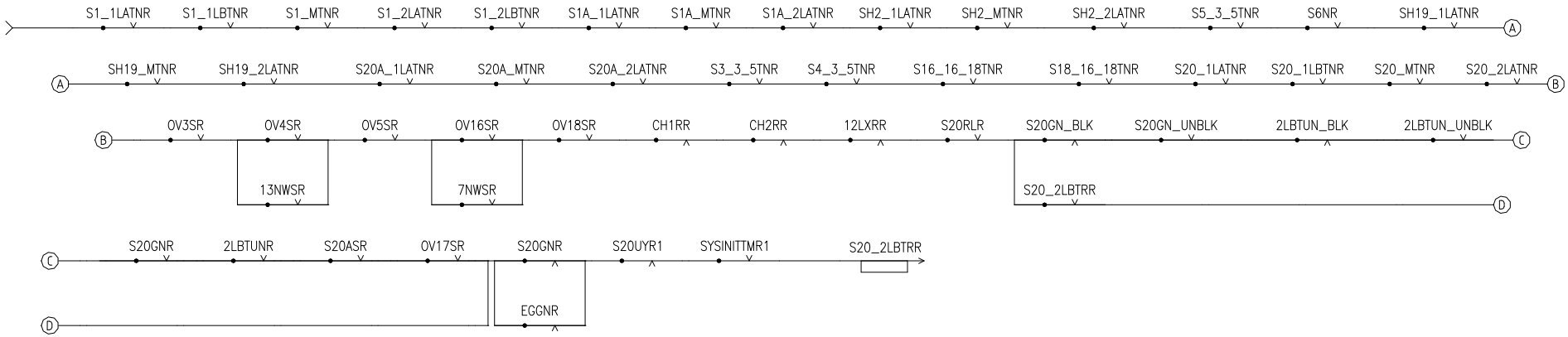
REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B
'khV SHEET 00073 | dqy 'khVSHEETS 00228 | CONT'D ON: 00074

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

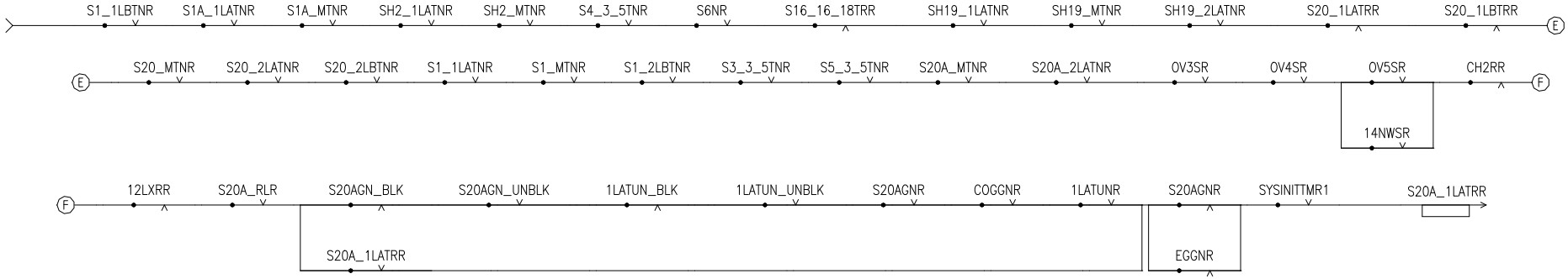
CAD FILE: 512_TAC_C1_00073_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN S1_1LATNR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S1_2LBTNR * S1A_1LATNR * S1A_MTNR * S1A_2LATNR * SH2_1LATNR * SH2_MTNR * SH2_2LATNR * S5_3_5TNR * S6NR * SH19_1LATNR * SH19_MTNR * SH19_2LATNR * S20A_1LATNR * S20A_MTNR * S20A_2LATNR * S3_3_5TNR * S4_3_5TNR * S16_16_18TNR * S18_16_18TNR * S20_1LATNR * S20_1LBTNR * S20_MTNR * S20_2LATNR * OV3SR * (OV4SR + 13NWSR) * OV5SR * (OV16SR + 7NWSR) * OV18SR * ~CH1RR * ~CH2RR * ~12LXRR * S20RLR * (~S20GN_BLK * S20GN_UNBLK * ~2LBTUN_BLK * 2LBTUN_UNBLK * S20GNR * 2LBTUNR * S20ASR * OV17SR + S20_2LBTRR) * (~S20GNR + ~EGGNR) * ~S20UYR1 * SYSINITTMR1 TO S20_2LBTRR;



ASSIGN S1_1LBTNR * S1A_1LATNR * S1A_MTNR * SH2_1LATNR * SH2_MTNR * S4_3_5TNR * S6NR * ~S16_16_18TRR * SH19_1LATNR * SH19_MTNR * SH19_2LATNR * ~S20_1LATRR * ~S20_1LBTRR * S20_MTNR * S20_2LATNR * S20_2LBTNR * S1_1LATNR * S1_MTNR * S1_2LBTNR * S3_3_5TNR * S5_3_5TNR * S20A_MTNR * S20A_2LATNR * OV3SR * OV4SR * (OV5SR + 14NWSR) * ~CH2RR * ~12LXRR * S20A_RLR * (~S20AGN_BLK * S20AGN_UNBLK * ~1LATUN_BLK * 1LATUN_UNBLK * S20AGNR * COGGNR * 1LATUNR + S20A_1LATRR) * (~S20AGNR + ~EGGNR) * SYSINITTMR1 TO S20A_1LATRR;



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00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

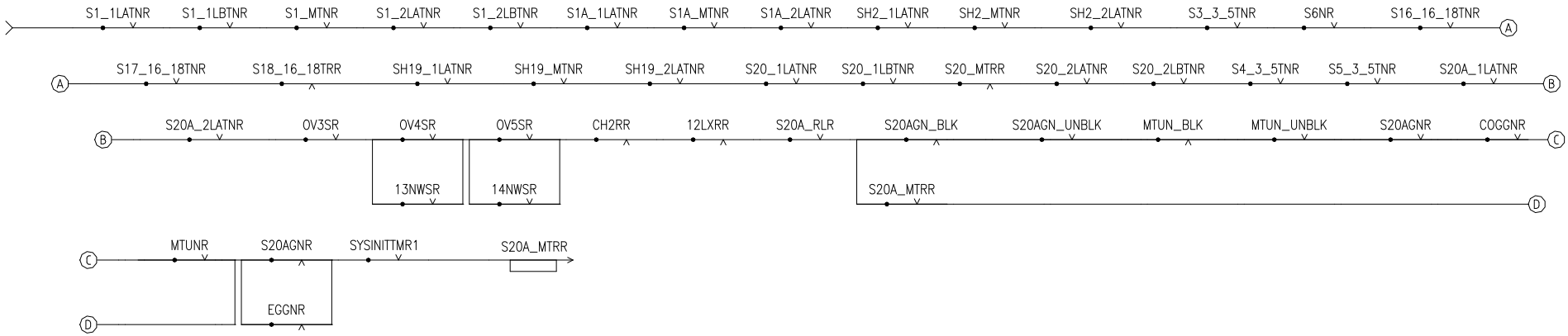
REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00074	00228	00075

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

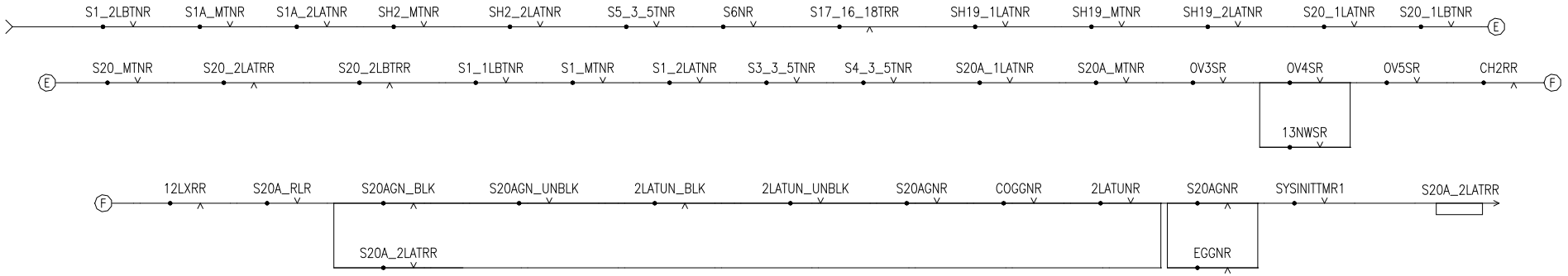
CAD FILE: 512_TAC_C1_00074_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN S1_1LATNR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S1_2LBTNR * S1A_1LATNR * S1A_MTNR * S1A_2LATNR * SH2_1LATNR * SH2_MTNR * SH2_2LATNR * S3_3_5TNR * S6NR * S16_16_18TNR * S17_16_18TNR * ~S18_16_18TRR * SH19_1LATNR * SH19_MTNR * SH19_2LATNR * S20_1LATNR * S20_1LBTNR * ~S20_MTRR * S20_2LATNR * S20_2LBTNR * S4_3_5TNR * S5_3_5TNR * S20A_1LATNR * S20A_2LATNR * OV3SR * (OV4SR + 13NWSR) * (OV5SR + 14NWSR) * ~CH2RR * ~12LXRR * S20A_RLR * (~S20AGN_BLK * S20AGN_UNBLK * ~MTUN_BLK * MTUN_UNBLK * S20AGNR * COGGNR * MTUNR + S20A_MTRR) * (~S20AGNR + ~EGGNR) * SYSINITTMR1 TO S20A_MTRR;



ASSIGN S1_2LBTNR * S1A_MTNR * S1A_2LATNR * SH2_MTNR * SH2_2LATNR * S5_3_5TNR * S6NR * ~S17_16_18TRR * SH19_1LATNR * SH19_MTNR * SH19_2LATNR * S20_1LATNR * S20_1LBTNR * S20_MTNR * ~S20_2LATRR * ~S20_2LBTRR * S1_1LBTNR * S1_MTNR * S1_2LATNR * S3_3_5TNR * S4_3_5TNR * S20A_1LATNR * S20A_MTNR * OV3SR * (OV4SR + 13NWSR) * OV5SR * ~CH2RR * ~12LXRR * S20A_RLR * (~S20AGN_BLK * S20AGN_UNBLK * ~2LATUN_BLK * 2LATUN_UNBLK * S20AGNR * COGGNR * 2LATUNR + S20A_2LATRR) * (~S20AGNR + ~EGGNR) * SYSINITTMR1 TO S20A_2LATRR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00075 | dqy 'khVSHEETS 00228 | CONT'D ON: 00076

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

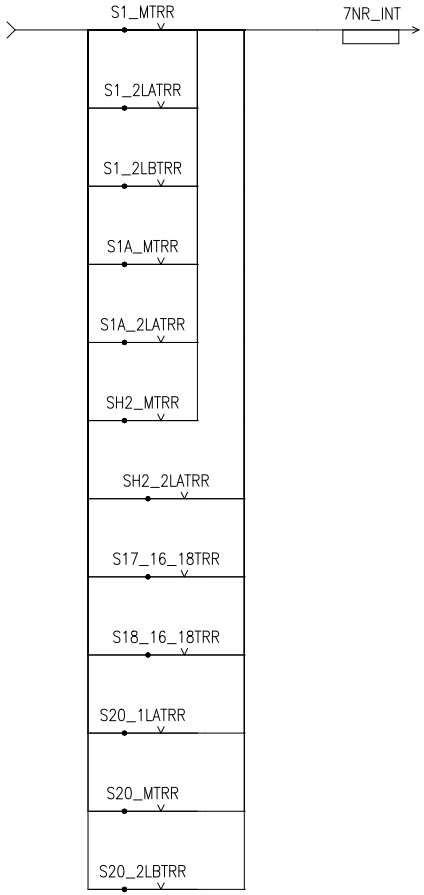
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

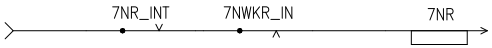
CAD FILE: 512_TAC_C1_00075_R00.DWG

(MICROLOK-II WARM STANDBY)

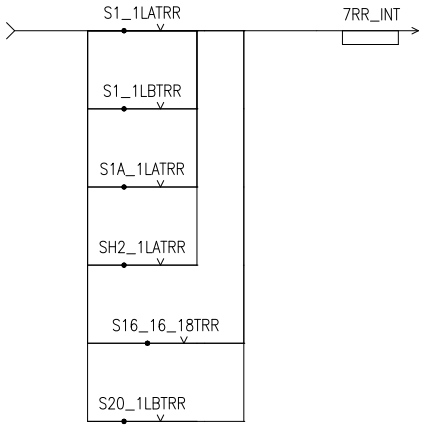
ASSIGN (S1_MTRR + S1_2LATRR + S1_2LBTRR + S1A_MTRR + S1A_2LATRR + SH2_MTRR + SH2_2LATRR + S17_16_18TRR + S18_16_18TRR + S20_1LATRR + S20_MTRR + S20_2LBTRR) TO 7NR_INT;



ASSIGN 7NR_INT * ~7NWKR_IN TO 7NR;



ASSIGN (S1_1LATRR + S1_1LBTRR + S1A_1LATRR + SH2_1LATRR + S16_16_18TRR + S20_1LBTRR) TO 7RR_INT;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross, 100 Feet Road, J.P.Nagar, 6th Phase, Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

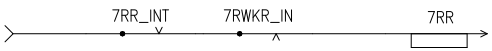
REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khV SHEET 00076 | dqy 'khVSHEETS 00228 | CONT'D ON: 00077

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

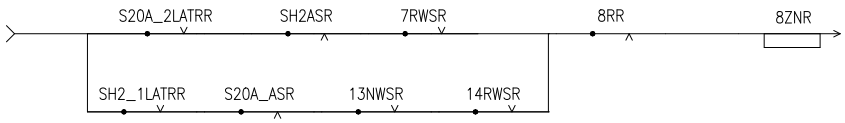
CAD FILE: 512_TAC_C1_00076_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN 7RR_INT * ~7RWKR_IN TO 7RR;



ASSIGN (S20A_2LATRR * ~SH2ASR * 7RWSR + SH2_1LATRR * ~S20A_ASR * 13NWSR * 14RWSR) * ~8RR TO 8ZNR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00077	00228	00078

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SIGNAL & TELECOMMUNICATIONS

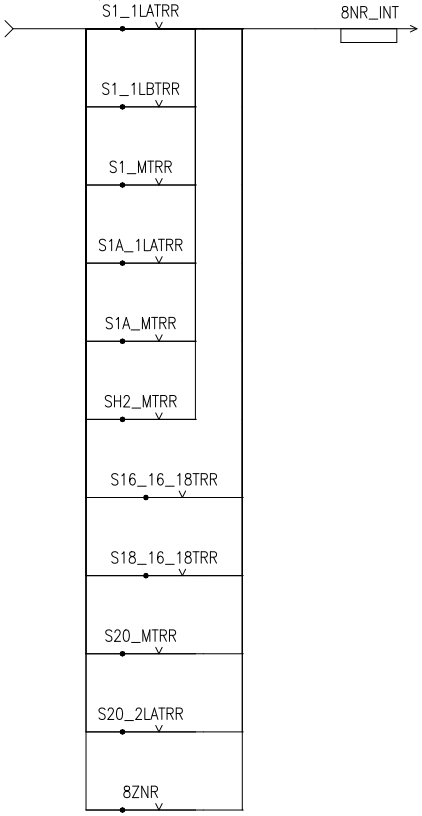
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

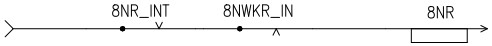
CAD FILE: 512_TAC_C1_00077_R00.DWG

(MICROLOK-II WARM STANDBY)

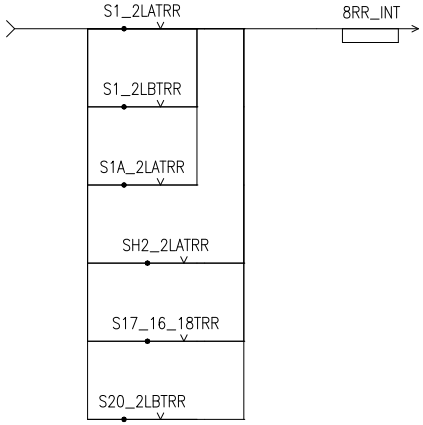
ASSIGN (S1_1LATRR + S1_1LBTRR + S1_MTRR + S1A_1LATRR + S1A_MTRR + SH2_MTRR + S16_16_18TRR + S18_16_18TRR + S20_MTRR + S20_2LATRR + 8ZNR) TO 8NR_INT;



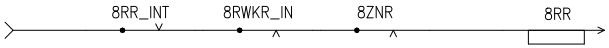
ASSIGN 8NR_INT * ~8NWKR_IN TO 8NR;



ASSIGN (S1_2LATRR + S1_2LBTRR + S1A_2LATRR + SH2_2LATRR + S17_16_18TRR + S20_2LBTRR) TO 8RR_INT;



ASSIGN 8RR_INT * ~8RWKR_IN * ~8ZNR TO 8RR;



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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO a fc B

'khV SHEET 00078 | dqy 'khVSHEETS 00228 | CONT'D ON: 00079

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

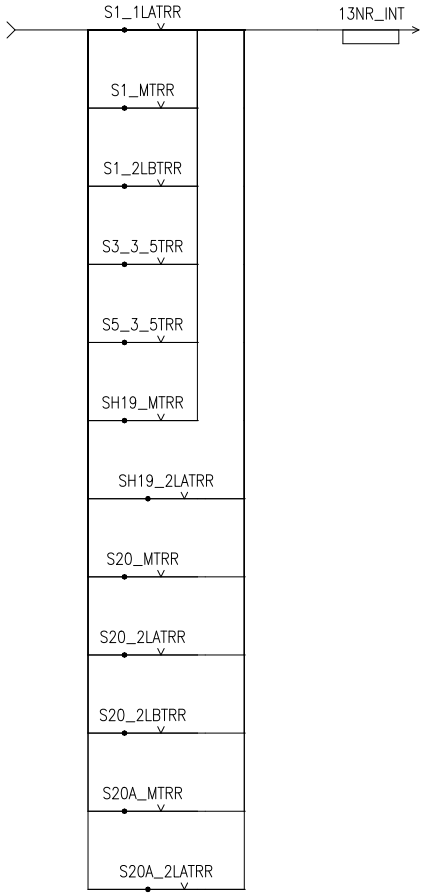
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

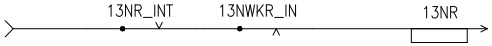
CAD FILE: 512_TAC_C1_00078_R00.DWG

(MICROLOK-II WARM STANDBY)

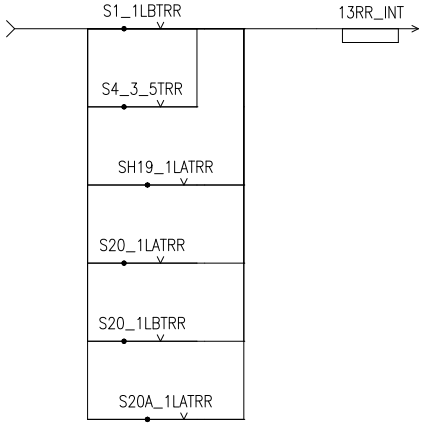
ASSIGN (S1_1LATRR + S1_MTRR + S1_2LBTRR + S3_3_5TRR + S5_3_5TRR + SH19_MTRR + SH19_2LATRR + S20_MTRR + S20_2LATRR + S20_2LBTRR + S20A_MTRR + S20A_2LATRR) TO 13NR_INT;



ASSIGN 13NR_INT * ~13NWKR_IN TO 13NR;



ASSIGN (S1_1LBTRR + S4_3_5TRR + SH19_1LATRR + S20_1LATRR + S20_1LBTRR + S20A_1LATRR) TO 13RR_INT;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	a fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00079	00228	00080

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SIGNAL & TELECOMMUNICATIONS

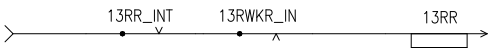
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

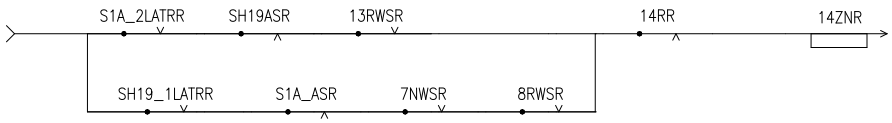
CAD FILE: 512_TAC_C1_00079_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN 13RR_INT * ~13RWKR_IN TO 13RR;



ASSIGN (S1A_2LATRR * ~SH19ASR * 13RWSR + SH19_1LATRR * ~S1A_ASR * 7NWSR * 8RWSR) * ~14RR TO 14ZNR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO a fc B

'khV SHEET 00080 | dqy 'khVSHEETS 00228 | CONT'D ON: 00081

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

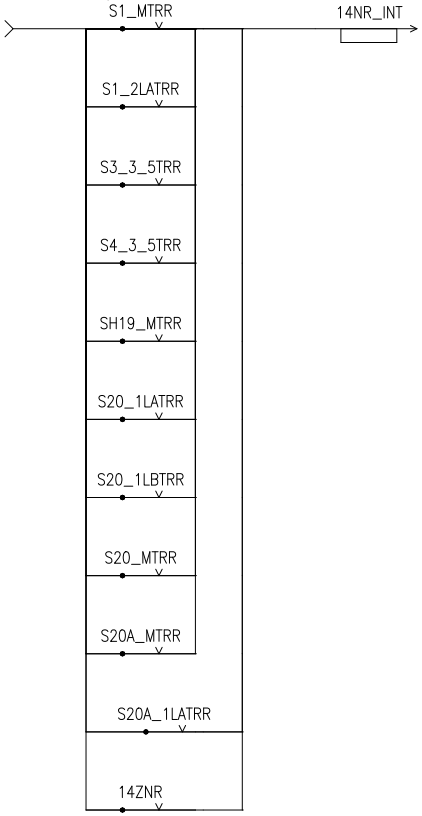
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

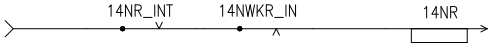
CAD FILE: 512_TAC_C1_00080_R00.DWG

(MICROLOK-II WARM STANDBY)

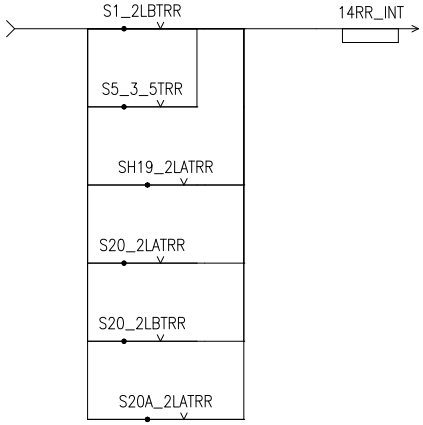
ASSIGN (S1_MTRR + S1_2LATRR + S3_3_5TRR + S4_3_5TRR + SH19_MTRR + S20_1LATRR + S20_1LBTRR + S20_MTRR + S20A_MTRR + S20A_1LATRR + 14ZNR) TO 14NR_INT;



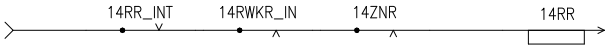
ASSIGN 14NR_INT * ~14NWKR_IN TO 14NR;



ASSIGN (S1_2LBTRR + S5_3_5TRR + SH19_2LATRR + S20_2LATRR + S20_2LBTRR + S20A_2LATRR) TO 14RR_INT;



ASSIGN 14RR_INT * ~14RWKR_IN * ~14ZNR TO 14RR;



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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B	
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SIGNAL & TELECOMMUNICATIONS

rkfVpøykZ
TATICHERLA

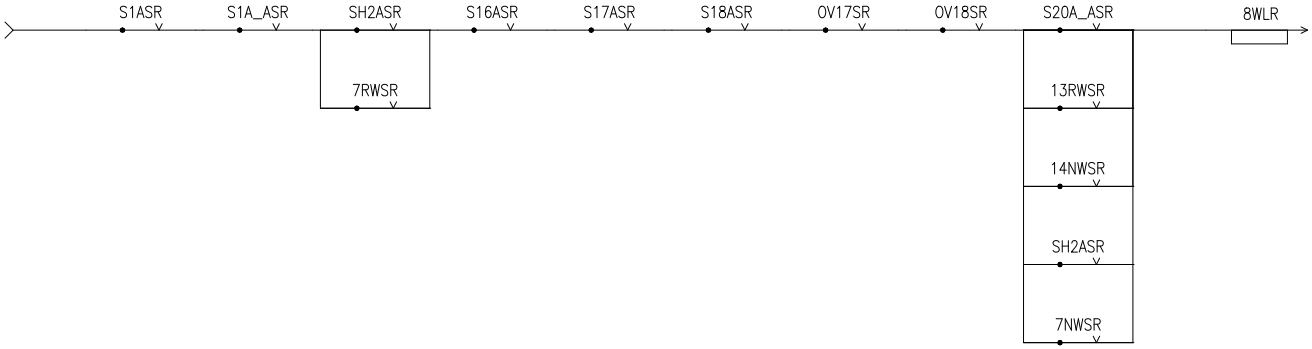
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00081_R00.DWG

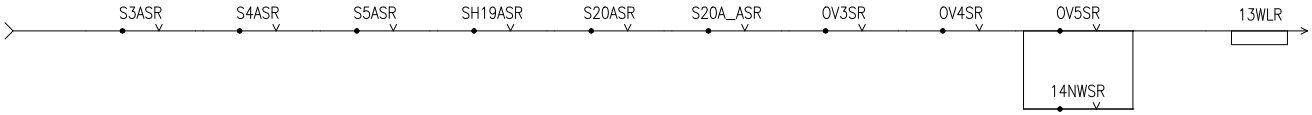
(MICROLOK-II WARM STANDBY)

ASSIGN S1ASR * S1A_ASR * SH2ASR * S16ASR * S17ASR * S18ASR * OV16SR * (OV17SR + 8NWSR) * OV18SR TO 7WLR;

ASSIGN S1ASR * S1A_ASR * (SH2ASR + 7RWSR) * S16ASR * S17ASR * S18ASR * OV17SR * OV18SR * ((S20A_ASR + 13RWSR + 14NWSR) + (SH2ASR + 7NWSR)) TO 8WLR;



ASSIGN S3ASR * S4ASR * S5ASR * SH19ASR * S20ASR * S20A_ASR * OV3SR * OV4SR * (OV5SR + 14NWSR) TO 13WLR;



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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO a fc B | 'khV SHEET 00082 | dqy 'khVSHEETS 00228 | CONT'D ON: 00083

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SIGNAL & TELECOMMUNICATIONS

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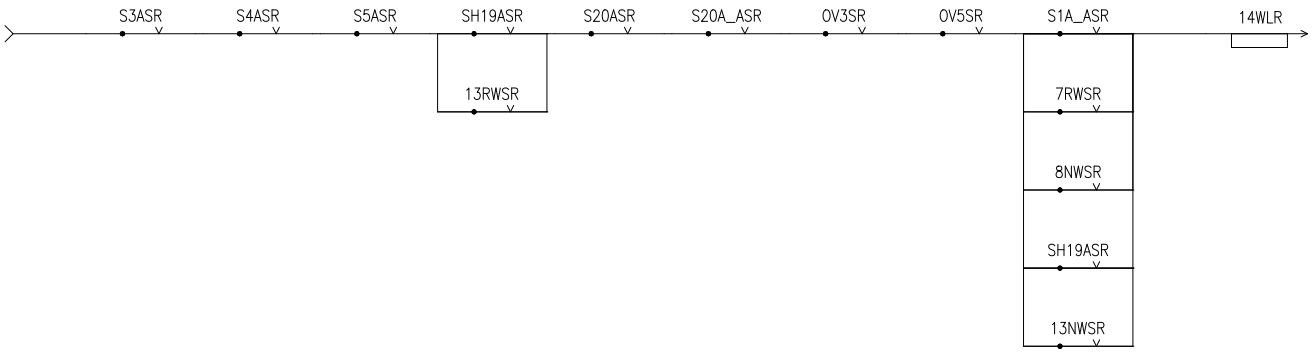
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

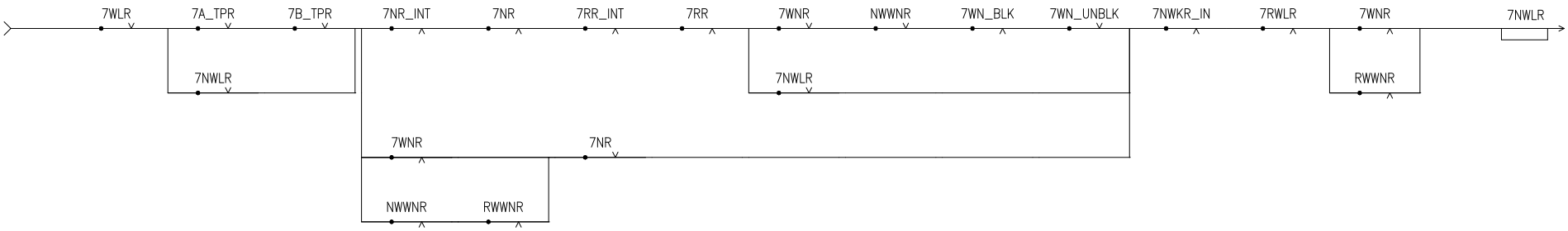
CAD FILE: 512_TAC_C1_00082_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN S3ASR * S4ASR * S5ASR * (SH19ASR + 13RWSR) * S20ASR * S20A_ASR * OV3SR * OV5SR * ((S1A_ASR + 7RWSR + 8NWSR) + (SH19ASR + 13NWSR)) TO 14WLR;



ASSIGN 7WLR * (7A_TPR * 7B_TPR + 7NWLR) * (~7NR_INT * ~7NR * ~7RR_INT * ~7RR * (7WNR * NWWNR * ~7WN_BLK * 7WN_UNBLK + 7NWLR) + (~7WNR + ~NWWNR * ~RWWNR) * 7NR) * ~7NWKR_IN * ~7RWLR * (~7WNR + ~RWWNR) TO 7NWLR;



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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

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SIGNAL & TELECOMMUNICATIONS

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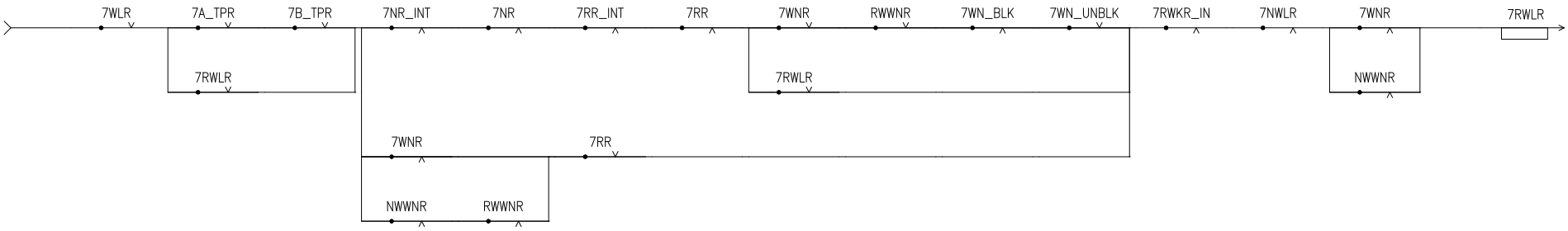
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

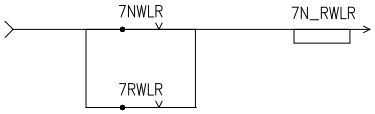
CAD FILE: 512_TAC_C1_00083_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN 7WLR * (7A_TPR * 7B_TPR + 7RWLR) * (~7NR_INT * ~7NR * ~7RR_INT * ~7RR * (7WNR * RWWNR * ~7WN_BLK * 7WN_UNBLK + 7RWLR) + (~7WNR + ~NWWNR * ~RWWNR) * 7RR) * ~7RWKR_IN * ~7NWLR * (~7WNR + ~NWWNR) TO 7RWLR;



ASSIGN (7NWLR + 7RWLR) TO 7N_RWLR;



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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00084 | dqy 'khVSHEETS 00228 | CONT'D ON: 00085

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

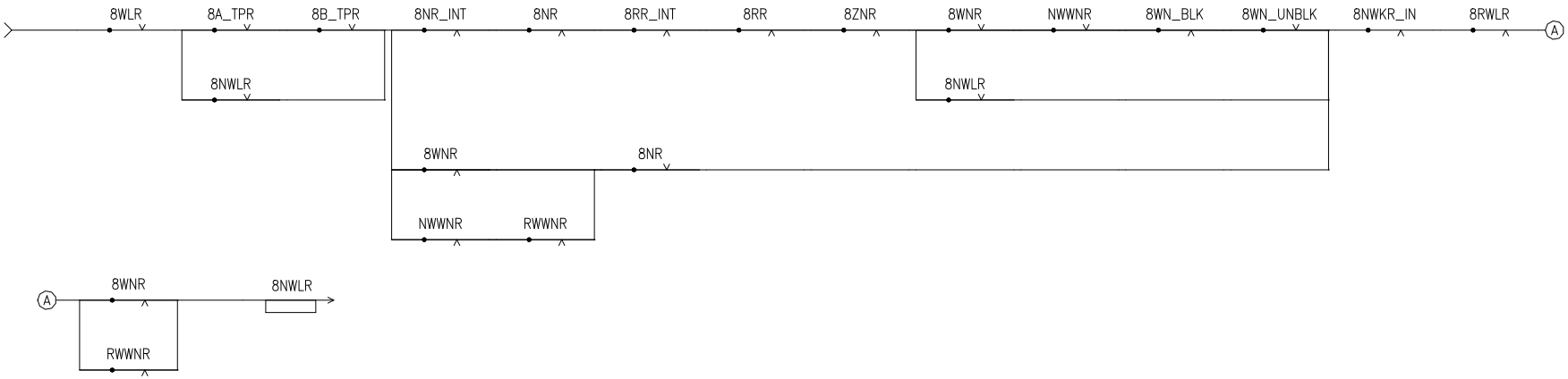
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00084_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN 8WLR * (8A_TPR * 8B_TPR + 8NWLR) * (~8NR_INT * ~8NR * ~8RR_INT * ~8RR * ~8ZNR * (8WNR * NWWNR * ~8WN_BLK * 8WN_UNBLK + 8NWLR) + (~8WNR + ~NWWNR * ~RWWNR) * 8NR) * ~8NWKR_IN * ~8RWLR * (~8WNR + ~RWWNR) TO 8NWLR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP:

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

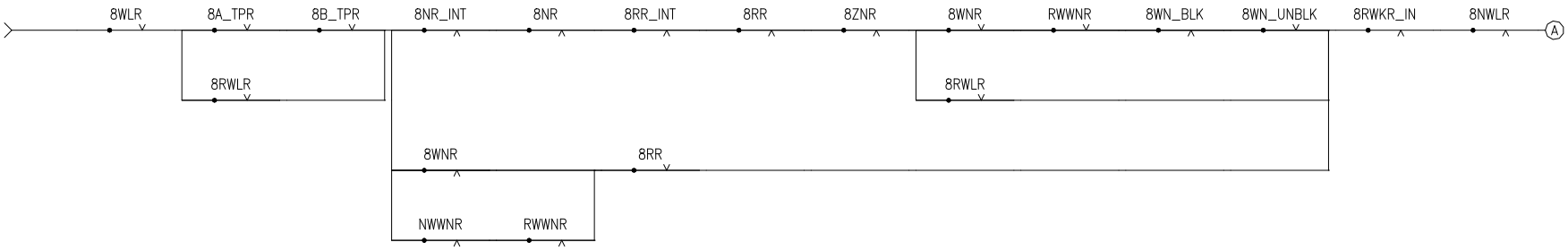
REF.SIG.PLAN.No. IPU.2107B
n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khV SHEET 00085
dqy 'khVSHEETS 00228
CONT'D ON: 00086

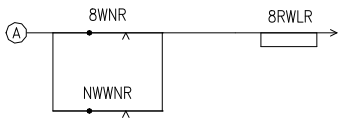
flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00085_R00.DWG

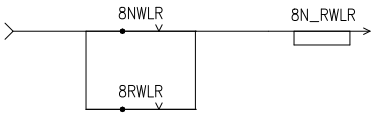
(MICROLOK-II WARM STANDBY)

ASSIGN 8WLR * (8A_TPR * 8B_TPR + 8RWLR) * (~8NR_INT * ~8NR * ~8RR_INT * ~8RR * ~8ZNR * (8WNR * RWWNR * ~8WN_BLK * 8WN_UNBLK + 8RWLR) + (~8WNR + ~NWWNR * ~RWWNR) * 8RR) * ~8RWKR_IN * ~8NWLR * (~8WNR + ~NWWNR) TO 8RWLR;

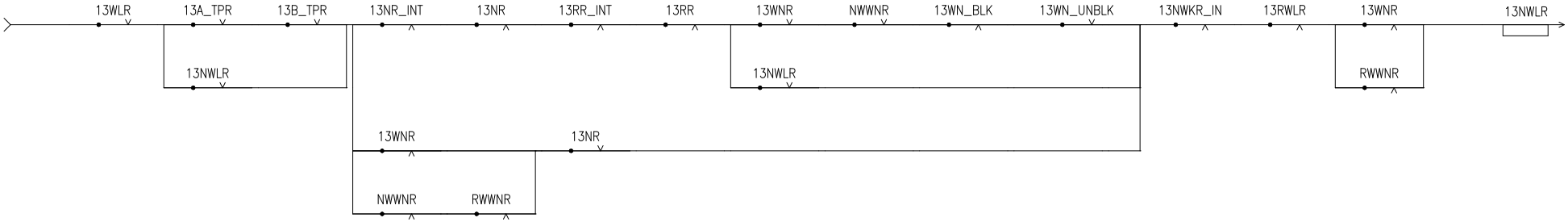




ASSIGN (8NWLR + 8RWLR) TO 8N_RWLR;



ASSIGN 13WLR * (13A_TPR * 13B_TPR + 13NWLR) * (~13NR_INT * ~13NR * ~13RR_INT * ~13RR * (13WNR * NWWNR * ~13WN_BLK * 13WN_UNBLK + 13NWLR) + (~13WNR + ~NWWNR * ~RWWNR) * 13NR) * ~13NWKR_IN * ~13RWLR * (~13WNR + ~RWWNR) TO 13NWLR;



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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

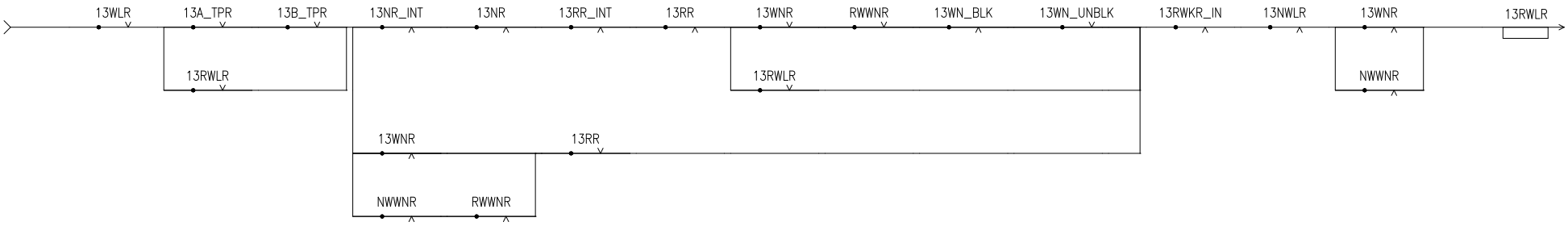
REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khVSHEET 00086 | dqy 'khVSHEETS 00228 | CONT'D ON: 00087

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

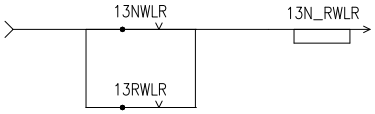
CAD FILE: 512_TAC_C1_00086_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN 13WLR * (13A_TPR * 13B_TPR + 13RWLR) * (~13NR_INT * ~13NR * ~13RR_INT * ~13RR * (13WNR * RWWNR * ~13WN_BLK * 13WN_UNBLK + 13RWLR) + (~13WNR + ~NWWNR * ~RWWNR) * 13RR) * ~13RWKR_IN * ~13NWLR * (~13WNR + ~NWWNR) TO 13RWLR;



ASSIGN (13NWLR + 13RWLR) TO 13N_RWLR;



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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00087 | dqy 'khVSHEETS 00228 | CONT'D ON: 00088

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

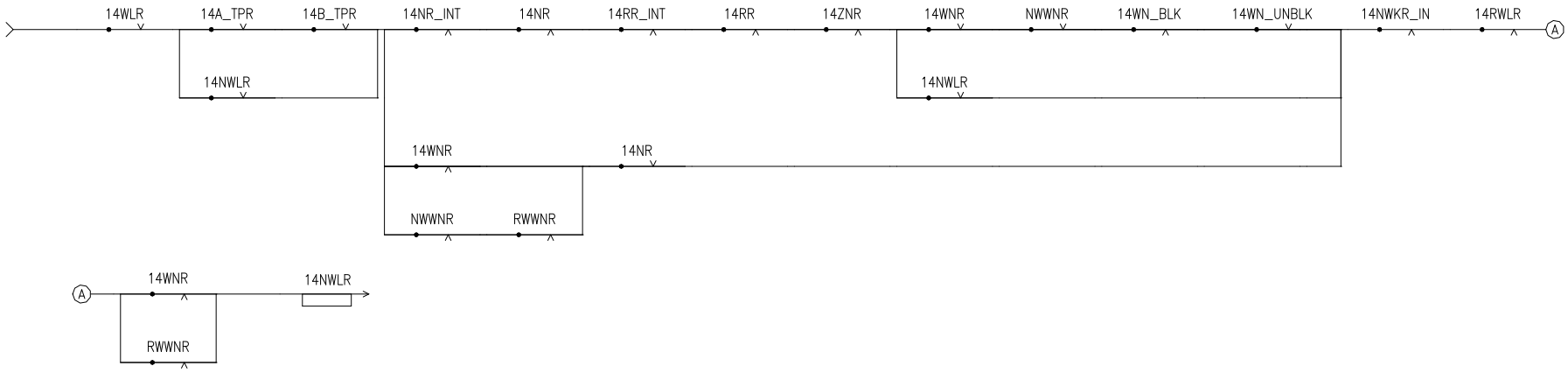
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00087_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN 14WLR * (14A_TPR * 14B_TPR + 14NWLR) * (~14NR_INT * ~14NR * ~14RR_INT * ~14RR * ~14ZNR * (14WNR * NWWNR * ~14WN_BLK * 14WN_UNBLK + 14NWLR) + (~14WNR + ~NWWNR * ~RWWNR) * 14NR) * ~14NWKR_IN * ~14RWLR * (~14WNR + ~RWWNR) TO 14NWLR;



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EQUIVALENT CIRCUITS - C1

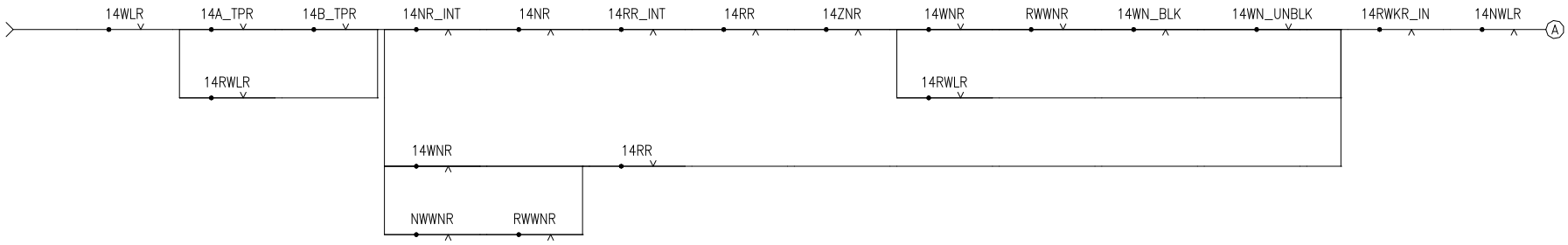
REF.SIG.PLAN.No. IPU.2107B
n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khV SHEET 00088
dqy 'khVSHEETS 00228
CONT'D ON: 00089

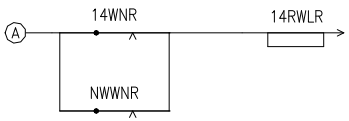
flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00088_R00.DWG

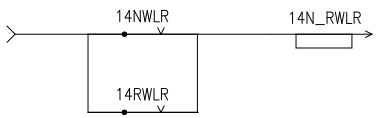
(MICROLOK-II WARM STANDBY)

ASSIGN 14WLR * (14A_TPR * 14B_TPR + 14RWLR) * (~14NR_INT * ~14NR * ~14RR_INT * ~14RR * ~14ZNR * (14WNR * RWWNR * ~14WN_BLK * 14WN_UNBLK + 14RWLR) + (~14WNR + ~NWWNR * ~RWWNR) * 14RR) * ~14RWKR_IN * ~14NWLR * (~14WNR + ~NWWNR) TO 14RWLR;

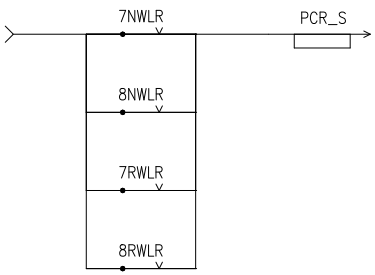




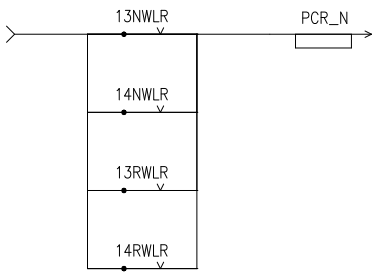
ASSIGN (14NWLR + 14RWLR) TO 14N_RWLR;



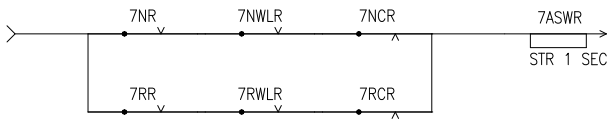
ASSIGN 7NWLR + 8NWLR + 7RWLR + 8RWLR TO PCR_S;



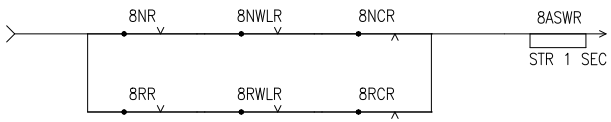
ASSIGN 13NWLR + 14NWLR + 13RWLR + 14RWLR TO PCR_N;



ASSIGN 7NR * 7NWLR * ~7NCR + 7RR * 7RWLR * ~7RCR TO 7ASWR;



ASSIGN 8NR * 8NWLR * ~8NCR + 8RR * 8RWLR * ~8RCR TO 8ASWR;



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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khVSHEET 00089	dqy 'khVSHEETS 00228	CONT'D ON: 00090

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

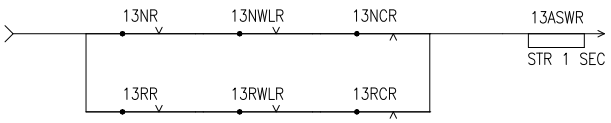
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

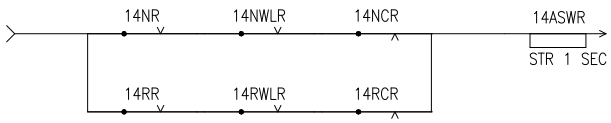
CAD FILE: 512_TAC_C1_00089_R00.DWG

(MICROLOK-II WARM STANDBY)

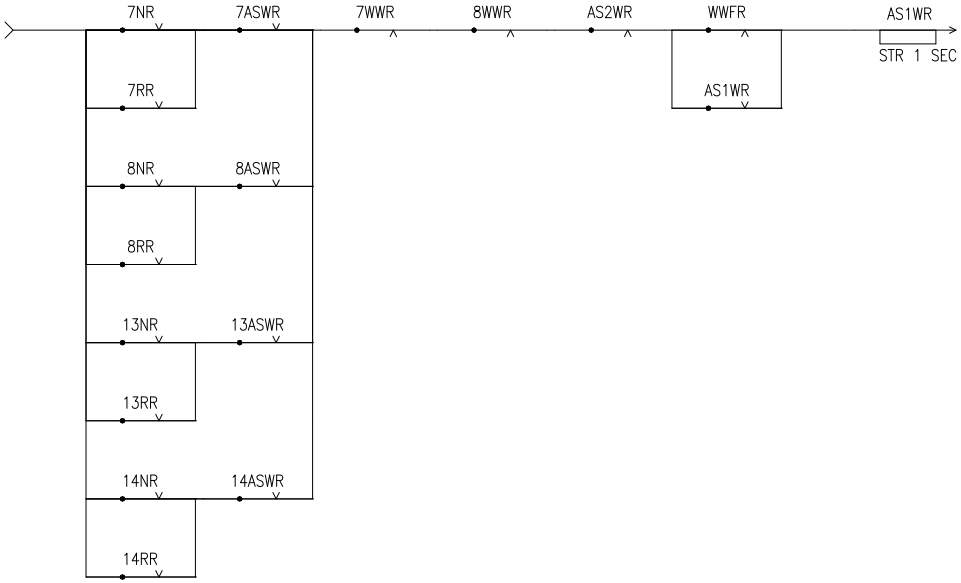
ASSIGN 13NR * 13NWLR * ~13NCR + 13RR * 13RWLR * ~13RCR TO 13ASWR;



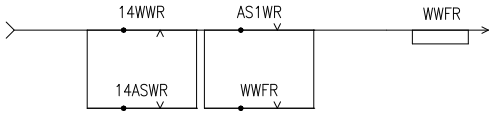
ASSIGN 14NR * 14NWLR * ~14NCR + 14RR * 14RWLR * ~14RCR TO 14ASWR;



ASSIGN ((7NR + 7RR) * 7ASWR + (8NR + 8RR) * 8ASWR + (13NR + 13RR) * 13ASWR + (14NR + 14RR) * 14ASWR) * ~7WWR * ~8WWR * ~AS2WR * (~WWFR + AS1WR) TO AS1WR;



ASSIGN (~14WWR + 14ASWR) * (AS1WR + WWFR) TO WWFR;



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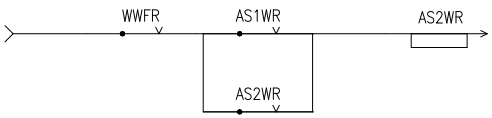
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

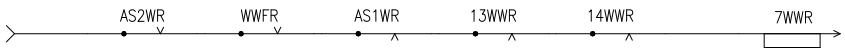
CAD FILE: 512_TAC_C1_00090_R00.DWG

(MICROLOK-II WARM STANDBY)

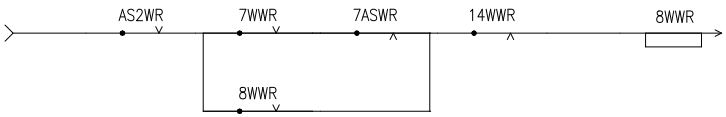
ASSIGN WWFR * (AS1WR + AS2WR) TO AS2WR;



ASSIGN AS2WR * WWFR * ~AS1WR * ~13WWR * ~14WWR TO 7WWR;



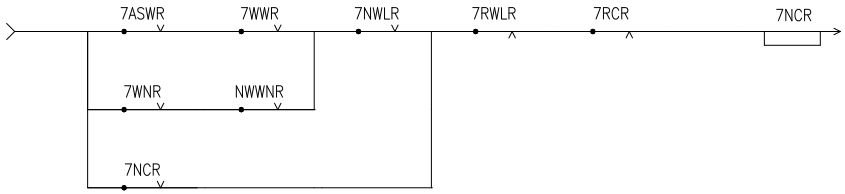
ASSIGN AS2WR * (7WWR * ~7ASWR + 8WWR) * ~14WWR TO 8WWR;



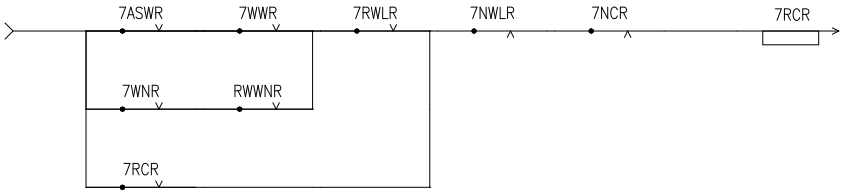
ASSIGN AS2WR * (8WWR * ~8ASWR + 13WWR) TO 13WWR;

ASSIGN AS2WR * (13WWR * ~13ASWR + 14WWR) TO 14WWR;

ASSIGN ((7ASWR * 7WWR + 7WNR * NWWNR) * 7NWLR + 7NCR) * ~7RWLR * ~7RCR TO 7NCR;



ASSIGN ((7ASWR * 7WWR + 7WNR * RWWNR) * 7RWLR + 7RCR) * ~7NWLR * ~7NCR TO 7RCR;



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'khVSHEET 00091	dqy 'khVSHEETS 00228	CONT'D ON: 00092

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

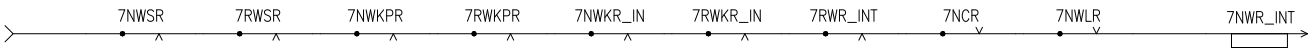
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

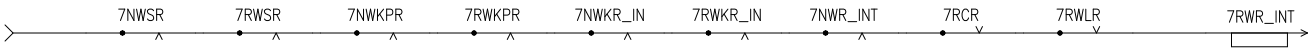
CAD FILE: 512_TAC_C1_00091_R00.DWG

(MICROLOK-II WARM STANDBY)

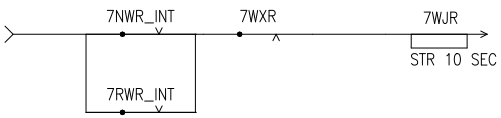
ASSIGN ~7NWSR * ~7RWSR * ~7NWKPR * ~7RWKPR * ~7NWKR_IN * ~7RWKR_IN * ~7RWR_INT * 7NCR * 7NWLR TO 7NWR_INT;



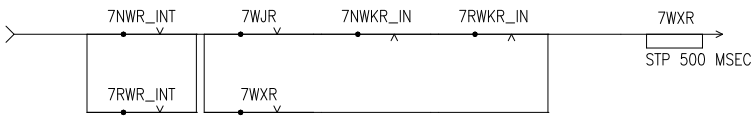
ASSIGN ~7NWSR * ~7RWSR * ~7NWKPR * ~7RWKPR * ~7NWKR_IN * ~7RWKR_IN * ~7NWR_INT * 7RCR * 7RWLR TO 7RWR_INT;



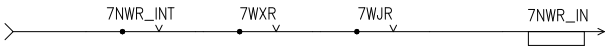
ASSIGN (7NWR_INT + 7RWR_INT) * ~7WXR TO 7WJR;



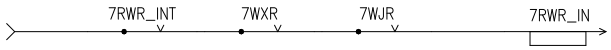
ASSIGN (7NWR_INT + 7RWR_INT) * (7WJR * ~7NWKR_IN * ~7RWKR_IN + 7WXR) TO 7WXR;



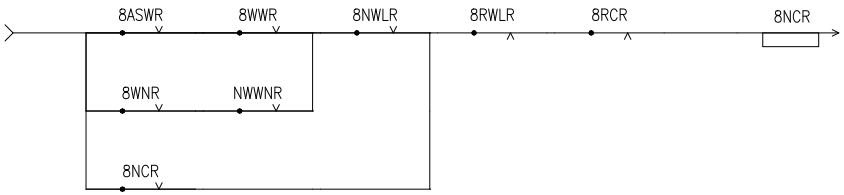
ASSIGN 7NWR_INT * 7WXR * 7WJR TO 7NWR_IN;



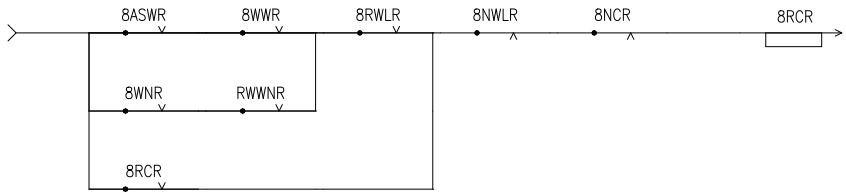
ASSIGN 7RWR_INT * 7WXR * 7WJR TO 7RWR_IN;



ASSIGN ((8ASWR * 8WWR + 8WNR * NWWNR) * 8NWLR + 8NCR) * ~8RWLR * ~8RCR TO 8NCR;



ASSIGN ((8ASWR * 8WWR + 8WNR * RWWNR) * 8RWLR + 8RCR) * ~8NWLR * ~8NCR TO 8RCR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross, 100 Feet Road, J.P.Nagar, 6th Phase, Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khVSHEET 00092 | dqy 'khVSHEETS 00228 | CONT'D ON: 00093

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

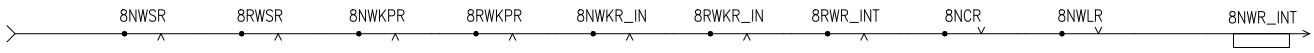
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

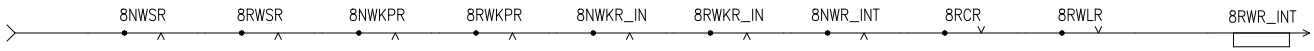
CAD FILE: 512_TAC_C1_00092_R00.DWG

(MICROLOK-II WARM STANDBY)

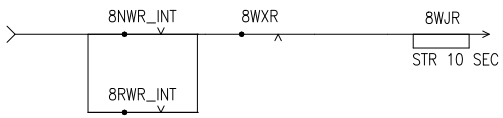
ASSIGN ~8NWSR * ~8RWSR * ~8NWKPR * ~8RWKPR * ~8NWKR_IN * ~8RWKR_IN * ~8RWR_INT * 8NCR * 8NWLR TO 8NWR_INT;



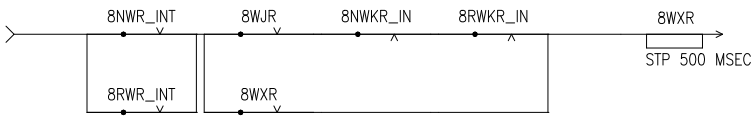
ASSIGN ~8NWSR * ~8RWSR * ~8NWKPR * ~8RWKPR * ~8NWKR_IN * ~8RWKR_IN * ~8NWR_INT * 8RCR * 8RWLR TO 8RWR_INT;



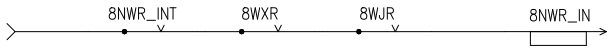
ASSIGN (8NWR_INT + 8RWR_INT) * ~8WXR TO 8WJR;



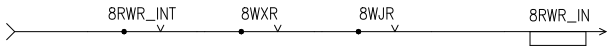
ASSIGN (8NWR_INT + 8RWR_INT) * (8WJR * ~8NWKR_IN * ~8RWKR_IN + 8WXR) TO 8WXR;



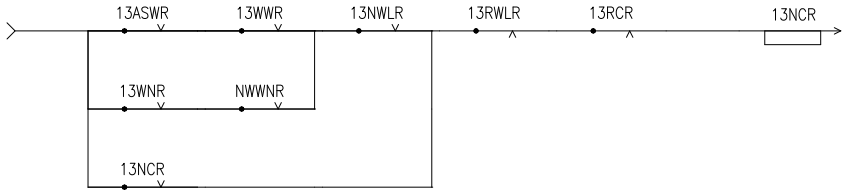
ASSIGN 8NWR_INT * 8WXR * 8WJR TO 8NWR_IN;



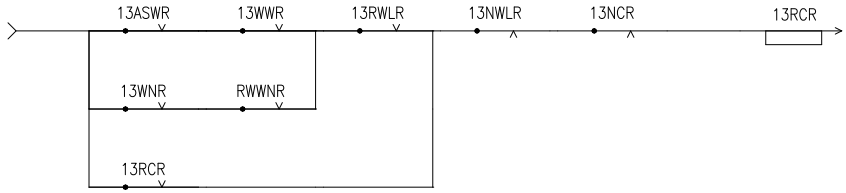
ASSIGN 8RWR_INT * 8WXR * 8WJR TO 8RWR_IN;



ASSIGN ((13ASWR * 13WWR + 13WNR * NWWNR) * 13NWLR + 13NCR) * ~13RWLR * ~13RCR TO 13NCR;



ASSIGN ((13ASWR * 13WWR + 13WNR * RWWNR) * 13RWLR + 13RCR) * ~13NWLR * ~13NCR TO 13RCR;



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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
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Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
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CHK.

APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00093	00228	00094

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

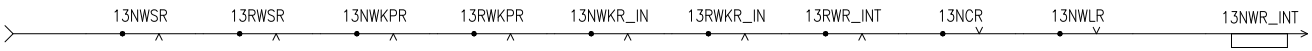
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

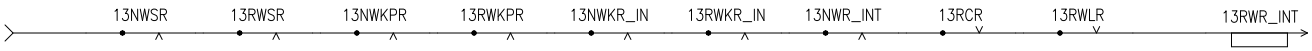
CAD FILE: 512_TAC_C1_00093_R00.DWG

(MICROLOK-II WARM STANDBY)

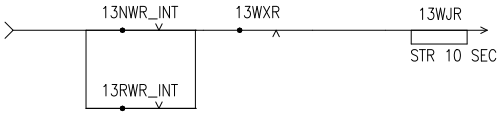
ASSIGN ~13NWSR * ~13RWSR * ~13NWKPR * ~13RWKPR * ~13NWKR_IN * ~13RWKR_IN * ~13RWR_INT * 13NCR * 13NWLR TO 13NWR_INT;



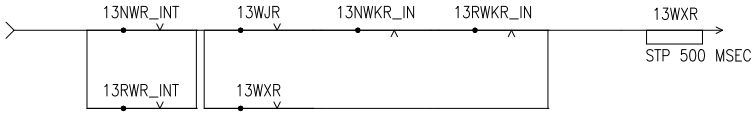
ASSIGN ~13NWSR * ~13RWSR * ~13NWKPR * ~13RWKPR * ~13NWKR_IN * ~13RWKR_IN * ~13NWR_INT * 13RCR * 13RWLR TO 13RWR_INT;



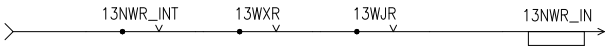
ASSIGN (13NWR_INT + 13RWR_INT) * ~13WXR TO 13WJR;



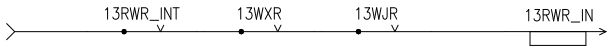
ASSIGN (13NWR_INT + 13RWR_INT) * (13WJR * ~13NWKR_IN * ~13RWKR_IN + 13WXR) TO 13WXR;



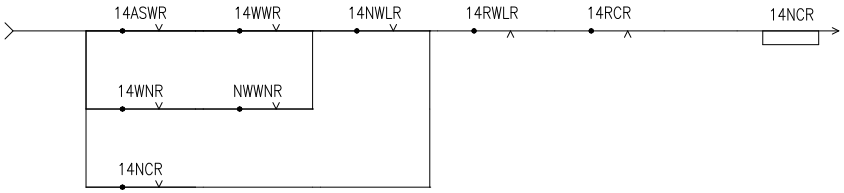
ASSIGN 13NWR_INT * 13WXR * 13WJR TO 13NWR_IN;



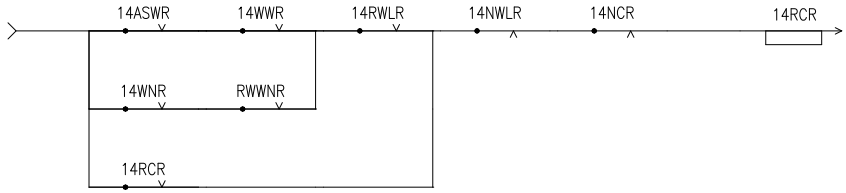
ASSIGN 13RWR_INT * 13WXR * 13WJR TO 13RWR_IN;



ASSIGN ((14ASWR * 14WWR + 14WNR * NWWNR) * 14NWLR + 14NCR) * ~14RWLR * ~14RCR TO 14NCR;



ASSIGN ((14ASWR * 14WWR + 14WNR * RWWNR) * 14RWLR + 14RCR) * ~14NWLR * ~14NCR TO 14RCR;



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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
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APP.

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

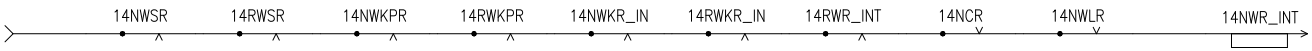
REF.SIG.PLAN.No. IPU.2107B
n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khVSHEET 00094
dqy 'khVSHEETS 00228
CONT'D ON: 00095

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

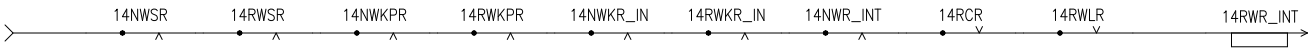
CAD FILE: 512_TAC_C1_00094_R00.DWG

(MICROLOK-II WARM STANDBY)

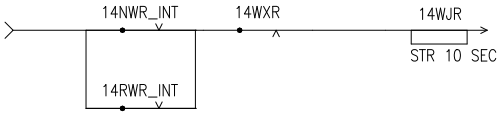
ASSIGN ~14NWSR * ~14RWSR * ~14NWKPR * ~14RWKPR * ~14NWKR_IN * ~14RWKR_IN * ~14RWR_INT * 14NCR * 14NWLR TO 14NWR_INT;



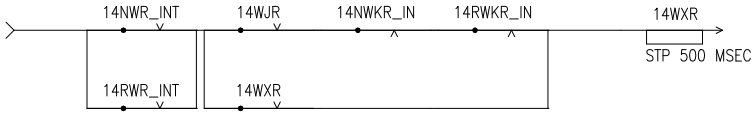
ASSIGN ~14NWSR * ~14RWSR * ~14NWKPR * ~14RWKPR * ~14NWKR_IN * ~14RWKR_IN * ~14NWR_INT * 14RCR * 14RWLR TO 14RWR_INT;



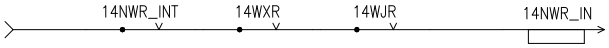
ASSIGN (14NWR_INT + 14RWR_INT) * ~14WXR TO 14WJR;



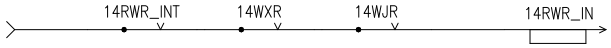
ASSIGN (14NWR_INT + 14RWR_INT) * (14WJR * ~14NWKR_IN * ~14RWKR_IN + 14WXR) TO 14WXR;



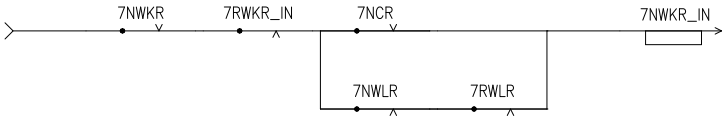
ASSIGN 14NWR_INT * 14WXR * 14WJR TO 14NWR_IN;



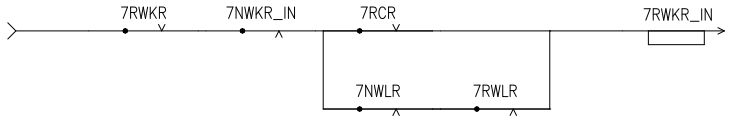
ASSIGN 14RWR_INT * 14WXR * 14WJR TO 14RWR_IN;



ASSIGN 7NWKR * ~7RWKR_IN * (7NCR + ~7NWLR * ~7RWLR) TO 7NWKR_IN;



ASSIGN 7RWKR * ~7NWKR_IN * (7RCR + ~7NWLR * ~7RWLR) TO 7RWKR_IN;



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TVM Signalling & Transportation Systems Pvt. Ltd.
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	CHK.
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00095 | dqy 'khVSHEETS 00228 | CONT'D ON: 00096

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

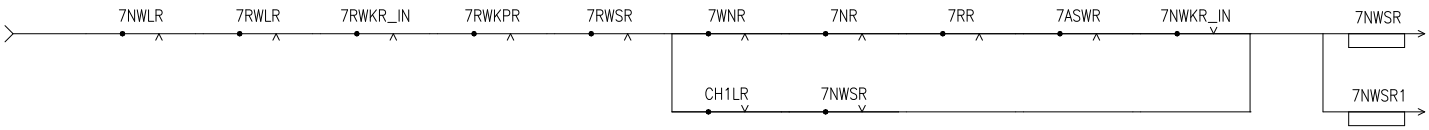
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

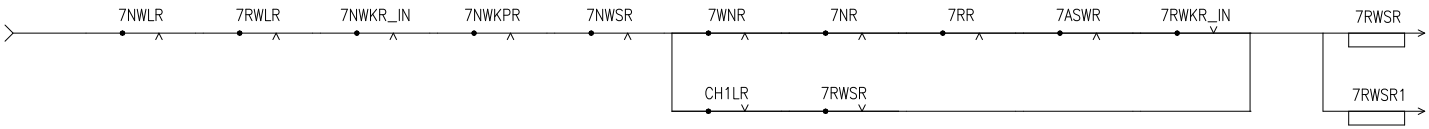
CAD FILE: 512_TAC_C1_00095_R00.DWG

(MICROLOK-II WARM STANDBY)

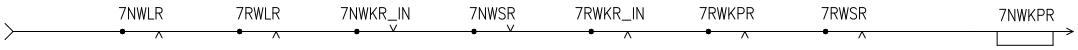
ASSIGN ~7NWLR * ~7RWLR * ~7RWKR_IN * ~7RWKPR * ~7RWSR * (~7WNR * ~7NR * ~7RR * ~7ASWR * 7NWKR_IN + CH1LR * 7NWSR) TO 7NWSR,7NWSR1;



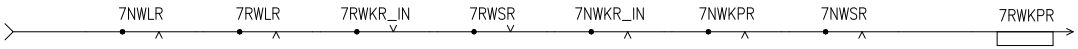
ASSIGN ~7NWLR * ~7RWLR * ~7NWKR_IN * ~7NWKPR * ~7NWSR * (~7WNR * ~7NR * ~7RR * ~7ASWR * 7RWKR_IN + CH1LR * 7RWSR) TO 7RWSR,7RWSR1;



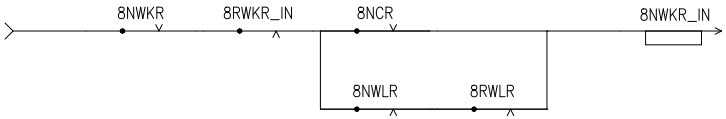
ASSIGN ~7NWLR * ~7RWLR * 7NWKR_IN * 7NWSR * ~7RWKR_IN * ~7RWKPR * ~7RWSR TO 7NWKPR;



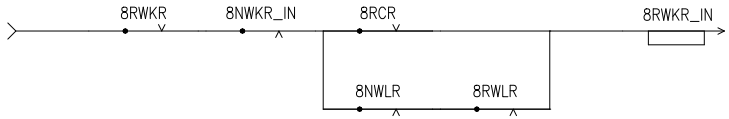
ASSIGN ~7NWLR * ~7RWLR * 7RWKR_IN * 7RWSR * ~7NWKR_IN * ~7NWKPR * ~7NWSR TO 7RWKPR;



ASSIGN 8NWKR * ~8RWKR_IN * (8NCR + ~8NWLR * ~8RWLR) TO 8NWKR_IN;



ASSIGN 8RWKR * ~8NWKR_IN * (8RCR + ~8NWLR * ~8RWLR) TO 8RWKR_IN;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
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APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00096	00228	00097

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

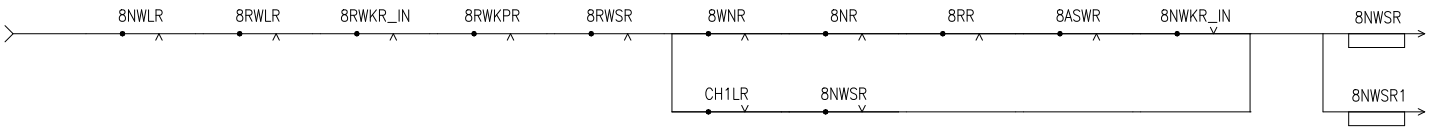
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

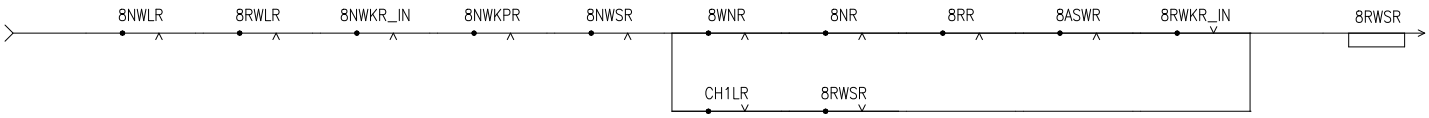
CAD FILE: 512_TAC_C1_00096_R00.DWG

(MICROLOK-II WARM STANDBY)

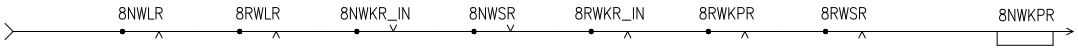
ASSIGN ~8NWLR * ~8RWLR * ~8RWKR_IN * ~8RWKPR * ~8RWSR * (~8WNR * ~8NR * ~8RR * ~8ASWR * 8NWKR_IN + CH1LR * 8NWSR) TO 8NWSR,8NWSR1;



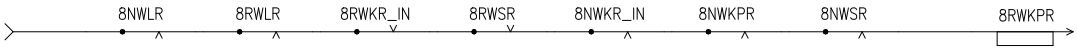
ASSIGN ~8NWLR * ~8RWLR * ~8NWKR_IN * ~8NWKPR * ~8NWSR * (~8WNR * ~8NR * ~8RR * ~8ASWR * 8RWKR_IN + CH1LR * 8RWSR) TO 8RWSR;



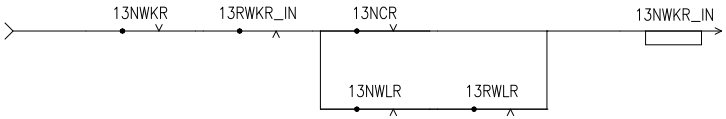
ASSIGN ~8NWLR * ~8RWLR * 8NWKR_IN * 8NWSR * ~8RWKR_IN * ~8RWKPR * ~8RWSR TO 8NWKPR;



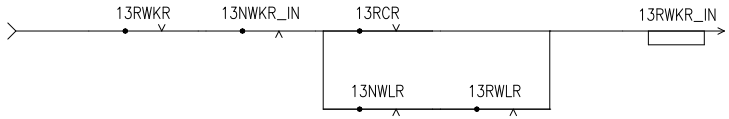
ASSIGN ~8NWLR * ~8RWLR * 8RWKR_IN * 8RWSR * ~8NWKR_IN * ~8NWKPR * ~8NWSR TO 8RWKPR;



ASSIGN 13NWKR * ~13RWKR_IN * (13NCR + ~13NWLR * ~13RWLR) TO 13NWKR_IN;



ASSIGN 13RWKR * ~13NWKR_IN * (13RCR + ~13NWLR * ~13RWLR) TO 13RWKR_IN;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00097 | dqy 'khVSHEETS 00228 | CONT'D ON: 00098

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

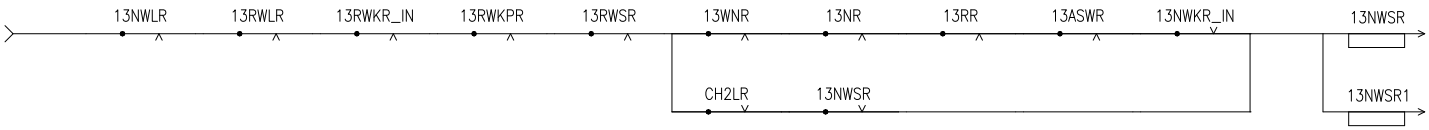
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

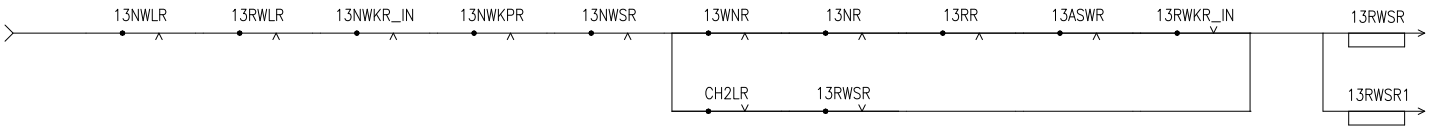
CAD FILE: 512_TAC_C1_00097_R00.DWG

(MICROLOK-II WARM STANDBY)

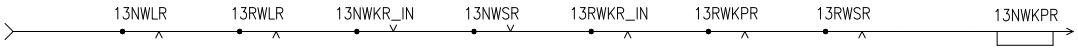
ASSIGN ~13NWLR * ~13RWLR * ~13RWKR_IN * ~13RWKPR * ~13RWSR * (~13WNR * ~13NR * ~13RR * ~13ASWR * 13NWKR_IN + CH2LR * 13NWSR) TO 13NWSR,13NWSR1;



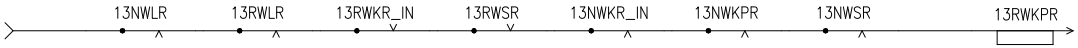
ASSIGN ~13NWLR * ~13RWLR * ~13NWKR_IN * ~13NWKPR * ~13NWSR * (~13WNR * ~13NR * ~13RR * ~13ASWR * 13RWKR_IN + CH2LR * 13RWSR) TO 13RWSR,13RWSR1;



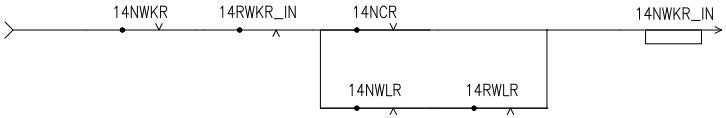
ASSIGN ~13NWLR * ~13RWLR * 13NWKR_IN * 13NWSR * ~13RWKR_IN * ~13RWKPR * ~13RWSR TO 13NWKPR;



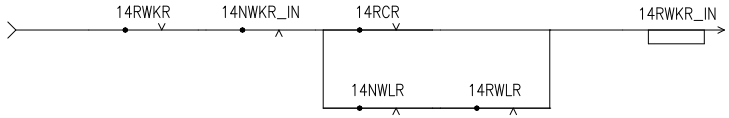
ASSIGN ~13NWLR * ~13RWLR * 13RWKR_IN * 13RWSR * ~13NWKR_IN * ~13NWKPR * ~13NWSR TO 13RWKPR;



ASSIGN 14NWKR * ~14RWKR_IN * (14NCR + ~14NWLR * ~14RWLR) TO 14NWKR_IN;



ASSIGN 14RWKR * ~14NWKR_IN * (14RCR + ~14NWLR * ~14RWLR) TO 14RWKR_IN;



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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khVSHEET 00098 | dqy 'khVSHEETS 00228 | CONT'D ON: 00099

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

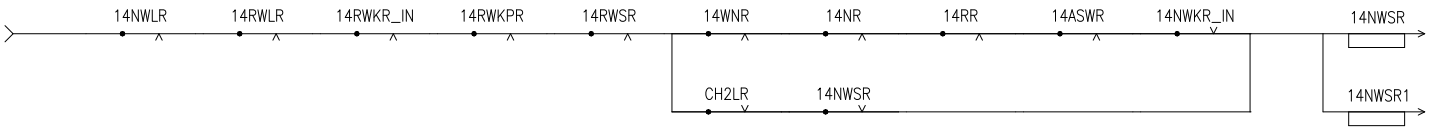
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

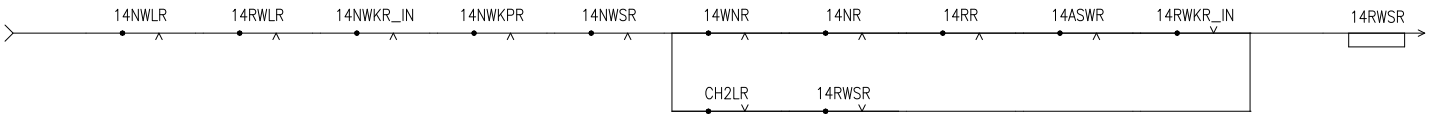
CAD FILE: 512_TAC_C1_00098_R00.DWG

(MICROLOK-II WARM STANDBY)

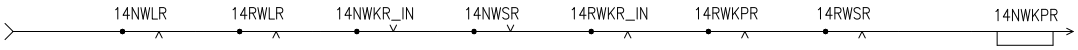
ASSIGN ~14NWLR * ~14RWLR * ~14RWKR_IN * ~14RWKPR * ~14RWSR * (~14WNR * ~14NR * ~14RR * ~14ASWR * 14NWKR_IN + CH2LR * 14NWSR) TO 14NWSR,14NWSR1;



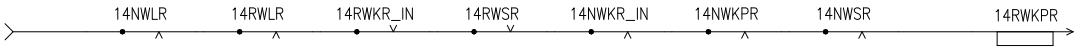
ASSIGN ~14NWLR * ~14RWLR * ~14NWKR_IN * ~14NWKPR * ~14NWSR * (~14WNR * ~14NR * ~14RR * ~14ASWR * 14RWKR_IN + CH2LR * 14RWSR) TO 14RWSR;



ASSIGN ~14NWLR * ~14RWLR * 14NWKR_IN * 14NWSR * ~14RWKR_IN * ~14RWKPR * ~14RWSR TO 14NWKPR;



ASSIGN ~14NWLR * ~14RWLR * 14RWKR_IN * 14RWSR * ~14NWKR_IN * ~14NWKPR * ~14NWSR TO 14RWKPR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00099 | dqy 'khVSHEETS 00228 | CONT'D ON: 00100

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

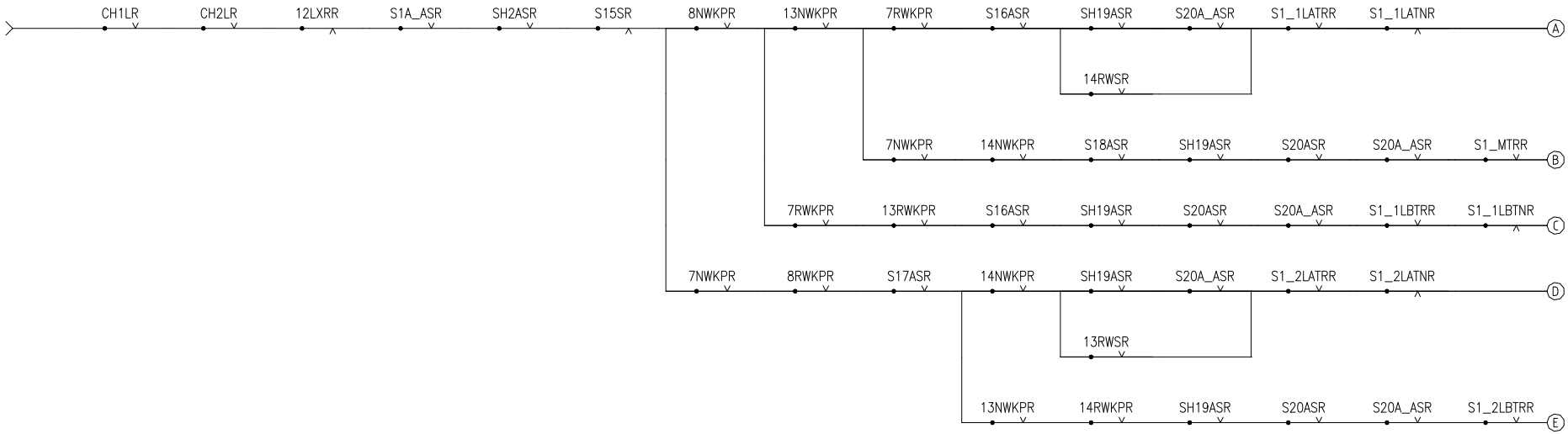
rkfVpøykZ
TATICHERLA

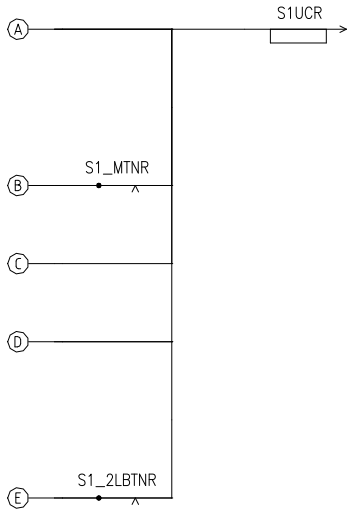
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00099_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN CH1LR * CH2LR * ~12LXRR * S1A_ASR * SH2ASR * ~S15SR * (8NWKPR * (13NWKPR * (7RWKPR * S16ASR * (SH19ASR * S20A_ASR + 14RWSR) * S1_1LATRR * ~S1_1LATNR + 7NWKPR * 14NWKPR * S18ASR * SH19ASR * S20ASR * S20A_ASR * S1_MTRR * ~S1_MTNR) + 7RWKPR * 13RWKPR * S16ASR * SH19ASR * S20ASR * S20A_ASR * S1_1LBTRR * ~S1_1LBTNR) + 7NWKPR * 8RWKPR * S17ASR * (14NWKPR * (SH19ASR * S20A_ASR + 13RWSR) * S1_2LATRR * ~S1_2LATNR + 13NWKPR * 14RWKPR * SH19ASR * S20ASR * S20A_ASR * S1_2LBTRR * ~S1_2LBTNR)) TO S1UCR;





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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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CHK.
APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00100 | dqy 'khVSHEETS 00228 | CONT'D ON: 00101

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

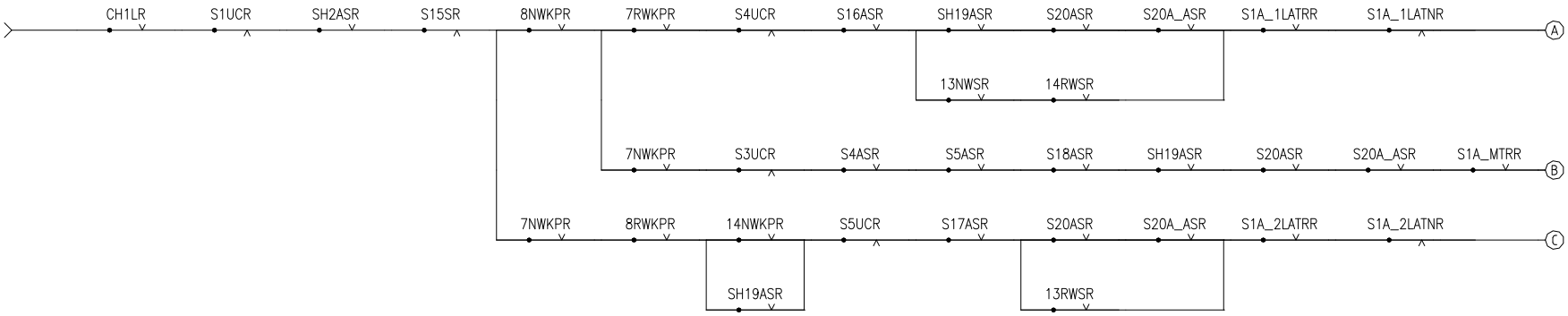
rkfVpøykZ
TATICHERLA

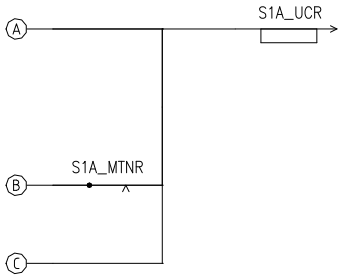
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00100_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN CH1LR * ~S1UCR * SH2ASR * ~S15SR * (8NWKPR * (7RWKPR * ~S4UCR * S16ASR * (SH19ASR * S20ASR * S20A_ASR + 13NWSR * 14RWSR) * S1A_1LATRR * ~S1A_1LATNR + 7NWKPR * ~S3UCR * S4ASR * S5ASR * S18ASR * SH19ASR * S20ASR * S20A_ASR * S1A_MTRR * ~S1A_MTNR) + 7NWKPR * 8RWKPR * (14NWKPR + SH19ASR) * ~S5UCR * S17ASR * (S20ASR * S20A_ASR + 13RWSR) * S1A_2LATRR * ~S1A_2LATNR) TO S1A_UCR;





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00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

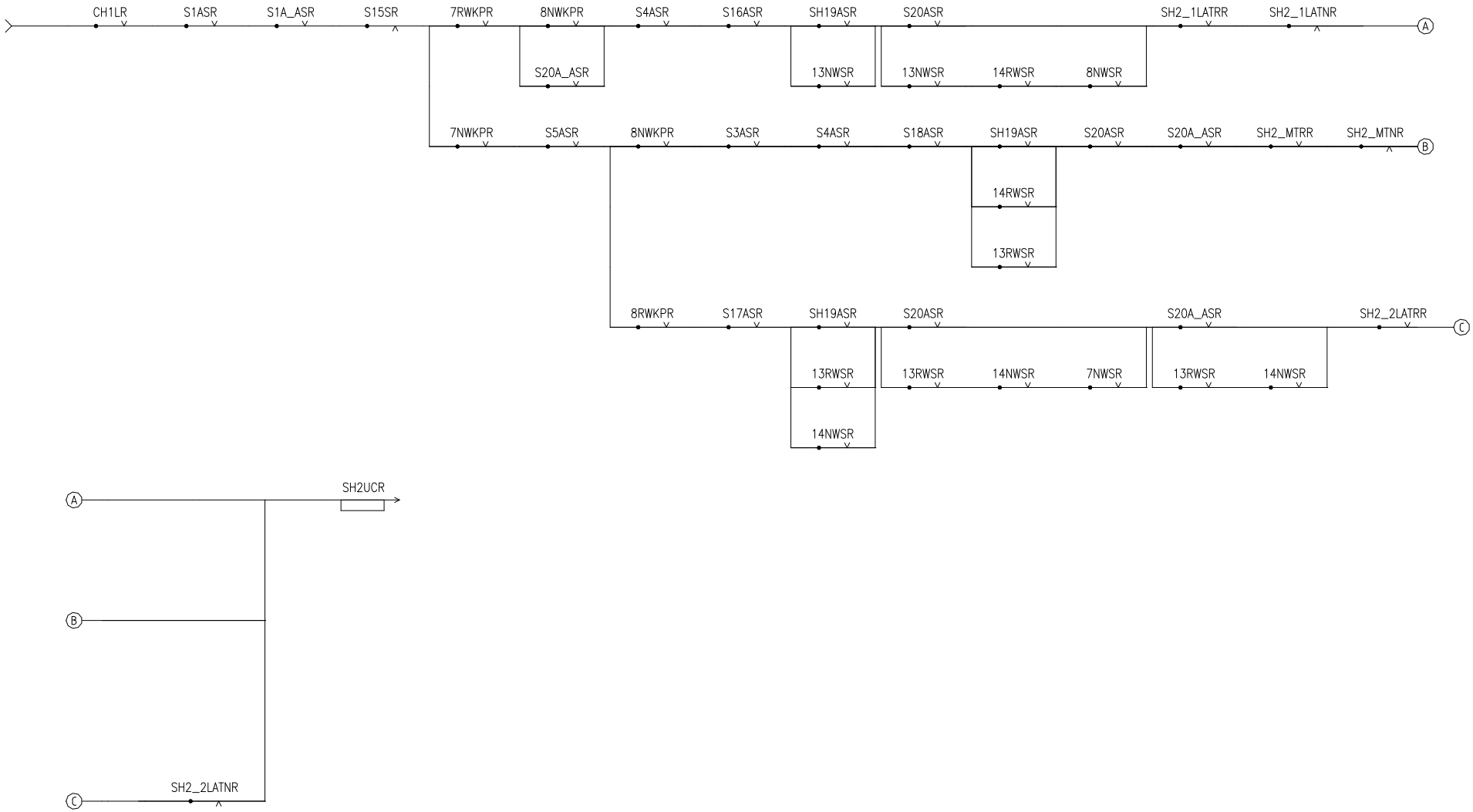
REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00101	00228	00102

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00101_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN CH1LR * S1ASR * S1A_ASR * ~S15SR * (7RWKPR * (8NWKPR + S20A_ASR) * S4ASR * S16ASR * (SH19ASR + 13NWSR) * (S20ASR + 13NWSR * 14RWSR * 8NWSR) * SH2_1LATRR * ~SH2_1LATNR + 7NWKPR * S5ASR * (8NWKPR * S3ASR * S4ASR * S18ASR * (SH19ASR + 14RWSR + 13RWSR) * S20ASR * S20A_ASR * SH2_MTRR * ~SH2_MTNR + 8RWKPR * S17ASR * (SH19ASR + 13RWSR + 14NWSR) * (S20ASR + 13RWSR * 14NWSR * 7NWSR) * (S20A_ASR + 13RWSR * 14NWSR) * SH2_2LATRR * ~SH2_2LATNR)) TO SH2UCR;



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TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

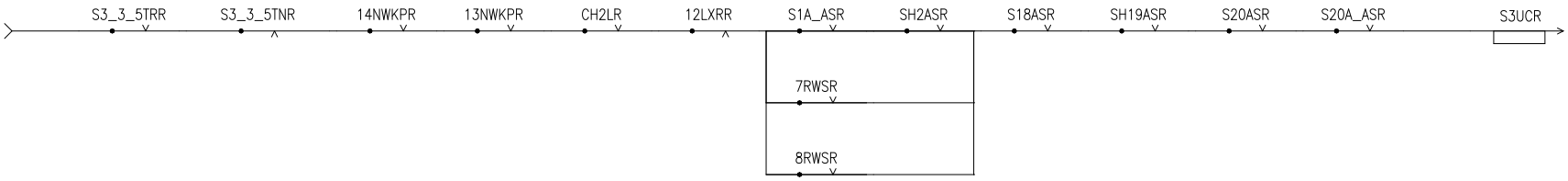
REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00102	00228	00103

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

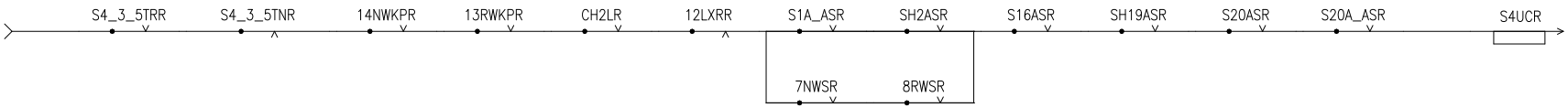
CAD FILE: 512_TAC_C1_00102_R00.DWG

(MICROLOK-II WARM STANDBY)

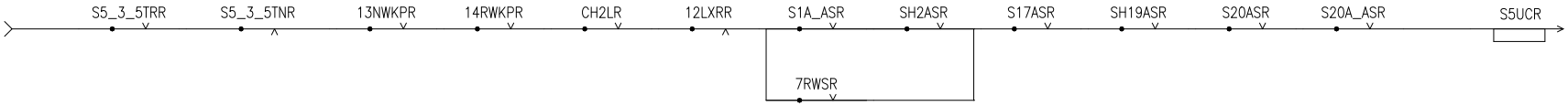
ASSIGN S3_3_5TRR * ~S3_3_5TNR * 14NWKPR * 13NWKPR * CH2LR * ~12LXRR * (S1A_ASR * SH2ASR + 7RWSR + 8RWSR) * S18ASR * SH19ASR * S20ASR * S20A_ASR TO S3UCR;



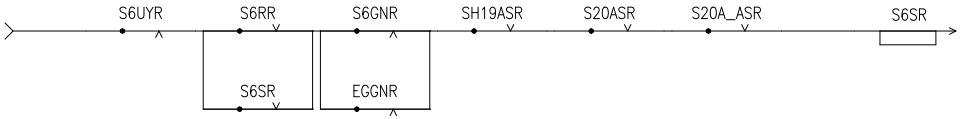
ASSIGN S4_3_5TRR * ~S4_3_5TNR * 14NWKPR * 13RWKPR * CH2LR * ~12LXRR * (S1A_ASR * SH2ASR + 7NWSR * 8RWSR) * S16ASR * SH19ASR * S20ASR * S20A_ASR TO S4UCR;



ASSIGN S5_3_5TRR * ~S5_3_5TNR * 13NWKPR * 14RWKPR * CH2LR * ~12LXRR * (S1A_ASR * SH2ASR + 7RWSR) * S17ASR * SH19ASR * S20ASR * S20A_ASR TO S5UCR;



ASSIGN ~S6UYR * (S6RR + S6SR) * (~S6GNR + ~EGGNR) * SH19ASR * S20ASR * S20A_ASR TO S6SR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	CHK.
SE/SSE	
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00103	00228	00104

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

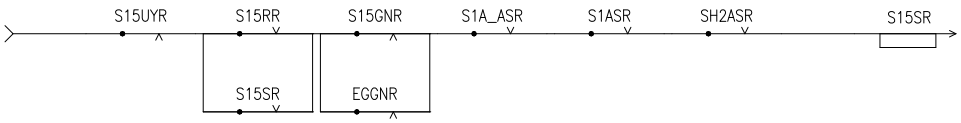
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

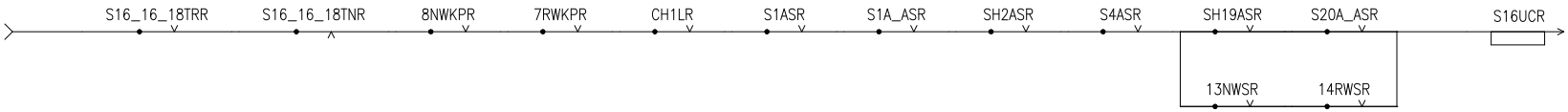
CAD FILE: 512_TAC_C1_00103_R00.DWG

(MICROLOK-II WARM STANDBY)

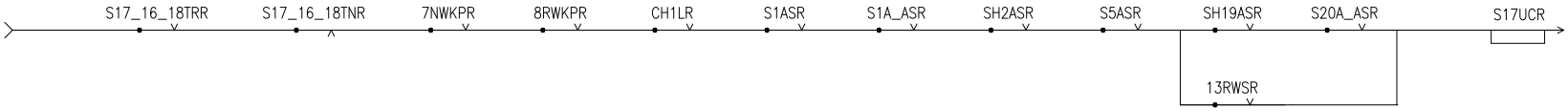
ASSIGN ~S15UYR * (S15RR + S15SR) * (~S15GNR + ~EGGNR) * S1A_ASR * S1ASR * SH2ASR TO S15SR;



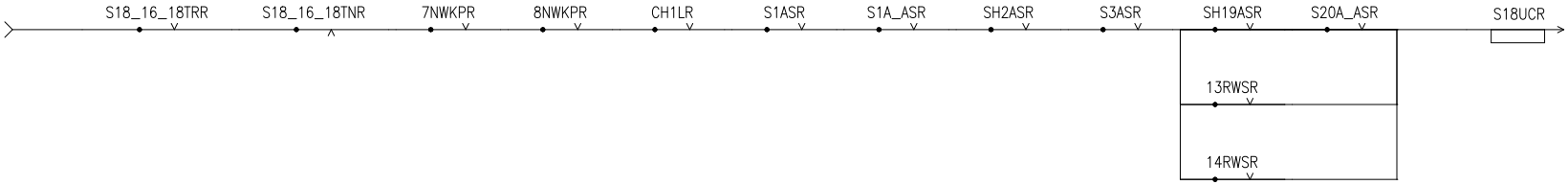
ASSIGN S16_16_18TRR * ~S16_16_18TNR * 8NWKPR * 7RWKPR * CH1LR * S1ASR * S1A_ASR * SH2ASR * S4ASR * (SH19ASR * S20A_ASR + 13NWSR * 14RWSR) TO S16UCR;



ASSIGN S17_16_18TRR * ~S17_16_18TNR * 7NWKPR * 8RWKPR * CH1LR * S1ASR * S1A_ASR * SH2ASR * S5ASR * (SH19ASR * S20A_ASR + 13RWSR) TO S17UCR;



ASSIGN S18_16_18TRR * ~S18_16_18TNR * 7NWKPR * 8NWKPR * CH1LR * S1ASR * S1A_ASR * SH2ASR * S3ASR * (SH19ASR * S20A_ASR + 13RWSR + 14RWSR) TO S18UCR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	CHK.
SE/SSE	
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	a fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON: 00105
00104	00228	

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

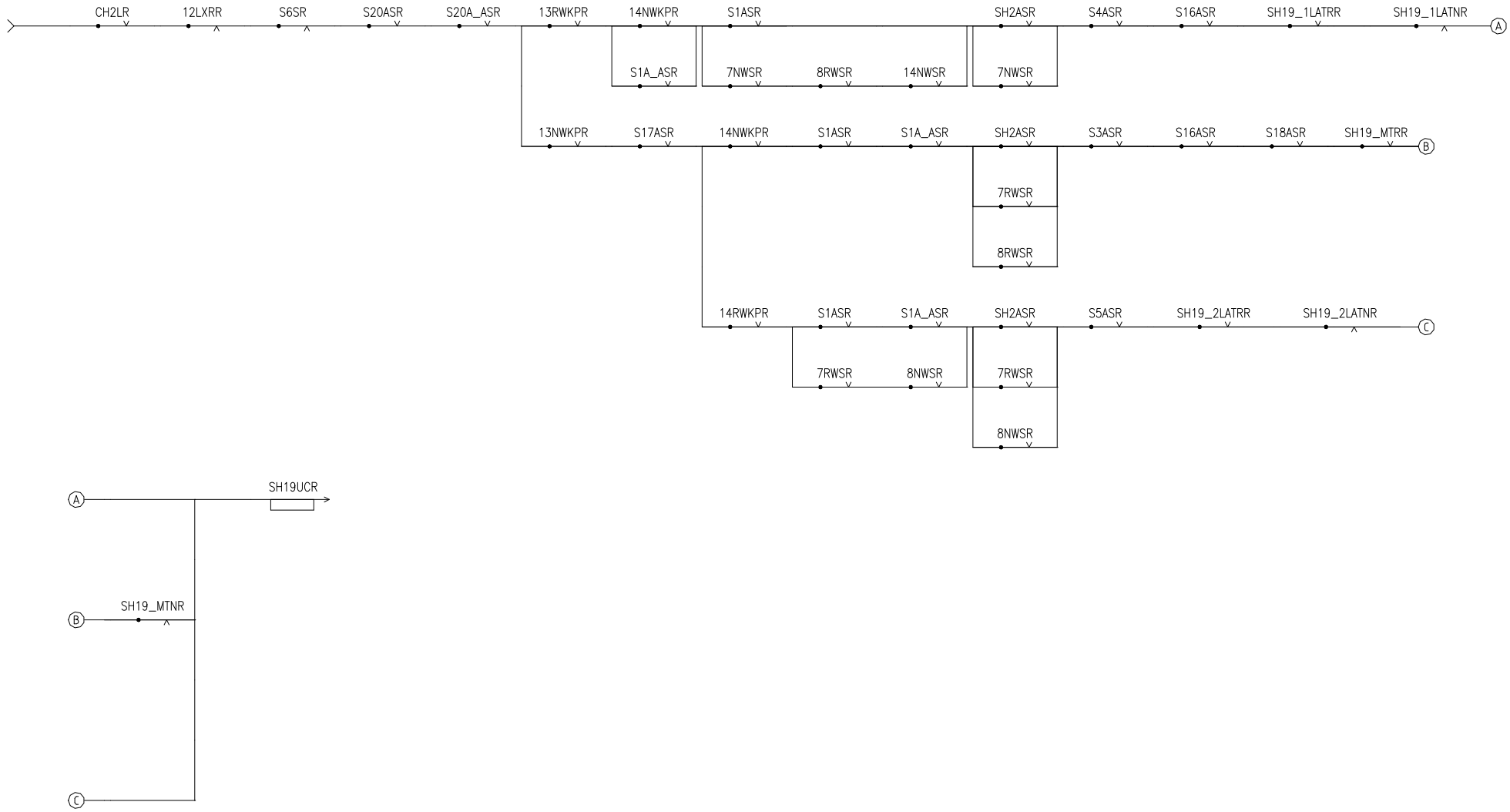
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00104_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN CH2LR * ~12LXRR * ~S6SR * S20ASR * S20A_ASR * (13RWKPR * (14NWKPR + S1A_ASR) * (S1ASR + 7NWSR * 8RWSR * 14NWSR) * (SH2ASR + 7NWSR) * S4ASR * S16ASR * SH19_1LATRR * ~SH19_1LATNR + 13NWKPR * S17ASR * (14NWKPR * S1ASR * S1A_ASR * (SH2ASR + 7RWSR + 8RWSR) * S3ASR * S16ASR * S18ASR * SH19_MTRR * ~SH19_MTNR + 14RWKPR * (S1ASR * S1A_ASR + 7RWSR * 8NWSR) * (SH2ASR + 7RWSR + 8NWSR) * S5ASR * SH19_2LATRR * ~SH19_2LATNR)) TO SH19UCR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
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00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00105 | dqy 'khVSHEETS 00228 | CONT'D ON: 00106

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SIGNAL & TELECOMMUNICATIONS

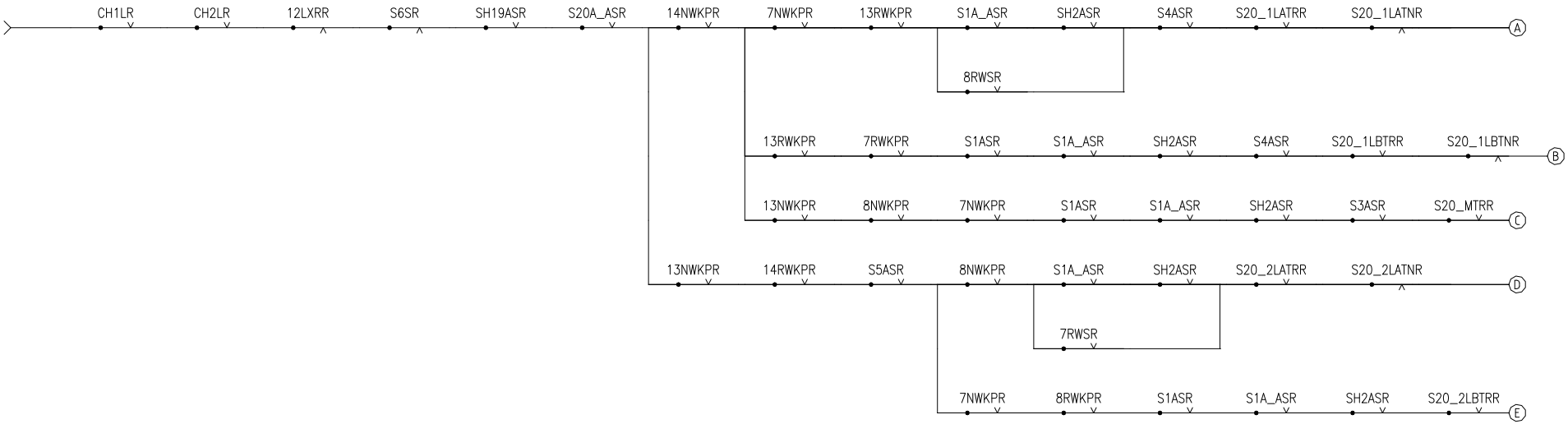
rkfVpøykZ
TATICHERLA

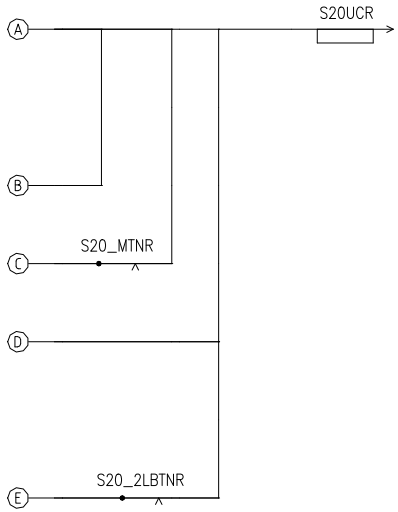
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00105_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN CH1LR * CH2LR * ~12LXRR * ~S6SR * SH19ASR * S20A_ASR * (14NWKPR * (7NWKPR * 13RWKPR * (S1A_ASR * SH2ASR + 8RWSR) * S4ASR * S20_1LATRR * ~S20_1LATNR + 13RWKPR * 7RWKPR * S1ASR * S1A_ASR * SH2ASR * S4ASR * S20_1LBTRR * ~S20_1LBTNR + 13NWKPR * 8NWKPR * 7NWKPR * S1ASR * S1A_ASR * SH2ASR * S3ASR * S20_MTRR * ~S20_MTNR) + 13NWKPR * 14RWKPR * S5ASR * (8NWKPR * (S1A_ASR * SH2ASR + 7RWSR) * S20_2LATRR * ~S20_2LATNR + 7NWKPR * 8RWKPR * S1ASR * S1A_ASR * SH2ASR * S20_2LBTRR * ~S20_2LBTNR)) TO S20UCR;





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TVM Signalling & Transportation Systems Pvt. Ltd.
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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO a fc B

'khV SHEET 00106 | dqy 'khVSHEETS 00228 | CONT'D ON: 00107

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

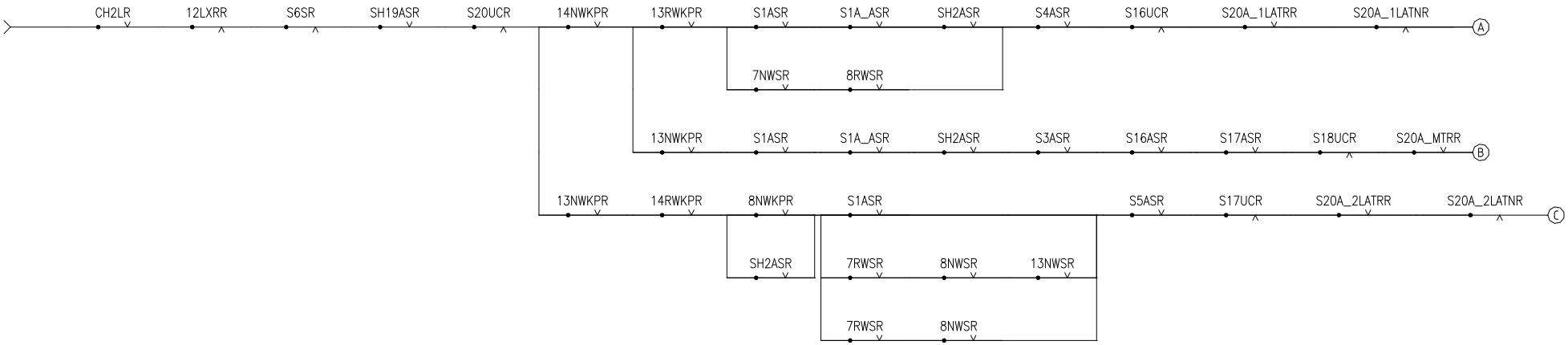
rkfVpøykZ
TATICHERLA

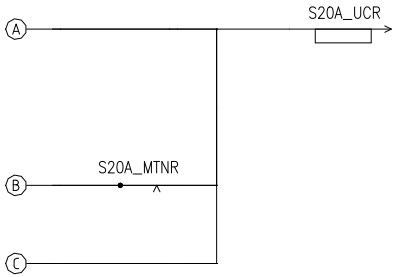
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00106_R00.DWG

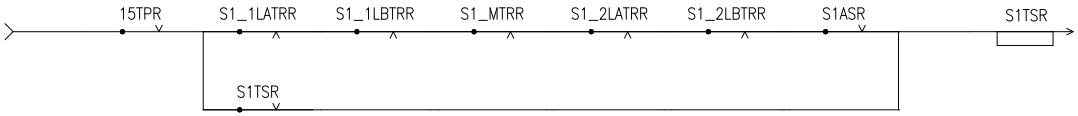
(MICROLOK-II WARM STANDBY)

ASSIGN CH2LR * ~12LXRR * ~S6SR * SH19ASR * ~S20UCR * (14NWKPR * (13RWKPR * (S1ASR * S1A_ASR * SH2ASR + 7NWSR * 8RWSR) * S4ASR * ~S16UCR * S20A_1LATRR * ~S20A_1LATNR + 13NWKPR * S1ASR * S1A_ASR * SH2ASR * S3ASR * S16ASR * S17ASR * ~S18UCR * S20A_MTRR * ~S20A_MTNR) + 13NWKPR * 14RWKPR * (8NWKPR + SH2ASR) * (S1ASR + 7RWSR * 8NWSR * 13NWSR + 7RWSR * 8NWSR) * S5ASR * ~S17UCR * S20A_2LATRR * ~S20A_2LATNR) TO S20A_UCR;





ASSIGN 15TPR * (~S1_1LATRR * ~S1_1LBTRR * ~S1_MTRR * ~S1_2LATRR * ~S1_2LBTRR * S1ASR + S1TSR) TO S1TSR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00107 | dqy 'khVSHEETS 00228 | CONT'D ON: 00108

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SIGNAL & TELECOMMUNICATIONS

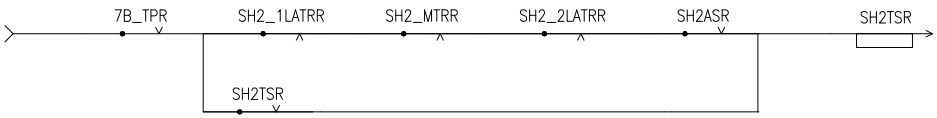
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

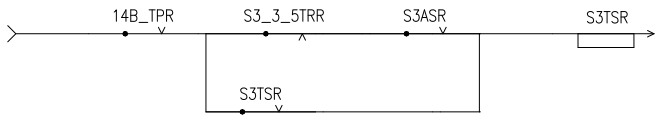
CAD FILE: 512_TAC_C1_00107_R00.DWG

(MICROLOK-II WARM STANDBY)

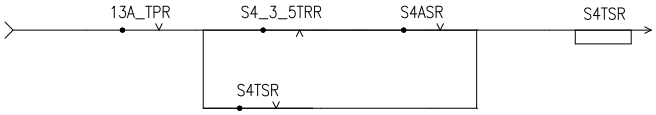
ASSIGN 7B_TPR * (~SH2_1LATRR * ~SH2_MTRR * ~SH2_2LATRR * SH2ASR + SH2TSR) TO SH2TSR;



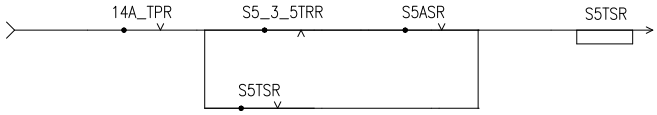
ASSIGN 14B_TPR * (~S3_3_5TRR * S3ASR + S3TSR) TO S3TSR;



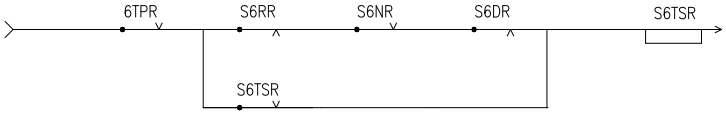
ASSIGN 13A_TPR * (~S4_3_5TRR * S4ASR + S4TSR) TO S4TSR;



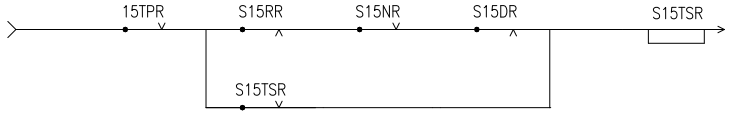
ASSIGN 14A_TPR * (~S5_3_5TRR * S5ASR + S5TSR) TO S5TSR;



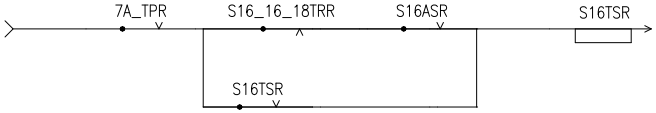
ASSIGN 6TPR * (~S6RR * S6NR * ~S6DR + S6TSR) TO S6TSR;



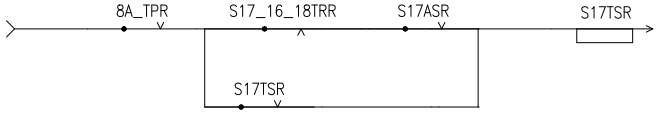
ASSIGN 15TPR * (~S15RR * S15NR * ~S15DR + S15TSR) TO S15TSR;



ASSIGN 7A_TPR * (~S16_16_18TRR * S16ASR + S16TSR) TO S16TSR;



ASSIGN 8A_TPR * (~S17_16_18TRR * S17ASR + S17TSR) TO S17TSR;



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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00108 | dqy 'khVSHEETS 00228 | CONT'D ON: 00109

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SIGNAL & TELECOMMUNICATIONS

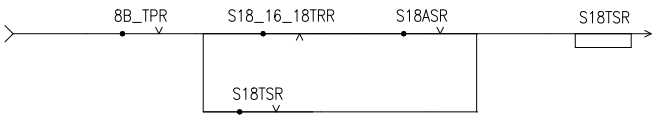
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

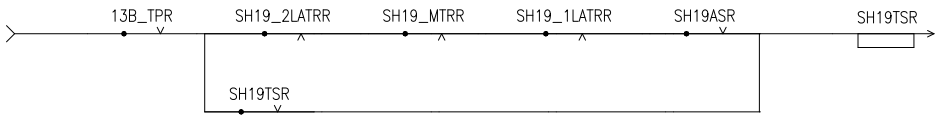
CAD FILE: 512_TAC_C1_00108_R00.DWG

(MICROLOK-II WARM STANDBY)

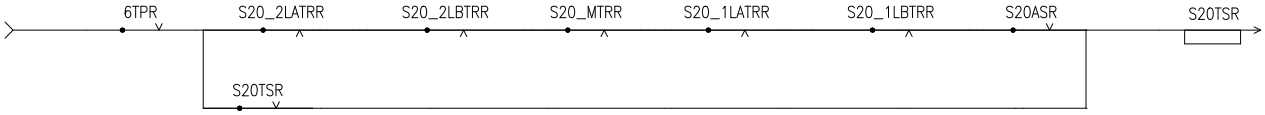
ASSIGN 8B_TPR * (~S18_16_18TRR * S18ASR + S18TSR) TO S18TSR;



ASSIGN 13B_TPR * (~SH19_2LATRR * ~SH19_MTRR * ~SH19_1LATRR * SH19ASR + SH19TSR) TO SH19TSR;



ASSIGN 6TPR * (~S20_2LATRR * ~S20_2LBTRR * ~S20_MTRR * ~S20_1LATRR * ~S20_1LBTRR * S20ASR + S20TSR) TO S20TSR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00109 | dqy 'khVSHEETS 00228 | CONT'D ON: 00110

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

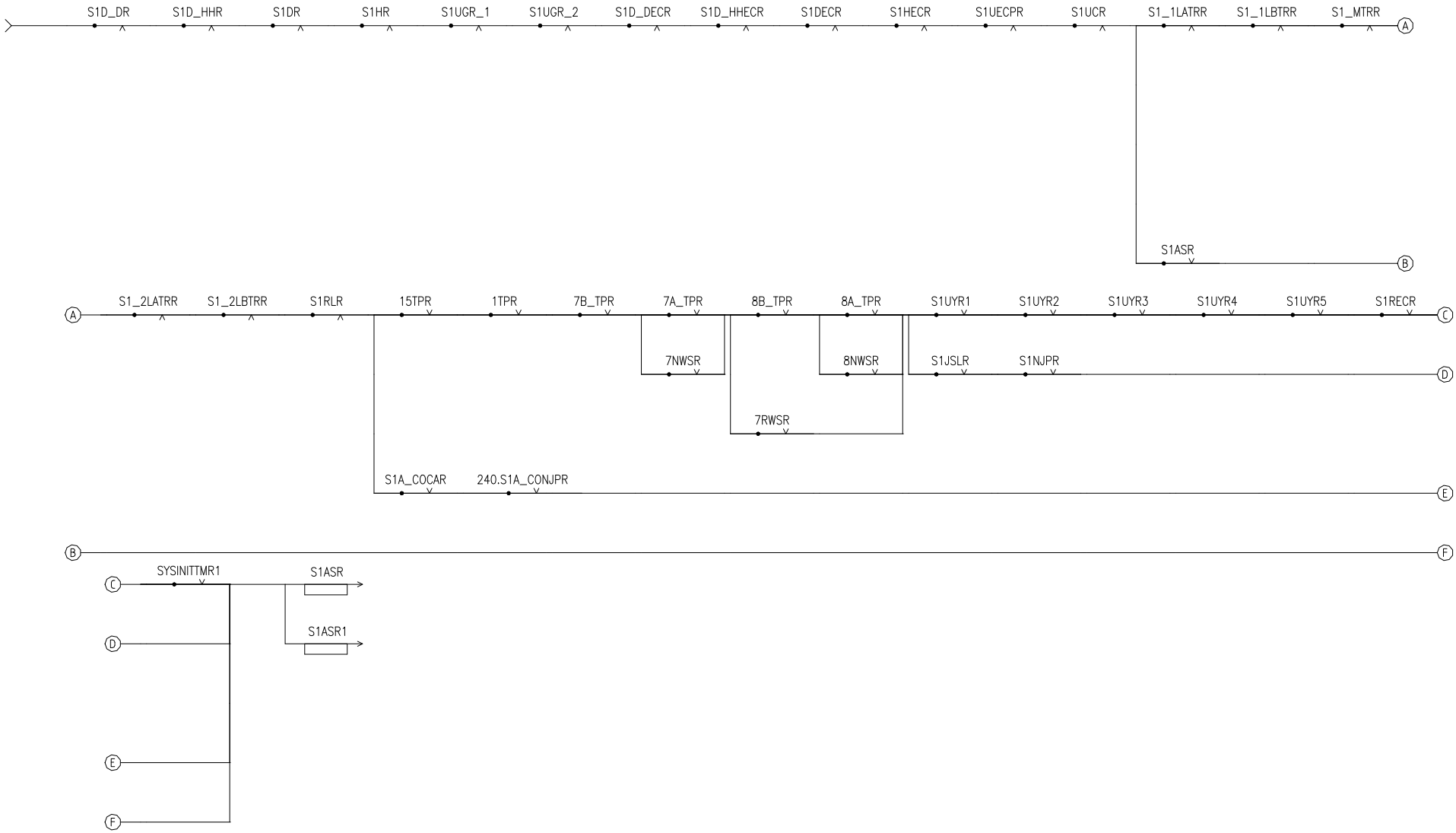
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

(MICROLOK-II WARM STANDBY)

CAD FILE: 512_TAC_C1_00109_R00.DWG

ASSIGN ~S1D_DR * ~S1D_HHR * ~S1DR * ~S1HR * ~S1UGR_1 * ~S1UGR_2 * ~S1D_DECR * ~S1D_HHECR * ~S1DECR * ~S1HECR * ~S1UECPR * ~S1UCR * (~S1_1LATRR * ~S1_1LBTRR * ~S1_MTRR * ~S1_2LATRR * ~S1_2LBTRR * ~S1RLR * (15TPR * 1TPR * 7B_TPR * (7A_TPR + 7NWSR) * (8B_TPR * (8A_TPR + 8NWSR) + 7RWSR) * (S1UYR1 * S1UYR2 * S1UYR3 * S1UYR4 * S1UYR5 * S1RECR * SYSINITTMR1 + S1JSLR * S1NJPR) + S1A_COCAR * 240.S1A_CONJPR) + S1ASR) TO S1ASR,S1ASR1;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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SE/SSE	
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00110 | dqy 'khVSHEETS 00228 | CONT'D ON: 00111

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

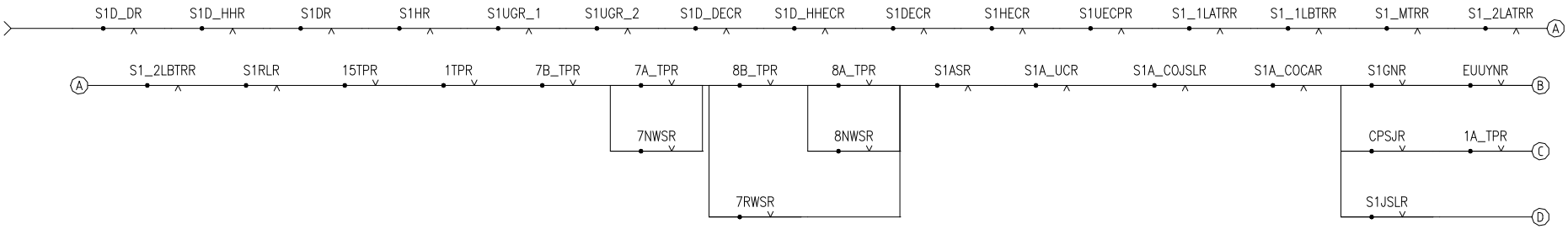
rkfVpøykZ
TATICHERLA

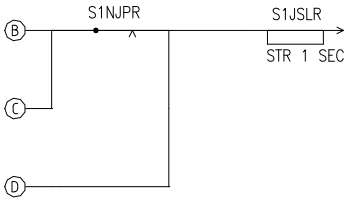
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00110_R00.DWG

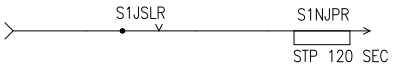
(MICROLOK-II WARM STANDBY)

ASSIGN ~S1D_DR * ~S1D_HHR * ~S1DR * ~S1HR * ~S1UGR_1 * ~S1UGR_2 * ~S1D_DECR * ~S1D_HHECR * ~S1DECR * ~S1HECR * ~S1UECPR * ~S1_1LATRR * ~S1_1LBTRR * ~S1_MTRR * ~S1_2LATRR * ~S1_2LBTRR * ~S1RLR * 15TPR * 1TPR * 7B_TPR * (7A_TPR + 7NWSR) * (8B_TPR * (8A_TPR + 8NWSR) + 7RWSR) * ~S1ASR * ~S1A_UCR * ~S1A_COJSLR * ~S1A_COCAR * ((S1GNR * EUUYNR + CPSJR * 1A_TPR) * ~S1NJPR + S1JSLR) TO S1JSLR;

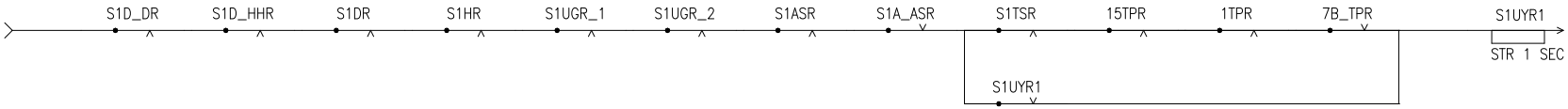




ASSIGN S1JSLR TO S1NJPR;



ASSIGN ~S1D_DR * ~S1D_HHR * ~S1DR * ~S1HR * ~S1UGR_1 * ~S1UGR_2 * ~S1ASR * S1A_ASR * (~S1TSR * ~15TPR * ~1TPR * 7B_TPR + S1UYR1) TO S1UYR1;



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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

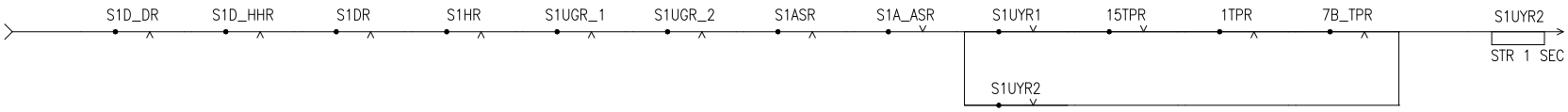
REF.SIG.PLAN.No. IPU.2107B
n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khV SHEET 00111
dqy 'khVSHEETS 00228
CONT'D ON: 00112

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

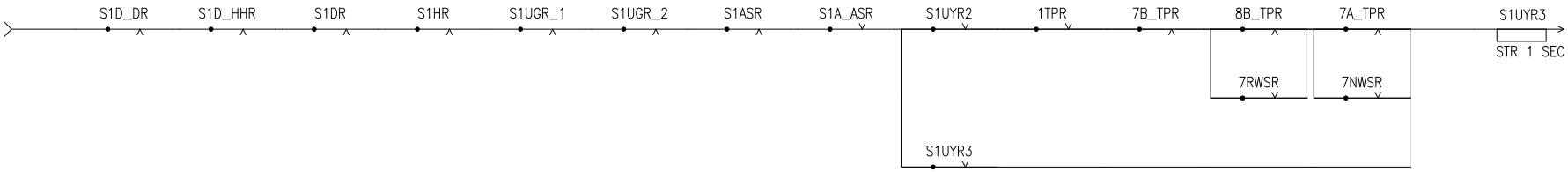
CAD FILE: 512_TAC_C1_00111_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ~S1D_DR * ~S1D_HHR * ~S1DR * ~S1HR * ~S1UGR_1 * ~S1UGR_2 * ~S1ASR * S1A_ASR * (S1UYR1 * 15TPR * ~1TPR * ~7B_TPR + S1UYR2) TO S1UYR2;



ASSIGN ~S1D_DR * ~S1D_HHR * ~S1DR * ~S1HR * ~S1UGR_1 * ~S1UGR_2 * ~S1ASR * S1A_ASR * (S1UYR2 * 1TPR * ~7B_TPR * (~8B_TPR + 7RWSR) * (~7A_TPR + 7NWSR) + S1UYR3) TO S1UYR3;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B | 'khVSHEET 00112 | dqy 'khVSHEETS 00228 | CONT'D ON: 00113

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

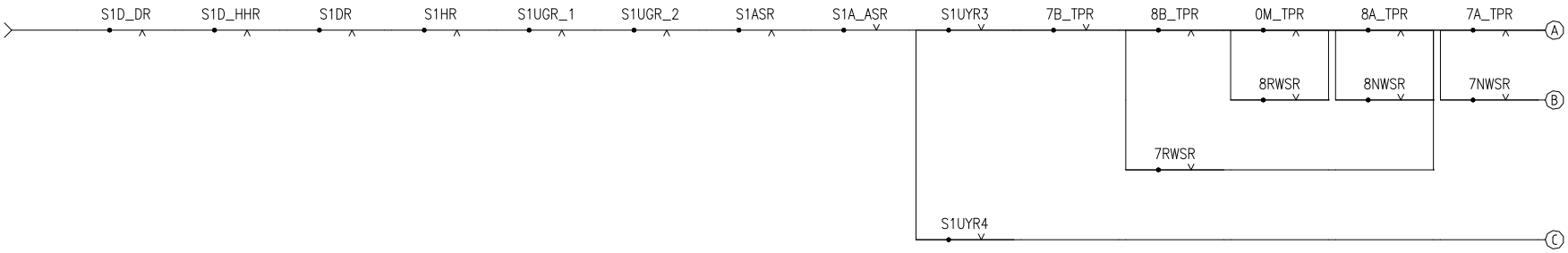
rkfVpøykZ
TATICHERLA

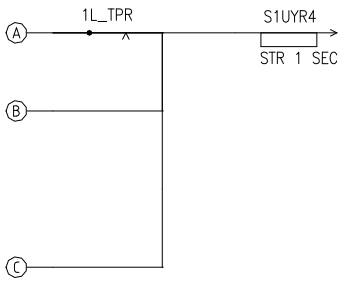
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00112_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ~S1D_DR * ~S1D_HHR * ~S1DR * ~S1HR * ~S1UGR_1 * ~S1UGR_2 * ~S1ASR * S1A_ASR * (S1UYR3 * 7B_TPR * (~8B_TPR * (~0M_TPR + 8RWSR) * (~8A_TPR + 8NWSR) + 7RWSR) * (~7A_TPR * ~1L_TPR + 7NWSR) + S1UYR4) TO S1UYR4;





ASSIGN ~S1D_DR * ~S1D_HHR * ~S1DR * ~S1HR * ~S1UGR_1 * ~S1UGR_2 * ~S1ASR * S1A_ASR * (S1UYR4 * (8B_TPR * (~8A_TPR * ~2L_TPR + 8NWSR) + 7RWSR + 8NWSR) + S1UYR5) TO S1UYR5;

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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00113 | dqy 'khVSHEETS 00228 | CONT'D ON: 00114

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

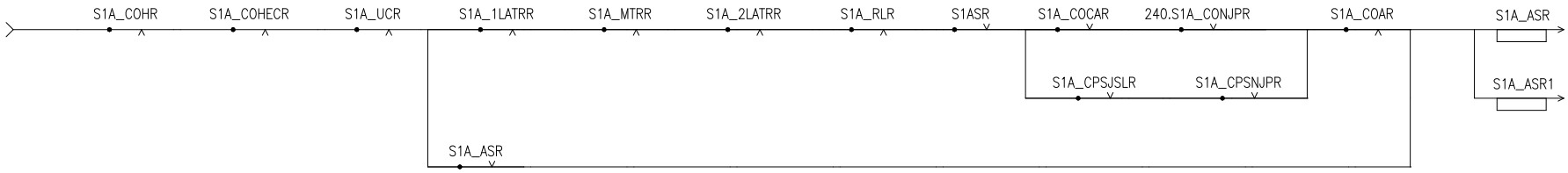
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

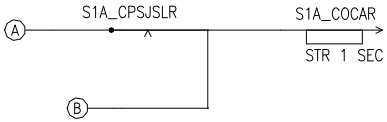
CAD FILE: 512_TAC_C1_00113_R00.DWG

(MICROLOK-II WARM STANDBY)

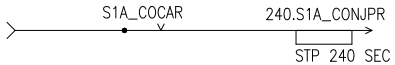
ASSIGN ~S1A_COHR * ~S1A_COHECR * ~S1A_UCR * (~S1A_1LATRR * ~S1A_MTRR * ~S1A_2LATRR * ~S1A_RLR * S1ASR * (S1A_COCAR * 240.S1A_CONJPR + S1A_CPSJSLR * S1A_CPSNJPR) * ~S1A_COAR + S1A_ASR) TO S1A_ASR,S1A_ASR1;



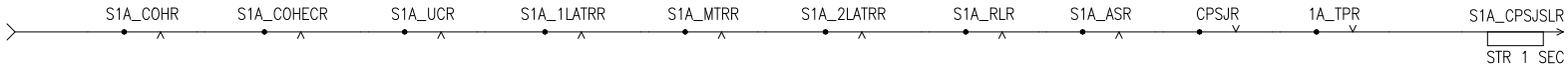
ASSIGN SYSINITTMR1 * ~S1A_COHR * ~S1A_COHECR * ~S1A_UCR * ~S1A_1LATRR * ~S1A_MTRR * ~S1A_2LATRR * ~S1A_RLR * ~S1JSLR * ~S1A_ASR * (S1AGNR * COCANR * ~240.S1A_CONJPR * ~S1A_CPSJSLR + S1A_COCAR) TO S1A_COCAR;



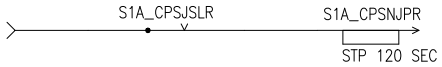
ASSIGN S1A_COCAR TO 240.S1A_CONJPR;



ASSIGN ~S1A_COHR * ~S1A_COHECR * ~S1A_UCR * ~S1A_1LATRR * ~S1A_MTRR * ~S1A_2LATRR * ~S1A_RLR * ~S1A_ASR * CPSJR * 1A_TPR TO S1A_CPSJSLR;



ASSIGN S1A_CPSJSLR TO S1A_CPSNJPR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
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Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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f CSTE	APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khVSHEET 00114 | dqy 'khVSHEETS 00228 | CONT'D ON: 00115

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

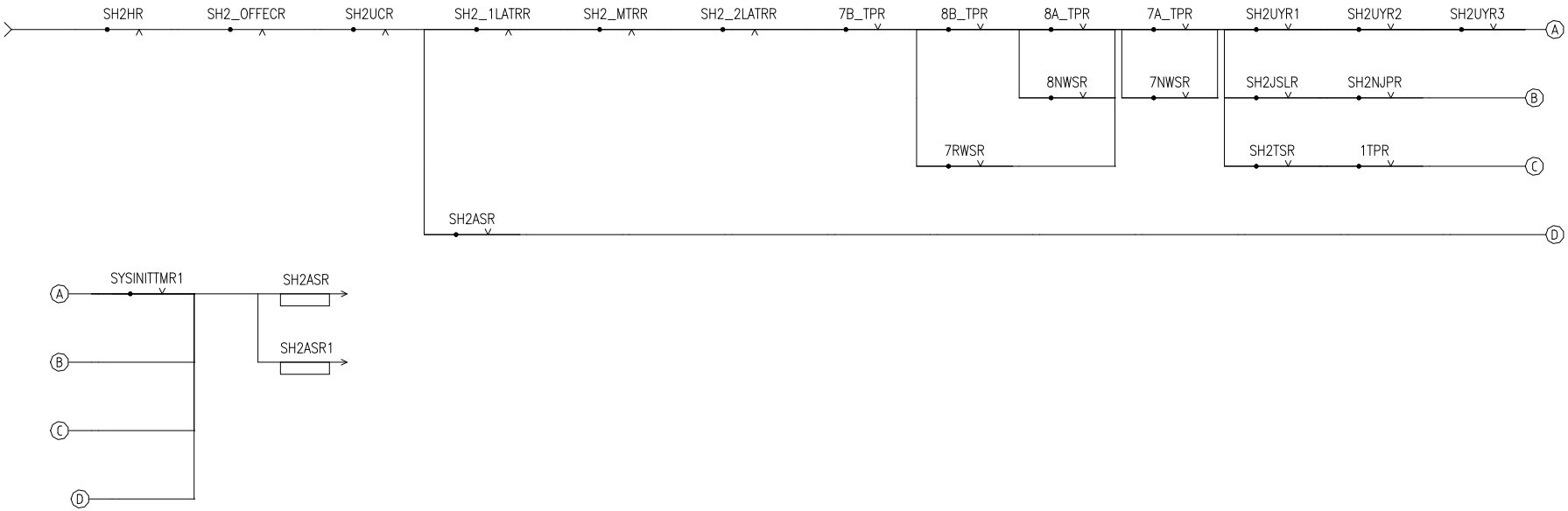
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00114_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ~SH2HR * ~SH2_OFFECR * ~SH2UCR * (~SH2_1LATRR * ~SH2_MTRR * ~SH2_2LATRR * 7B_TPR * (8B_TPR * (8A_TPR + 8NWSR) + 7RWSR) * (7A_TPR + 7NWSR) * (SH2UYR1 * SH2UYR2 * SH2UYR3 * SYSINITTMR1 + SH2JSLR * SH2NJPR + SH2TSR * 1TPR) + SH2ASR) TO SH2ASR,SH2ASR1;



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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00115 | dqy 'khVSHEETS 00228 | CONT'D ON: 00116

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

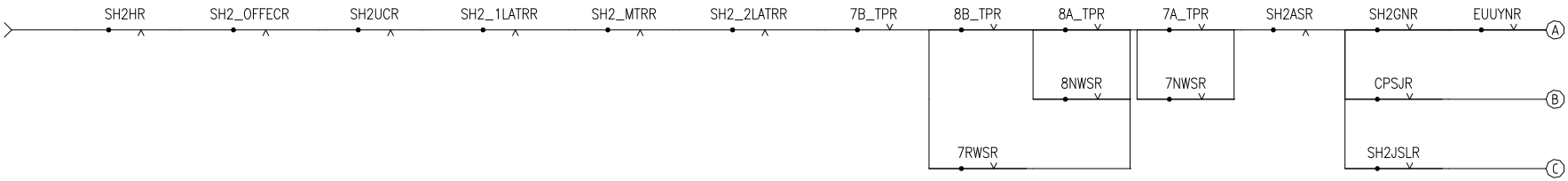
rkfVpøykZ
TATICHERLA

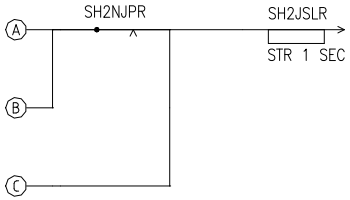
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00115_R00.DWG

(MICROLOK-II WARM STANDBY)

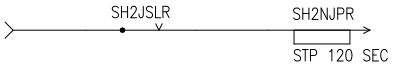
ASSIGN ~SH2HR * ~SH2_OFFECR * ~SH2UCR * ~SH2_1LATRR * ~SH2_MTRR * ~SH2_2LATRR * 7B_TPR * (8B_TPR * (8A_TPR + 8NWSR) + 7RWSR) * (7A_TPR + 7NWSR) * ~SH2ASR * ((SH2GNR * EUUYNR + CPSJR) * ~SH2NJPR + SH2JSLR) TO SH2JSLR;

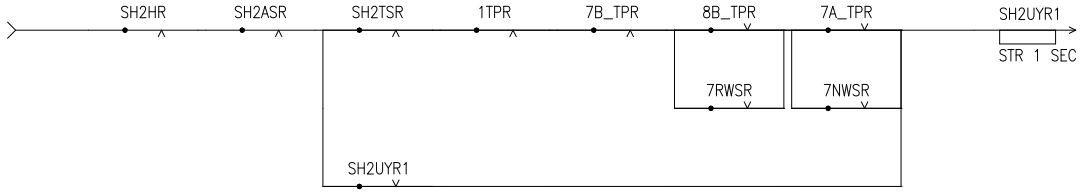




ASSIGN SH2JSLR TO SH2NJPR;

ASSIGN ~SH2HR * ~SH2ASR * (~SH2TSR * ~1TPR * ~7B_TPR * (8B_TPR + 7RWSR) * (7A_TPR + 7NWSR) + SH2UYR1) TO SH2UYR1;





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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00116 | dqy 'khVSHEETS 00228 | CONT'D ON: 00117

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

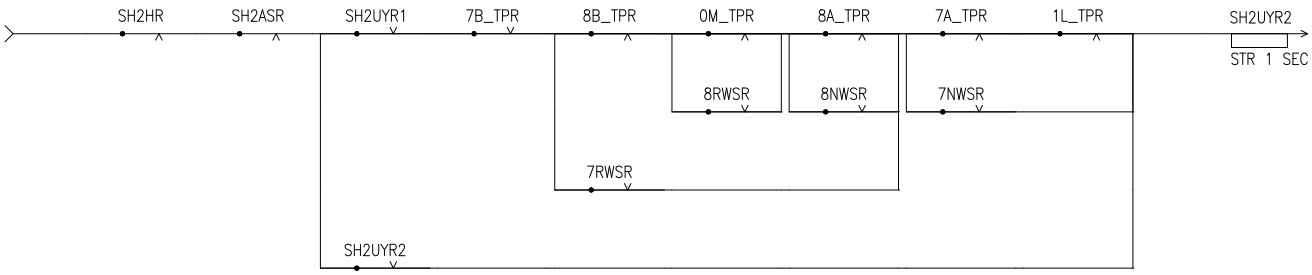
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

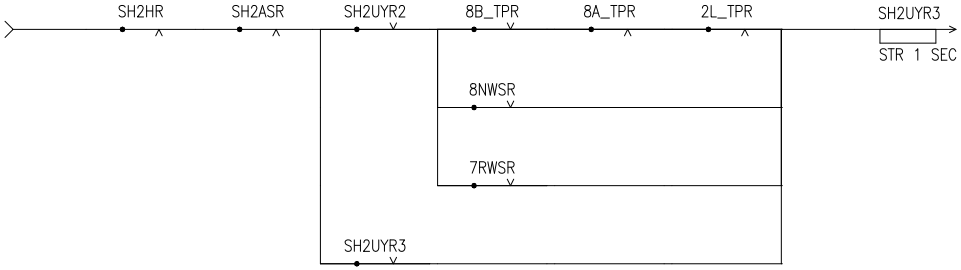
CAD FILE: 512_TAC_C1_00116_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ~SH2HR * ~SH2ASR * (SH2UYR1 * 7B_TPR * (~8B_TPR * (~0M_TPR + 8RWSR) * (~8A_TPR + 8NWSR) + 7RWSR) * (~7A_TPR * ~1L_TPR + 7NWSR) + SH2UYR2) TO SH2UYR2;



ASSIGN ~SH2HR * ~SH2ASR * (SH2UYR2 * (8B_TPR * ~8A_TPR * ~2L_TPR + 8NWSR + 7RWSR) + SH2UYR3) TO SH2UYR3;



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f CSTE	APP:

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flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

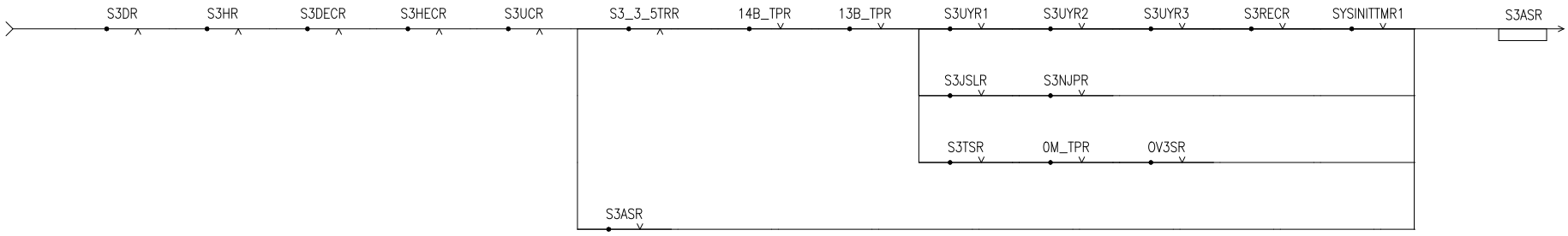
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

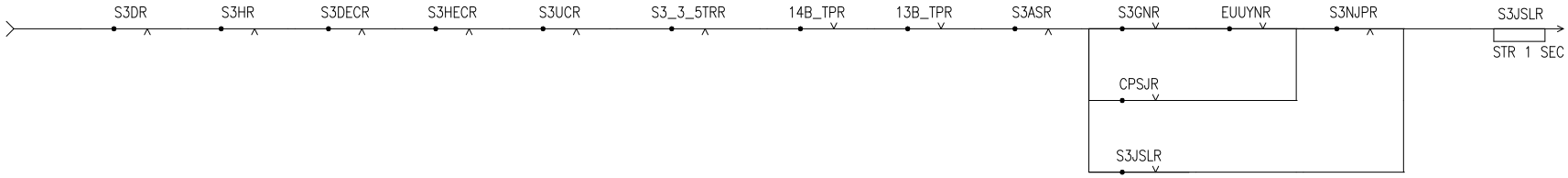
CAD FILE: 512_TAC_C1_00117_R00.DWG

(MICROLOK-II WARM STANDBY)

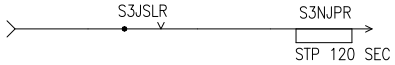
ASSIGN ~S3DR * ~S3HR * ~S3DECR * ~S3HECR * ~S3UCR * (~S3_3_5TRR * 14B_TPR * 13B_TPR * (S3UYR1 * S3UYR2 * S3UYR3 * S3RECR * SYSINITTMR1 + S3JSLR * S3NJPR + S3TSR * 0M_TPR * 0V3SR) + S3ASR) TO S3ASR;



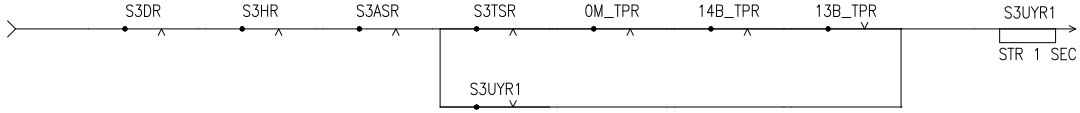
ASSIGN ~S3DR * ~S3HR * ~S3DECR * ~S3HECR * ~S3UCR * ~S3_3_5TRR * 14B_TPR * 13B_TPR * ~S3ASR * ((S3GNR * EUUYNR + CPSJR) * ~S3NJPR + S3JSLR) TO S3JSLR;



ASSIGN S3JSLR TO S3NJPR;



ASSIGN ~S3DR * ~S3HR * ~S3ASR * (~S3TSR * ~0M_TPR * ~14B_TPR * 13B_TPR + S3UYR1) TO S3UYR1;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00118 | dqy 'khVSHEETS 00228 | CONT'D ON: 00119

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

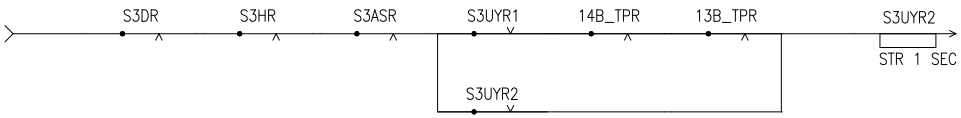
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

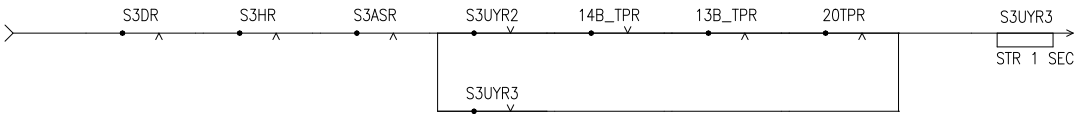
CAD FILE: 512_TAC_C1_00118_R00.DWG

(MICROLOK-II WARM STANDBY)

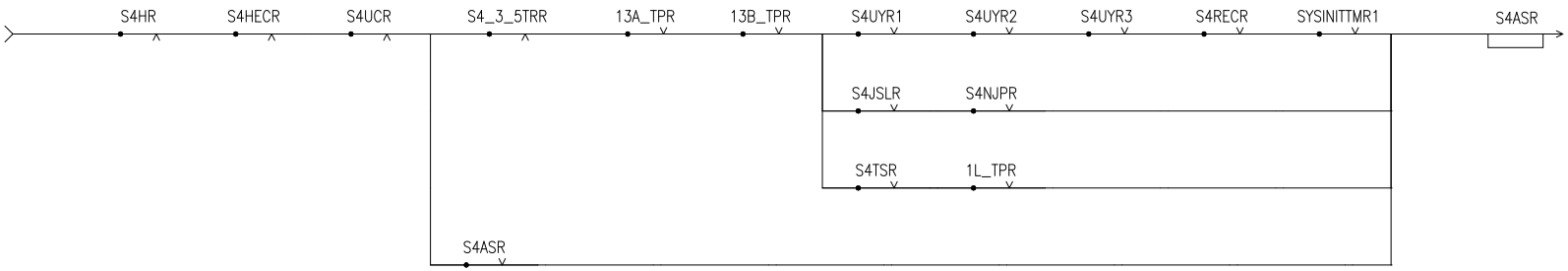
ASSIGN ~S3DR * ~S3HR * ~S3ASR * (S3UYR1 * ~14B_TPR * ~13B_TPR + S3UYR2) TO S3UYR2;



ASSIGN ~S3DR * ~S3HR * ~S3ASR * (S3UYR2 * 14B_TPR * ~13B_TPR * ~20TPR + S3UYR3) TO S3UYR3;



ASSIGN ~S4HR * ~S4HECR * ~S4UCR * (~S4_3_5TRR * 13A_TPR * 13B_TPR * (S4UYR1 * S4UYR2 * S4UYR3 * S4RECR * SYSINITTMR1 + S4JSLR * S4NJPR + S4TSR * 1L_TPR) + S4ASR) TO S4ASR;



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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
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Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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f CSTE	

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APP:

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

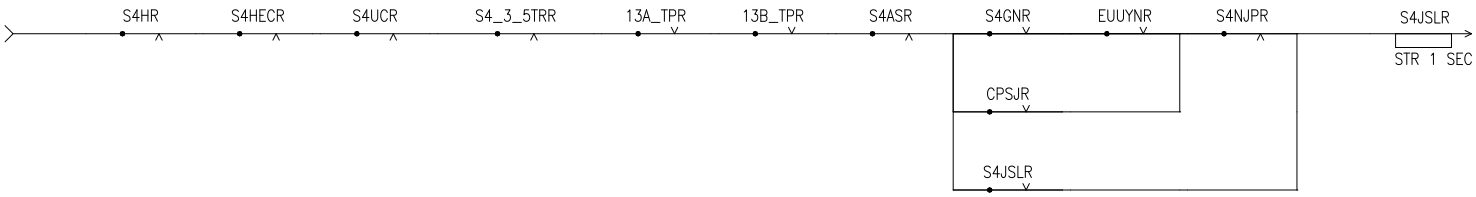
REF.SIG.PLAN.No. IPU.2107B
n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khVSHEET 00119
dqy 'khVSHEETS 00228
CONT'D ON: 00120

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

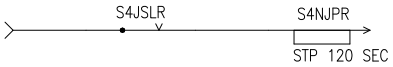
CAD FILE: 512_TAC_C1_00119_R00.DWG

(MICROLOK-II WARM STANDBY)

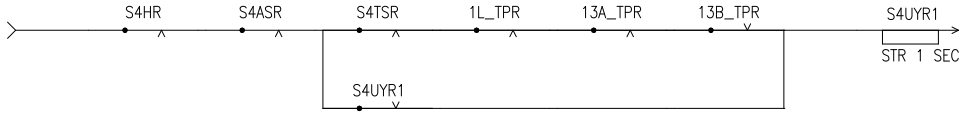
ASSIGN ~S4HR * ~S4HECR * ~S4UCR * ~S4_3_5TRR * 13A_TPR * 13B_TPR * ~S4ASR * ((S4GNR * EUUYNR + CPSJR) * ~S4NJPR + S4JSLR) TO S4JSLR;



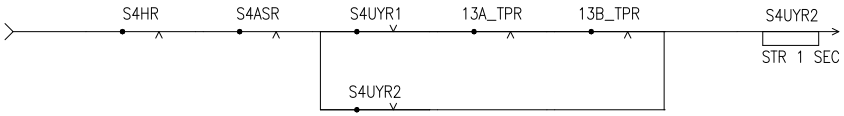
ASSIGN S4JSLR TO S4NJPR;



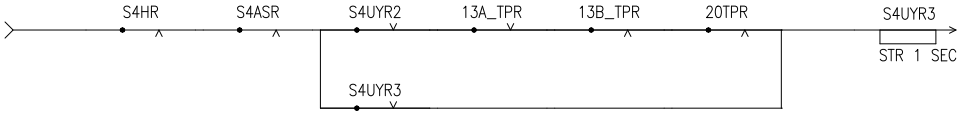
ASSIGN ~S4HR * ~S4ASR * (~S4TSR * ~1L_TPR * ~13A_TPR * 13B_TPR + S4UYR1) TO S4UYR1;



ASSIGN ~S4HR * ~S4ASR * (S4UYR1 * ~13A_TPR * ~13B_TPR + S4UYR2) TO S4UYR2;



ASSIGN ~S4HR * ~S4ASR * (S4UYR2 * 13A_TPR * ~13B_TPR * ~20TPR + S4UYR3) TO S4UYR3;



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#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
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Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00120	00228	00121

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

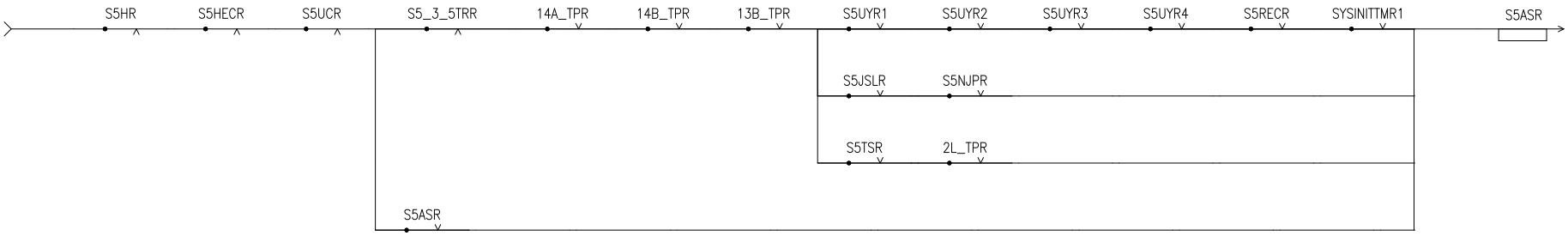
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

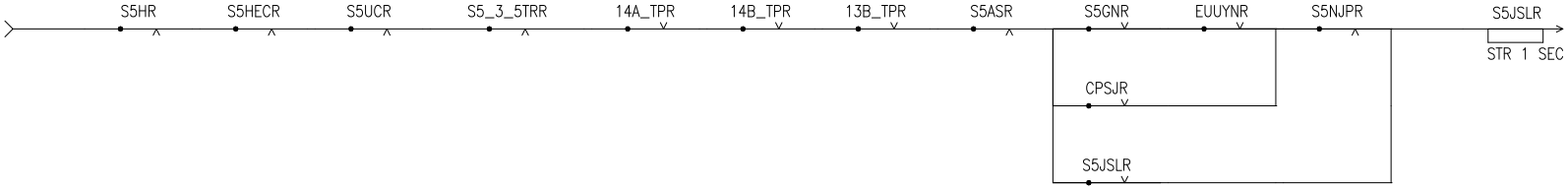
CAD FILE: 512_TAC_C1_00120_R00.DWG

(MICROLOK-II WARM STANDBY)

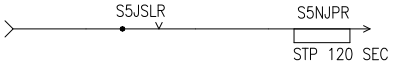
ASSIGN ~S5HR * ~S5HECR * ~S5UCR * (~S5_3_5TRR * 14A_TPR * 14B_TPR * 13B_TPR * (S5UYR1 * S5UYR2 * S5UYR3 * S5UYR4 * S5RECR * SYSINITTMR1 + S5JSLR * S5NJPR + S5TSR * 2L_TPR) + S5ASR) TO S5ASR;



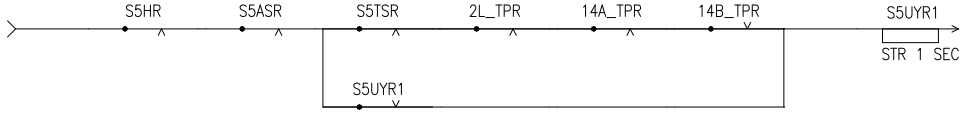
ASSIGN ~S5HR * ~S5HECR * ~S5UCR * ~S5_3_5TRR * 14A_TPR * 14B_TPR * 13B_TPR * ~S5ASR * ((S5GNR * EUUYNR + CPSJR) * ~S5NJPR + S5JSLR) TO S5JSLR;



ASSIGN S5JSLR TO S5NJPR;



ASSIGN ~S5HR * ~S5ASR * (~S5TSR * ~2L_TPR * ~14A_TPR * 14B_TPR + S5UYR1) TO S5UYR1;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

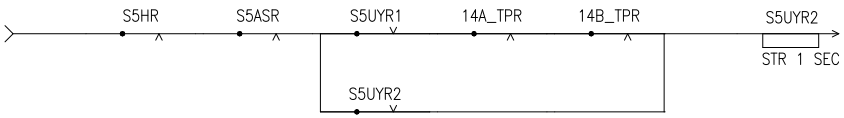
REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khV SHEET 00121 | dqy 'khVSHEETS 00228 | CONT'D ON: 00122

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

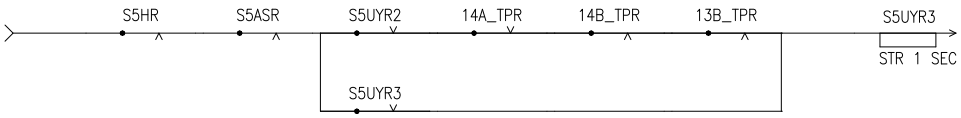
CAD FILE: 512_TAC_C1_00121_R00.DWG

(MICROLOK-II WARM STANDBY)

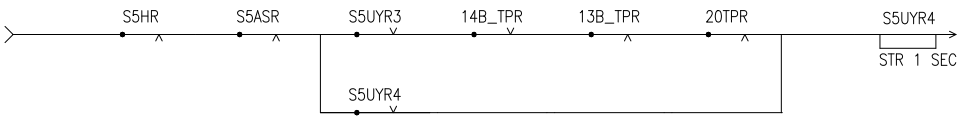
ASSIGN ~S5HR * ~S5ASR * (S5UYR1 * ~14A_TPR * ~14B_TPR + S5UYR2) TO S5UYR2;



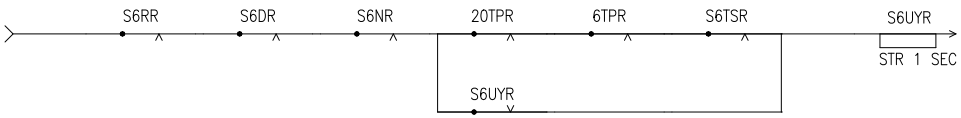
ASSIGN ~S5HR * ~S5ASR * (S5UYR2 * 14A_TPR * ~14B_TPR * ~13B_TPR + S5UYR3) TO S5UYR3;



ASSIGN ~S5HR * ~S5ASR * (S5UYR3 * 14B_TPR * ~13B_TPR * ~20TPR + S5UYR4) TO S5UYR4;



ASSIGN ~S6RR * ~S6DR * ~S6NR * (~20TPR * ~6TPR * ~S6TSR + S6UYR) TO S6UYR;



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TVM Signalling & Transportation Systems Pvt. Ltd.
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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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JE-1	CHK.
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00122	00228	00123

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

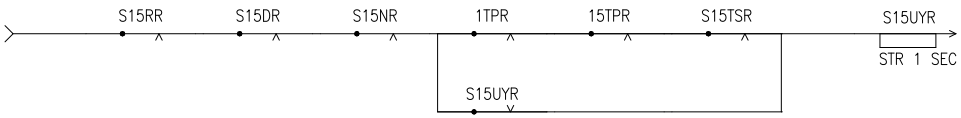
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

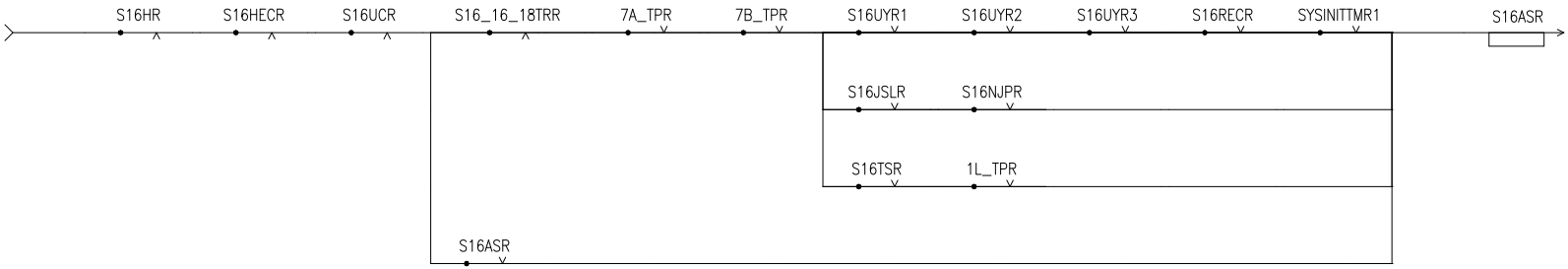
(MICROLOK-II WARM STANDBY)

CAD FILE: 512_TAC_C1_00122_R00.DWG

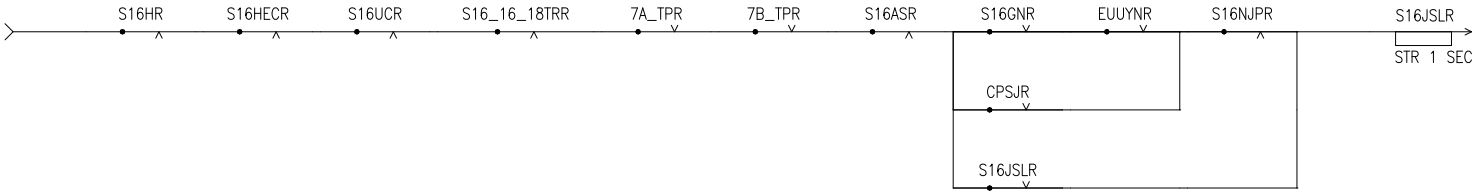
ASSIGN ~S15RR * ~S15DR * ~S15NR * (~1TPR * ~15TPR * ~S15TSR + S15UYR) TO S15UYR;



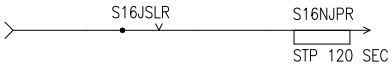
ASSIGN ~S16HR * ~S16HECR * ~S16UCR * (~S16_16_18TRR * 7A_TPR * 7B_TPR * (S16UYR1 * S16UYR2 * S16UYR3 * S16RECR * SYSINITTMR1 + S16JSLR * S16NJPR + S16TSR * 1L_TPR) + S16ASR) TO S16ASR;



ASSIGN ~S16HR * ~S16HECR * ~S16UCR * ~S16_16_18TRR * 7A_TPR * 7B_TPR * ~S16ASR * ((S16GNR * EUUYNR + CPSJR) * ~S16NJPR + S16JSLR) TO S16JSLR;



ASSIGN S16JSLR TO S16NJPR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B	n e jsyos vkjsjo S.C.RLY.DRG.NO	a fc B	
'khVSHEET 00123	dqy 'khVSHEETS 00228	CONT'D ON: 00124	

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

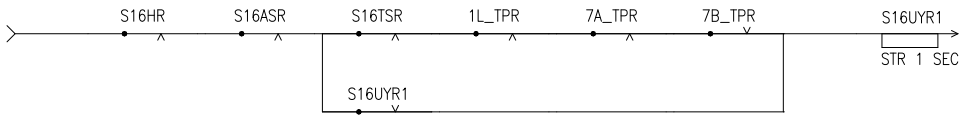
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

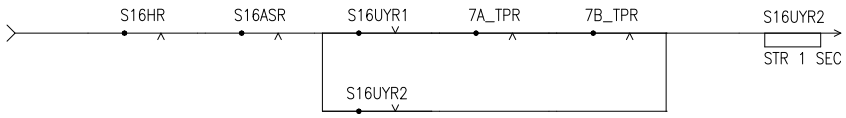
CAD FILE: 512_TAC_C1_00123_R00.DWG

(MICROLOK-II WARM STANDBY)

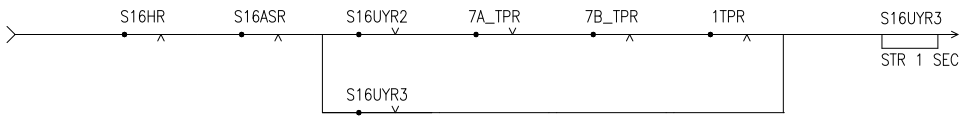
ASSIGN ~S16HR * ~S16ASR * (~S16TSR * ~1L_TPR * ~7A_TPR * 7B_TPR + S16UYR1) TO S16UYR1;



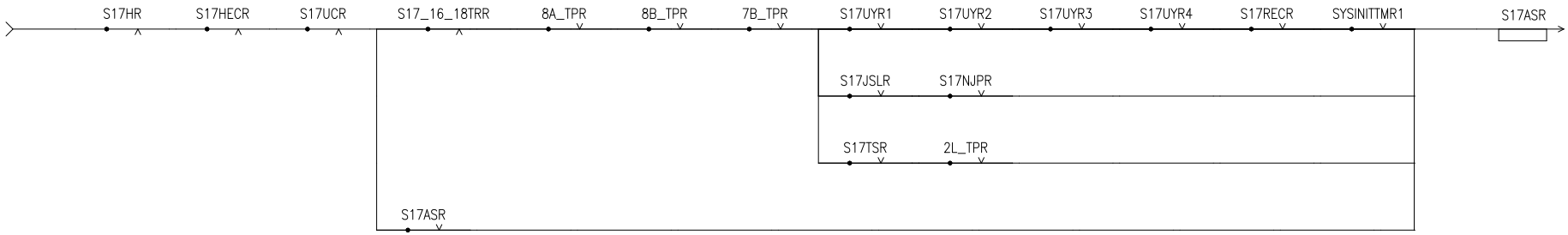
ASSIGN ~S16HR * ~S16ASR * (S16UYR1 * ~7A_TPR * ~7B_TPR + S16UYR2) TO S16UYR2;



ASSIGN ~S16HR * ~S16ASR * (S16UYR2 * 7A_TPR * ~7B_TPR * ~1TPR + S16UYR3) TO S16UYR3;



ASSIGN ~S17HR * ~S17HECR * ~S17UCR * (~S17_16_18TRR * 8A_TPR * 8B_TPR * 7B_TPR * (S17UYR1 * S17UYR2 * S17UYR3 * S17UYR4 * S17RECR * SYSINITTMR1 + S17JSLR * S17NJPR + S17TSR * 2L_TPR) + S17ASR) TO S17ASR;



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00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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CHK. APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00124	00228	00125

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

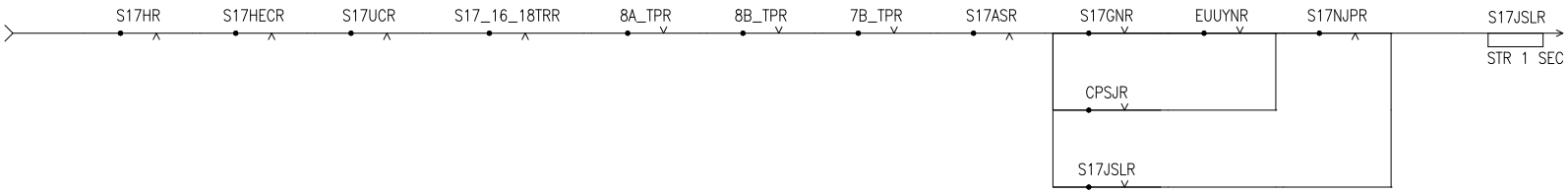
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

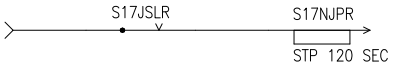
CAD FILE: 512_TAC_C1_00124_R00.DWG

(MICROLOK-II WARM STANDBY)

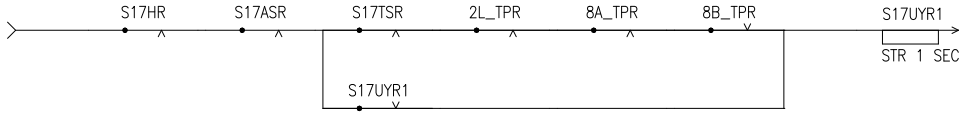
ASSIGN ~S17HR * ~S17HECR * ~S17UCR * ~S17_16_18TRR * 8A_TPR * 8B_TPR * 7B_TPR * ~S17ASR * ((S17GNR * EUUYNR + CPSJR) * ~S17NJPR + S17JSLR) TO S17JSLR;



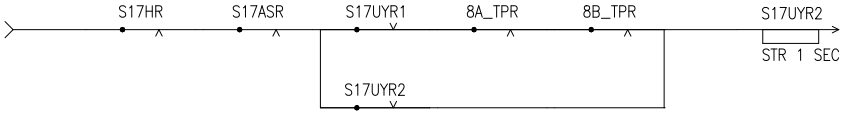
ASSIGN S17JSLR TO S17NJPR;



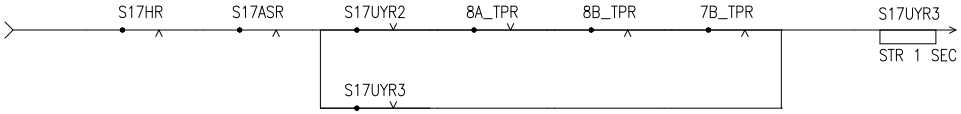
ASSIGN ~S17HR * ~S17ASR * (~S17TSR * ~2L_TPR * ~8A_TPR * 8B_TPR + S17UYR1) TO S17UYR1;



ASSIGN ~S17HR * ~S17ASR * (S17UYR1 * ~8A_TPR * ~8B_TPR + S17UYR2) TO S17UYR2;



ASSIGN ~S17HR * ~S17ASR * (S17UYR2 * 8A_TPR * ~8B_TPR * ~7B_TPR + S17UYR3) TO S17UYR3;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

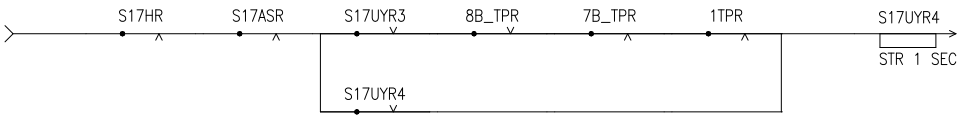
REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khVSHEET 00125 | dqy 'khVSHEETS 00228 | CONT'D ON: 00126

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

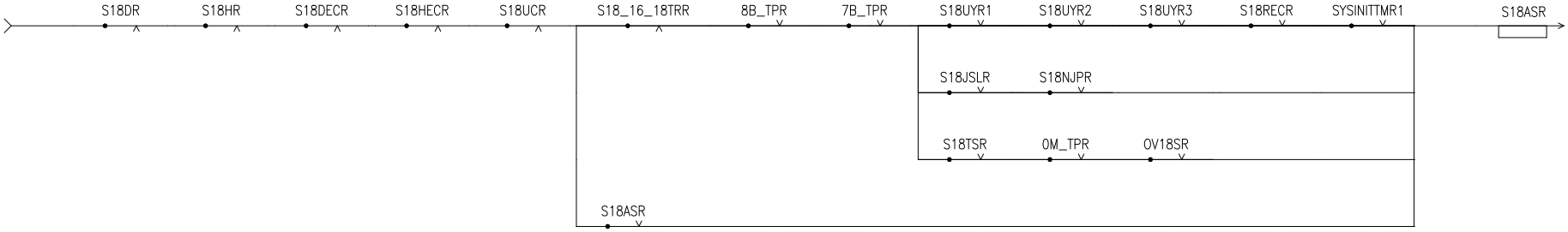
CAD FILE: 512_TAC_C1_00125_R00.DWG

(MICROLOK-II WARM STANDBY)

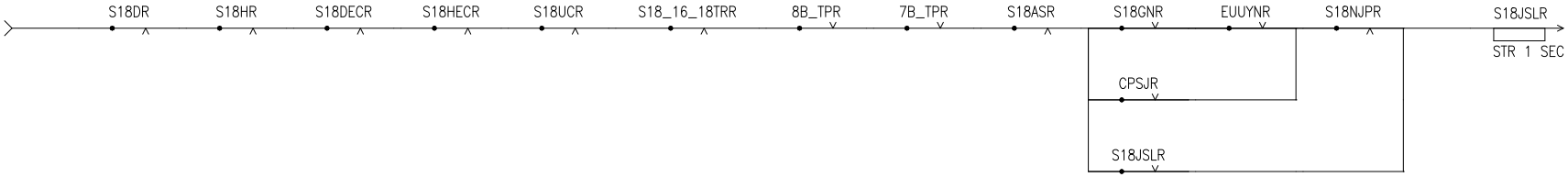
ASSIGN ~S17HR * ~S17ASR * (S17UYR3 * 8B_TPR * ~7B_TPR * ~1TPR + S17UYR4) TO S17UYR4;



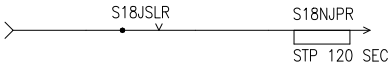
ASSIGN ~S18DR * ~S18HR * ~S18DECR * ~S18HECR * ~S18UCR * (~S18_16_18TRR * 8B_TPR * 7B_TPR * (S18UYR1 * S18UYR2 * S18UYR3 * S18RECR * SYSINITTMR1 + S18JSLR * S18NJPR + S18TSR * 0M_TPR * 0V18SR) + S18ASR) TO S18ASR;



ASSIGN ~S18DR * ~S18HR * ~S18DECR * ~S18HECR * ~S18UCR * ~S18_16_18TRR * 8B_TPR * 7B_TPR * ~S18ASR * ((S18GNR * EUUYNR + CPSJR) * ~S18NJPR + S18JSLR) TO S18JSLR;



ASSIGN S18JSLR TO S18NJPR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khVSHEET	dqy 'khVSHEETS	CONT'D ON:
00126	00228	00127

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

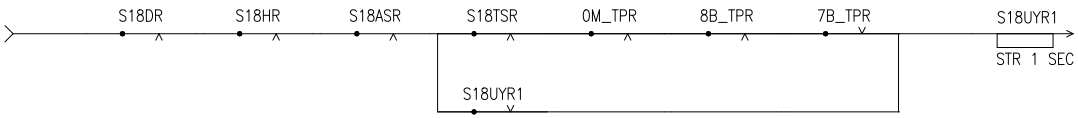
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

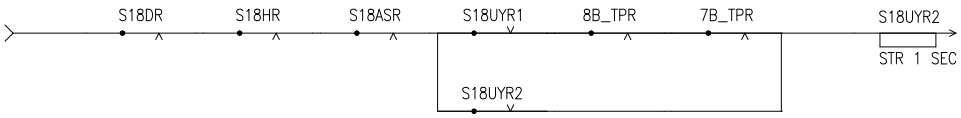
CAD FILE: 512_TAC_C1_00126_R00.DWG

(MICROLOK-II WARM STANDBY)

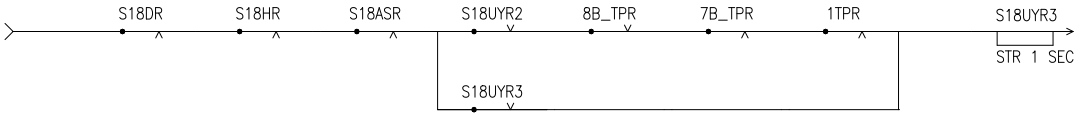
ASSIGN ~S18DR * ~S18HR * ~S18ASR * (~S18TSR * ~0M_TPR * ~8B_TPR * 7B_TPR + S18UYR1) TO S18UYR1;



ASSIGN ~S18DR * ~S18HR * ~S18ASR * (S18UYR1 * ~8B_TPR * ~7B_TPR + S18UYR2) TO S18UYR2;



ASSIGN ~S18DR * ~S18HR * ~S18ASR * (S18UYR2 * 8B_TPR * ~7B_TPR * ~1TPR + S18UYR3) TO S18UYR3;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00127 | dqy 'khVSHEETS 00228 | CONT'D ON: 00128

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

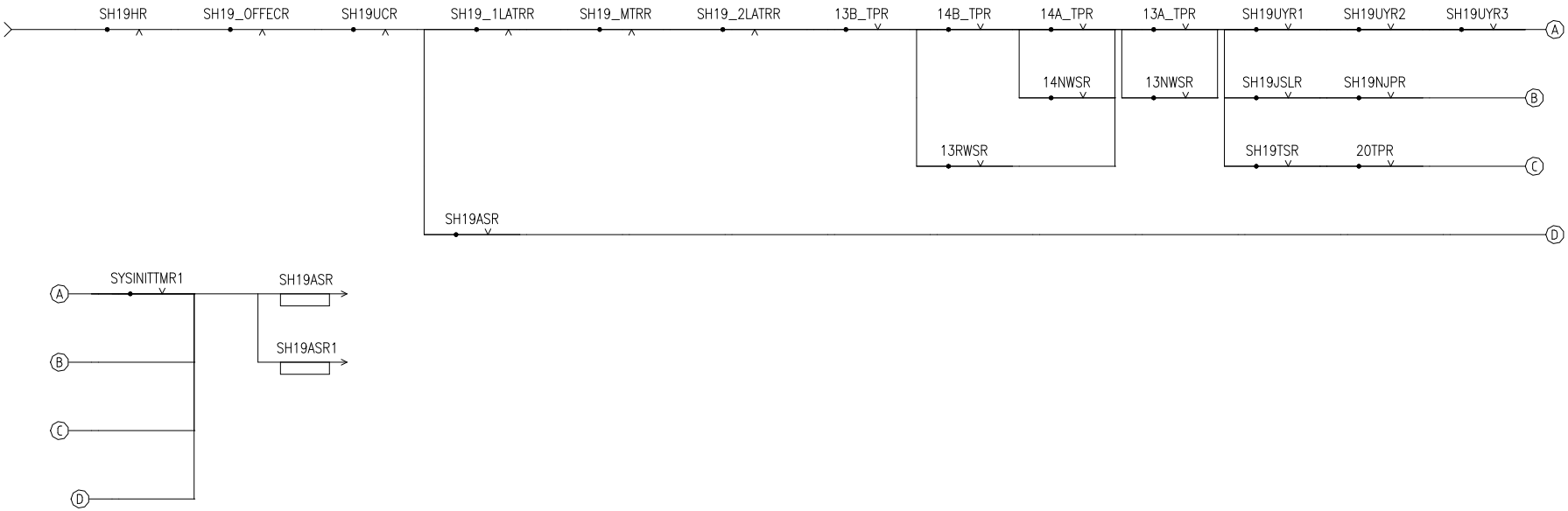
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00127_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ~SH19HR * ~SH19_OFFECR * ~SH19UCR * (~SH19_1LATRR * ~SH19_MTRR * ~SH19_2LATRR * 13B_TPR * (14B_TPR * (14A_TPR + 14NWSR) + 13RWSR) * (13A_TPR + 13NWSR) * (SH19UYR1 * SH19UYR2 * SH19UYR3 * SYSINITTMR1 + SH19JSLR * SH19NJPR + SH19TSR * 20TPR) + SH19ASR) TO SH19ASR,SH19ASR1;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00128 | dqy 'khVSHEETS 00228 | CONT'D ON: 00129

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SIGNAL & TELECOMMUNICATIONS

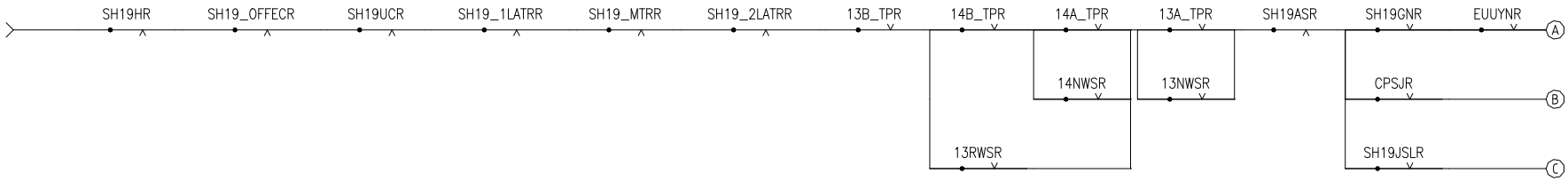
rkfVpøykZ
TATICHERLA

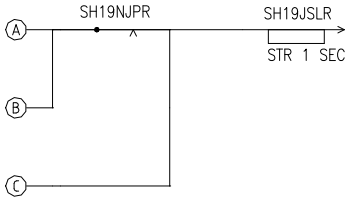
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00128_R00.DWG

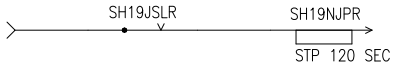
(MICROLOK-II WARM STANDBY)

ASSIGN ~SH19HR * ~SH19_OFFECR * ~SH19UCR * ~SH19_1LATRR * ~SH19_MTRR * ~SH19_2LATRR * 13B_TPR * (14B_TPR * (14A_TPR + 14NWSR) + 13RWSR) * (13A_TPR + 13NWSR) * ~SH19ASR * ((SH19GNR * EUUYNR + CPSJR) * ~SH19NJPR + SH19JSLR) TO SH19JSLR;

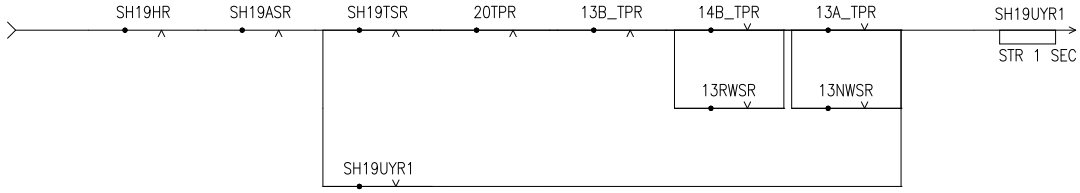




ASSIGN SH19JSLR TO SH19NJPR;



ASSIGN ~SH19HR * ~SH19ASR * (~SH19TSR * ~20TPR * ~13B_TPR * (14B_TPR + 13RWSR) * (13A_TPR + 13NWSR) + SH19UYR1) TO SH19UYR1;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00129 | dqy 'khVSHEETS 00228 | CONT'D ON: 00130

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SIGNAL & TELECOMMUNICATIONS

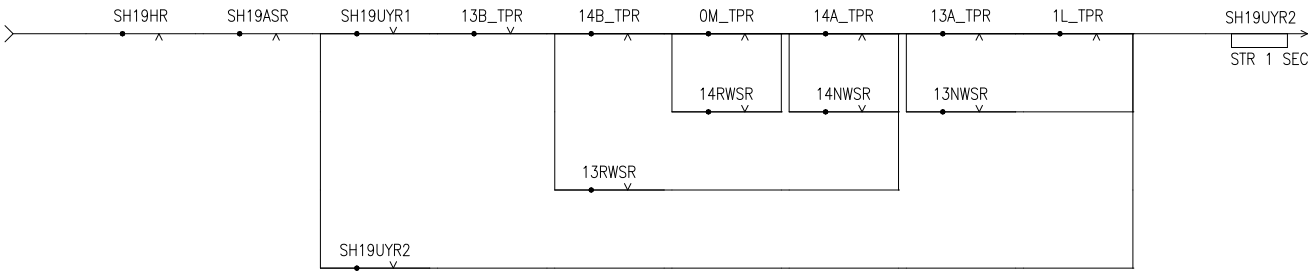
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

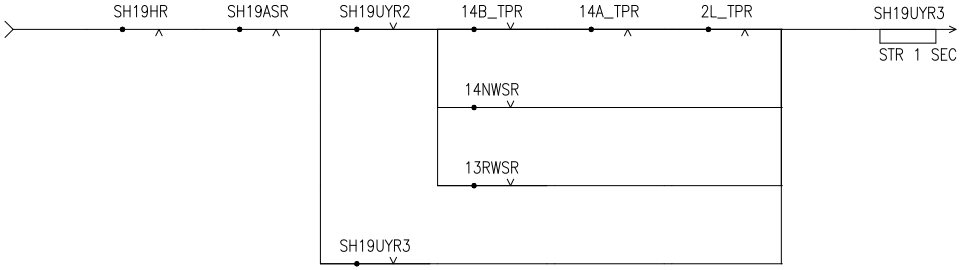
CAD FILE: 512_TAC_C1_00129_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ~SH19HR * ~SH19ASR * (SH19UYR1 * 13B_TPR * (~14B_TPR * (~0M_TPR + 14RWSR) * (~14A_TPR + 14NWSR) + 13RWSR) * (~13A_TPR * ~1L_TPR + 13NWSR) + SH19UYR2) TO SH19UYR2;



ASSIGN ~SH19HR * ~SH19ASR * (SH19UYR2 * (14B_TPR * ~14A_TPR * ~2L_TPR + 14NWSR + 13RWSR) + SH19UYR3) TO SH19UYR3;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00130 | dqy 'khVSHEETS 00228 | CONT'D ON: 00131

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SIGNAL & TELECOMMUNICATIONS

rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

(MICROLOK-II WARM STANDBY)

CAD FILE: 512_TAC_C1_00130_R00.DWG

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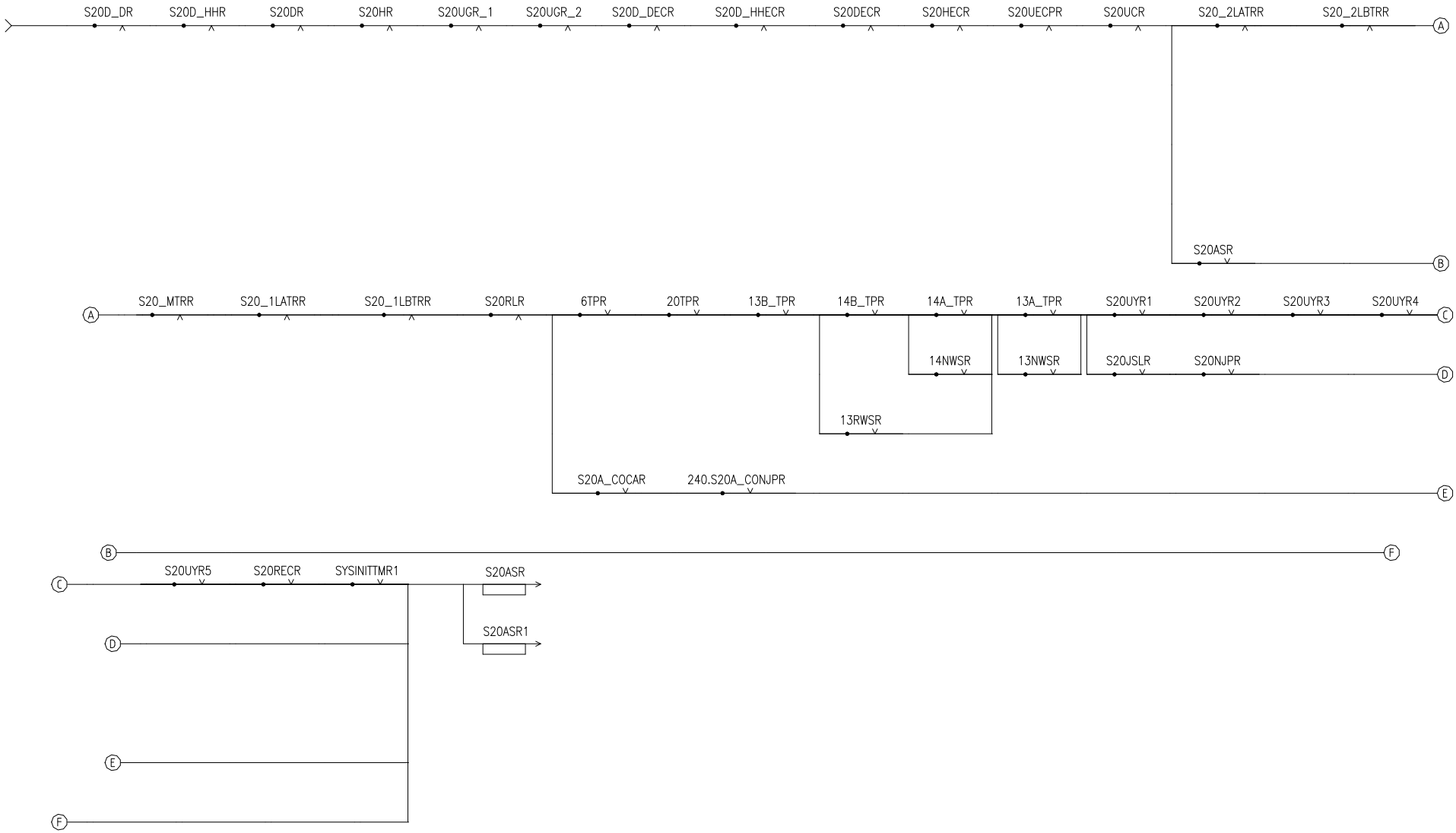
ASSIGN  ~S20D_DR * ~S20D_HHR * ~S20DR * ~S20HR * ~S20UGR_1 * ~S20UGR_2 * ~S20D_DECR * ~S20D_HHECR * ~S20DECR * ~S20HECR * ~S20UECPR * ~S20UCR * (~S20_2LATRR * ~S20_2LBTRR * ~S20_MTRR * ~S20_1LATRR * ~S20_1LBTRR *
~S20RLR * (6TPR * 20TPR * 13B_TPR * (14B_TPR * (14A_TPR + 14NWSR) + 13RWSR) * (13A_TPR + 13NWSR) * (S20UYR1 * S20UYR2 * S20UYR3 * S20UYR4 * S20UYR5 * S20RECR * SYSINITTMR1 + S20JSLR * S20NJPR) + S20A_COCAR * 240.S20A_CONJPR)
+ S20ASR) TO  S20ASR,S20ASR1;
```



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
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SIGNAL & TELECOMMUNICATIONS

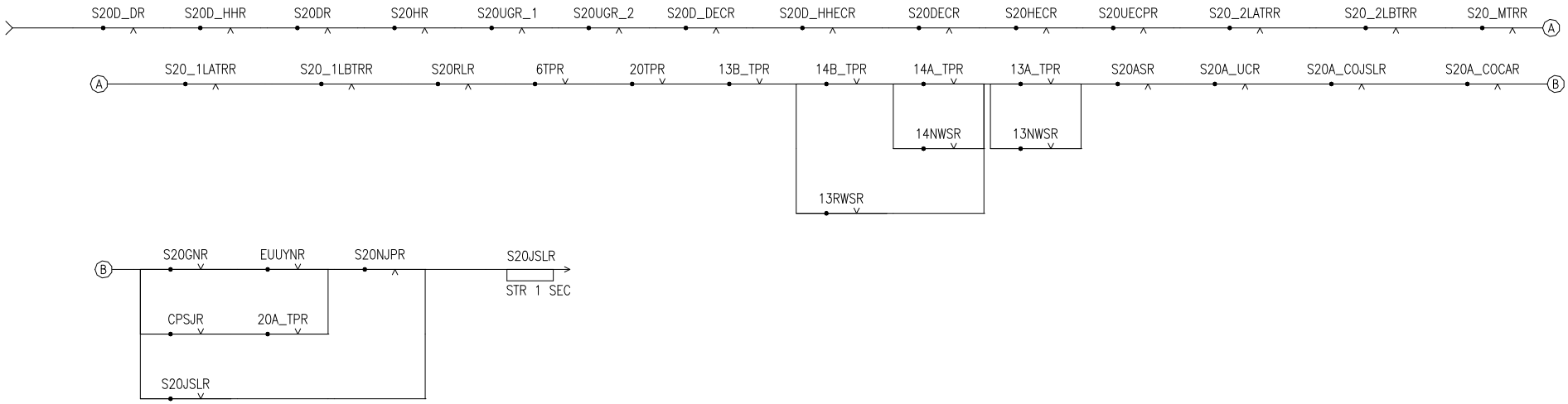
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

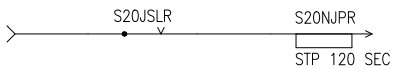
CAD FILE: 512_TAC_C1_00131_R00.DWG

(MICROLOK-II WARM STANDBY)

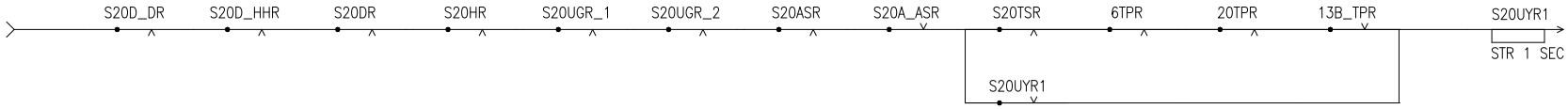
ASSIGN ~S20D_DR * ~S20D_HHR * ~S20DR * ~S20HR * ~S20UGR_1 * ~S20UGR_2 * ~S20D_DECR * ~S20D_HHECR * ~S20DECR * ~S20HECR * ~S20UECPR * ~S20_2LATRR * ~S20_2LBTRR * ~S20_MTRR * ~S20_1LATRR * ~S20_1LBTRR * ~S20RLR * 6TPR * 20TPR * 13B_TPR * (14B_TPR * (14A_TPR + 14NWSR) + 13RWSR) * (13A_TPR + 13NWSR) * ~S20ASR * ~S20A_UCR * ~S20A_COJSLR * ~S20A_COCAR * ((S20GNR * EUUYNR + CPSJR * 20A_TPR) * ~S20NJPR + S20JSLR) TO S20JSLR;



ASSIGN S20JSLR TO S20NJPR;



ASSIGN ~S20D_DR * ~S20D_HHR * ~S20DR * ~S20HR * ~S20UGR_1 * ~S20UGR_2 * ~S20ASR * S20A_ASR * (~S20TSR * ~6TPR * ~20TPR * 13B_TPR + S20UYR1) TO S20UYR1;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross, 100 Feet Road, J.P.Nagar, 6th Phase, Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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SE/SSE	
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00132 | dqy 'khVSHEETS 00228 | CONT'D ON: 00133

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

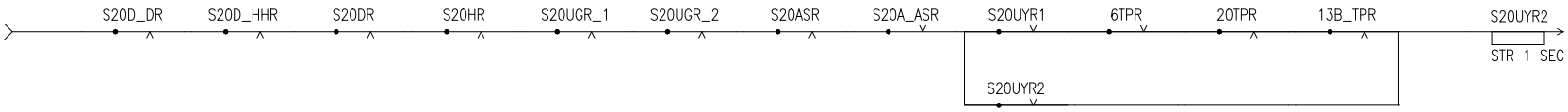
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

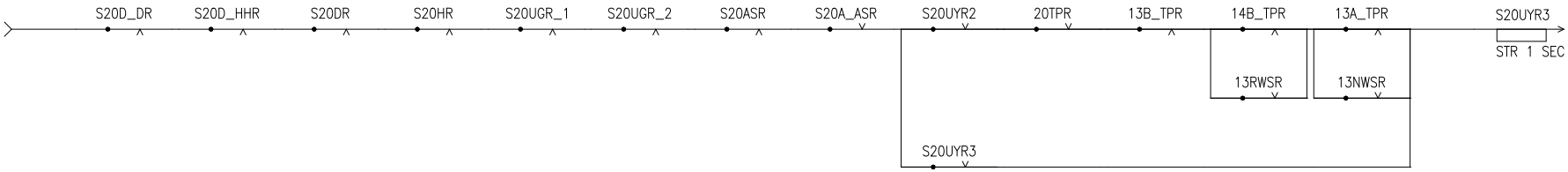
CAD FILE: 512_TAC_C1_00132_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ~S20D_DR * ~S20D_HHR * ~S20DR * ~S20HR * ~S20UGR_1 * ~S20UGR_2 * ~S20ASR * S20A_ASR * (S20UYR1 * 6TPR * ~20TPR * ~13B_TPR + S20UYR2) TO S20UYR2;



ASSIGN ~S20D_DR * ~S20D_HHR * ~S20DR * ~S20HR * ~S20UGR_1 * ~S20UGR_2 * ~S20ASR * S20A_ASR * (S20UYR2 * 20TPR * ~13B_TPR * (~14B_TPR + 13RWSR) * (~13A_TPR + 13NWSR) + S20UYR3) TO S20UYR3;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO a fc B

'khV SHEET 00133 | dqy 'khVSHEETS 00228 | CONT'D ON: 00134

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

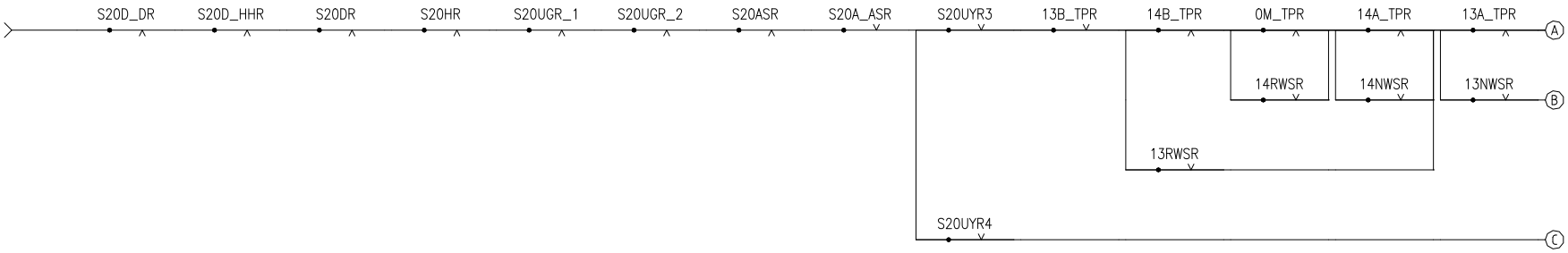
rkfVpøykZ
TATICHERLA

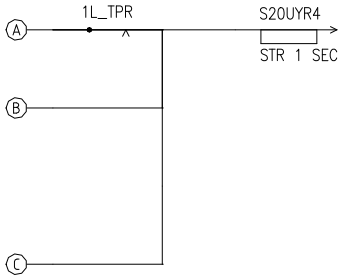
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00133_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ~S20D_DR * ~S20D_HHR * ~S20DR * ~S20HR * ~S20UGR_1 * ~S20UGR_2 * ~S20ASR * S20A_ASR * (S20UYR3 * 13B_TPR * (~14B_TPR * (~0M_TPR + 14RWSR) * (~14A_TPR + 14NWSR) + 13RWSR) * (~13A_TPR * ~1L_TPR + 13NWSR) + S20UYR4) TO S20UYR4;





ASSIGN ~S20D_DR * ~S20D_HHR * ~S20DR * ~S20HR * ~S20UGR_1 * ~S20UGR_2 * ~S20ASR * S20A_ASR * (S20UYR4 * (14B_TPR * (~14A_TPR * ~2L_TPR + 14NWSR) + 13RWSR + 14NWSR) + S20UYR5) TO S20UYR5;

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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00134 | dqy 'khVSHEETS 00228 | CONT'D ON: 00135

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SIGNAL & TELECOMMUNICATIONS

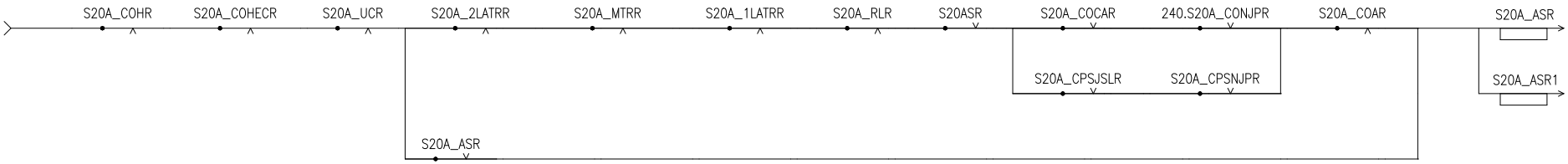
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

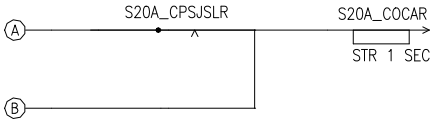
CAD FILE: 512_TAC_C1_00134_R00.DWG

(MICROLOK-II WARM STANDBY)

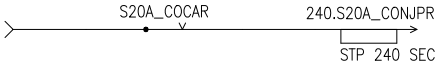
ASSIGN ~S20A_COHR * ~S20A_COHECR * ~S20A_UCR * (~S20A_2LATRR * ~S20A_MTRR * ~S20A_1LATRR * ~S20A_RLR * S20ASR * (S20A_COCAR * 240.S20A_CONJPR + S20A_CPSJSLR * S20A_CPSNJPR) * ~S20A_COAR + S20A_ASR) TO S20A_ASR,S20A_ASR1;



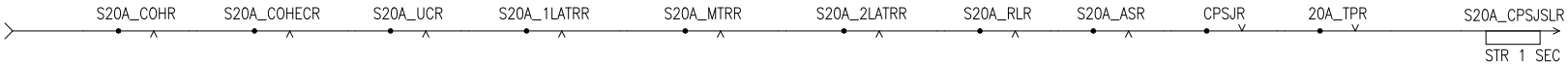
ASSIGN SYSINITTMR1 * ~S20A_COHR * ~S20A_COHECR * ~S20A_UCR * ~S20A_1LATRR * ~S20A_MTRR * ~S20A_2LATRR * ~S20A_RLR * ~S1JSLR * ~S20A_ASR * (S20AGNR * COCANR * ~240.S20A_CONJPR * ~S20A_CPSJSLR + S20A_COCAR) TO S20A_COCAR;



ASSIGN S20A_COCAR TO 240.S20A_CONJPR;



ASSIGN ~S20A_COHR * ~S20A_COHECR * ~S20A_UCR * ~S20A_1LATRR * ~S20A_MTRR * ~S20A_2LATRR * ~S20A_RLR * ~S20A_ASR * CPSJR * 20A_TPR TO S20A_CPSJSLR;



ASSIGN S20A_CPSJSLR TO S20A_CPSNJPR;

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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khVSHEET 00135 | dqy 'khVSHEETS 00228 | CONT'D ON: 00136

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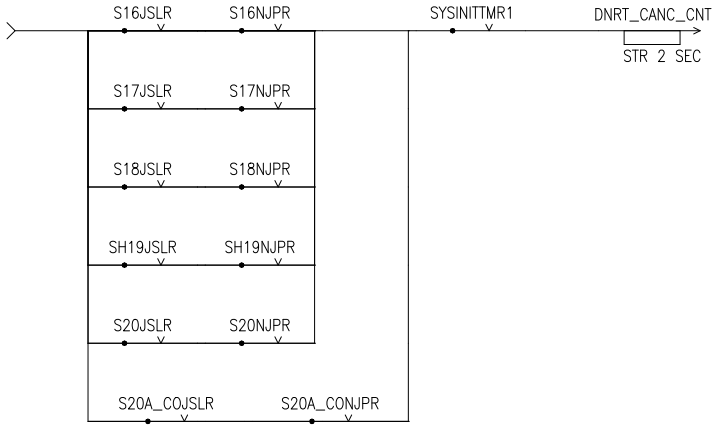
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

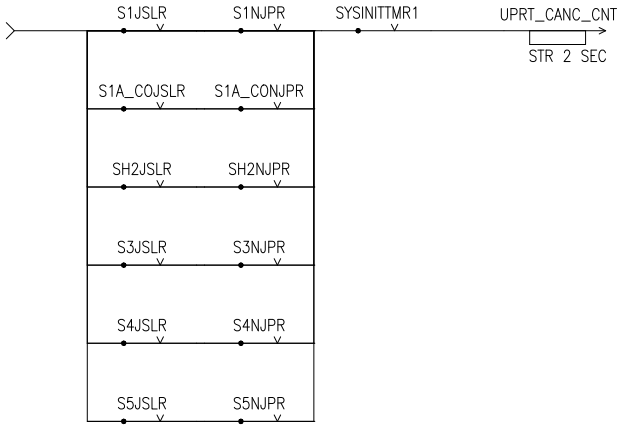
CAD FILE: 512_TAC_C1_00135_R00.DWG

(MICROLOK-II WARM STANDBY)

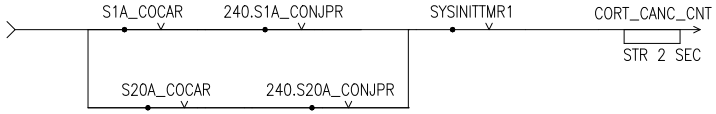
NV.ASSIGN (S16JSLR * S16NJPR + S17JSLR * S17NJPR + S18JSLR * S18NJPR + SH19JSLR * SH19NJPR + S20JSLR * S20NJPR + S20A_COJSLR * S20A_CONJPR) * SYSINITTMR1 TO DNRT_CANC_CNT;



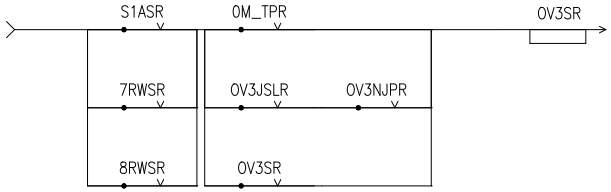
NV.ASSIGN (S1JSLR * S1NJPR + S1A_COJSLR * S1A_CONJPR + SH2JSLR * SH2NJPR + S3JSLR * S3NJPR + S4JSLR * S4NJPR + S5JSLR * S5NJPR) * SYSINITTMR1 TO UPRT_CANC_CNT;



NV.ASSIGN (S1A_COCAR * 240.S1A_CONJPR + S20A_COCAR * 240.S20A_CONJPR) * SYSINITTMR1 TO CORT_CANC_CNT;



ASSIGN (S1ASR + 7RWSR + 8RWSR) * (OM_TPR + OV3JSLR * OV3NJPR + OV3SR) TO OV3SR;



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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO a fc B

'khV SHEET 00136 | dqy 'khVSHEETS 00228 | CONT'D ON: 00137

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SIGNAL & TELECOMMUNICATIONS

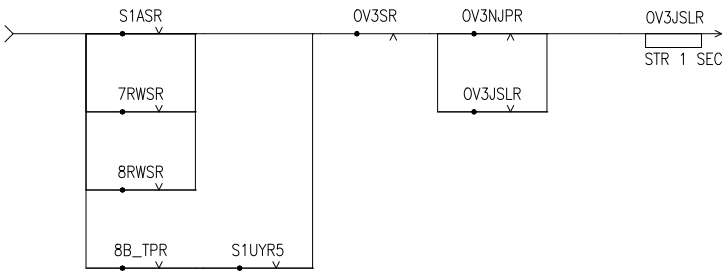
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

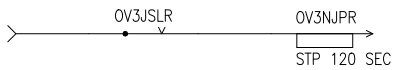
CAD FILE: 512_TAC_C1_00136_R00.DWG

(MICROLOK-II WARM STANDBY)

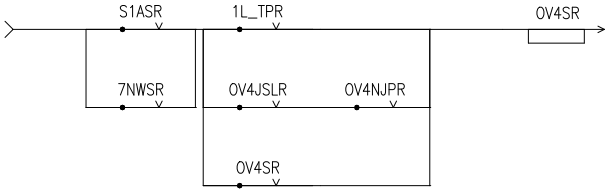
ASSIGN (S1ASR + 7RWSR + 8RWSR + 8B_TPR * S1UYR5) * ~OV3SR * (~OV3NJPR + OV3JSLR) TO OV3JSLR;



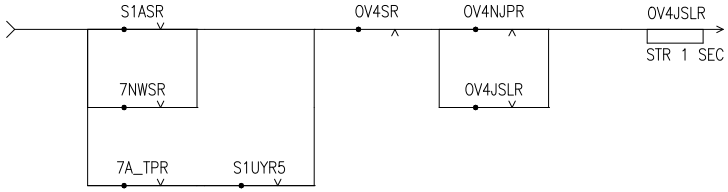
ASSIGN OV3JSLR TO OV3NJPR;



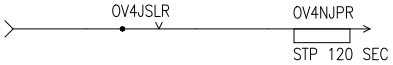
ASSIGN (S1ASR + 7NWSR) * (1L_TPR + OV4JSLR * OV4NJPR + OV4SR) TO OV4SR;



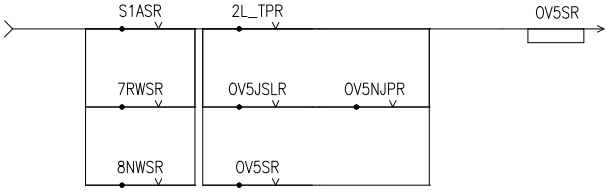
ASSIGN (S1ASR + 7NWSR + 7A_TPR * S1UYR5) * ~OV4SR * (~OV4NJPR + OV4JSLR) TO OV4JSLR;



ASSIGN OV4JSLR TO OV4NJPR;



ASSIGN (S1ASR + 7RWSR + 8NWSR) * (2L_TPR + OV5JSLR * OV5NJPR + OV5SR) TO OV5SR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00137 | dqy 'khVSHEETS 00228 | CONT'D ON: 00138

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SIGNAL & TELECOMMUNICATIONS

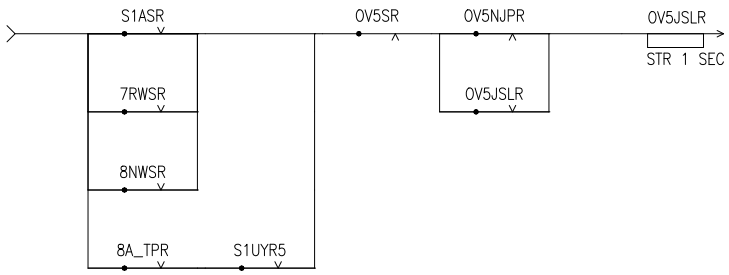
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

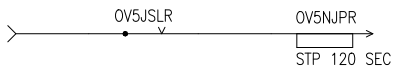
CAD FILE: 512_TAC_C1_00137_R00.DWG

(MICROLOK-II WARM STANDBY)

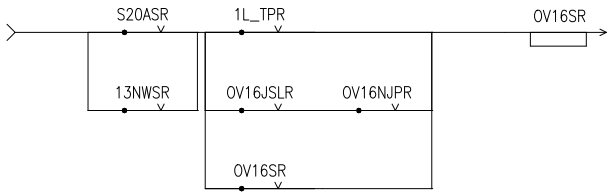
ASSIGN (S1ASR + 7RWSR + 8NWSR + 8A_TPR * S1UYR5) * ~OV5SR * (~OV5NJPR + OV5JSLR) TO OV5JSLR;



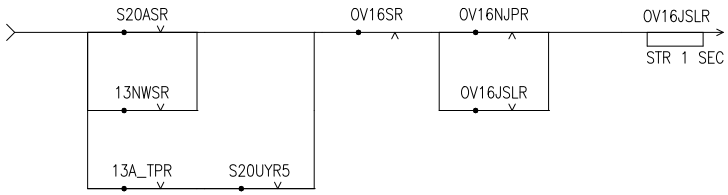
ASSIGN OV5JSLR TO OV5NJPR;



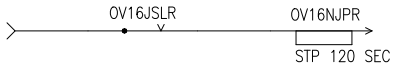
ASSIGN (S20ASR + 13NWSR) * (1L_TPR + OV16JSLR * OV16NJPR + OV16SR) TO OV16SR;



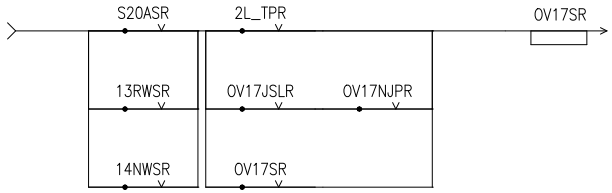
ASSIGN (S20ASR + 13NWSR + 13A_TPR * S20UYR5) * ~OV16SR * (~OV16NJPR + OV16JSLR) TO OV16JSLR;



ASSIGN OV16JSLR TO OV16NJPR;



ASSIGN (S20ASR + 13RWSR + 14NWSR) * (2L_TPR + OV17JSLR * OV17NJPR + OV17SR) TO OV17SR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
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00138	00228	00139

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SIGNAL & TELECOMMUNICATIONS

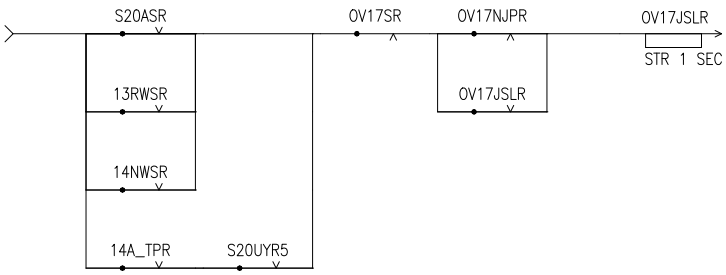
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

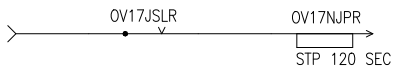
CAD FILE: 512_TAC_C1_00138_R00.DWG

(MICROLOK-II WARM STANDBY)

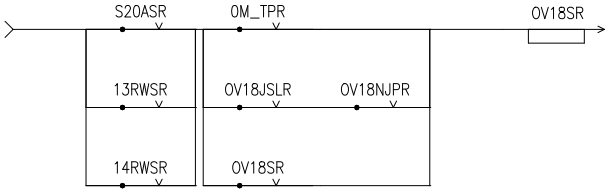
ASSIGN (S20ASR + 13RWSR + 14NWSR + 14A_TPR * S20UYR5) * ~OV17SR * (~OV17NJPR + OV17JSLR) TO OV17JSLR;



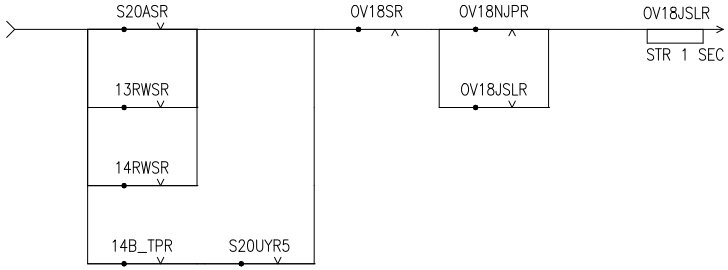
ASSIGN OV17JSLR TO OV17NJPR;



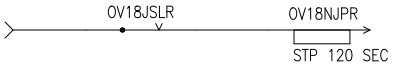
ASSIGN (S20ASR + 13RWSR + 14RWSR) * (0M_TPR + OV18JSLR * OV18NJPR + OV18SR) TO OV18SR;



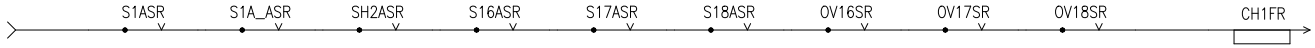
ASSIGN (S20ASR + 13RWSR + 14RWSR + 14B_TPR * S20UYR5) * ~OV18SR * (~OV18NJPR + OV18JSLR) TO OV18JSLR;



ASSIGN OV18JSLR TO OV18NJPR;



ASSIGN S1ASR * S1A_ASR * SH2ASR * S16ASR * S17ASR * S18ASR * OV16SR * OV17SR * OV18SR TO CH1FR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO a fc B
'khV SHEET 00139 | dqy 'khVSHEETS 00228 | CONT'D ON: 00140

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

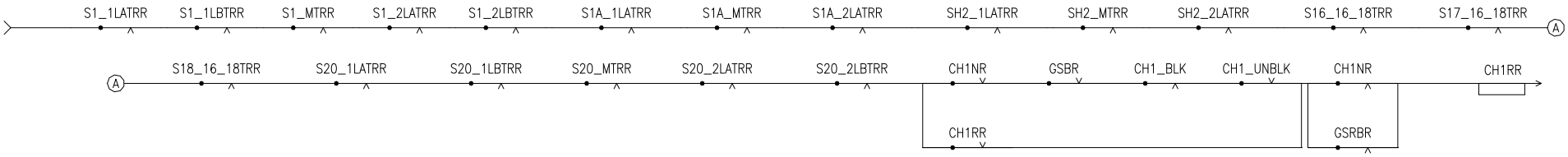
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

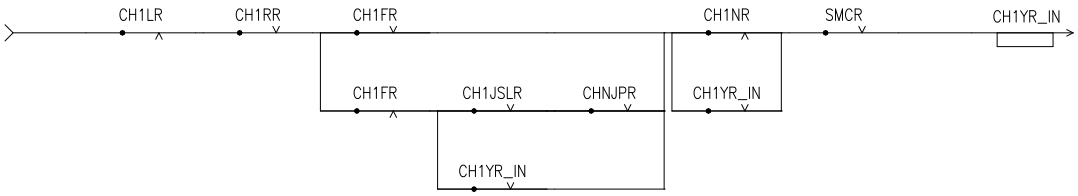
CAD FILE: 512_TAC_C1_00139_R00.DWG

(MICROLOK-II WARM STANDBY)

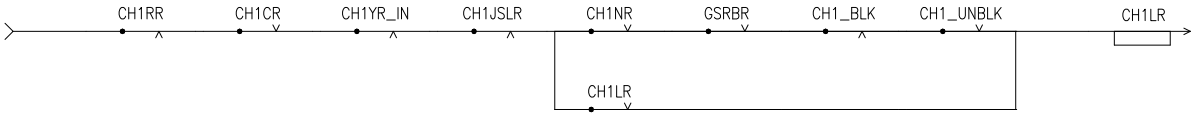
ASSIGN ~S1_1LATRR * ~S1_1LBTRR * ~S1_MTRR * ~S1_2LATRR * ~S1_2LBTRR * ~S1A_1LATRR * ~S1A_MTRR * ~S1A_2LATRR * ~SH2_1LATRR * ~SH2_MTRR * ~SH2_2LATRR * ~S16_16_18TRR * ~S17_16_18TRR * ~S18_16_18TRR * ~S20_1LATRR * ~S20_1LBTRR * ~S20_MTRR * ~S20_2LATRR * ~S20_2LBTRR * (CH1NR * GSBR * ~CH1_BLK * CH1_UNBLK + CH1RR) * (~CH1NR + ~GSRBR) TO CH1RR;



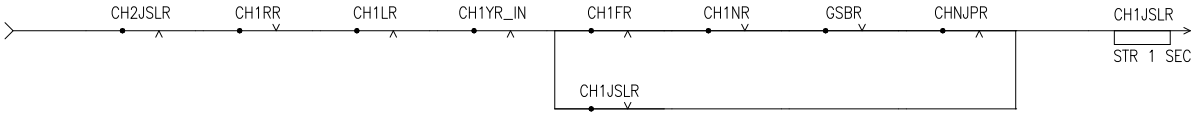
ASSIGN ~CH1LR * CH1RR * (CH1FR + ~CH1FR * (CH1JSLR * CHNJPR + CH1YR_IN)) * (~CH1NR + CH1YR_IN) * SMCR TO CH1YR_IN;



ASSIGN ~CH1RR * CH1CR * ~CH1YR_IN * ~CH1JSLR * (CH1NR * GSRBR * ~CH1_BLK * CH1_UNBLK + CH1LR) TO CH1LR;



ASSIGN ~CH2JSLR * CH1RR * ~CH1LR * ~CH1YR_IN * (~CH1FR * CH1NR * GSBR * ~CHNJPR + CH1JSLR) TO CH1JSLR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
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SE/SSE	
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f CSTE	

CHK. APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00140 | dqy 'khVSHEETS 00228 | CONT'D ON: 00141

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

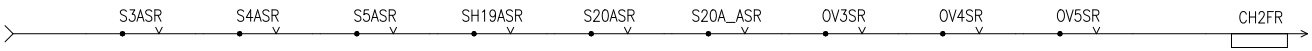
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

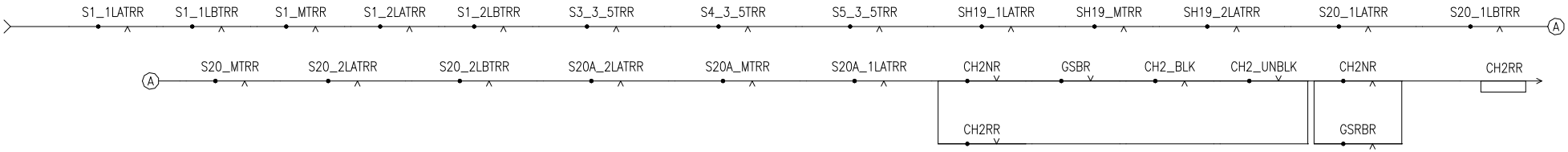
CAD FILE: 512_TAC_C1_00140_R00.DWG

(MICROLOK-II WARM STANDBY)

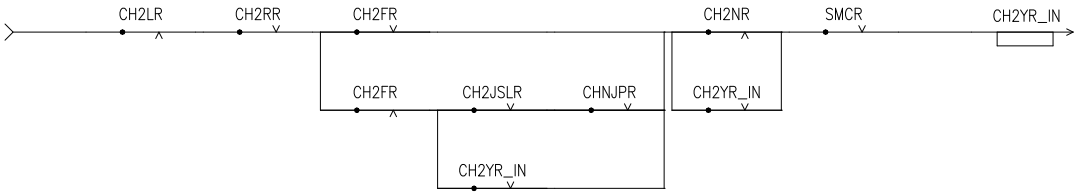
ASSIGN S3ASR * S4ASR * S5ASR * SH19ASR * S20ASR * S20A_ASR * OV3SR * OV4SR * OV5SR TO CH2FR;



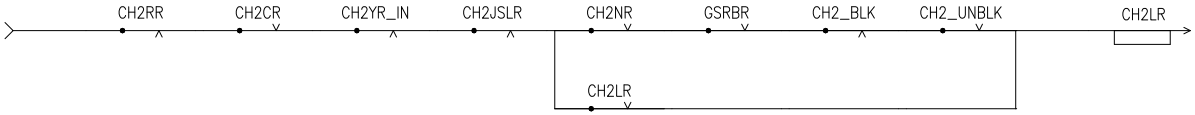
ASSIGN ~S1_1LATRR * ~S1_1LBTRR * ~S1_MTRR * ~S1_2LATRR * ~S1_2LBTRR * ~S3_3_5TRR * ~S4_3_5TRR * ~S5_3_5TRR * ~SH19_1LATRR * ~SH19_MTRR * ~SH19_2LATRR * ~S20_1LATRR * ~S20_1LBTRR * ~S20_MTRR * ~S20_2LATRR * ~S20_2LBTRR * ~S20A_2LATRR * ~S20A_MTRR * ~S20A_1LATRR * (CH2NR * GSBR * ~CH2_BLK * CH2_UNBLK + CH2RR) * (~CH2NR + ~GSRBR) TO CH2RR;



ASSIGN ~CH2LR * CH2RR * (CH2FR + ~CH2FR * (CH2JSLR * CHNJPR + CH2YR_IN)) * (~CH2NR + CH2YR_IN) * SMCR TO CH2YR_IN;



ASSIGN ~CH2RR * CH2CR * ~CH2YR_IN * ~CH2JSLR * (CH2NR * GSRBR * ~CH2_BLK * CH2_UNBLK + CH2LR) TO CH2LR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B | 'khV SHEET 00141 | dqy 'khVSHEETS 00228 | CONT'D ON: 00142

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

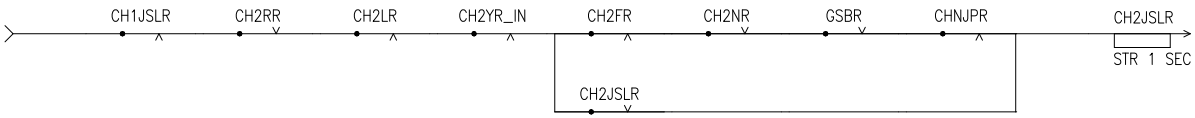
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

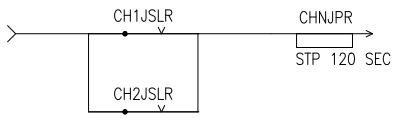
CAD FILE: 512_TAC_C1_00141_R00.DWG

(MICROLOK-II WARM STANDBY)

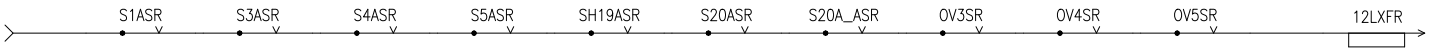
ASSIGN ~CH1JSLR * CH2RR * ~CH2LR * ~CH2YR_IN * (~CH2FR * CH2NR * GSBR * ~CHNJPR + CH2JSLR) TO CH2JSLR;



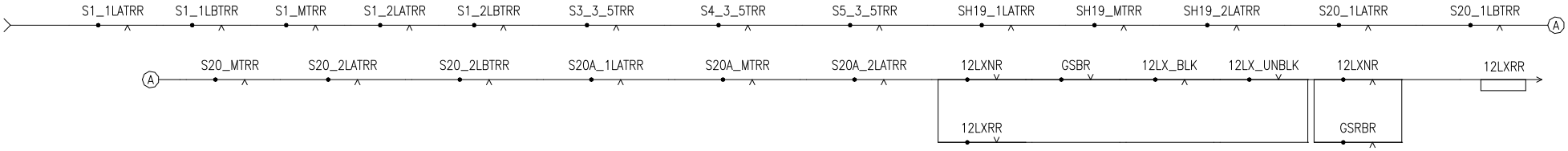
ASSIGN CH1JSLR + CH2JSLR TO CHNJPR;



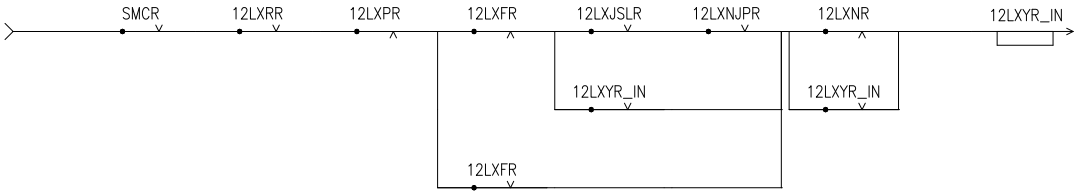
ASSIGN S1ASR * S3ASR * S4ASR * S5ASR * SH19ASR * S20ASR * S20A_ASR * OV3SR * OV4SR * OV5SR TO 12LXFR;



ASSIGN ~S1_1LATRR * ~S1_1LBTRR * ~S1_MTRR * ~S1_2LATRR * ~S1_2LBTRR * ~S3_3_5TRR * ~S4_3_5TRR * ~S5_3_5TRR * ~SH19_1LATRR * ~SH19_MTRR * ~SH19_2LATRR * ~S20_1LATRR * ~S20_1LBTRR * ~S20_MTRR * ~S20_2LATRR * ~S20_2LBTRR * ~S20A_1LATRR * ~S20A_MTRR * ~S20A_2LATRR * (12LXNR * GSBR * ~12LX_BLK * 12LX_UNBLK + 12LXRR) * (~12LXNR + ~GSRBR) TO 12LXRR;



ASSIGN SMCR * 12LXRR * ~12LXPR * (~12LXFR * (12LXJSLR * 12LXNJPR + 12LXYR_IN) + 12LXFR) * (~12LXNR + 12LXYR_IN) TO 12LXYR_IN;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
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APP.

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

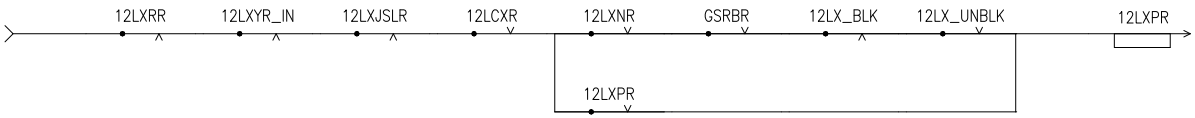
REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khVSHEET	dqy 'khVSHEETS	CONT'D ON:
00142	00228	00143

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

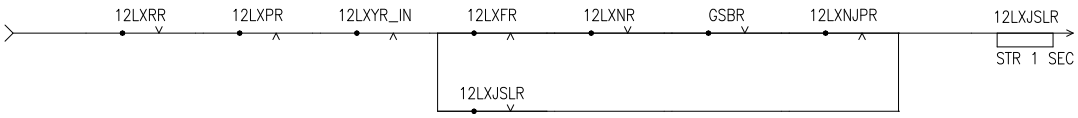
CAD FILE: 512_TAC_C1_00142_R00.DWG

(MICROLOK-II WARM STANDBY)

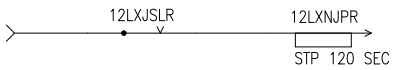
ASSIGN ~12LXRR * ~12LXYR_IN * ~12LXJSLR * 12LCXR * (12LXNR * GSRBR * ~12LX_BLK * 12LX_UNBLK + 12LXPR) TO 12LXPR;



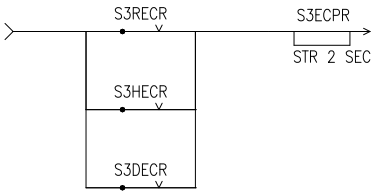
ASSIGN 12LXRR * ~12LXPR * ~12LXYR_IN * (~12LXFR * 12LXNR * GSBR * ~12LXNJPR + 12LXJSLR) TO 12LXJSLR;



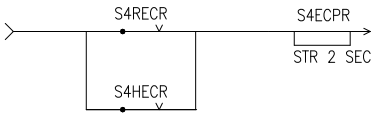
ASSIGN 12LXJSLR TO 12LXNJPR;



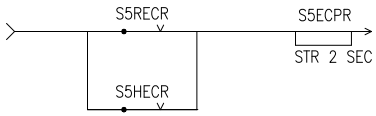
ASSIGN S3RECR + S3HECR + S3DECR TO S3ECPR;



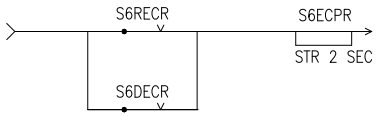
ASSIGN S4RECR + S4HECR TO S4ECPR;



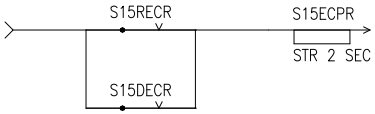
ASSIGN S5RECR + S5HECR TO S5ECPR;



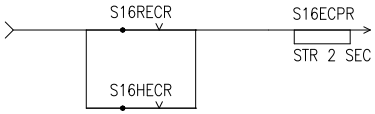
ASSIGN S6RECR + S6DECR TO S6ECPR;



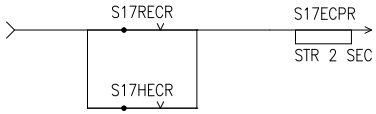
ASSIGN S15RECR + S15DECR TO S15ECPR;



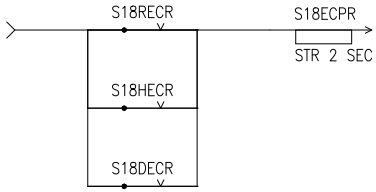
ASSIGN S16RECR + S16HECR TO S16ECPR;



ASSIGN S17RECR + S17HECR TO S17ECPR;



ASSIGN S18RECR + S18HECR + S18DECR TO S18ECPR;



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REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
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'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00143	00228	00144

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

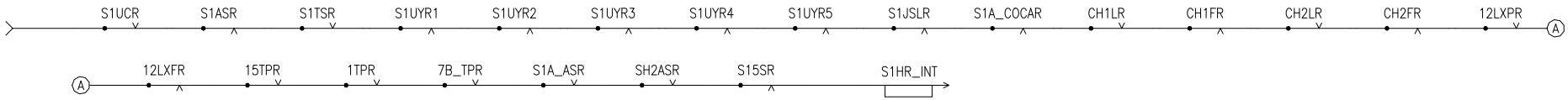
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

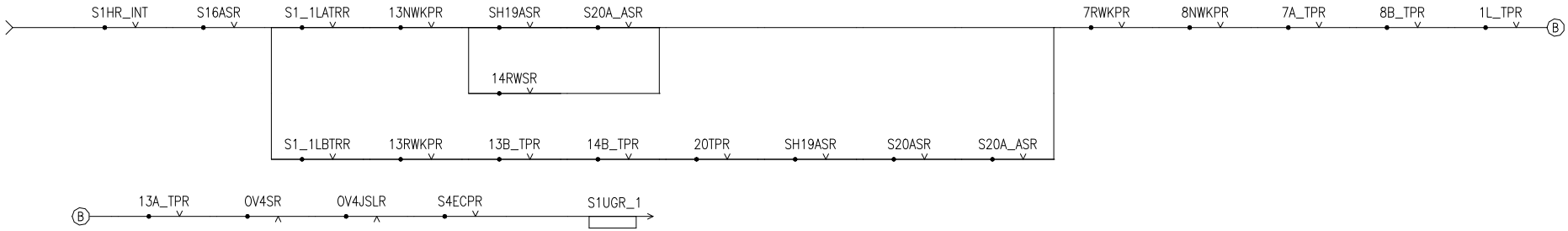
CAD FILE: 512_TAC_C1_00143_R00.DWG

(MICROLOK-II WARM STANDBY)

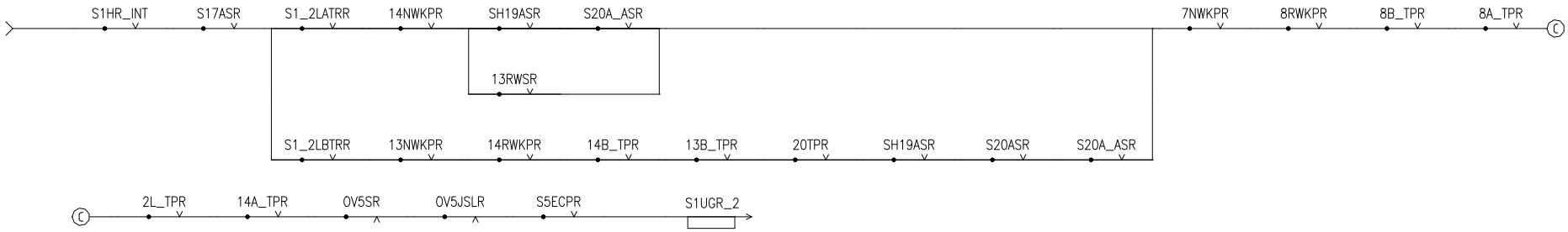
ASSIGN S1UCR * ~S1ASR * S1TSR * ~S1UYR1 * ~S1UYR2 * ~S1UYR3 * ~S1UYR4 * ~S1UYR5 * ~S1JSLR * ~S1A_COCAR * CH1LR * ~CH1FR * CH2LR * ~CH2FR * 12LXPR * ~12LXFR * 15TPR * 1TPR * 7B_TPR * S1A_ASR * SH2ASR * ~S15SR TO S1HR_INT;



ASSIGN S1HR_INT * S16ASR * (S1_1LATRR * 13NWKPR * (SH19ASR * S20A_ASR + 14RWSR) + S1_1LBTRR * 13RWKPR * 13B_TPR * 14B_TPR * 20TPR * SH19ASR * S20ASR * S20A_ASR) * 7RWKPR * 8NWKPR * 7A_TPR * 8B_TPR * 1L_TPR * 13A_TPR * ~OV4SR * ~OV4JSLR * S4ECPR TO S1UGR_1;



ASSIGN S1HR_INT * S17ASR * (S1_2LATRR * 14NWKPR * (SH19ASR * S20A_ASR + 13RWSR) + S1_2LBTRR * 13NWKPR * 14RWKPR * 14B_TPR * 13B_TPR * 20TPR * SH19ASR * S20ASR * S20A_ASR) * 7NWKPR * 8RWKPR * 8B_TPR * 8A_TPR * 2L_TPR * 14A_TPR * ~OV5SR * ~OV5JSLR * S5ECPR TO S1UGR_2;



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00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
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Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00144	00228	00145

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

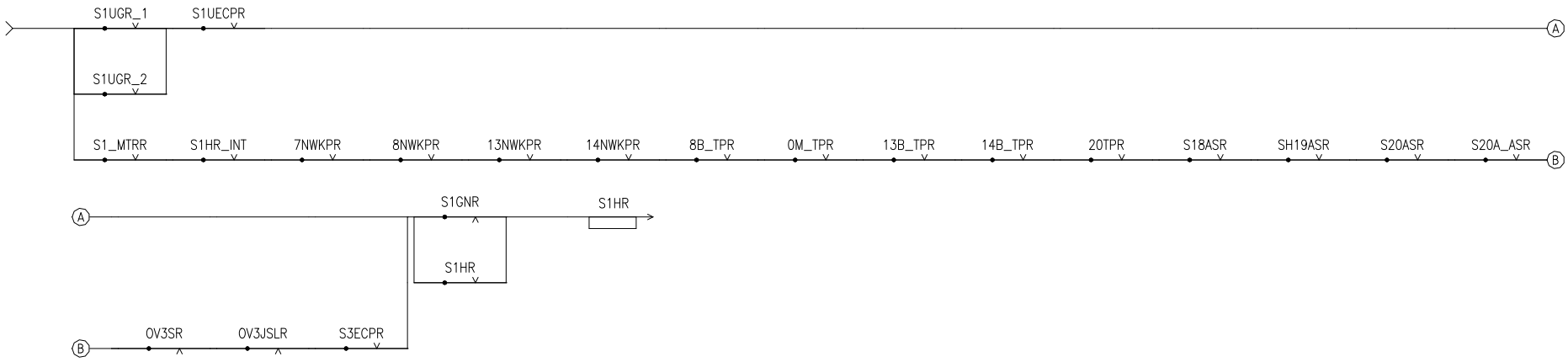
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

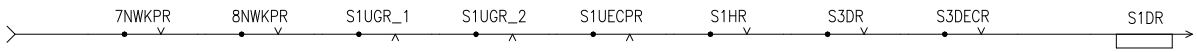
CAD FILE: 512_TAC_C1_00144_R00.DWG

(MICROLOK-II WARM STANDBY)

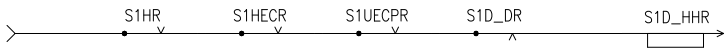
ASSIGN ((S1UGR_1 + S1UGR_2) * S1UECPR + S1_MTRR * S1HR_INT * 7NWKPR * 8NWKPR * 13NWKPR * 14NWKPR * 8B_TPR * 0M_TPR * 13B_TPR * 14B_TPR * 20TPR * S18ASR * SH19ASR * S20ASR * S20A_ASR * ~0V3SR * ~0V3JSLR * S3ECPR) * (~S1GNR + S1HR) TO S1HR;



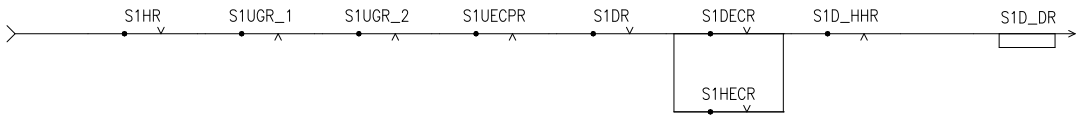
ASSIGN 7NWKPR * 8NWKPR * ~S1UGR_1 * ~S1UGR_2 * ~S1UECPR * S1HR * S3DR * S3DECR TO S1DR;



ASSIGN S1HR * S1HECR * S1UECPR * ~S1D_DR TO S1D_HHR;



ASSIGN S1HR * ~S1UGR_1 * ~S1UGR_2 * ~S1UECPR * S1DR * (S1DECR + S1HECR) * ~S1D_HHR TO S1D_DR;



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SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00145 | dqy 'khVSHEETS 00228 | CONT'D ON: 00146

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

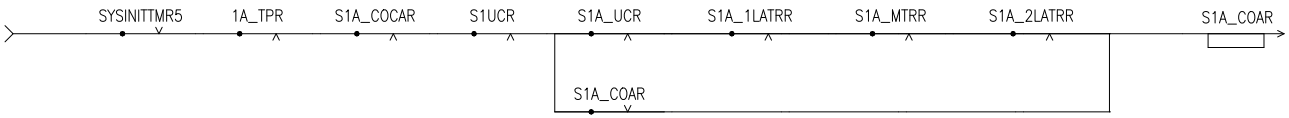
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

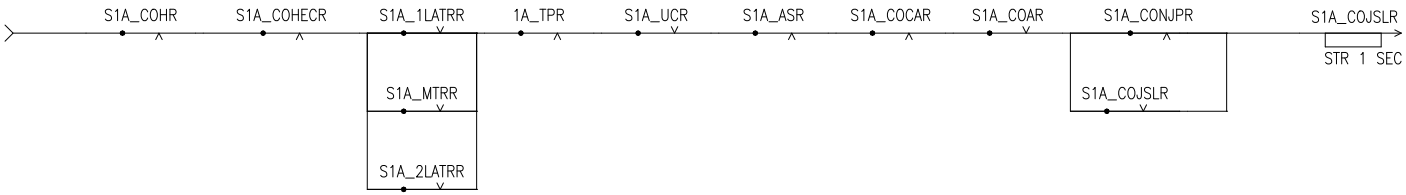
CAD FILE: 512_TAC_C1_00145_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN SYSINITTMR5 * ~1A_TPR * ~S1A_COCAR * ~S1UCR * (~S1A_UCR * ~S1A_1LATRR * ~S1A_MTRR * ~S1A_2LATRR + S1A_COAR) TO S1A_COAR;

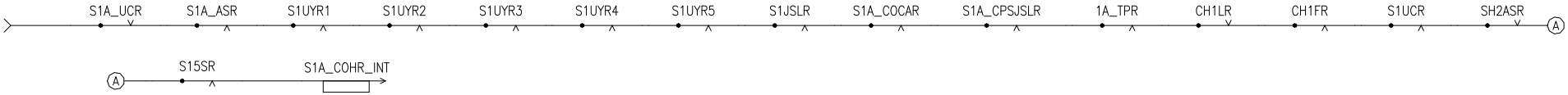


ASSIGN ~S1A_COHR * ~S1A_COHECR * (S1A_1LATRR + S1A_MTRR + S1A_2LATRR) * ~1A_TPR * S1A_UCR * ~S1A_ASR * ~S1A_COCAR * S1A_COAR * (~S1A_CONJPR + S1A_COJSLR) TO S1A_COJSLR;



ASSIGN S1A_COJSLR TO S1A_CONJPR;

ASSIGN S1A_UCR * ~S1A_ASR * ~S1UYR1 * ~S1UYR2 * ~S1UYR3 * ~S1UYR4 * ~S1UYR5 * ~S1JSLR * ~S1A_COCAR * ~S1A_CPSJSLR * ~1A_TPR * CH1LR * ~CH1FR * ~S1UCR * SH2ASR * ~S15SR TO S1A_COHR_INT;



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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00146	00228	00147

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SIGNAL & TELECOMMUNICATIONS

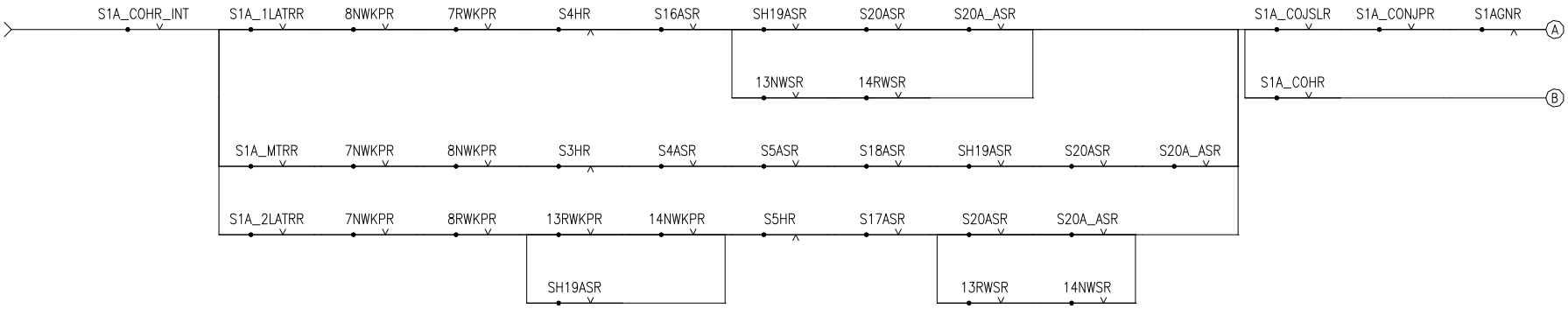
rkfVpøykZ
TATICHERLA

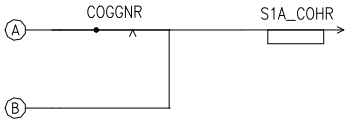
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00146_R00.DWG

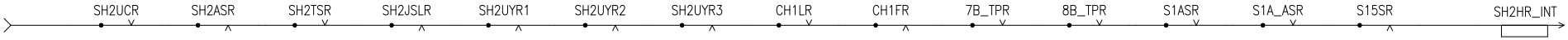
(MICROLOK-II WARM STANDBY)

ASSIGN S1A_COHR_INT * (S1A_1LATRR * 8NWKPR * 7RWKPR * ~S4HR * S16ASR * (SH19ASR * S20ASR * S20A_ASR + 13NWSR * 14RWSR) + S1A_MTRR * 7NWKPR * 8NWKPR * ~S3HR * S4ASR * S5ASR * S18ASR * SH19ASR * S20ASR * S20A_ASR + S1A_2LATRR * 7NWKPR * 8RWKPR * (13RWKPR * 14NWKPR + SH19ASR) * ~S5HR * S17ASR * (S20ASR * S20A_ASR + 13RWSR * 14NWSR)) * (S1A_COJSLR * S1A_CONJPR * ~S1AGNR * ~COGGNR + S1A_COHR) TO S1A_COHR;





ASSIGN SH2UCR * ~SH2ASR * SH2TSR * ~SH2JSLR * ~SH2UYR1 * ~SH2UYR2 * ~SH2UYR3 * CH1LR * ~CH1FR * 7B_TPR * 8B_TPR * S1ASR * S1A_ASR * ~S15SR TO SH2HR_INT;



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REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00147	00228	00148

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SIGNAL & TELECOMMUNICATIONS

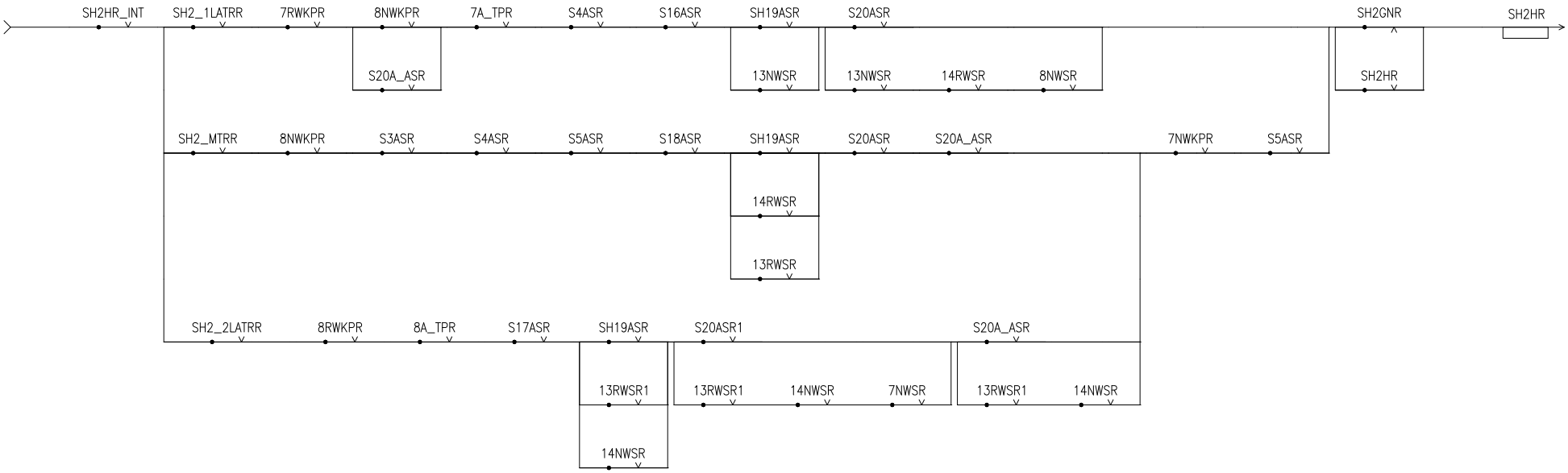
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00147_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN SH2HR_INT * (SH2_1LATRR * 7RWKPR * (8NWKPR + S20A_ASR) * 7A_TPR * S4ASR * S16ASR * (SH19ASR + 13NWSR) * (S20ASR + 13NWSR * 14RWSR * 8NWSR) + (SH2_MTRR * 8NWKPR * S3ASR * S4ASR * S5ASR * S18ASR * (SH19ASR + 14RWSR + 13RWSR) * S20ASR * S20A_ASR + SH2_2LATRR * 8RWKPR * 8A_TPR * S17ASR * (SH19ASR + 13RWSR1 + 14NWSR) * (S20ASR1 + 13RWSR1 * 14NWSR * 7NWSR) * (S20A_ASR + 13RWSR1 * 14NWSR)) * 7NWKPR * S5ASR) * (~SH2GNR + SH2HR) TO SH2HR;



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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00148 | dqy 'khVSHEETS 00228 | CONT'D ON: 00149

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SIGNAL & TELECOMMUNICATIONS

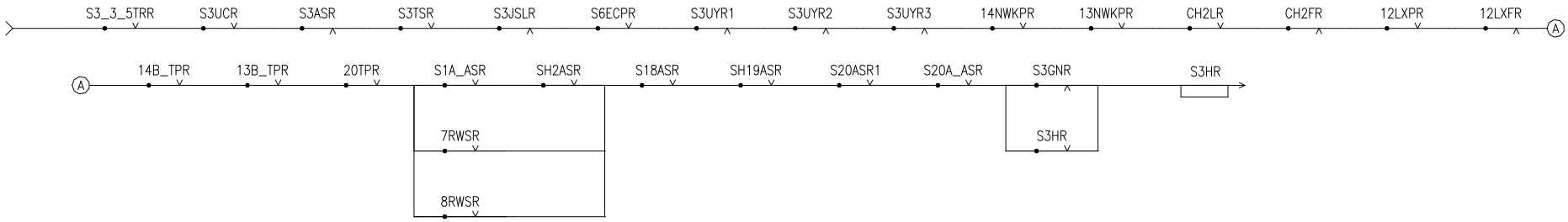
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

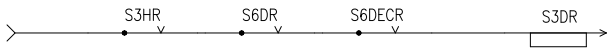
CAD FILE: 512_TAC_C1_00148_R00.DWG

(MICROLOK-II WARM STANDBY)

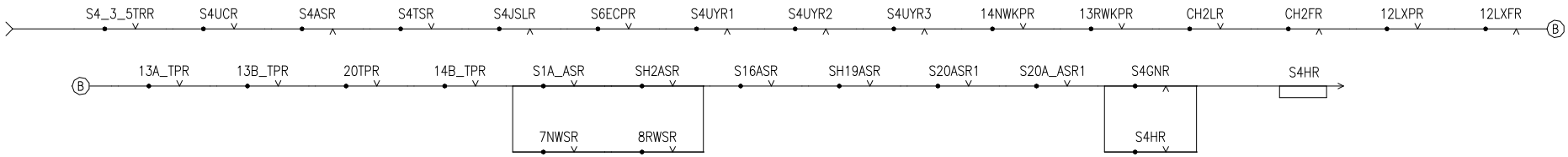
ASSIGN S3_3_5TRR * S3UCR * ~S3ASR * S3TSR * ~S3JSLR * S6ECPR * ~S3UYR1 * ~S3UYR2 * ~S3UYR3 * 14NWKPR * 13NWKPR * CH2LR * ~CH2FR * 12LXPR * ~12LXFR * 14B_TPR * 13B_TPR * 20TPR * (S1A_ASR * SH2ASR + 7RWSR + 8RWSR) * S18ASR * SH19ASR * S20ASR1 * S20A_ASR * (~S3GNR + S3HR) TO S3HR;



ASSIGN S3HR * S6DR * S6DECR TO S3DR;



ASSIGN S4_3_5TRR * S4UCR * ~S4ASR * S4TSR * ~S4JSLR * S6ECPR * ~S4UYR1 * ~S4UYR2 * ~S4UYR3 * 14NWKPR * 13RWKPR * CH2LR * ~CH2FR * 12LXPR * ~12LXFR * 13A_TPR * 13B_TPR * 20TPR * 14B_TPR * (S1A_ASR * SH2ASR + 7NWSR * 8RWSR) * S16ASR * SH19ASR * S20ASR1 * S20A_ASR1 * (~S4GNR + S4HR) TO S4HR;



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'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
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SIGNAL & TELECOMMUNICATIONS

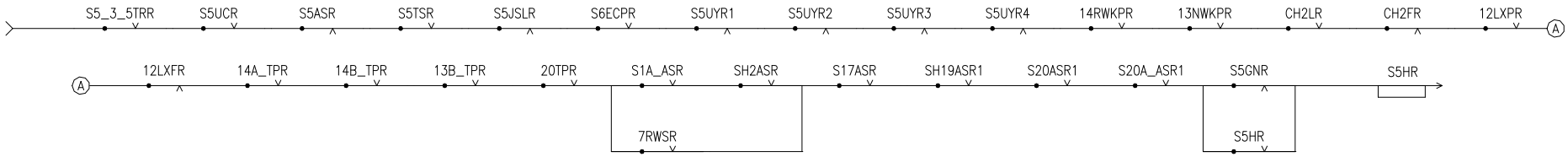
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

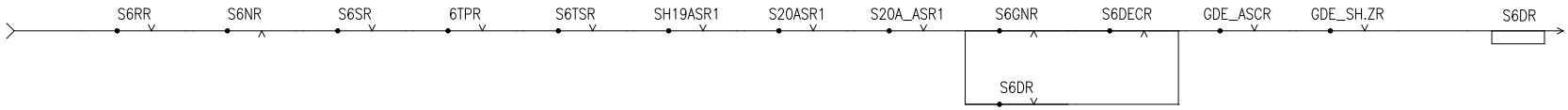
CAD FILE: 512_TAC_C1_00149_R00.DWG

(MICROLOK-II WARM STANDBY)

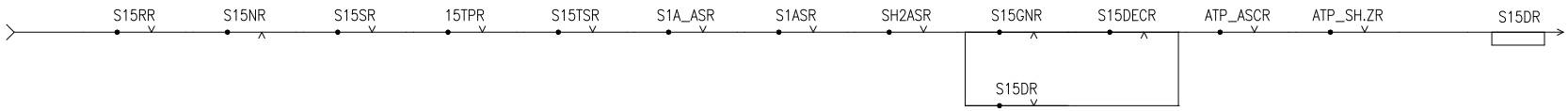
ASSIGN S5_3_5TRR * S5UCR * ~S5ASR * S5TSR * ~S5JSLR * S6ECPR * ~S5UYR1 * ~S5UYR2 * ~S5UYR3 * ~S5UYR4 * 14RWKPR * 13NWKPR * CH2LR * ~CH2FR * 12LXPR * ~12LXFR * 14A_TPR * 14B_TPR * 13B_TPR * 20TPR * (S1A_ASR * SH2ASR + 7RWSR) * S17ASR * SH19ASR1 * S20ASR1 * S20A_ASR1 * (~S5GNR + S5HR) TO S5HR;



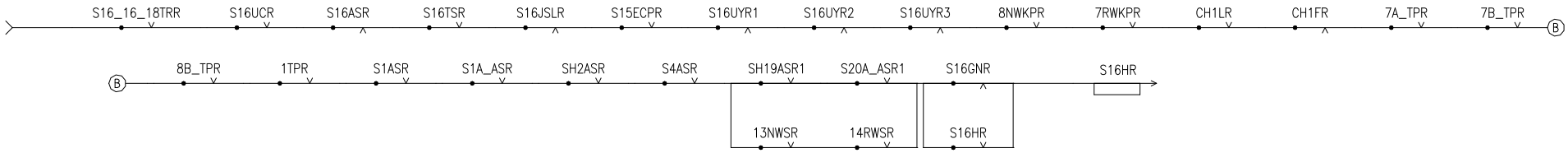
ASSIGN S6RR * ~S6NR * S6SR* 6TPR * S6TSR * SH19ASR1 * S20ASR1 * S20A_ASR1 * (~S6GNR * ~S6DECR + S6DR) * GDE_ASCR * GDE_SH.ZR TO S6DR;



ASSIGN S15RR * ~S15NR * S15SR * 15TPR * S15TSR * S1A_ASR * S1ASR * SH2ASR * (~S15GNR * ~S15DECR + S15DR) * ATP_ASCR * ATP_SH.ZR TO S15DR;



ASSIGN S16_16_18TRR * S16UCR * ~S16ASR * S16TSR * ~S16JSLR * S15ECPR * ~S16UYR1 * ~S16UYR2 * ~S16UYR3 * 8NWKPR * 7RWKPR * CH1LR * ~CH1FR * 7A_TPR * 7B_TPR * 8B_TPR * 1TPR * S1ASR * S1A_ASR * SH2ASR * S4ASR * (SH19ASR1 * S20A_ASR1 + 13NWSR * 14RWSR)* (~S16GNR + S16HR) TO S16HR;



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'khVSHEET 00150 | dqy 'khVSHEETS 00228 | CONT'D ON: 00151

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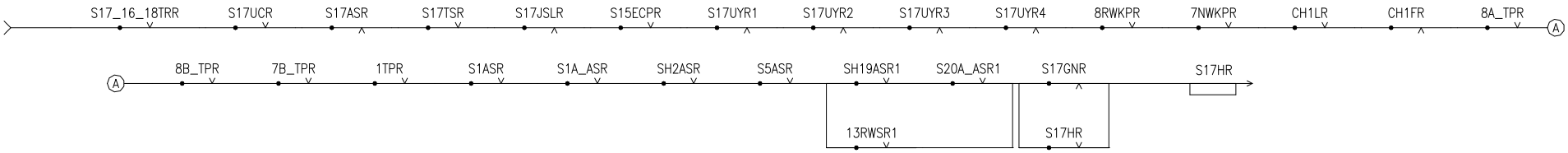
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

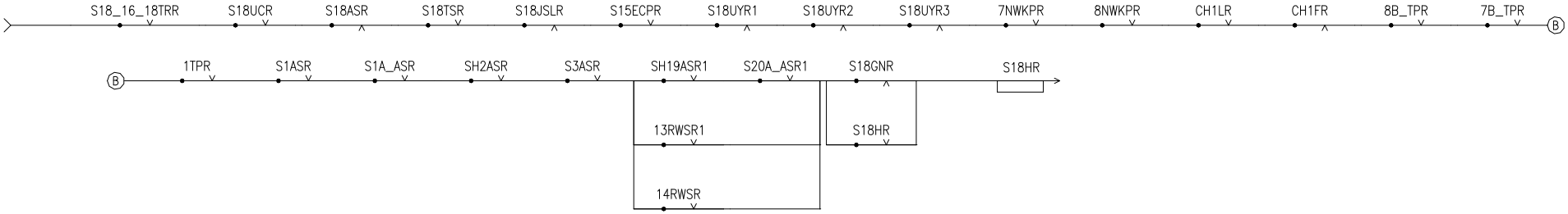
CAD FILE: 512_TAC_C1_00150_R00.DWG

(MICROLOK-II WARM STANDBY)

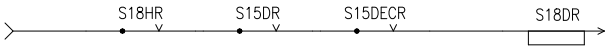
ASSIGN S17_16_18TRR * S17UCR * ~S17ASR * S17TSR * ~S17JSLR * S15ECPR * ~S17UYR1 * ~S17UYR2 * ~S17UYR3 * ~S17UYR4 * 8RWKPR * 7NWKPR * CH1LR * ~CH1FR * 8A_TPR * 8B_TPR * 7B_TPR * 1TPR * S1ASR * S1A_ASR * SH2ASR * S5ASR * (SH19ASR1 * S20A_ASR1 + 13RWSR1)* (~S17GNR + S17HR) TO S17HR;



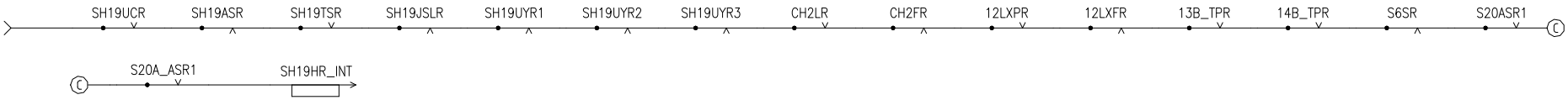
ASSIGN S18_16_18TRR * S18UCR * ~S18ASR * S18TSR * ~S18JSLR * S15ECPR * ~S18UYR1 * ~S18UYR2 * ~S18UYR3 * 7NWKPR * 8NWKPR * CH1LR * ~CH1FR * 8B_TPR * 7B_TPR * 1TPR * S1ASR * S1A_ASR * SH2ASR * S3ASR * (SH19ASR1 * S20A_ASR1 + 13RWSR1 + 14RWSR)* (~S18GNR + S18HR) TO S18HR;



ASSIGN S18HR * S15DR * S15DECR TO S18DR;



ASSIGN SH19UCR * ~SH19ASR * SH19TSR * ~SH19JSLR * ~SH19UYR1 * ~SH19UYR2 * ~SH19UYR3 * CH2LR * ~CH2FR * 12LXPR * ~12LXFR * 13B_TPR * 14B_TPR * ~S6SR * S20ASR1 * S20A_ASR1 TO SH19HR_INT;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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SSTE	
f CSTE	

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APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khVSHEET 00151 | dqy 'khVSHEETS 00228 | CONT'D ON: 00152

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

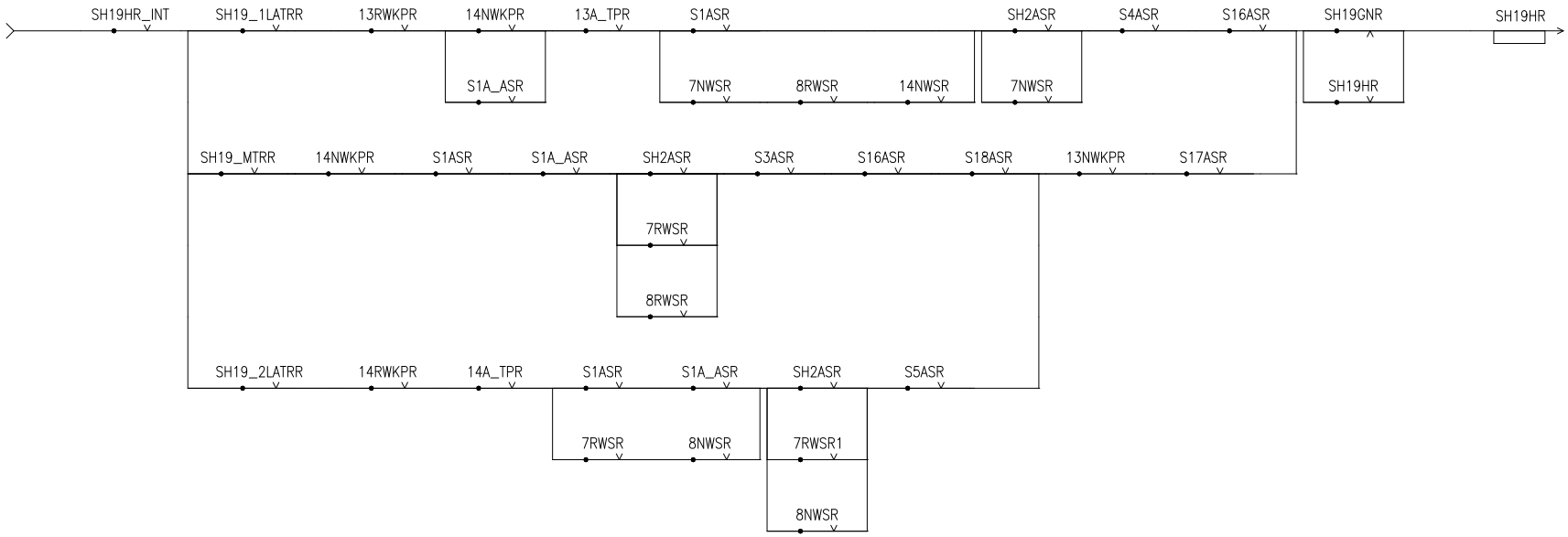
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

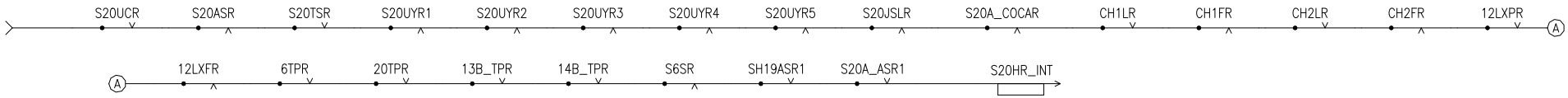
CAD FILE: 512_TAC_C1_00151_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN SH19HR_INT * (SH19_1LATRR * 13RWKPR * (14NWKPR + S1A_ASR) * 13A_TPR * (S1ASR + 7NWSR * 8RWSR * 14NWSR) * (SH2ASR + 7NWSR) * S4ASR * S16ASR + (SH19_MTRR * 14NWKPR * S1ASR * S1A_ASR * (SH2ASR + 7RWSR + 8RWSR) * S3ASR * S16ASR * S18ASR + SH19_2LATRR * 14RWKPR * 14A_TPR * (S1ASR * S1A_ASR + 7RWSR * 8NWSR) * (SH2ASR + 7RWSR1 + 8NWSR) * S5ASR) * 13NWKPR * S17ASR) * (~SH19GNR + SH19HR) TO SH19HR;



ASSIGN S20UCR * ~S20ASR * S20TSR * ~S20UYR1 * ~S20UYR2 * ~S20UYR3 * ~S20UYR4 * ~S20UYR5 * ~S20JSLR * ~S20A_COCAR * CH1LR * ~CH1FR * CH2LR * ~CH2FR * 12LXPR * ~12LXFR * 6TPR * 20TPR * 13B_TPR * 14B_TPR * ~S6SR * SH19ASR1 * S20A_ASR1 TO S20HR_INT;



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00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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ASTE	
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f CSTE	APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00152	00228	00153

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

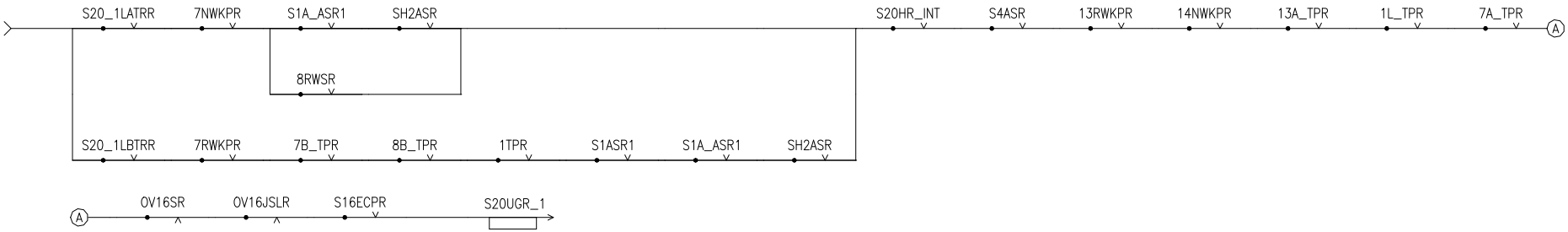
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

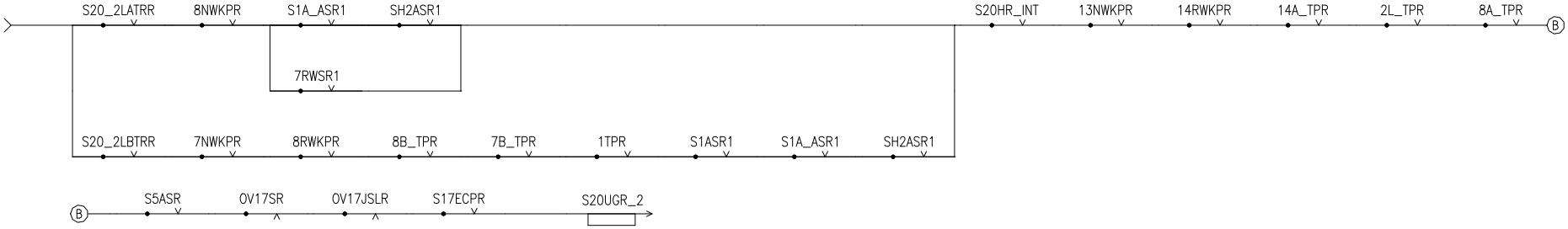
CAD FILE: 512_TAC_C1_00152_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN (S20_1LATRR * 7NWKPR * (S1A_ASR1 * SH2ASR + 8RWSR) + S20_1LBTRR * 7RWKPR * 7B_TPR * 8B_TPR * 1TPR * S1ASR1 * S1A_ASR1 * SH2ASR) * S20HR_INT * S4ASR * 13RWKPR * 14NWKPR * 13A_TPR * 1L_TPR * 7A_TPR * ~OV16SR * ~OV16JSLR * S16ECPR TO S20UGR_1;



ASSIGN (S20_2LATRR * 8NWKPR * (S1A_ASR1 * SH2ASR1 + 7RWSR1) + S20_2LBTRR * 7NWKPR * 8RWKPR * 8B_TPR * 7B_TPR * 1TPR * S1ASR1 * S1A_ASR1 * SH2ASR1) * S20HR_INT * 13NWKPR * 14RWKPR * 14A_TPR * 2L_TPR * 8A_TPR * S5ASR * ~OV17SR * ~OV17JSLR * S17ECPR TO S20UGR_2;



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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
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Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00153	00228	00154

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SIGNAL & TELECOMMUNICATIONS

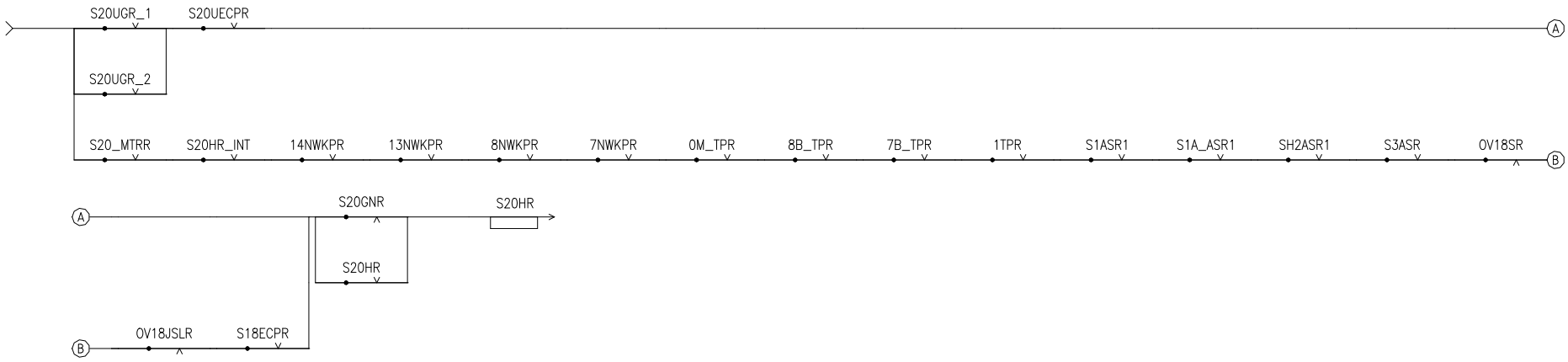
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

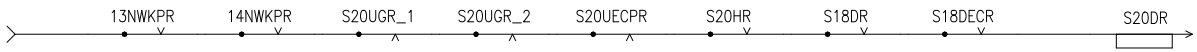
CAD FILE: 512_TAC_C1_00153_R00.DWG

(MICROLOK-II WARM STANDBY)

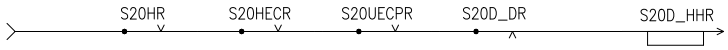
ASSIGN ((S20UGR_1 + S20UGR_2) * S20UECPR + S20_MTRR * S20HR_INT * 14NWKPR * 13NWKPR * 8NWKPR * 7NWKPR * 0M_TPR * 8B_TPR * 7B_TPR * 1TPR * S1ASR1 * S1A_ASR1 * SH2ASR1 * S3ASR * ~0V18SR * ~0V18JSLR * S18ECPR) * (~S20GNR + S20HR) TO S20HR;



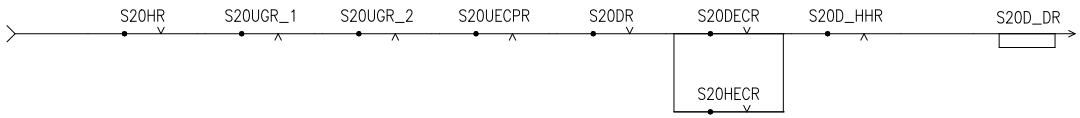
ASSIGN 13NWKPR * 14NWKPR * ~S20UGR_1 * ~S20UGR_2 * ~S20UECPR * S20HR * S18DR * S18DECR TO S20DR;



ASSIGN S20HR * S20HECR * S20UECPR * ~S20D_DR TO S20D_HHR;



ASSIGN S20HR * ~S20UGR_1 * ~S20UGR_2 * ~S20UECPR * S20DR * (S20DECR + S20HECR) * ~S20D_HHR TO S20D_DR;



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#610, "NARAYANA", 1st Floor, 15th Cross,
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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00154 | dqy 'khVSHEETS 00228 | CONT'D ON: 00155

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SIGNAL & TELECOMMUNICATIONS

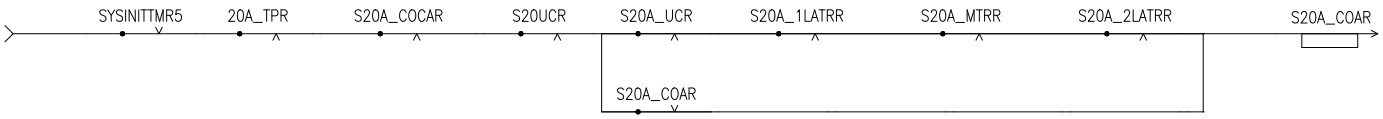
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

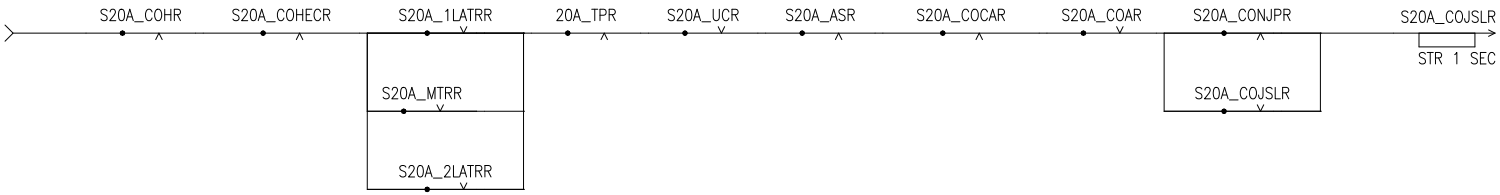
CAD FILE: 512_TAC_C1_00154_R00.DWG

(MICROLOK-II WARM STANDBY)

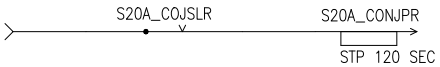
ASSIGN SYSINITTMR5 * ~20A_TPR * ~S20A_COCAR * ~S20UCR * (~S20A_UCR * ~S20A_1LATRR * ~S20A_MTRR * ~S20A_2LATRR + S20A_COAR) TO S20A_COAR;



ASSIGN ~S20A_COHR * ~S20A_COHECR * (S20A_1LATRR + S20A_MTRR + S20A_2LATRR) * ~20A_TPR * S20A_UCR * ~S20A_ASR * ~S20A_COCAR * S20A_COAR * (~S20A_CONJPR + S20A_COJSLR) TO S20A_COJSLR;



ASSIGN S20A_COJSLR TO S20A_CONJPR;



ASSIGN S20A_UCR * ~S20A_ASR * ~S20UYR1 * ~S20UYR2 * ~S20UYR3 * ~S20UYR4 * ~S20UYR5 * ~S20JSLR * ~S20A_COCAR * ~S20A_CPSJSLR * ~20A_TPR * CH2LR * ~CH2FR * 12LXPR * ~12LXFR * ~S6SR * SH19ASR1 * ~S20UCR TO S20A_COHR_INT;

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TVM Signalling & Transportation Systems Pvt. Ltd.
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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f CSTE	APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00155 | dqy 'khVSHEETS 00228 | CONT'D ON: 00156

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SIGNAL & TELECOMMUNICATIONS

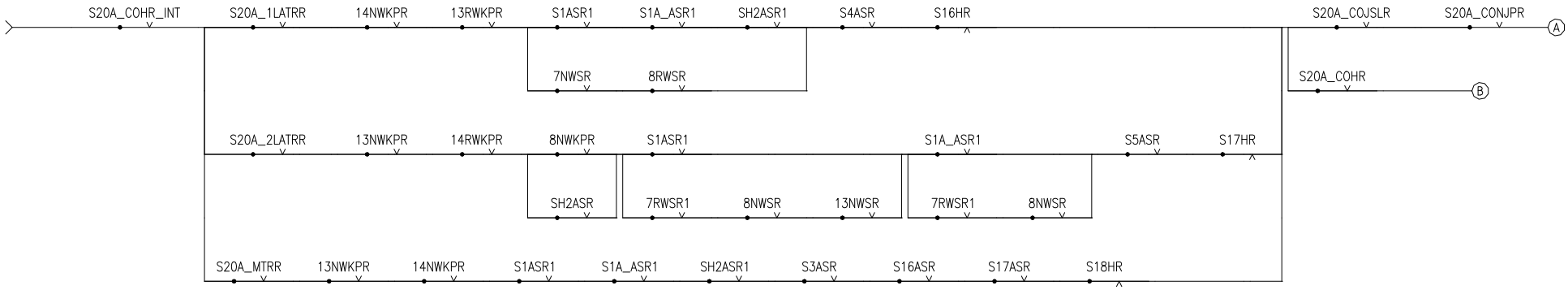
rkfVpøykZ
TATICHERLA

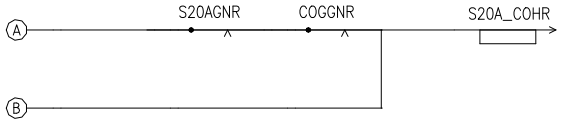
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

(MICROLOK-II WARM STANDBY)

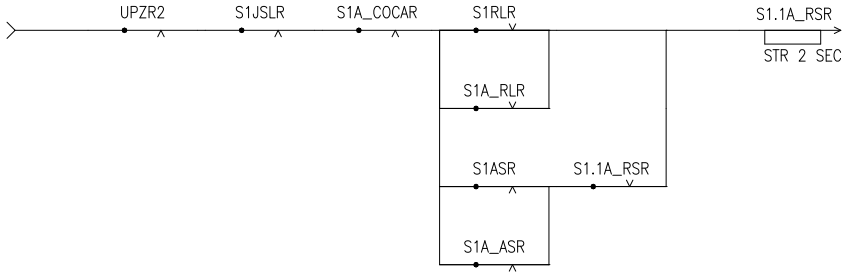
CAD FILE: 512_TAC_C1_00155_R00.DWG

ASSIGN S20A_COHR_INT * (S20A_1LATRR * 14NWKPR * 13RWKPR * (S1ASR1 * S1A_ASR1 * SH2ASR1 + 7NWSR * 8RWSR) * S4ASR * ~S16HR + S20A_2LATRR * 13NWKPR * 14RWKPR * (8NWKPR + SH2ASR) * (S1ASR1 + 7RWSR1 * 8NWSR * 13NWSR) * (S1A_ASR1 + 7RWSR1 * 8NWSR) * S5ASR * ~S17HR + S20A_MTRR * 13NWKPR * 14NWKPR * S1ASR1 * S1A_ASR1 * SH2ASR1 * S3ASR * S16ASR * S17ASR * ~S18HR) * (S20A_COJSLR * S20A_CONJPR * ~S20AGNR * ~COGGNR + S20A_COHR) TO S20A_COHR;

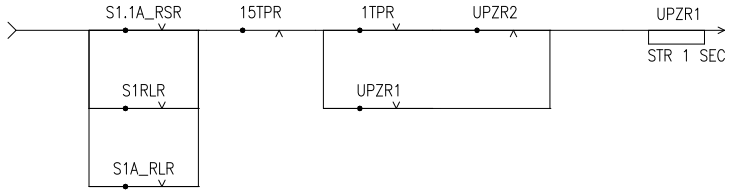




ASSIGN ~UPZR2 * ~S1JSLR * ~S1A_COCAR * (S1RLR + S1A_RLR + (~S1ASR + ~S1A_ASR) * S1.1A_RSR) TO S1.1A_RSR;



ASSIGN (S1.1A_RSR + S1RLR + S1A_RLR) * ~15TPR * (1TPR * ~UPZR2 + UPZR1) TO UPZR1;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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f CSTE	APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khVSHEET 00156 | dqy 'khVSHEETS 00228 | CONT'D ON: 00157

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

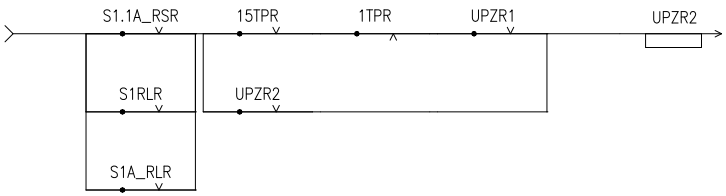
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

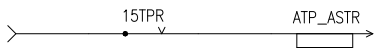
CAD FILE: 512_TAC_C1_00156_R00.DWG

(MICROLOK-II WARM STANDBY)

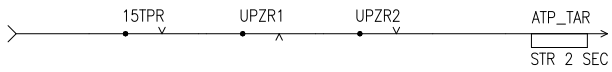
ASSIGN (S1.1A_RSR + S1RLR + S1A_RLR) * (15TPR * ~1TPR * UPZR1 + UPZR2) TO UPZR2;



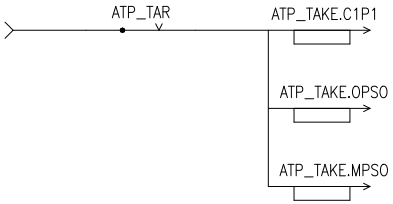
ASSIGN 15TPR TO ATP_ASTR;



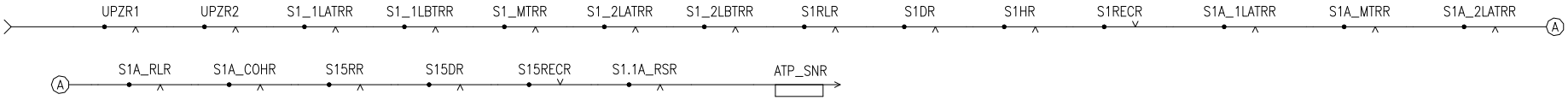
ASSIGN 15TPR * ~UPZR1 * UPZR2 TO ATP_TAR;



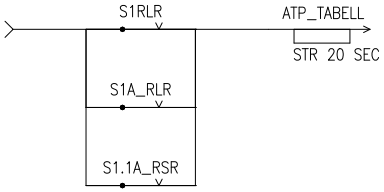
ASSIGN ATP_TAR TO
ATP_TAKE.C1P1,ATP_TAKE.OPSO,ATP_TAKE.MPSO;



ASSIGN ~UPZR1 * ~UPZR2 * ~S1_1LATRR * ~S1_1LBTRR * ~S1_MTRR * ~S1_2LATRR * ~S1_2LBTRR * ~S1RLR * ~S1DR * ~S1HR * S1RECR * ~S1A_1LATRR * ~S1A_MTRR * ~S1A_2LATRR * ~S1A_RLR * ~S1A_COHR * ~S15RR * ~S15DR * S15RECR *
~S1.1A_RSR TO ATP_SNR;



ASSIGN S1RLR + S1A_RLR + S1.1A_RSR TO
ATP_TABELL;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

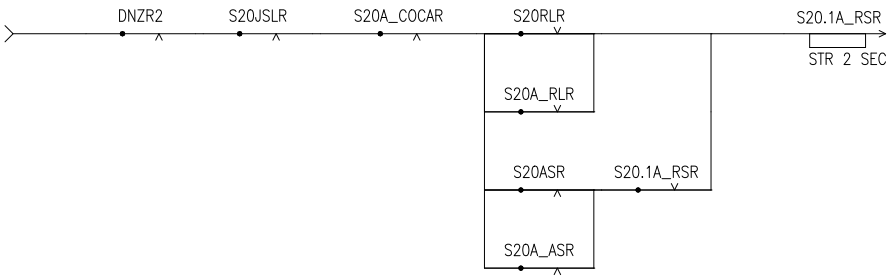
REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khVSHEET 00157 | dqy 'khVSHEETS 00228 | CONT'D ON: 00158

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

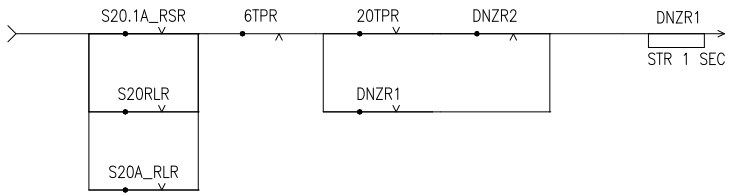
CAD FILE: 512_TAC_C1_00157_R00.DWG

(MICROLOK-II WARM STANDBY)

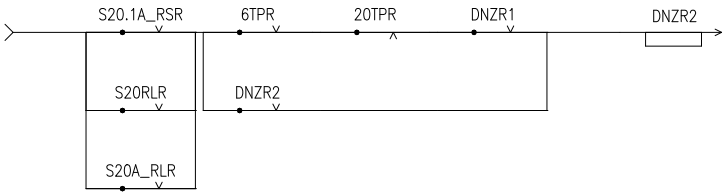
ASSIGN ~DNZR2 * ~S20JSLR * ~S20A_COCAR * (S20RLR + S20A_RLR + (~S20ASR + ~S20A_ASR) * S20.1A_RSR) TO S20.1A_RSR;



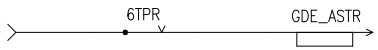
ASSIGN (S20.1A_RSR + S20RLR + S20A_RLR) * ~6TPR * (20TPR * ~DNZR2 + DNZR1) TO DNZR1;



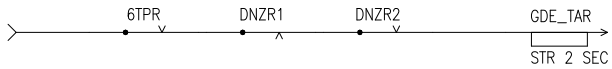
ASSIGN (S20.1A_RSR + S20RLR + S20A_RLR) * (6TPR * ~20TPR * DNZR1 + DNZR2) TO DNZR2;



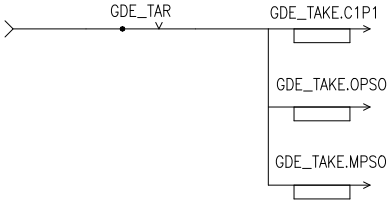
ASSIGN 6TPR TO GDE_ASTR;



ASSIGN 6TPR * ~DNZR1 * DNZR2 TO GDE_TAR;



ASSIGN GDE_TAR TO GDE_TAKE.C1P1,GDE_TAKE.OPSO,GDE_TAKE.MPSO;



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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
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SE/SSE	
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SSTE	
f CSTE	

CHK.

APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00158 | dqy 'khVSHEETS 00228 | CONT'D ON: 00159

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

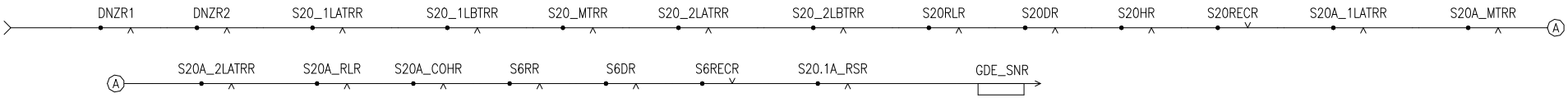
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

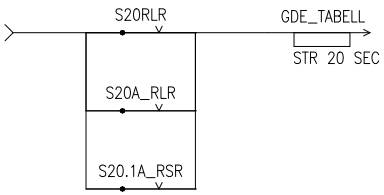
CAD FILE: 512_TAC_C1_00158_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN ~DNZR1 * ~DNZR2 * ~S20_1LATRR * ~S20_1LBTRR * ~S20_MTRR * ~S20_2LATRR * ~S20_2LBTRR * ~S20RLR * ~S20DR * ~S20HR * S20RECR * ~S20A_1LATRR * ~S20A_MTRR * ~S20A_2LATRR * ~S20A_RLR * ~S20A_COHR * ~S6RR * ~S6DR * S6RECR * ~S20.1A_RSR TO GDE_SNR;



ASSIGN S20RLR + S20A_RLR + S20.1A_RSR TO GDE_TABELL;



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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00159 | dqy 'khVSHEETS 00228 | CONT'D ON: 00160

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00159_R00.DWG

(MICROLOK-II WARM STANDBY)

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ASSIGN  (~S1D_DR * S1D_DR_F + ~S1D_HHR * S1D_HHR_F + ~S1DR * S1DR_F +
~S1HR * S1HR_F + ~S1UGR_1 * S1_UGR1_F + ~S1UGR_2 * S1_UGR2_F +
~S1A_COHR * S1A_COHR_F + ~SH2HR * SH2HR_F + ~S15DR * S15DR_F + ~S16HR *
S16HR_F + ~S17HR * S17HR_F + ~S18DR * S18DR_F + ~S18HR * S18HR_F) *
CPS.STATUS TO  FCOR_INT;
```

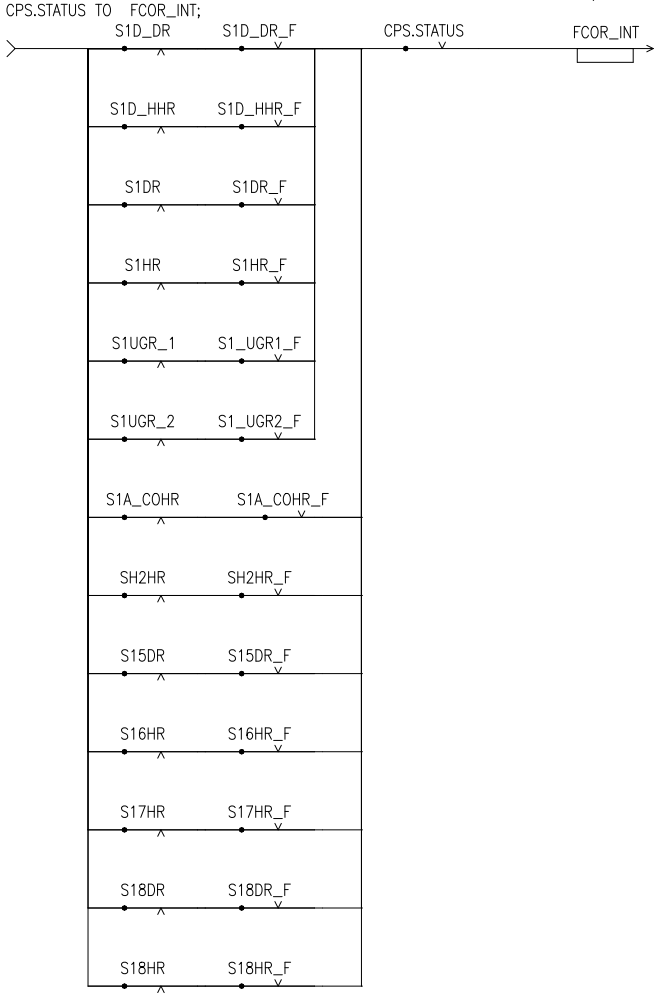

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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00160 | dqy 'khVSHEETS 00228 | CONT'D ON: 00161

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

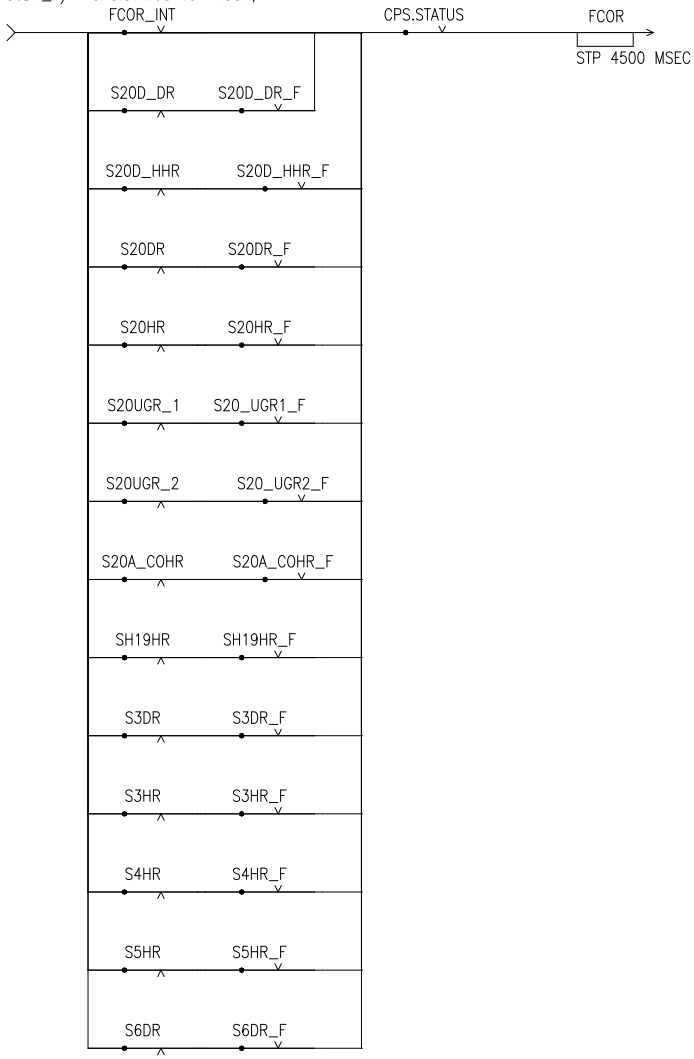
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

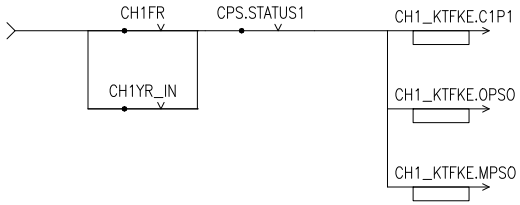
CAD FILE: 512_TAC_C1_00160_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN (FCOR_INT + ~S20D_DR * S20D_DR_F + ~S20D_HHR * S20D_HHR_F + ~S20DR * S20DR_F + ~S20HR * S20HR_F + ~S20UGR_1 * S20_UGR1_F + ~S20UGR_2 * S20_UGR2_F + ~S20A_COHR * S20A_COHR_F + ~SH19HR * SH19HR_F + ~S3DR * S3DR_F + ~S3HR * S3HR_F + ~S4HR * S4HR_F + ~S5HR * S5HR_F + ~S6DR * S6DR_F) * CPS.STATUS TO FCOR;



ASSIGN (CH1FR + CH1YR_IN) * CPS.STATUS1 TO CH1_KTFKE.C1P1,CH1_KTFKE.OPSO,CH1_KTFKE.MPSO;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO a fc B

'khVSHEET 00161 | dqy 'khVSHEETS 00228 | CONT'D ON: 00162

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

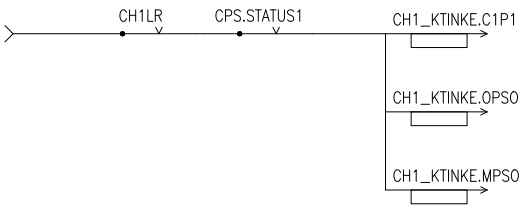
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

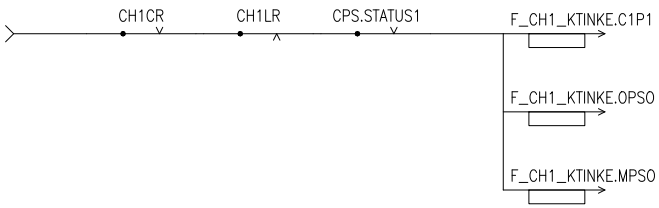
CAD FILE: 512_TAC_C1_00161_R00.DWG

(MICROLOK-II WARM STANDBY)

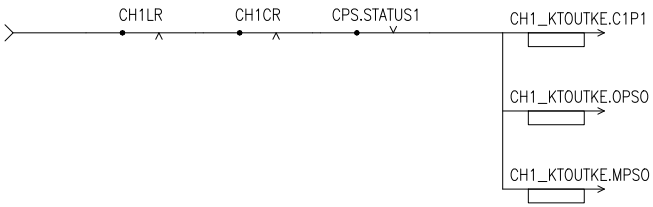
ASSIGN CH1LR * CPS.STATUS1 TO
CH1_KTINKE.C1P1,CH1_KTINKE.OPS0,CH1_KTINKE.MPS0;



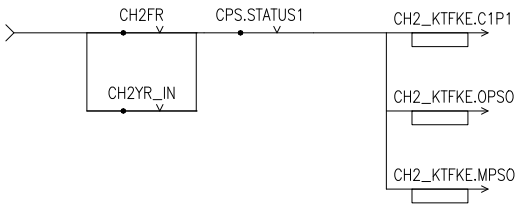
ASSIGN CH1CR * ~CH1LR * CPS.STATUS1 TO
F_CH1_KTINKE.C1P1,F_CH1_KTINKE.OPS0,F_CH1_KTINKE.MPS0;



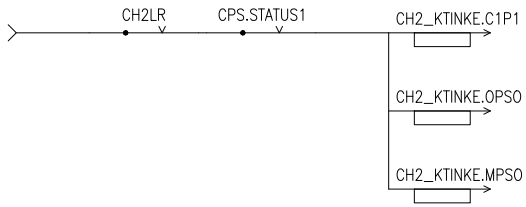
ASSIGN ~CH1LR * ~CH1CR * CPS.STATUS1 TO
CH1_KTOUTKE.C1P1,CH1_KTOUTKE.OPS0,CH1_KTOUTKE.MPS0;



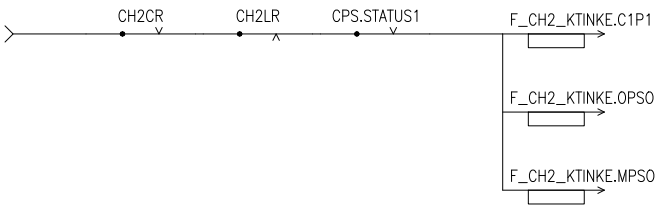
ASSIGN (CH2FR + CH2YR_IN) * CPS.STATUS1 TO
CH2_KTFKE.C1P1,CH2_KTFKE.OPS0,CH2_KTFKE.MPS0;



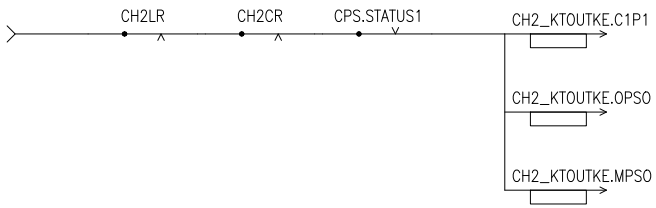
ASSIGN CH2LR * CPS.STATUS1 TO
CH2_KTINKE.C1P1,CH2_KTINKE.OPS0,CH2_KTINKE.MPS0;



ASSIGN CH2CR * ~CH2LR * CPS.STATUS1 TO
F_CH2_KTINKE.C1P1,F_CH2_KTINKE.OPS0,F_CH2_KTINKE.MPS0;



ASSIGN ~CH2LR * ~CH2CR * CPS.STATUS1 TO
CH2_KTOUTKE.C1P1,CH2_KTOUTKE.OPS0,CH2_KTOUTKE.MPS0;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP:

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00162	00228	00163

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SIGNAL & TELECOMMUNICATIONS

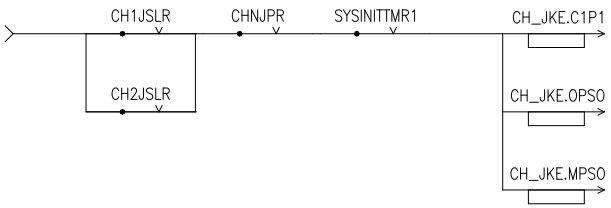
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

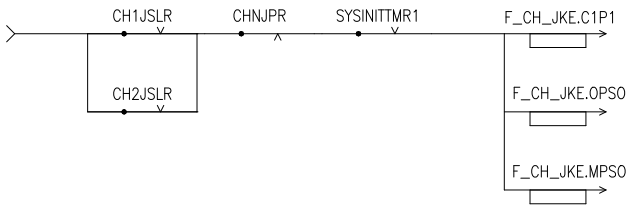
CAD FILE: 512_TAC_C1_00162_R00.DWG

(MICROLOK-II WARM STANDBY)

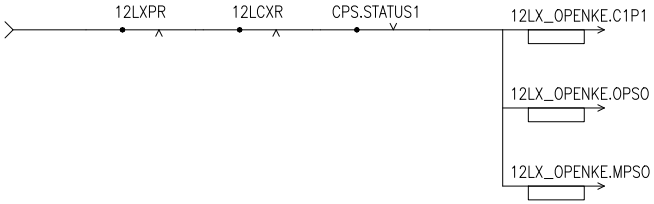
ASSIGN (CH1JSLR + CH2JSLR) * CHNJPR * SYSINITTMR1 TO
CH_JKE.C1P1,CH_JKE.OPS0,CH_JKE.MPS0;



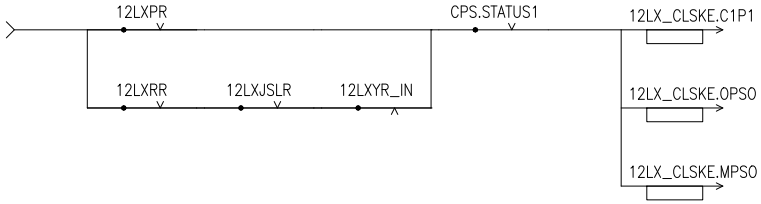
ASSIGN (CH1JSLR + CH2JSLR) * ~CHNJPR * SYSINITTMR1 TO
F_CH_JKE.C1P1,F_CH_JKE.OPS0,F_CH_JKE.MPS0;



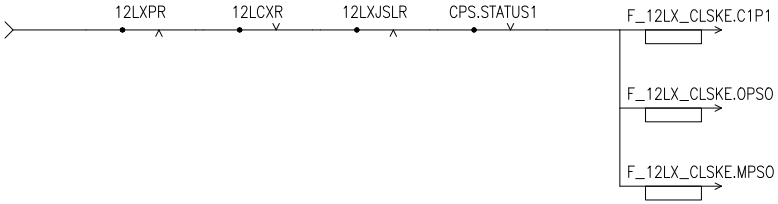
ASSIGN ~12LXPR * ~12LCXR * CPS.STATUS1 TO
12LX_OPENKE.C1P1,12LX_OPENKE.OPS0,12LX_OPENKE.MPS0;



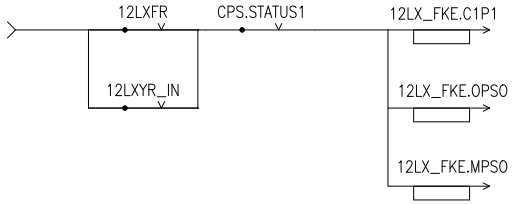
ASSIGN (12LXPR + 12LXRR * 12LXJSLR * ~12LXYR_IN) * CPS.STATUS1 TO
12LX_CLSKE.C1P1,12LX_CLSKE.OPS0,12LX_CLSKE.MPS0;



ASSIGN ~12LXPR * 12LCXR * ~12LXJSLR * CPS.STATUS1 TO
F_12LX_CLSKE.C1P1,F_12LX_CLSKE.OPS0,F_12LX_CLSKE.MPS0;



ASSIGN (12LXFR + 12LXYR_IN) * CPS.STATUS1 TO
12LX_FKE.C1P1,12LX_FKE.OPS0,12LX_FKE.MPS0;



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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khVSHEET 00163 | dqy 'khVSHEETS 00228 | CONT'D ON: 00164

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

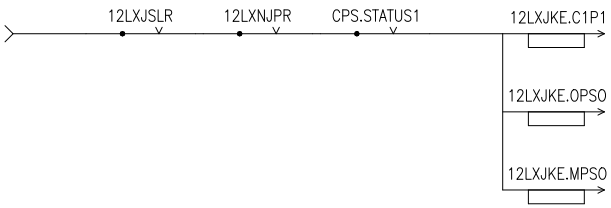
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

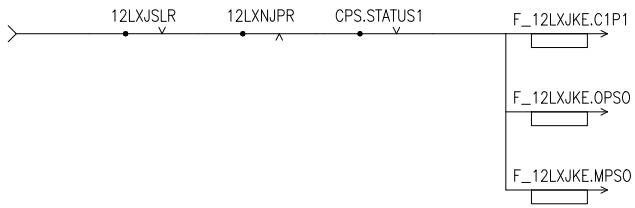
CAD FILE: 512_TAC_C1_00163_R00.DWG

(MICROLOK-II WARM STANDBY)

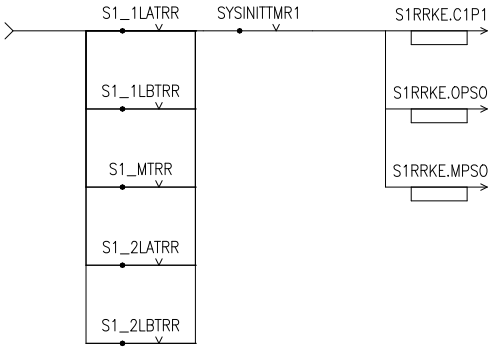
ASSIGN 12LXJSLR * 12LXNJPR * CPS.STATUS1 TO
12LXJKE.C1P1,12LXJKE.OPS0,12LXJKE.MPS0;



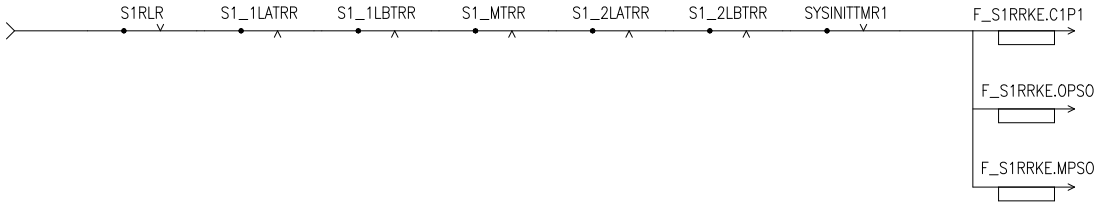
ASSIGN 12LXJSLR * ~12LXNJPR * CPS.STATUS1 TO
F_12LXJKE.C1P1,F_12LXJKE.OPS0,F_12LXJKE.MPS0;



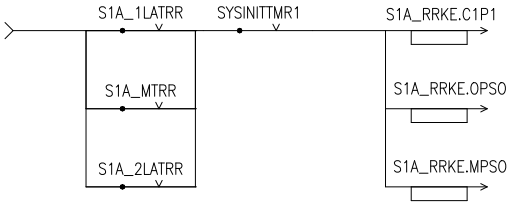
ASSIGN (S1_1LATRR + S1_1LBTRR + S1_MTRR + S1_2LATRR +
S1_2LBTRR) * SYSINITTMR1 TO
S1RRKE.C1P1,S1RRKE.OPS0,S1RRKE.MPS0;



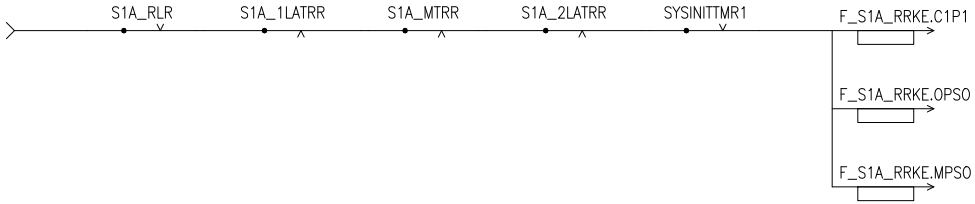
ASSIGN S1RLR * ~S1_1LATRR * ~S1_1LBTRR * ~S1_MTRR * ~S1_2LATRR * ~S1_2LBTRR * SYSINITTMR1 TO
F_S1RRKE.C1P1,F_S1RRKE.OPS0,F_S1RRKE.MPS0;



ASSIGN (S1A_1LATRR + S1A_MTRR + S1A_2LATRR) * SYSINITTMR1
TO S1A_RRKE.C1P1,S1A_RRKE.OPS0,S1A_RRKE.MPS0;



ASSIGN S1A_RLR * ~S1A_1LATRR * ~S1A_MTRR * ~S1A_2LATRR * SYSINITTMR1 TO
F_S1A_RRKE.C1P1,F_S1A_RRKE.OPS0,F_S1A_RRKE.MPS0;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khVSHEET	dqy 'khVSHEETS	CONT'D ON:
00164	00228	00165

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

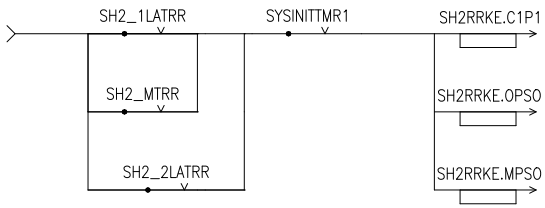
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

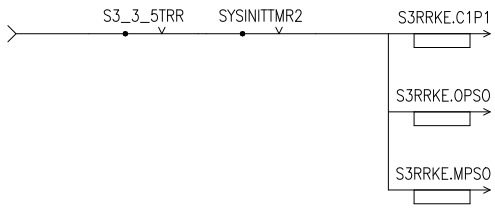
CAD FILE: 512_TAC_C1_00164_R00.DWG

(MICROLOK-II WARM STANDBY)

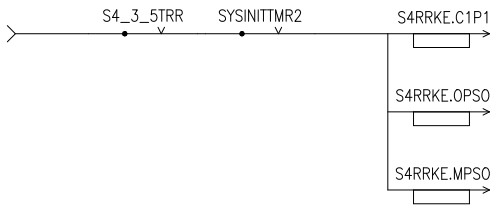
ASSIGN (SH2_1LATRR + SH2_MTRR + SH2_2LATRR) * SYSINITTMR1 TO
SH2RRKE.C1P1,SH2RRKE.OPS0,SH2RRKE.MPS0;



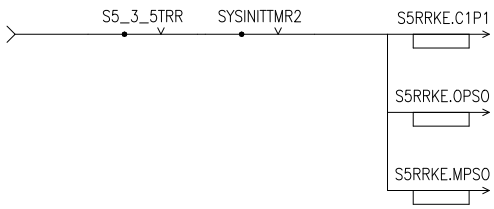
ASSIGN S3_3_5TRR * SYSINITTMR2 TO
S3RRKE.C1P1,S3RRKE.OPS0,S3RRKE.MPS0;



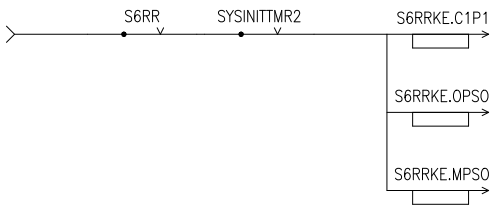
ASSIGN S4_3_5TRR * SYSINITTMR2 TO
S4RRKE.C1P1,S4RRKE.OPS0,S4RRKE.MPS0;



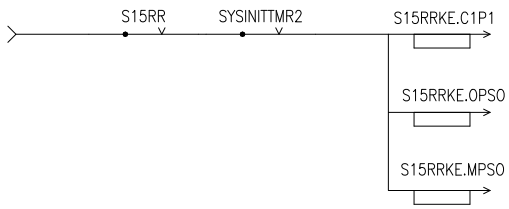
ASSIGN S5_3_5TRR * SYSINITTMR2 TO
S5RRKE.C1P1,S5RRKE.OPS0,S5RRKE.MPS0;



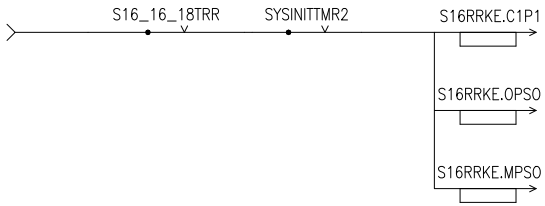
ASSIGN S6RR * SYSINITTMR2 TO
S6RRKE.C1P1,S6RRKE.OPS0,S6RRKE.MPS0;



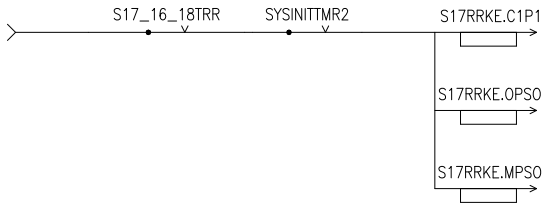
ASSIGN S15RR * SYSINITTMR2 TO
S15RRKE.C1P1,S15RRKE.OPS0,S15RRKE.MPS0;



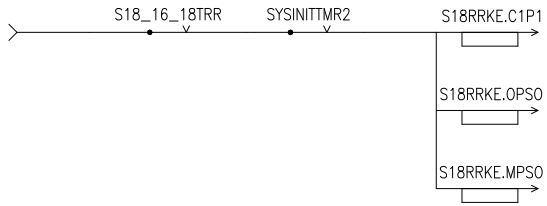
ASSIGN S16_16_18TRR * SYSINITTMR2 TO
S16RRKE.C1P1,S16RRKE.OPS0,S16RRKE.MPS0;



ASSIGN S17_16_18TRR * SYSINITTMR2 TO
S17RRKE.C1P1,S17RRKE.OPS0,S17RRKE.MPS0;



ASSIGN S18_16_18TRR * SYSINITTMR2 TO
S18RRKE.C1P1,S18RRKE.OPS0,S18RRKE.MPS0;



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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION
JE-1
SE/SSE
ASTE
SSTE
f CSTE

CHK.

APP:

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B | 'khV SHEET 00165 | dqy 'khVSHEETS 00228 | CONT'D ON: 00166

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

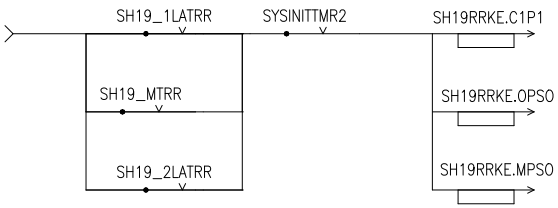
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

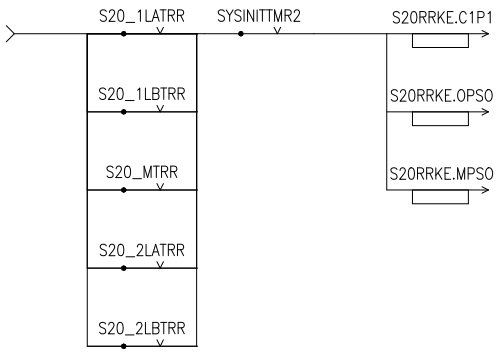
CAD FILE: 512_TAC_C1_00165_R00.DWG

(MICROLOK-II WARM STANDBY)

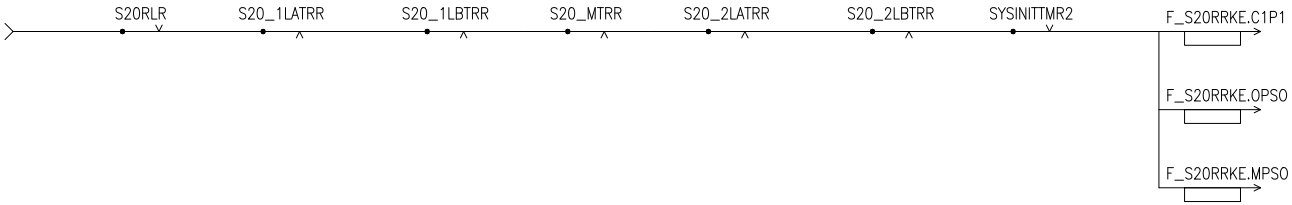
ASSIGN (SH19_1LATRR + SH19_MTRR + SH19_2LATRR) * SYSINITTMR2 TO SH19RRKE.C1P1,SH19RRKE.OPS0,SH19RRKE.MPS0;



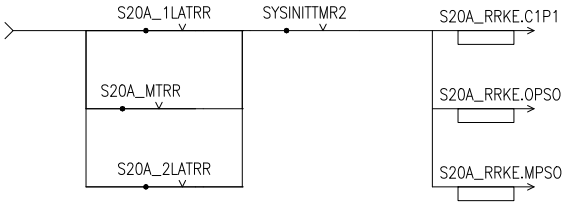
ASSIGN (S20_1LATRR + S20_1LBTRR + S20_MTRR + S20_2LATRR + S20_2LBTRR) * SYSINITTMR2 TO S20RRKE.C1P1,S20RRKE.OPS0,S20RRKE.MPS0;



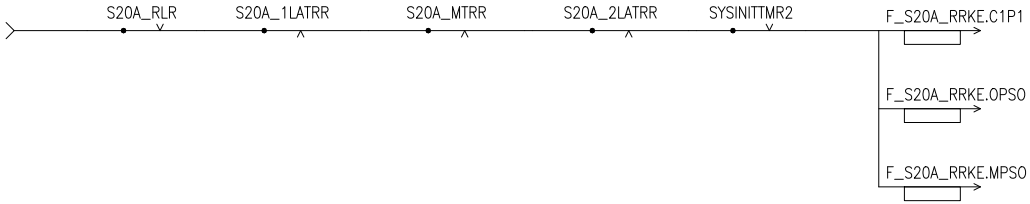
ASSIGN S20RLR * ~S20_1LATRR * ~S20_1LBTRR * ~S20_MTRR * ~S20_2LATRR * ~S20_2LBTRR * SYSINITTMR2 TO F_S20RRKE.C1P1,F_S20RRKE.OPS0,F_S20RRKE.MPS0;



ASSIGN (S20A_1LATRR + S20A_MTRR + S20A_2LATRR) * SYSINITTMR2 TO S20A_RRKE.C1P1,S20A_RRKE.OPS0,S20A_RRKE.MPS0;



ASSIGN S20A_RLR * ~S20A_1LATRR * ~S20A_MTRR * ~S20A_2LATRR * SYSINITTMR2 TO F_S20A_RRKE.C1P1,F_S20A_RRKE.OPS0,F_S20A_RRKE.MPS0;



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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	

CHK.

APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B	n e jsyos vkjsjo S.C.RLY.DRG.NO	a fc B	
'khV SHEET 00166	dqy 'khVSHEETS 00228	CONT'D ON: 00167	

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

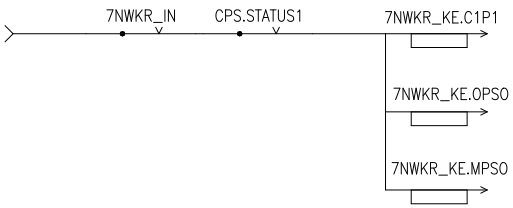
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

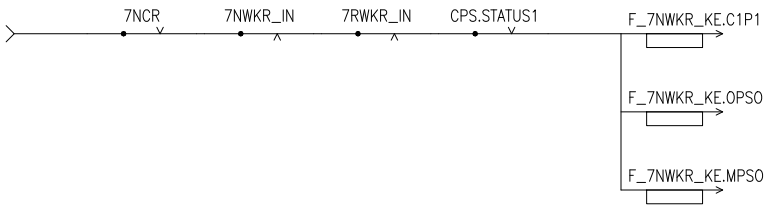
CAD FILE: 512_TAC_C1_00166_R00.DWG

(MICROLOK-II WARM STANDBY)

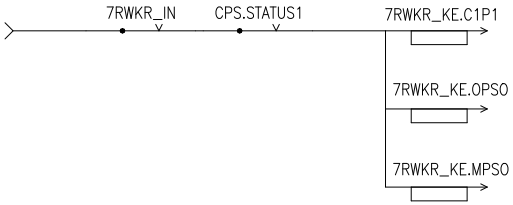
ASSIGN 7NWKR_IN * CPS.STATUS1 TO
7NWKR_KE.C1P1,7NWKR_KE.OPS0,7NWKR_KE.MPS0;



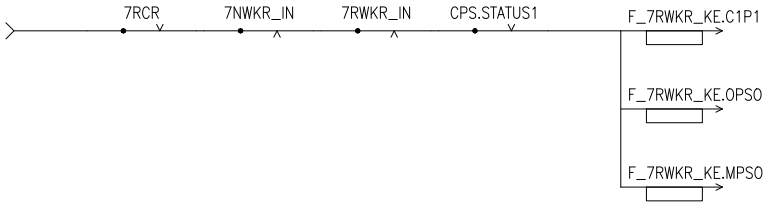
ASSIGN 7NCR * ~7NWKR_IN * ~7RWKR_IN * CPS.STATUS1 TO
F_7NWKR_KE.C1P1,F_7NWKR_KE.OPS0,F_7NWKR_KE.MPS0;



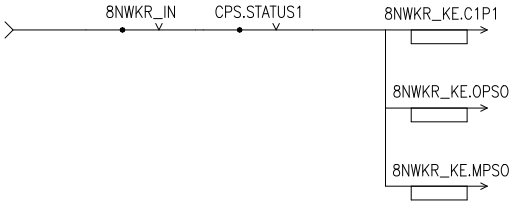
ASSIGN 7RWKR_IN * CPS.STATUS1 TO
7RWKR_KE.C1P1,7RWKR_KE.OPS0,7RWKR_KE.MPS0;



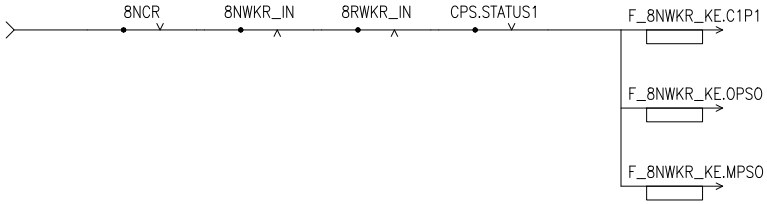
ASSIGN 7RCR * ~7NWKR_IN * ~7RWKR_IN * CPS.STATUS1 TO
F_7RWKR_KE.C1P1,F_7RWKR_KE.OPS0,F_7RWKR_KE.MPS0;



ASSIGN 8NWKR_IN * CPS.STATUS1 TO
8NWKR_KE.C1P1,8NWKR_KE.OPS0,8NWKR_KE.MPS0;



ASSIGN 8NCR * ~8NWKR_IN * ~8RWKR_IN * CPS.STATUS1 TO
F_8NWKR_KE.C1P1,F_8NWKR_KE.OPS0,F_8NWKR_KE.MPS0;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	a fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00167	00228	00168

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

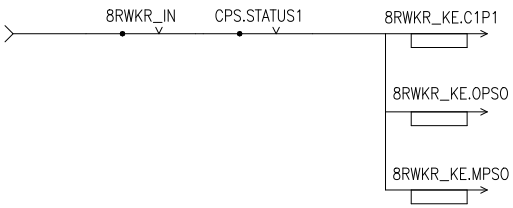
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

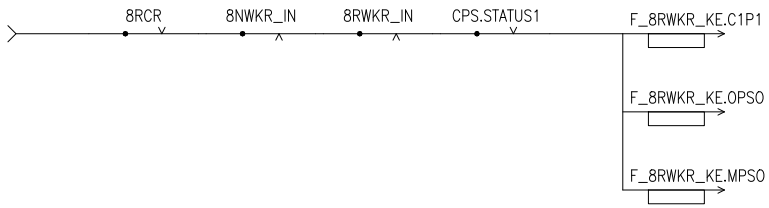
CAD FILE: 512_TAC_C1_00167_R00.DWG

(MICROLOK-II WARM STANDBY)

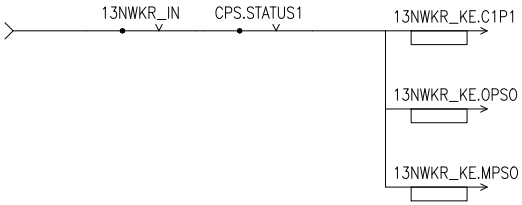
ASSIGN 8RWKR_IN * CPS.STATUS1 TO
8RWKR_KE.C1P1,8RWKR_KE.OPS0,8RWKR_KE.MPS0;



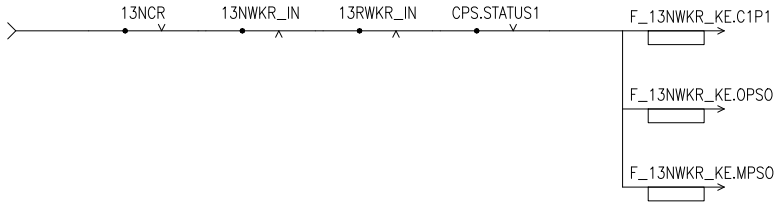
ASSIGN 8RCR * ~8NWKR_IN * ~8RWKR_IN * CPS.STATUS1 TO
F_8RWKR_KE.C1P1,F_8RWKR_KE.OPS0,F_8RWKR_KE.MPS0;



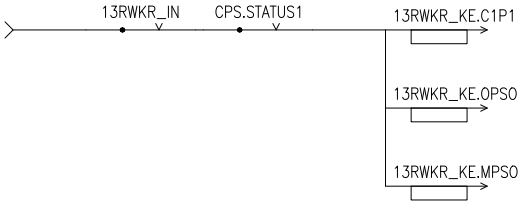
ASSIGN 13NWKR_IN * CPS.STATUS1 TO
13NWKR_KE.C1P1,13NWKR_KE.OPS0,13NWKR_KE.MPS0;



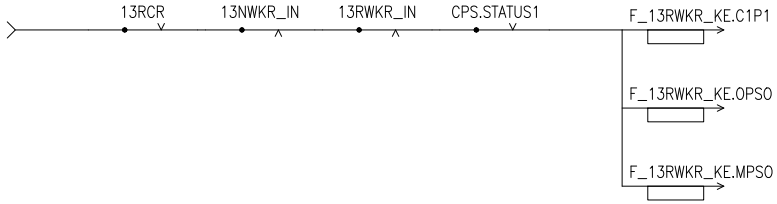
ASSIGN 13NCR * ~13NWKR_IN * ~13RWKR_IN * CPS.STATUS1 TO
F_13NWKR_KE.C1P1,F_13NWKR_KE.OPS0,F_13NWKR_KE.MPS0;



ASSIGN 13RWKR_IN * CPS.STATUS1 TO
13RWKR_KE.C1P1,13RWKR_KE.OPS0,13RWKR_KE.MPS0;



ASSIGN 13RCR * ~13NWKR_IN * ~13RWKR_IN * CPS.STATUS1 TO
F_13RWKR_KE.C1P1,F_13RWKR_KE.OPS0,F_13RWKR_KE.MPS0;



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00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00168	00228	00169

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

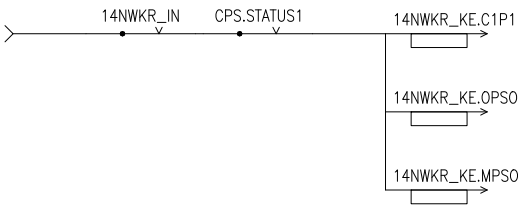
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

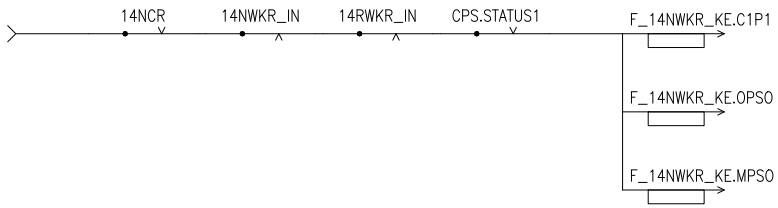
CAD FILE: 512_TAC_C1_00168_R00.DWG

(MICROLOK-II WARM STANDBY)

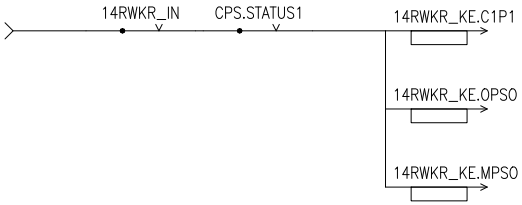
ASSIGN 14NWKR_IN * CPS.STATUS1 TO
14NWKR_KE.C1P1,14NWKR_KE.OPS0,14NWKR_KE.MPS0;



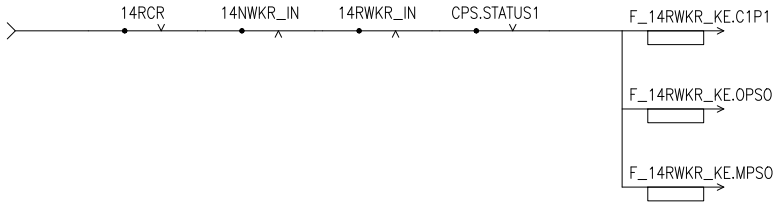
ASSIGN 14NCR * ~14NWKR_IN * ~14RWKR_IN * CPS.STATUS1 TO
F_14NWKR_KE.C1P1,F_14NWKR_KE.OPS0,F_14NWKR_KE.MPS0;



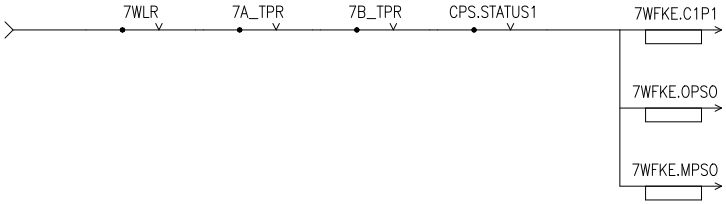
ASSIGN 14RWKR_IN * CPS.STATUS1 TO
14RWKR_KE.C1P1,14RWKR_KE.OPS0,14RWKR_KE.MPS0;



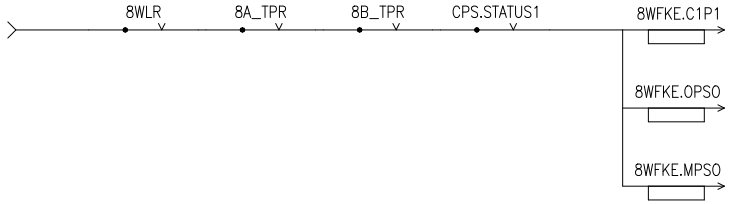
ASSIGN 14RCR * ~14NWKR_IN * ~14RWKR_IN * CPS.STATUS1 TO
F_14RWKR_KE.C1P1,F_14RWKR_KE.OPS0,F_14RWKR_KE.MPS0;



ASSIGN 7WLR * 7A_TPR * 7B_TPR * CPS.STATUS1 TO 7WFKE.C1P1,7WFKE.OPS0,7WFKE.MPS0;



ASSIGN 8WLR * 8A_TPR * 8B_TPR * CPS.STATUS1 TO 8WFKE.C1P1,8WFKE.OPS0,8WFKE.MPS0;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
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TVM Signalling & Transportation Systems Pvt. Ltd.
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00169	00228	00170

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

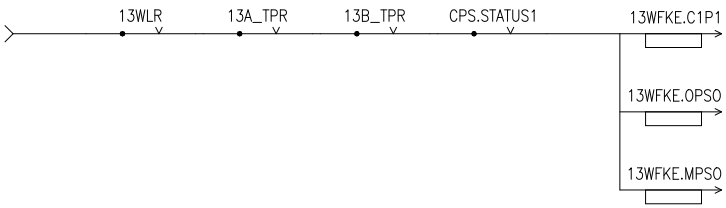
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

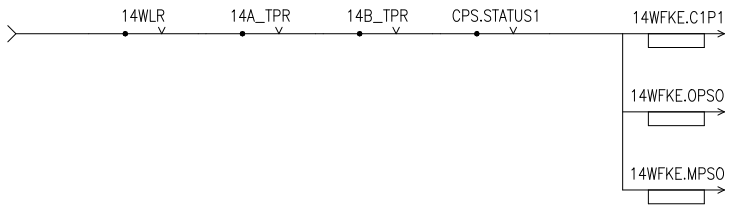
CAD FILE: 512_TAC_C1_00169_R00.DWG

(MICROLOK-II WARM STANDBY)

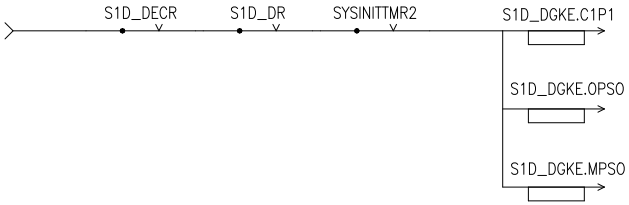
ASSIGN 13WLR * 13A_TPR * 13B_TPR * CPS.STATUS1 TO
13WFKE.C1P1,13WFKE.OPS0,13WFKE.MPS0;



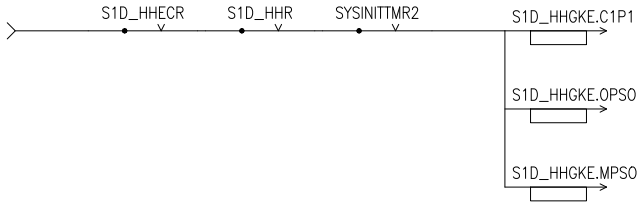
ASSIGN 14WLR * 14A_TPR * 14B_TPR * CPS.STATUS1 TO
14WFKE.C1P1,14WFKE.OPS0,14WFKE.MPS0;



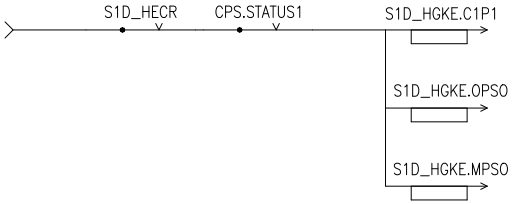
ASSIGN S1D_DECR * S1D_DR * SYSINITTMR2 TO
S1D_DGKE.C1P1,S1D_DGKE.OPS0,S1D_DGKE.MPS0;



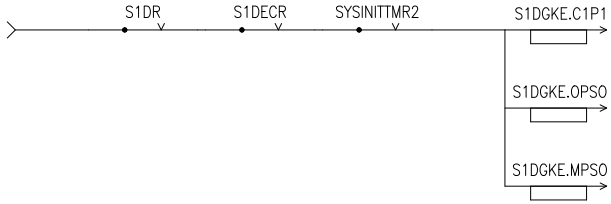
ASSIGN S1D_HHECR * S1D_HHR * SYSINITTMR2 TO
S1D_HHGKE.C1P1,S1D_HHGKE.OPS0,S1D_HHGKE.MPS0;



ASSIGN S1D_HECR * CPS.STATUS1 TO
S1D_HGKE.C1P1,S1D_HGKE.OPS0,S1D_HGKE.MPS0;



ASSIGN S1DR * S1DECR * SYSINITTMR2 TO
S1DGKE.C1P1,S1DGKE.OPS0,S1DGKE.MPS0;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
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CHK.

APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B	
'khV SHEET 00170	dqy 'khVSHEETS 00228	CONT'D ON: 00171	

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

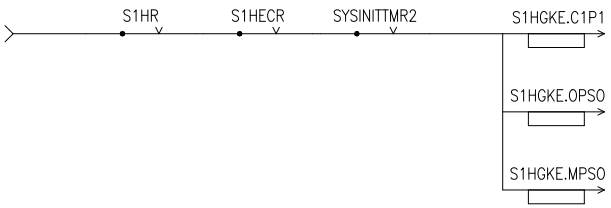
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

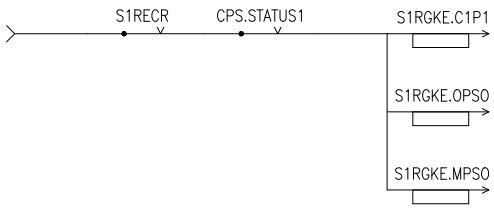
CAD FILE: 512_TAC_C1_00170_R00.DWG

(MICROLOK-II WARM STANDBY)

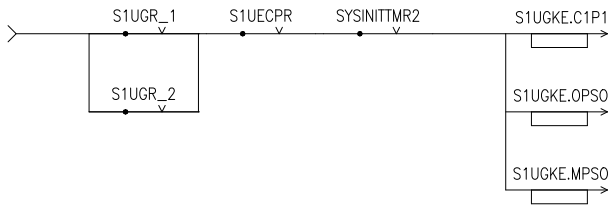
ASSIGN S1HR * S1HECR * SYSINITTMR2 TO
S1HGKE.C1P1,S1HGKE.OPSO,S1HGKE.MPSO;



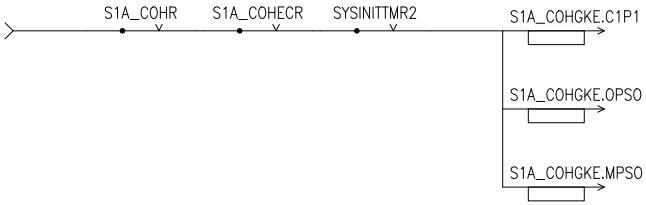
ASSIGN S1RECR * CPS.STATUS1 TO
S1RGKE.C1P1,S1RGKE.OPSO,S1RGKE.MPSO;



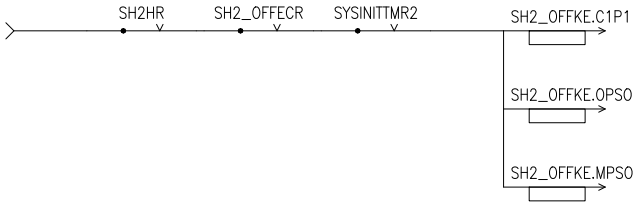
ASSIGN (S1UGR_1 + S1UGR_2) * S1UECPR * SYSINITTMR2 TO
S1UGKE.C1P1,S1UGKE.OPSO,S1UGKE.MPSO;



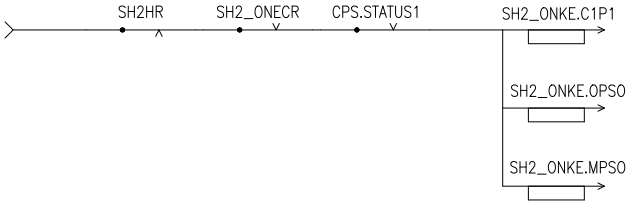
ASSIGN S1A_COHR * S1A_COHECR * SYSINITTMR2 TO
S1A_COHGKE.C1P1,S1A_COHGKE.OPSO,S1A_COHGKE.MPSO;



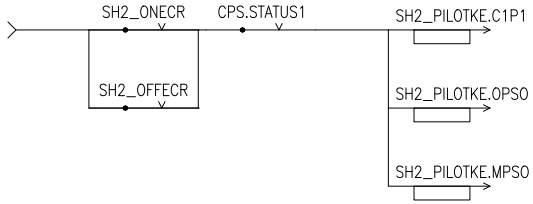
ASSIGN SH2HR * SH2_OFFECR * SYSINITTMR2 TO
SH2_OFFKE.C1P1,SH2_OFFKE.OPSO,SH2_OFFKE.MPSO;



ASSIGN ~SH2HR * SH2_ONECR * CPS.STATUS1 TO
SH2_ONKE.C1P1,SH2_ONKE.OPSO,SH2_ONKE.MPSO;



ASSIGN (SH2_ONECR + SH2_OFFECR) * CPS.STATUS1 TO
SH2_PILOTKE.C1P1,SH2_PILOTKE.OPSO,SH2_PILOTKE.MPSO;



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TVM Signalling & Transportation Systems Pvt. Ltd.
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JE-1	
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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

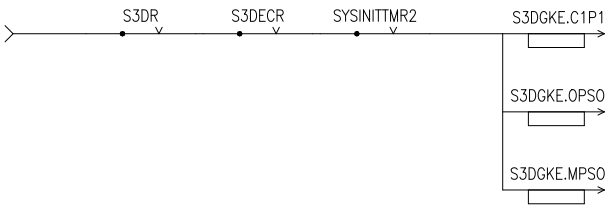
REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO a fc B
'khV SHEET 00171 | dqy 'khVSHEETS 00228 | CONT'D ON: 00172

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

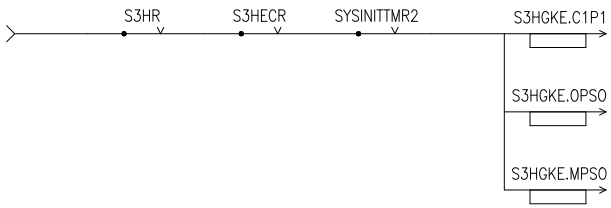
CAD FILE: 512_TAC_C1_00171_R00.DWG

(MICROLOK-II WARM STANDBY)

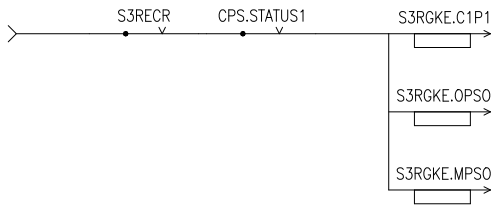
ASSIGN S3DR * S3DECR * SYSINITTMR2 TO
S3DGKE.C1P1,S3DGKE.OPS0,S3DGKE.MPS0;



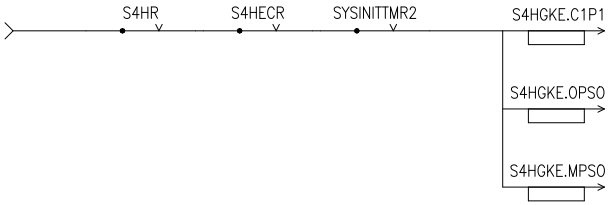
ASSIGN S3HR * S3HECR * SYSINITTMR2 TO
S3HGKE.C1P1,S3HGKE.OPS0,S3HGKE.MPS0;



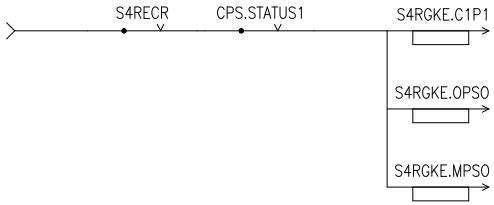
ASSIGN S3RECR * CPS.STATUS1 TO
S3RGKE.C1P1,S3RGKE.OPS0,S3RGKE.MPS0;



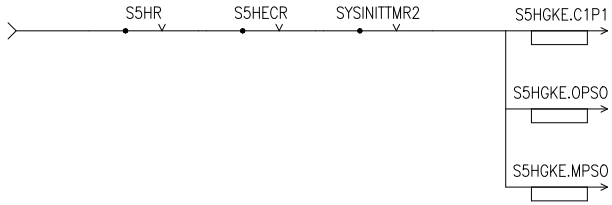
ASSIGN S4HR * S4HECR * SYSINITTMR2 TO
S4HGKE.C1P1,S4HGKE.OPS0,S4HGKE.MPS0;



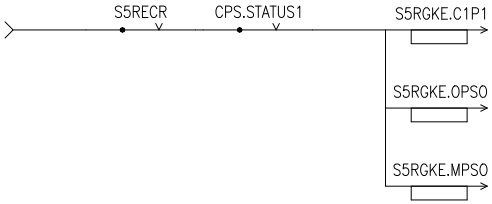
ASSIGN S4RECR * CPS.STATUS1 TO
S4RGKE.C1P1,S4RGKE.OPS0,S4RGKE.MPS0;



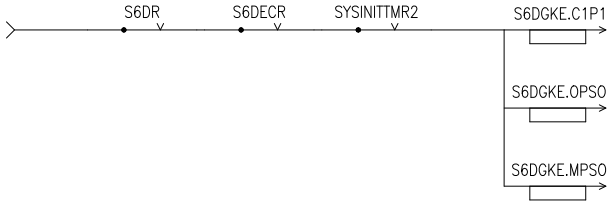
ASSIGN S5HR * S5HECR * SYSINITTMR2 TO
S5HGKE.C1P1,S5HGKE.OPS0,S5HGKE.MPS0;



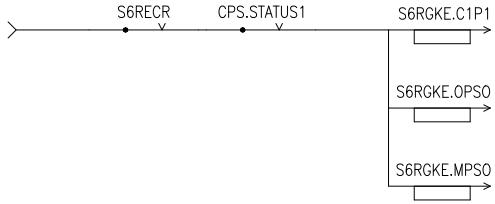
ASSIGN S5RECR * CPS.STATUS1 TO
S5RGKE.C1P1,S5RGKE.OPS0,S5RGKE.MPS0;



ASSIGN S6DR * S6DECR * SYSINITTMR2 TO
S6DGKE.C1P1,S6DGKE.OPS0,S6DGKE.MPS0;



ASSIGN S6RECR * CPS.STATUS1 TO
S6RGKE.C1P1,S6RGKE.OPS0,S6RGKE.MPS0;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
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SSTE	
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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

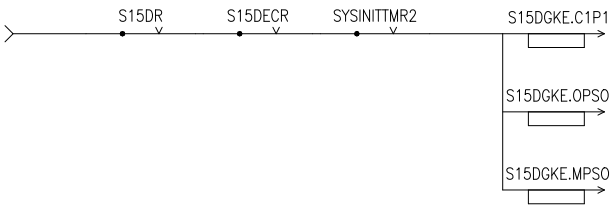
REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00172	00228	00173

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

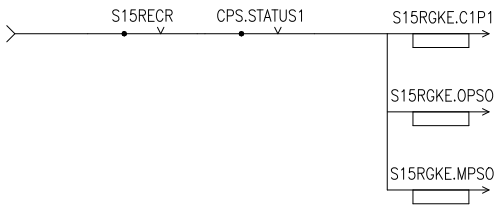
CAD FILE: 512_TAC_C1_00172_R00.DWG

(MICROLOK-II WARM STANDBY)

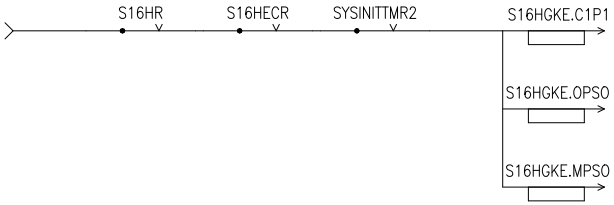
ASSIGN S15DR * S15DECR * SYSINITTMR2 TO
S15DGKE.C1P1,S15DGKE.OPSO,S15DGKE.MPSO;



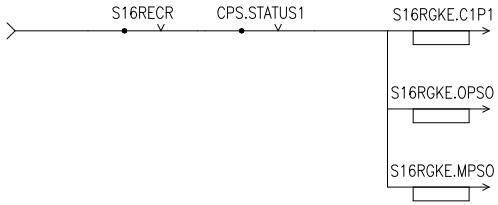
ASSIGN S15RECR * CPS.STATUS1 TO
S15RGKE.C1P1,S15RGKE.OPSO,S15RGKE.MPSO;



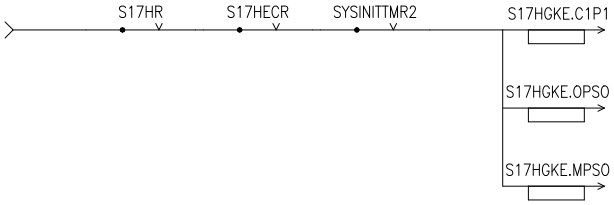
ASSIGN S16HR * S16HECR * SYSINITTMR2 TO
S16HGKE.C1P1,S16HGKE.OPSO,S16HGKE.MPSO;



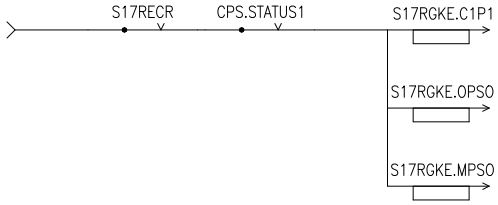
ASSIGN S16RECR * CPS.STATUS1 TO
S16RGKE.C1P1,S16RGKE.OPSO,S16RGKE.MPSO;



ASSIGN S17HR * S17HECR * SYSINITTMR2 TO
S17HGKE.C1P1,S17HGKE.OPSO,S17HGKE.MPSO;



ASSIGN S17RECR * CPS.STATUS1 TO
S17RGKE.C1P1,S17RGKE.OPSO,S17RGKE.MPSO;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
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f CSTE	

CHK.

APP.

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B	n e jsyos vkjsjo S.C.RLY.DRG.NO	a fc B	
'khV SHEET 00173	dqy 'khVSHEETS 00228	CONT'D ON: 00174	

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

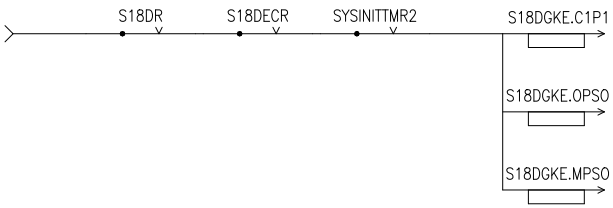
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

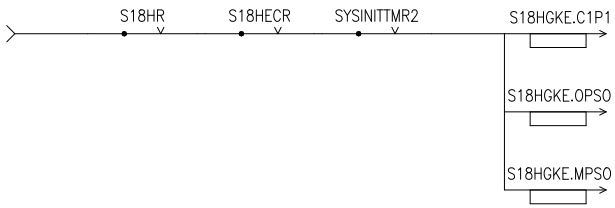
CAD FILE: 512_TAC_C1_00173_R00.DWG

(MICROLOK-II WARM STANDBY)

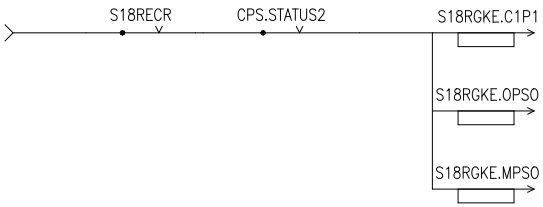
ASSIGN S18DR * S18DECR * SYSINITTMR2 TO
S18DGKE.C1P1,S18DGKE.OPS0,S18DGKE.MPS0;



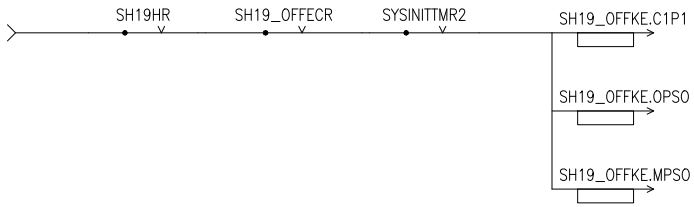
ASSIGN S18HR * S18HECR * SYSINITTMR2 TO
S18HGKE.C1P1,S18HGKE.OPS0,S18HGKE.MPS0;



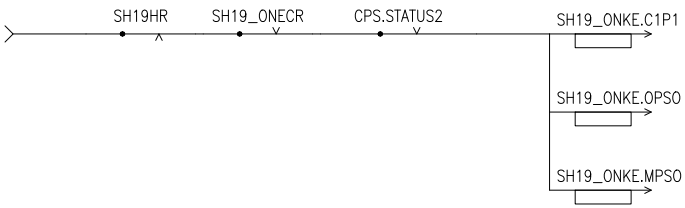
ASSIGN S18RECR * CPS.STATUS2 TO
S18RGKE.C1P1,S18RGKE.OPS0,S18RGKE.MPS0;



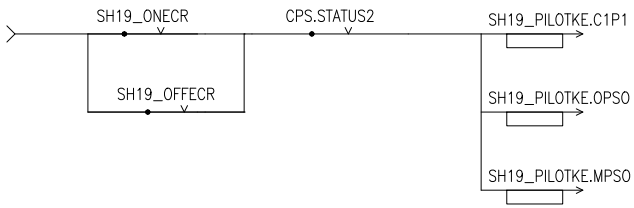
ASSIGN SH19HR * SH19_OFFECR * SYSINITTMR2 TO
SH19_OFFKE.C1P1,SH19_OFFKE.OPS0,SH19_OFFKE.MPS0;



ASSIGN ~SH19HR * SH19_ONECR * CPS.STATUS2 TO
SH19_ONKE.C1P1,SH19_ONKE.OPS0,SH19_ONKE.MPS0;



ASSIGN (SH19_ONECR + SH19_OFFECR) * CPS.STATUS2 TO
SH19_PILOTKE.C1P1,SH19_PILOTKE.OPS0,SH19_PILOTKE.MPS0;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00174 | dqy 'khVSHEETS 00228 | CONT'D ON: 00175

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

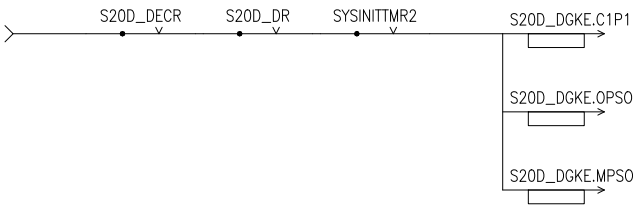
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

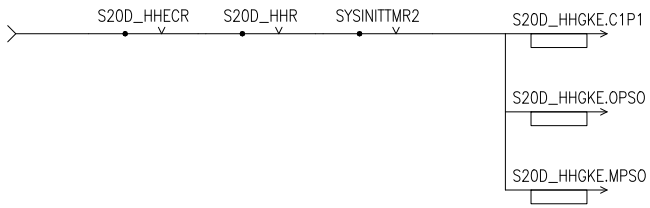
CAD FILE: 512_TAC_C1_00174_R00.DWG

(MICROLOK-II WARM STANDBY)

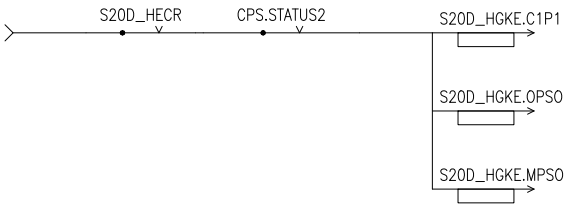
ASSIGN S20D_DECR * S20D_DR * SYSINITTMR2 TO
S20D_DGKE.C1P1,S20D_DGKE.OPSO,S20D_DGKE.MPSO;



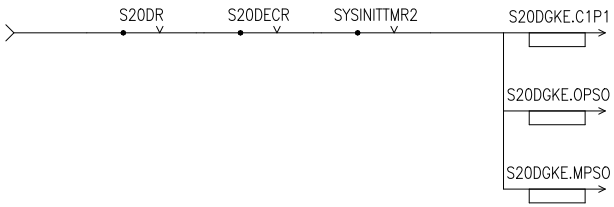
ASSIGN S20D_HHECR * S20D_HHR * SYSINITTMR2 TO
S20D_HHGKE.C1P1,S20D_HHGKE.OPSO,S20D_HHGKE.MPSO;



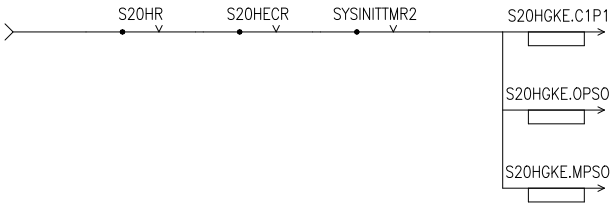
ASSIGN S20D_HECR * CPS.STATUS2 TO
S20D_HGKE.C1P1,S20D_HGKE.OPSO,S20D_HGKE.MPSO;



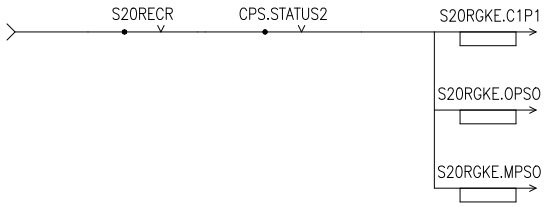
ASSIGN S20DR * S20DECR * SYSINITTMR2 TO
S20DGKE.C1P1,S20DGKE.OPSO,S20DGKE.MPSO;



ASSIGN S20HR * S20HECR * SYSINITTMR2 TO
S20HGKE.C1P1,S20HGKE.OPSO,S20HGKE.MPSO;



ASSIGN S20RECR * CPS.STATUS2 TO
S20RGKE.C1P1,S20RGKE.OPSO,S20RGKE.MPSO;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP:

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

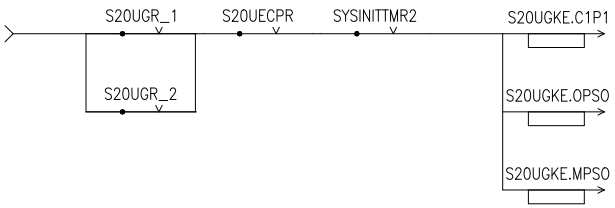
REF.SIG.PLAN.No. IPU.2107B	n e jsyos vkjsjo S.C.RLY.DRG.NO	a fc B	
'khV SHEET 00175	dqy 'khVSHEETS 00228	CONT'D ON:	00176

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

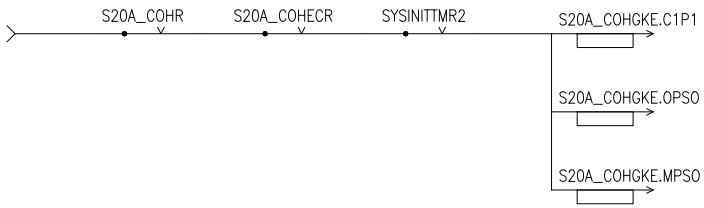
CAD FILE: 512_TAC_C1_00175_R00.DWG

(MICROLOK-II WARM STANDBY)

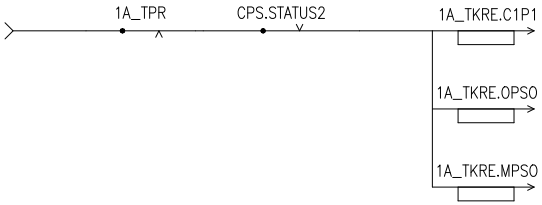
ASSIGN (S20UGR_1 + S20UGR_2) * S20UECPR * SYSINITTMR2 TO
S20UGKE.C1P1,S20UGKE.OPS0,S20UGKE.MPS0;



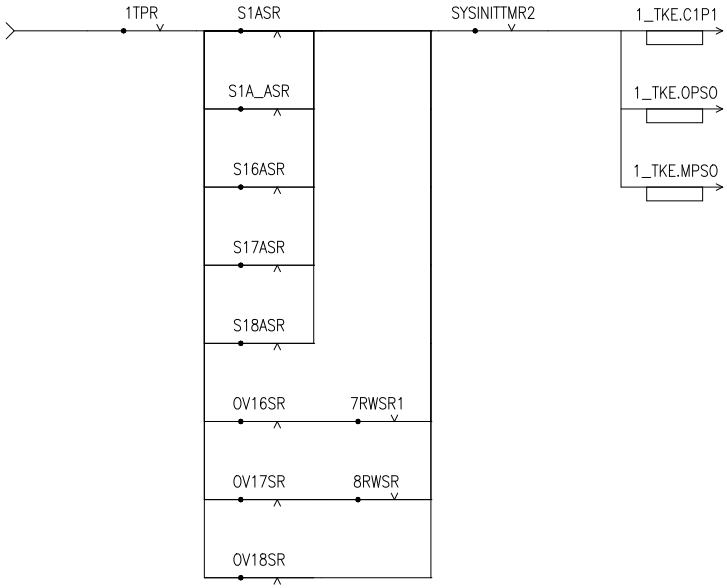
ASSIGN S20A_COHR * S20A_COHECR * SYSINITTMR2 TO
S20A_COHGKE.C1P1,S20A_COHGKE.OPS0,S20A_COHGKE.MPS0;



ASSIGN ~1A_TPR * CPS.STATUS2 TO
1A_TKRE.C1P1,1A_TKRE.OPS0,1A_TKRE.MPS0;



ASSIGN 1TPR * (~S1ASR + ~S1A_ASR + ~S16ASR + ~S17ASR + ~S18ASR + ~OV16SR *
7RWSR1 + ~OV17SR * 8RWSR + ~OV18SR) * SYSINITTMR2 TO
1_TKE.C1P1,1_TKE.OPS0,1_TKE.MPS0;



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APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00176	00228	00177

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

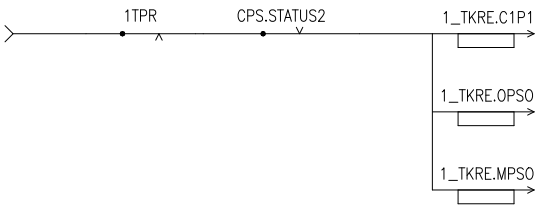
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

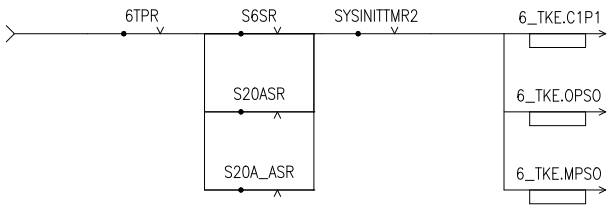
CAD FILE: 512_TAC_C1_00176_R00.DWG

(MICROLOK-II WARM STANDBY)

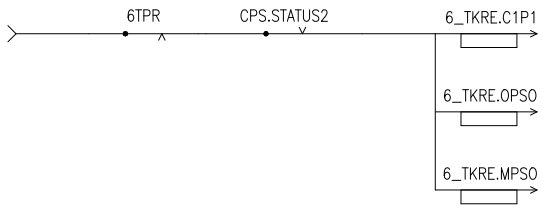
ASSIGN ~1TPR * CPS.STATUS2 TO
1_TKRE.C1P1,1_TKRE.OPS0,1_TKRE.MPS0;



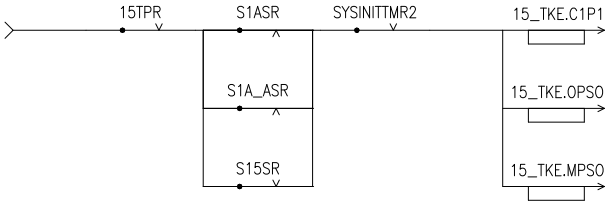
ASSIGN 6TPR * (S6SR + ~S20ASR + ~S20A_ASR) * SYSINITTMR2 TO
6_TKE.C1P1,6_TKE.OPS0,6_TKE.MPS0;



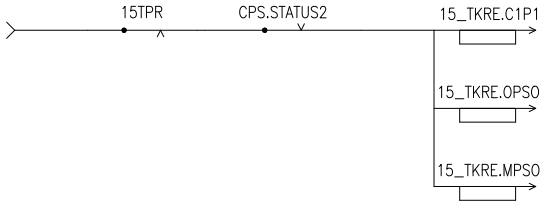
ASSIGN ~6TPR * CPS.STATUS2 TO
6_TKRE.C1P1,6_TKRE.OPS0,6_TKRE.MPS0;



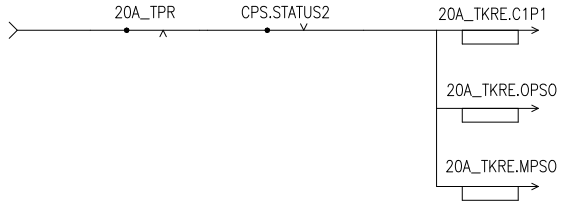
ASSIGN 15TPR * (~S1ASR + ~S1A_ASR + S15SR) * SYSINITTMR2 TO
15_TKE.C1P1,15_TKE.OPS0,15_TKE.MPS0;



ASSIGN ~15TPR * CPS.STATUS2 TO
15_TKRE.C1P1,15_TKRE.OPS0,15_TKRE.MPS0;



ASSIGN ~20A_TPR * CPS.STATUS2 TO
20A_TKRE.C1P1,20A_TKRE.OPS0,20A_TKRE.MPS0;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

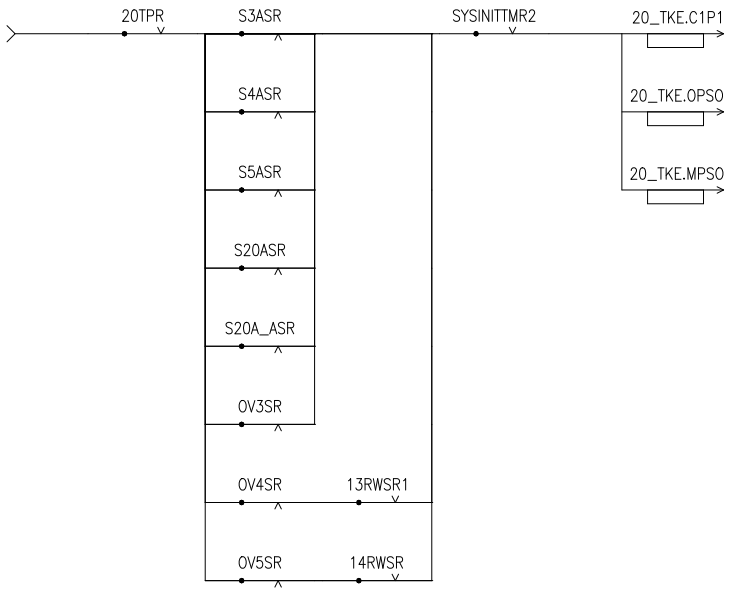
REF.SIG.PLAN.No. IPU.2107B
n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khV SHEET 00177
dqy 'khVSHEETS 00228
CONT'D ON: 00178

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

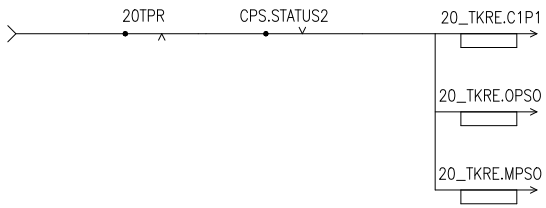
CAD FILE: 512_TAC_C1_00177_R00.DWG

(MICROLOK-II WARM STANDBY)

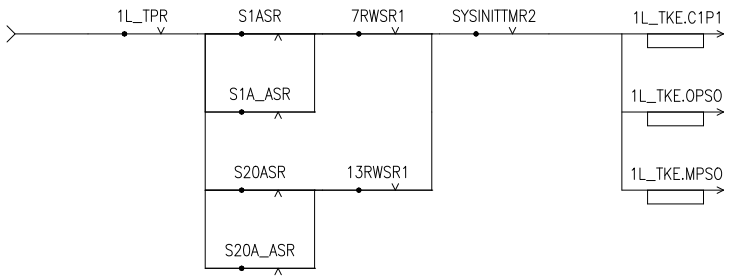
ASSIGN 20TPR * (~S3ASR + ~S4ASR + ~S5ASR + ~S20ASR + ~S20A_ASR + ~OV3SR + ~OV4SR * 13RWSR1 + ~OV5SR * 14RWSR) * SYSINITTMR2 TO 20_TKE.C1P1,20_TKE.OPS0,20_TKE.MPS0;



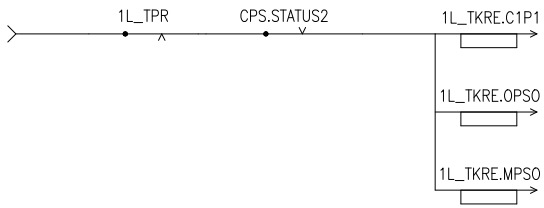
ASSIGN ~20TPR * CPS.STATUS2 TO 20_TKRE.C1P1,20_TKRE.OPS0,20_TKRE.MPS0;



ASSIGN 1L_TPR * ((~S1ASR + ~S1A_ASR) * 7RWSR1 + (~S20ASR + ~S20A_ASR) * 13RWSR1) * SYSINITTMR2 TO 1L_TKE.C1P1,1L_TKE.OPS0,1L_TKE.MPS0;



ASSIGN ~1L_TPR * CPS.STATUS2 TO 1L_TKRE.C1P1,1L_TKRE.OPS0,1L_TKRE.MPS0;



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APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00178 | dqy 'khVSHEETS 00228 | CONT'D ON: 00179

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

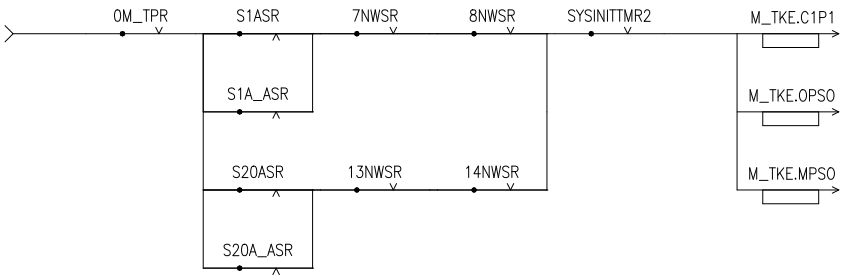
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

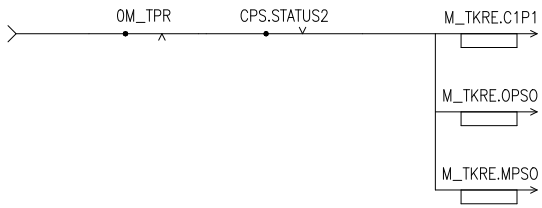
CAD FILE: 512_TAC_C1_00178_R00.DWG

(MICROLOK-II WARM STANDBY)

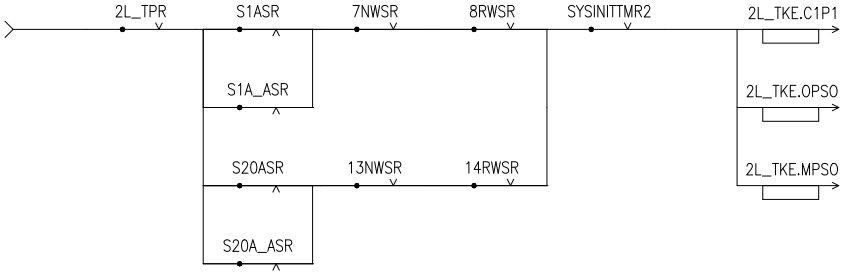
ASSIGN 0M_TPR * ((~S1ASR + ~S1A_ASR) * 7NWSR * 8NWSR + (~S20ASR + ~S20A_ASR) * 13NWSR * 14NWSR)
* SYSINITTMR2 TO M_TKE.C1P1,M_TKE.OPS0,M_TKE.MPS0;



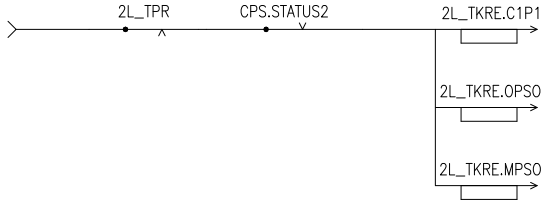
ASSIGN ~0M_TPR * CPS.STATUS2 TO
M_TKRE.C1P1,M_TKRE.OPS0,M_TKRE.MPS0;



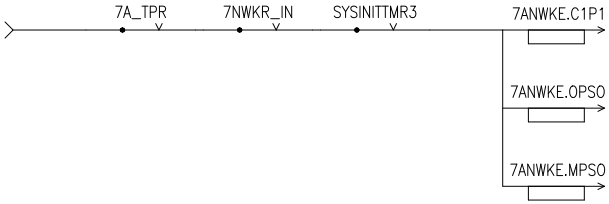
ASSIGN 2L_TPR * ((~S1ASR + ~S1A_ASR) * 7NWSR * 8RWSR + (~S20ASR + ~S20A_ASR) * 13NWSR * 14RWSR)
* SYSINITTMR2 TO 2L_TKE.C1P1,2L_TKE.OPS0,2L_TKE.MPS0;



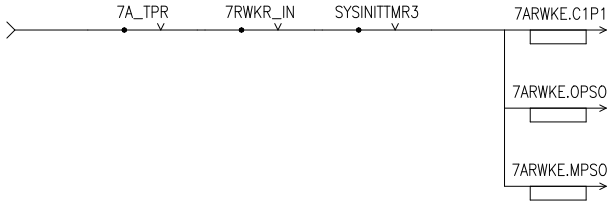
ASSIGN ~2L_TPR * CPS.STATUS2 TO
2L_TKRE.C1P1,2L_TKRE.OPS0,2L_TKRE.MPS0;



ASSIGN 7A_TPR * 7NWKR_IN * SYSINITTMR3 TO
7ANWKE.C1P1,7ANWKE.OPS0,7ANWKE.MPS0;



ASSIGN 7A_TPR * 7RWKR_IN * SYSINITTMR3 TO
7ARWKE.C1P1,7ARWKE.OPS0,7ARWKE.MPS0;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	

CHK.

APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khVSHEET 00179	dqy 'khVSHEETS 00228	CONT'D ON: 00180

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

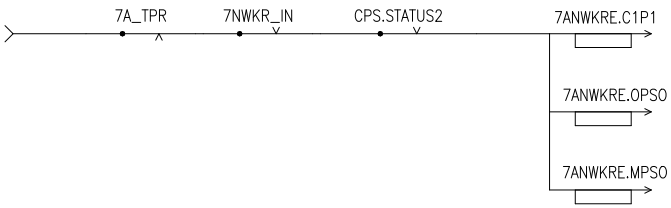
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

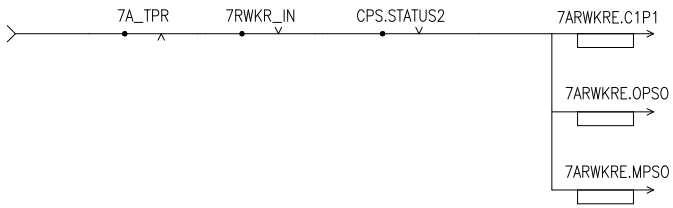
CAD FILE: 512_TAC_C1_00179_R00.DWG

(MICROLOK-II WARM STANDBY)

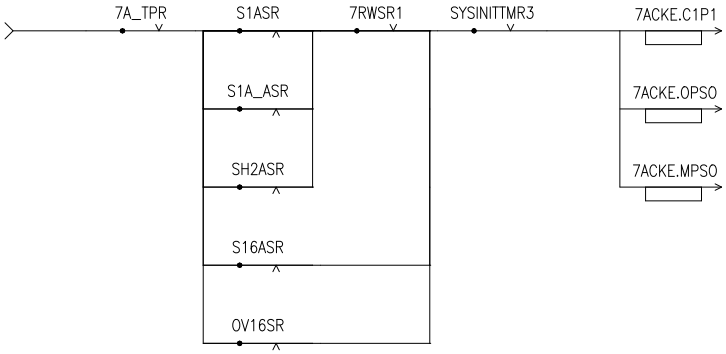
ASSIGN ~7A_TPR * 7NWKR_IN * CPS.STATUS2 TO
7ANWKRE.C1P1,7ANWKRE.OPS0,7ANWKRE.MPS0;



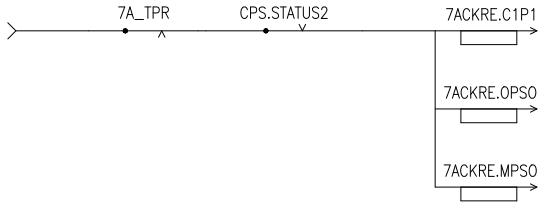
ASSIGN ~7A_TPR * 7RWKR_IN * CPS.STATUS2 TO
7ARWKRE.C1P1,7ARWKRE.OPS0,7ARWKRE.MPS0;



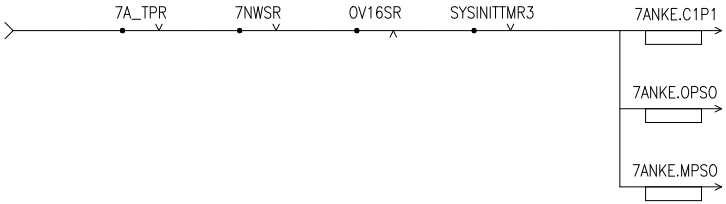
ASSIGN 7A_TPR * ((~S1ASR + ~S1A_ASR + ~SH2ASR) * 7RWSR1 + ~S16ASR + ~0V16SR) *
SYSINITTMR3 TO 7ACKE.C1P1,7ACKE.OPS0,7ACKE.MPS0;



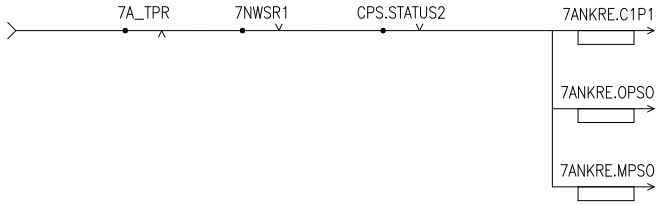
ASSIGN ~7A_TPR * CPS.STATUS2 TO
7ACKRE.C1P1,7ACKRE.OPS0,7ACKRE.MPS0;



ASSIGN 7A_TPR * 7NWSR * ~0V16SR * SYSINITTMR3 TO 7ANKE.C1P1,7ANKE.OPS0,7ANKE.MPS0;



ASSIGN ~7A_TPR * 7NWSR1 * CPS.STATUS2 TO 7ANKRE.C1P1,7ANKRE.OPS0,7ANKRE.MPS0;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00180 | dqy 'khVSHEETS 00228 | CONT'D ON: 00181

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

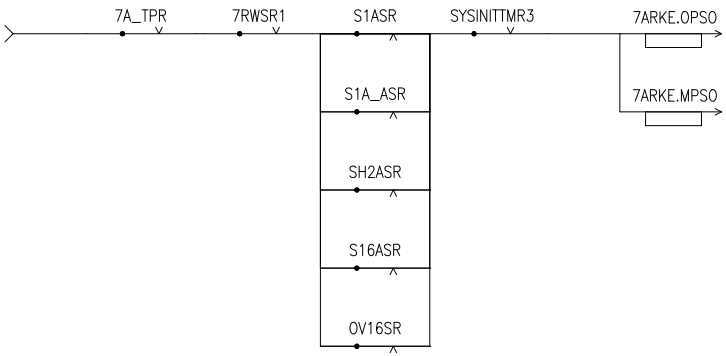
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

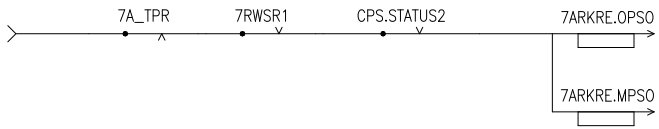
CAD FILE: 512_TAC_C1_00180_R00.DWG

(MICROLOK-II WARM STANDBY)

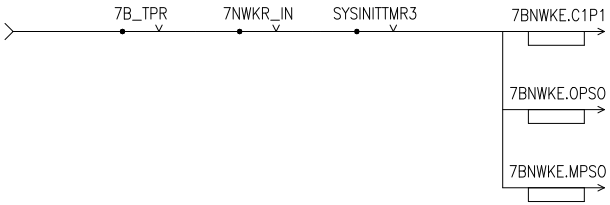
ASSIGN 7A_TPR * 7RWSR1 * (~S1ASR + ~S1A_ASR + ~SH2ASR + ~S16ASR + ~OV16SR) * SYSINITTMR3 TO 7ARKE.OPS0,7ARKE.MPS0;



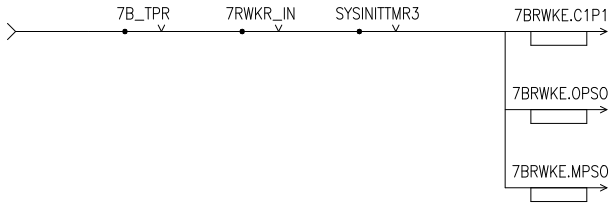
ASSIGN ~7A_TPR * 7RWSR1 * CPS.STATUS2 TO 7ARKRE.OPS0,7ARKRE.MPS0;



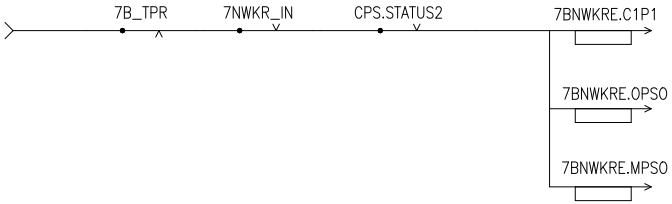
ASSIGN 7B_TPR * 7NWKR_IN * SYSINITTMR3 TO 7BNWKE.C1P1,7BNWKE.OPS0,7BNWKE.MPS0;



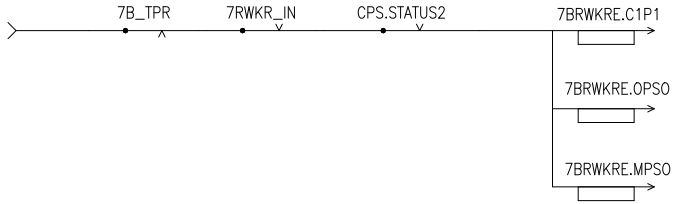
ASSIGN 7B_TPR * 7RWKR_IN * SYSINITTMR3 TO 7BRWKE.C1P1,7BRWKE.OPS0,7BRWKE.MPS0;



ASSIGN ~7B_TPR * 7NWKR_IN * CPS.STATUS2 TO 7BNWKRE.C1P1,7BNWKRE.OPS0,7BNWKRE.MPS0;



ASSIGN ~7B_TPR * 7RWKR_IN * CPS.STATUS2 TO 7BRWKRE.C1P1,7BRWKRE.OPS0,7BRWKRE.MPS0;



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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
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Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
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APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00181	00228	00182

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

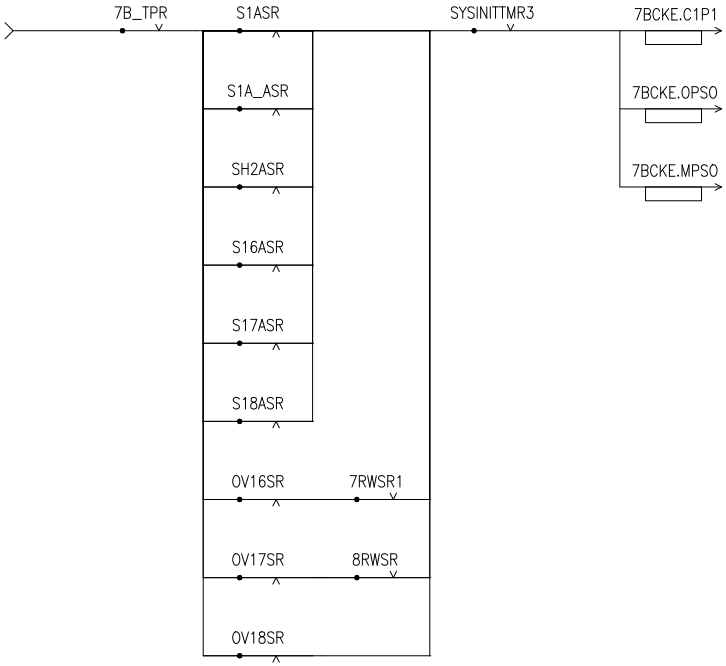
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

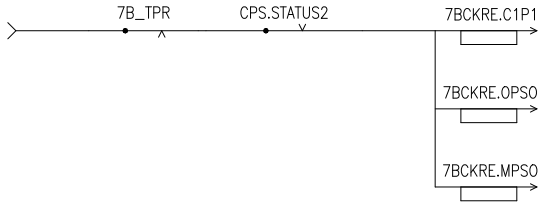
CAD FILE: 512_TAC_C1_00181_R00.DWG

(MICROLOK-II WARM STANDBY)

ASSIGN 7B_TPR * (~S1ASR + ~S1A_ASR + ~SH2ASR + ~S16ASR + ~S17ASR + ~S18ASR + ~OV16SR * 7RWSR1 + ~OV17SR * 8RWSR + ~OV18SR) * SYSINITTMR3 TO 7BCKE.C1P1,7BCKE.OPS0,7BCKE.MPS0;



ASSIGN ~7B_TPR * CPS.STATUS2 TO 7BCKRE.C1P1,7BCKRE.OPS0,7BCKRE.MPS0;



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00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

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Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

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APP.	f CSTE

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00182 | dqy 'khVSHEETS 00228 | CONT'D ON: 00183

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

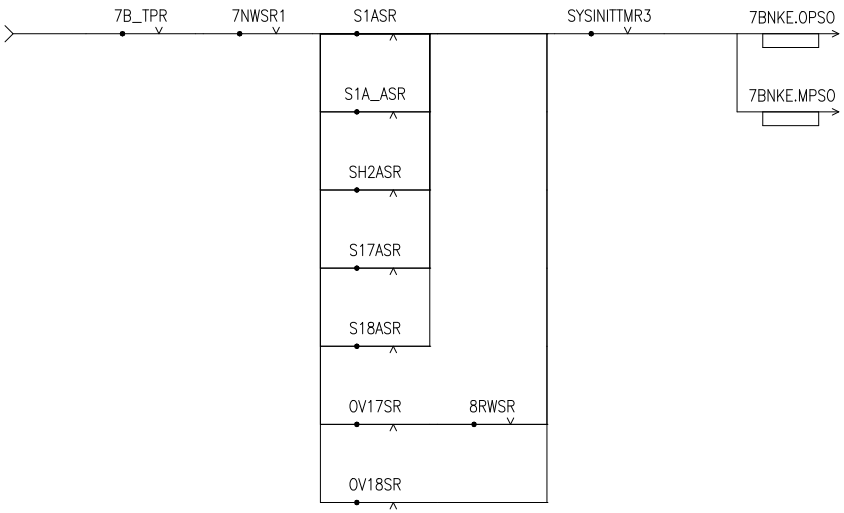
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

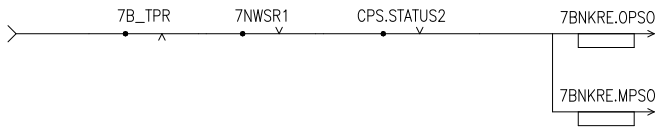
CAD FILE: 512_TAC_C1_00182_R00.DWG

(MICROLOK-II WARM STANDBY)

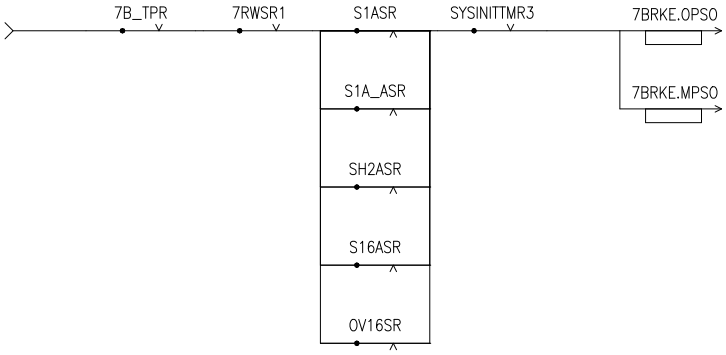
ASSIGN 7B_TPR * 7NWSR1 * (~S1ASR + ~S1A_ASR+ ~SH2ASR + ~S17ASR + ~S18ASR + ~OV17SR * 8RWSR + ~OV18SR) * SYSINITTMR3 TO 7BNKE.OPS0,7BNKE.MPS0;



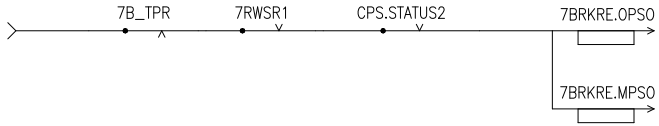
ASSIGN ~7B_TPR * 7NWSR1 * CPS.STATUS2 TO 7BNKRE.OPS0,7BNKRE.MPS0;



ASSIGN 7B_TPR * 7RWSR1 * (~S1ASR + ~S1A_ASR+ ~SH2ASR + ~S16ASR + ~OV16SR) * SYSINITTMR3 TO 7BRKE.OPS0,7BRKE.MPS0;



ASSIGN (~7B_TPR) * 7RWSR1 * CPS.STATUS2 TO 7BRKRE.OPS0,7BRKRE.MPS0;



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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00183	00228	00184

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

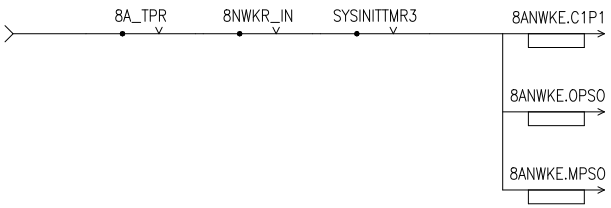
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

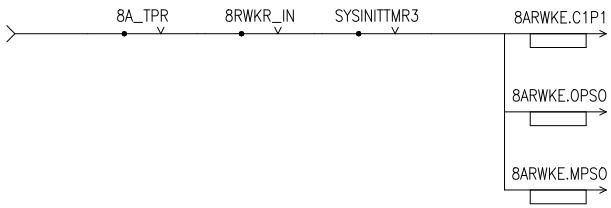
CAD FILE: 512_TAC_C1_00183_R00.DWG

(MICROLOK-II WARM STANDBY)

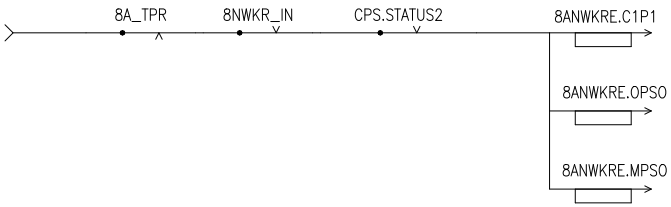
ASSIGN 8A_TPR * 8NWKR_IN * SYSINITTMR3 TO
8ANWKE.C1P1,8ANWKE.OPS0,8ANWKE.MPS0;



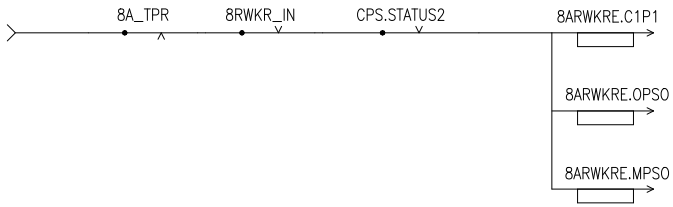
ASSIGN 8A_TPR * 8RWKR_IN * SYSINITTMR3 TO
8ARWKE.C1P1,8ARWKE.OPS0,8ARWKE.MPS0;



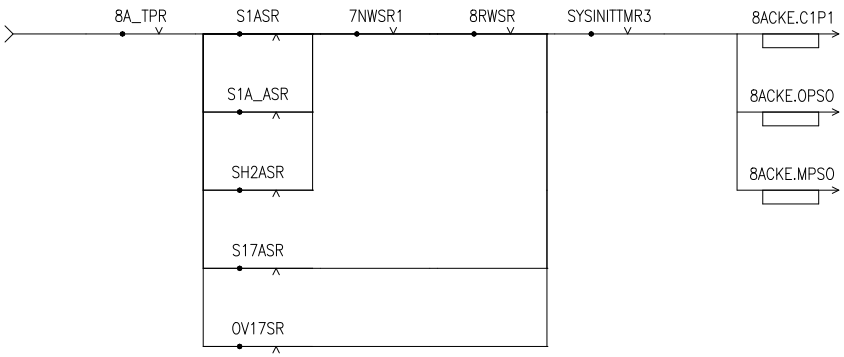
ASSIGN ~8A_TPR * 8NWKR_IN * CPS.STATUS2 TO
8ANWKRE.C1P1,8ANWKRE.OPS0,8ANWKRE.MPS0;



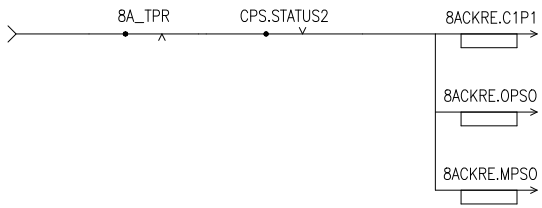
ASSIGN ~8A_TPR * 8RWKR_IN * CPS.STATUS2 TO
8ARWKRE.C1P1,8ARWKRE.OPS0,8ARWKRE.MPS0;



ASSIGN 8A_TPR * ((~S1ASR + ~S1A_ASR + ~SH2ASR) * 7NWSR1 * 8RWSR + ~S17ASR + ~OV17SR) *
SYSINITTMR3 TO 8ACKE.C1P1,8ACKE.OPS0,8ACKE.MPS0;



ASSIGN ~8A_TPR * CPS.STATUS2 TO
8ACKRE.C1P1,8ACKRE.OPS0,8ACKRE.MPS0;



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APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khVSHEET	dqy 'khVSHEETS	CONT'D ON:
00184	00228	00185

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

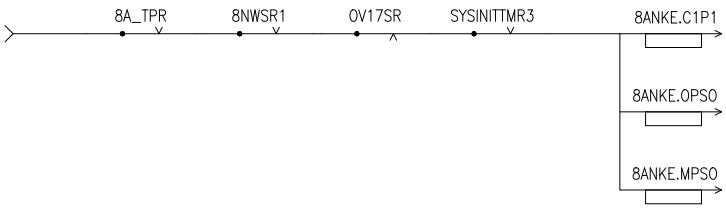
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

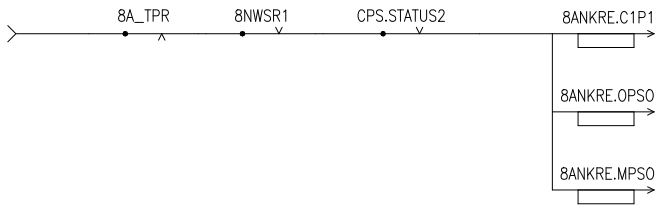
CAD FILE: 512_TAC_C1_00184_R00.DWG

(MICROLOK-II WARM STANDBY)

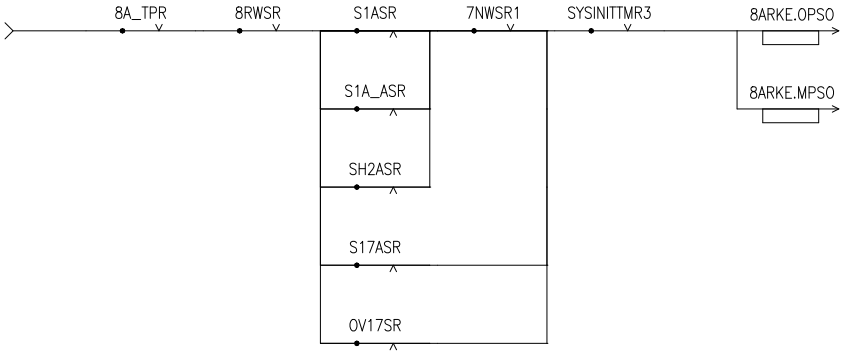
ASSIGN 8A_TPR * 8NWSR1 * ~0V17SR * SYSINITTMR3 TO 8ANKE.C1P1,8ANKE.OPS0,8ANKE.MPS0;



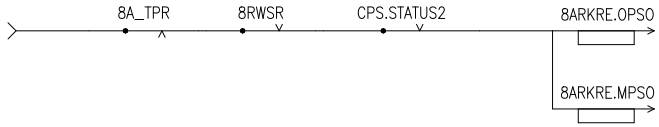
ASSIGN ~8A_TPR * 8NWSR1 * CPS.STATUS2 TO 8ANKRE.C1P1,8ANKRE.OPS0,8ANKRE.MPS0;



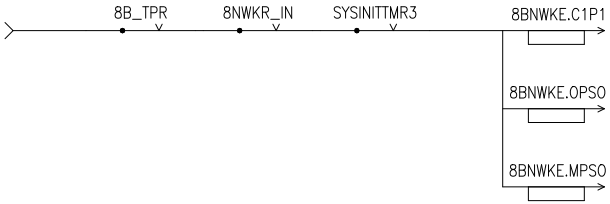
ASSIGN 8A_TPR * 8RWSR * ((~S1ASR + ~S1A_ASR + ~SH2ASR) * 7NWSR1 + ~S17ASR + ~0V17SR) * SYSINITTMR3 TO 8ARKE.OPS0,8ARKE.MPS0;



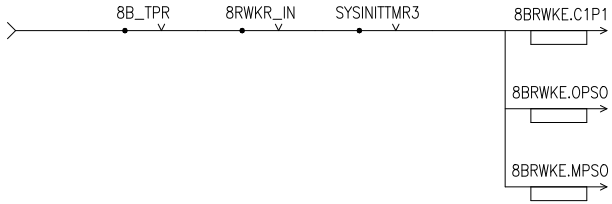
ASSIGN ~8A_TPR * 8RWSR * CPS.STATUS2 TO 8ARKRE.OPS0,8ARKRE.MPS0;



ASSIGN 8B_TPR * 8NWKR_IN * SYSINITTMR3 TO 8BNWKE.C1P1,8BNWKE.OPS0,8BNWKE.MPS0;



ASSIGN 8B_TPR * 8RWKR_IN * SYSINITTMR3 TO 8BRWKE.C1P1,8BRWKE.OPS0,8BRWKE.MPS0;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
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CHK.

APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO a fc B

'khV SHEET 00185 | dqy 'khVSHEETS 00228 | CONT'D ON: 00186

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

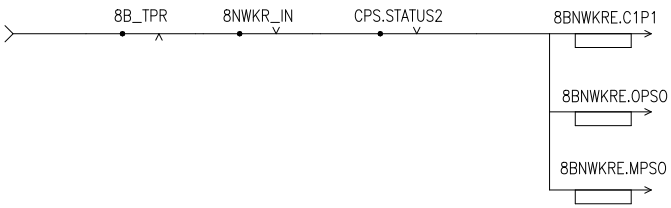
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

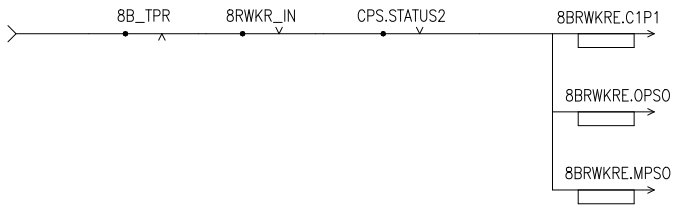
CAD FILE: 512_TAC_C1_00185_R00.DWG

(MICROLOK-II WARM STANDBY)

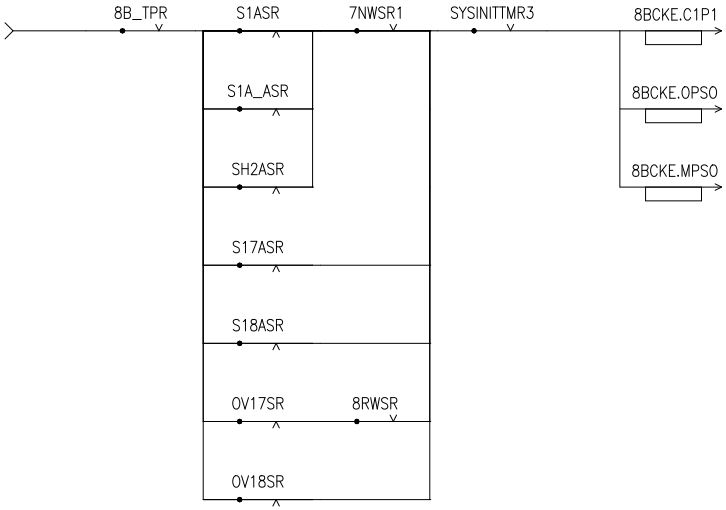
ASSIGN ~8B_TPR * 8NWKR_IN * CPS.STATUS2 TO
8BNWKRE.C1P1,8BNWKRE.OPS0,8BNWKRE.MPS0;



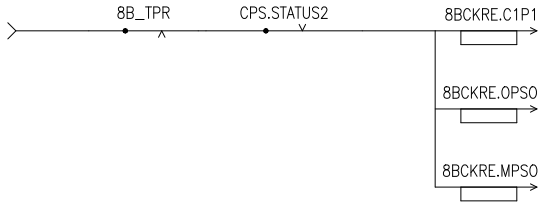
ASSIGN ~8B_TPR * 8RWKR_IN * CPS.STATUS2 TO
8BRWKRE.C1P1,8BRWKRE.OPS0,8BRWKRE.MPS0;



ASSIGN 8B_TPR * ((~S1ASR + ~S1A_ASR + ~SH2ASR) * 7NWSR1 + ~S17ASR + ~S18ASR +
~0V17SR * 8RWSR + ~0V18SR) * SYSINITTMR3 TO 8BCKE.C1P1,8BCKE.OPS0,8BCKE.MPS0;



ASSIGN ~8B_TPR * CPS.STATUS2 TO
8BCKRE.C1P1,8BCKRE.OPS0,8BCKRE.MPS0;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
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SE/SSE	
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00186 | dqy 'khVSHEETS 00228 | CONT'D ON: 00187

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

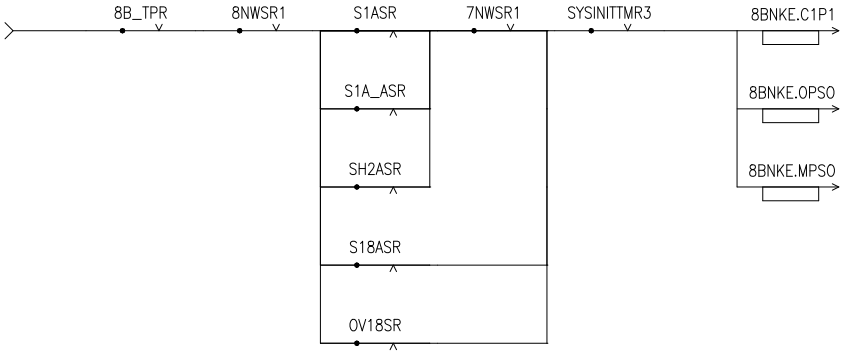
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

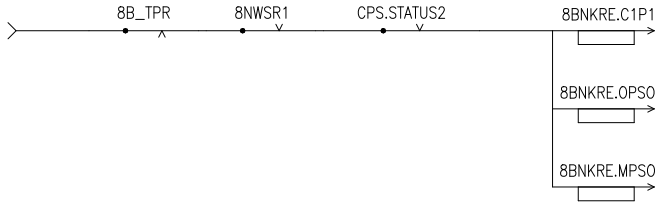
CAD FILE: 512_TAC_C1_00186_R00.DWG

(MICROLOK-II WARM STANDBY)

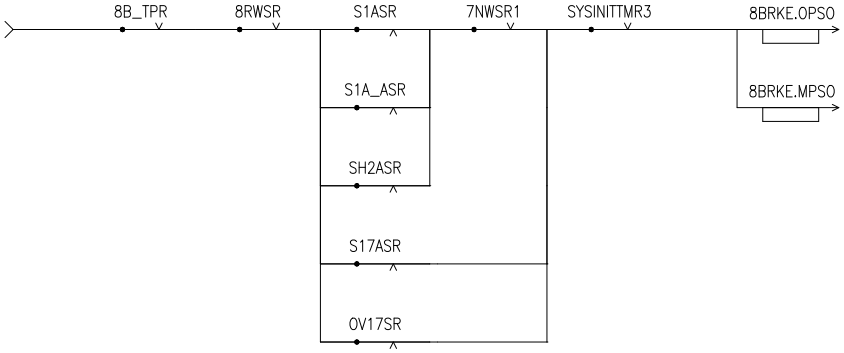
ASSIGN 8B_TPR * 8NWSR1 * ((~S1ASR + ~S1A_ASR + ~SH2ASR) * 7NWSR1 + ~S18ASR + ~OV18SR) *
SYSINITTMR3 TO 8BNKE.C1P1,8BNKE.OPSO,8BNKE.MPSO;



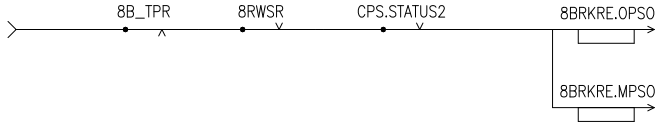
ASSIGN ~8B_TPR * 8NWSR1 * CPS.STATUS2 TO
8BNKRE.C1P1,8BNKRE.OPSO,8BNKRE.MPSO;



ASSIGN 8B_TPR * 8RWSR * ((~S1ASR + ~S1A_ASR + ~SH2ASR) * 7NWSR1 + ~S17ASR + ~OV17SR) *
SYSINITTMR3 TO 8BRKE.OPSO,8BRKE.MPSO;



ASSIGN ~8B_TPR * 8RWSR * CPS.STATUS2 TO 8BRKRE.OPSO,8BRKRE.MPSO;



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E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	CHK.
SE/SSE	
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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00187	00228	00188

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

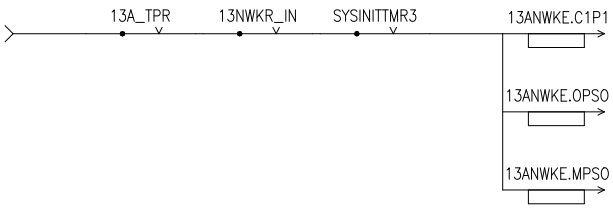
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

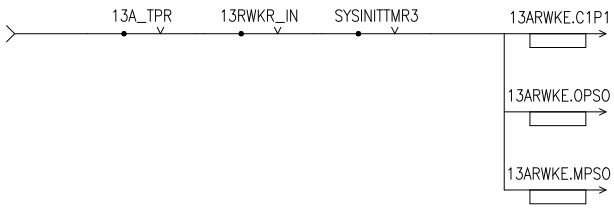
CAD FILE: 512_TAC_C1_00187_R00.DWG

(MICROLOK-II WARM STANDBY)

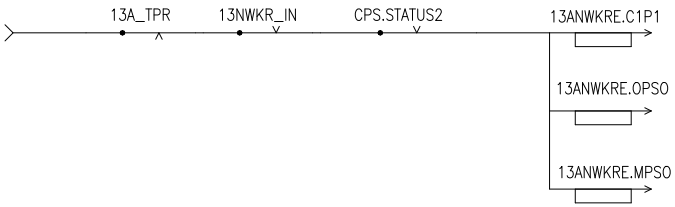
ASSIGN 13A_TPR * 13NWKR_IN * SYSINITTMR3 TO
13ANWKE.C1P1,13ANWKE.OPS0,13ANWKE.MPS0;



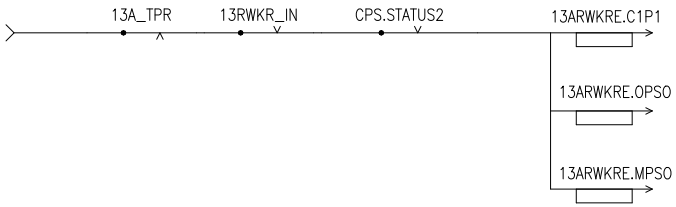
ASSIGN 13A_TPR * 13RWKR_IN * SYSINITTMR3 TO
13ARWKE.C1P1,13ARWKE.OPS0,13ARWKE.MPS0;



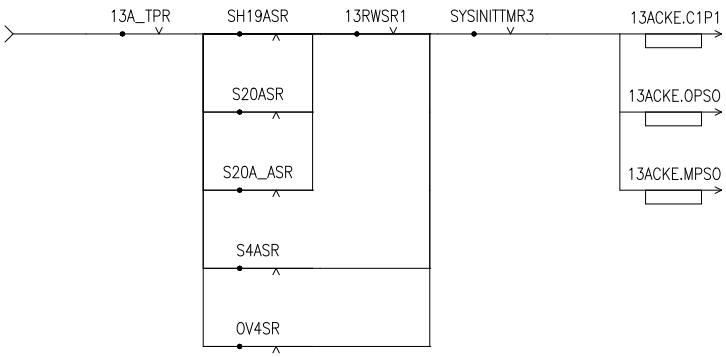
ASSIGN ~13A_TPR * 13NWKR_IN * CPS.STATUS2 TO
13ANWKRE.C1P1,13ANWKRE.OPS0,13ANWKRE.MPS0;



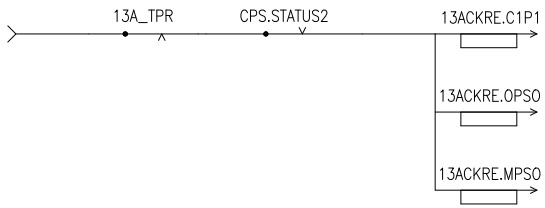
ASSIGN ~13A_TPR * 13RWKR_IN * CPS.STATUS2 TO
13ARWKRE.C1P1,13ARWKRE.OPS0,13ARWKRE.MPS0;



ASSIGN 13A_TPR * ((~SH19ASR + ~S20ASR + ~S20A_ASR) * 13RWSR1 + ~S4ASR + ~OV4SR) *
SYSINITTMR3 TO 13ACKE.C1P1,13ACKE.OPS0,13ACKE.MPS0;



ASSIGN ~13A_TPR * CPS.STATUS2 TO
13ACKRE.C1P1,13ACKRE.OPS0,13ACKRE.MPS0;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00188	00228	00189

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

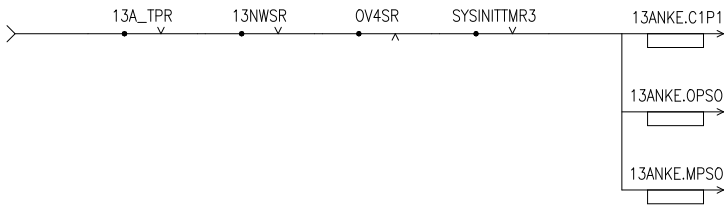
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

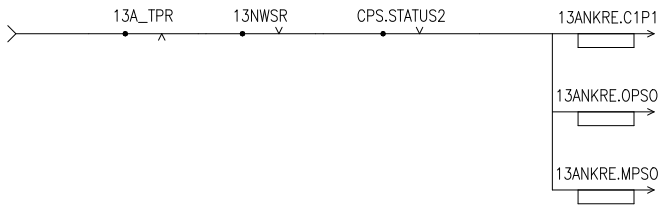
CAD FILE: 512_TAC_C1_00188_R00.DWG

(MICROLOK-II WARM STANDBY)

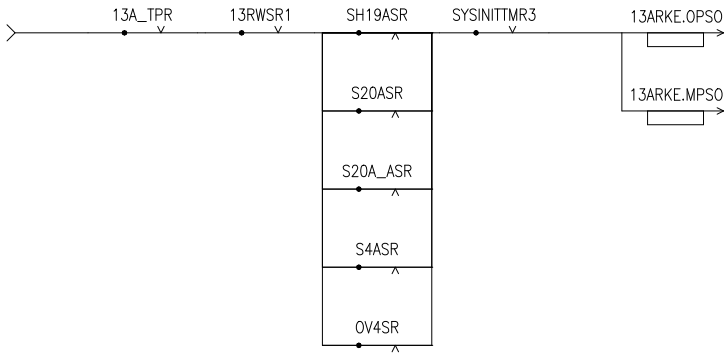
ASSIGN 13A_TPR * 13NWSR * ~0V4SR * SYSINITTMR3 TO
13ANKE.C1P1,13ANKE.OPS0,13ANKE.MPS0;



ASSIGN ~13A_TPR * 13NWSR * CPS.STATUS2 TO
13ANKRE.C1P1,13ANKRE.OPS0,13ANKRE.MPS0;

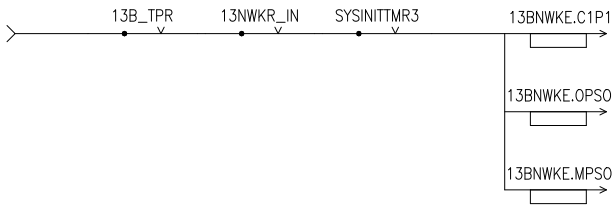


ASSIGN 13A_TPR * 13RWSR1 * (~SH19ASR + ~S20ASR+ ~S20A_ASR + ~S4ASR + ~0V4SR) *
SYSINITTMR3 TO 13ARKE.OPS0,13ARKE.MPS0;

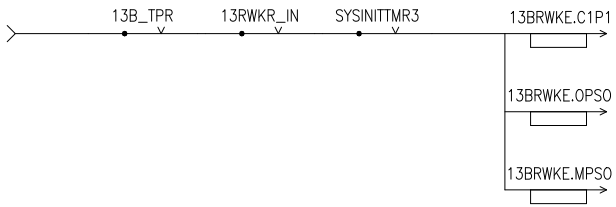


ASSIGN ~13A_TPR * 13RWSR1 * CPS.STATUS2 TO 13ARKRE.OPS0,13ARKRE.MPS0;

ASSIGN 13B_TPR * 13NWKR_IN * SYSINITTMR3 TO
13BNWKE.C1P1,13BNWKE.OPS0,13BNWKE.MPS0;



ASSIGN 13B_TPR * 13RWKR_IN * SYSINITTMR3 TO
13BRWKE.C1P1,13BRWKE.OPS0,13BRWKE.MPS0;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | fc B

'khV SHEET 00189 | dqy 'khVSHEETS 00228 | CONT'D ON: 00190

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

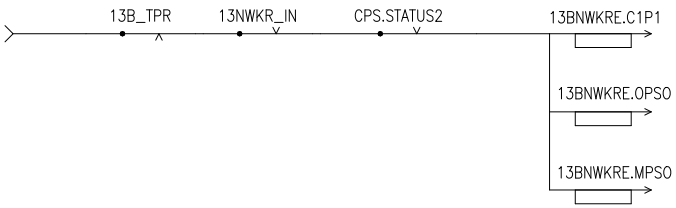
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

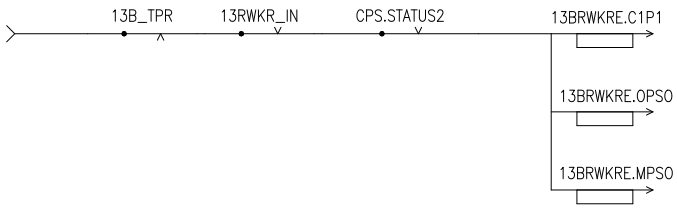
(MICROLOK-II WARM STANDBY)

CAD FILE: 512_TAC_C1_00189_R00.DWG

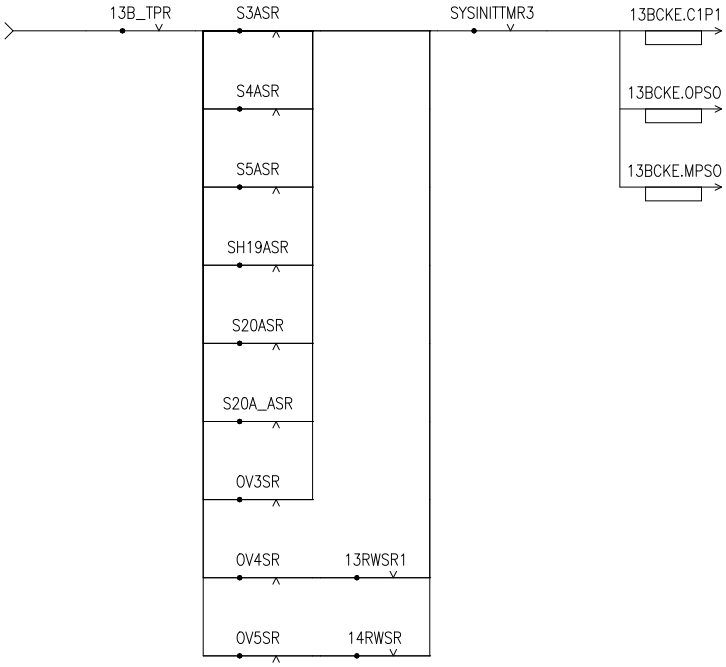
ASSIGN ~13B_TPR * 13NWKR_IN * CPS.STATUS2 TO
13BNWKRE.C1P1,13BNWKRE.OPS0,13BNWKRE.MPS0;



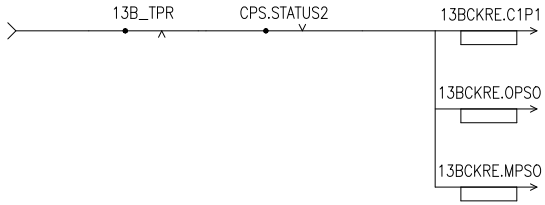
ASSIGN ~13B_TPR * 13RWKR_IN * CPS.STATUS2 TO
13BRWKRE.C1P1,13BRWKRE.OPS0,13BRWKRE.MPS0;



ASSIGN 13B_TPR * (~S3ASR + ~S4ASR + ~S5ASR + ~SH19ASR + ~S20ASR + ~S20A_ASR + ~OV3SR + ~OV4SR * 13RWSR1 + ~OV5SR * 14RWSR) * SYSINITTMR3 TO
13BCKE.C1P1,13BCKE.OPS0,13BCKE.MPS0;



ASSIGN ~13B_TPR * CPS.STATUS2 TO
13BCKRE.C1P1,13BCKRE.OPS0,13BCKRE.MPS0;



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REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00190	00228	00191

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

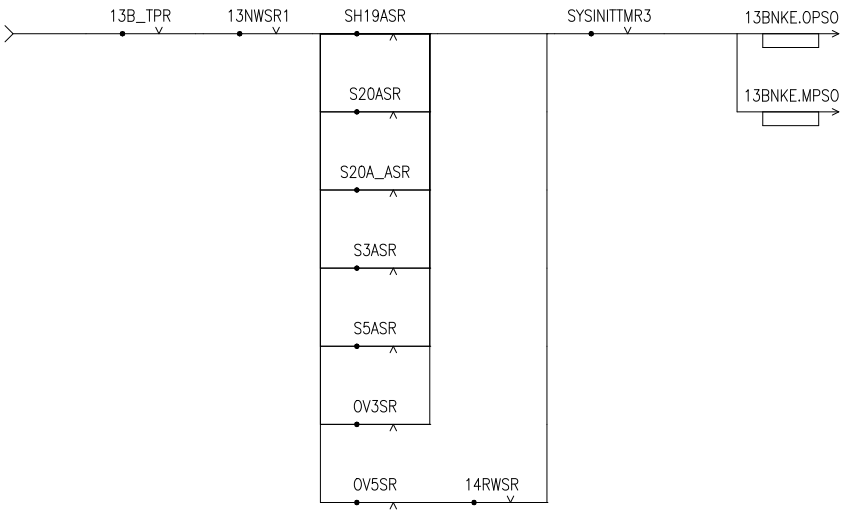
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

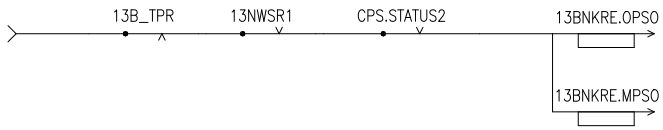
CAD FILE: 512_TAC_C1_00190_R00.DWG

(MICROLOK-II WARM STANDBY)

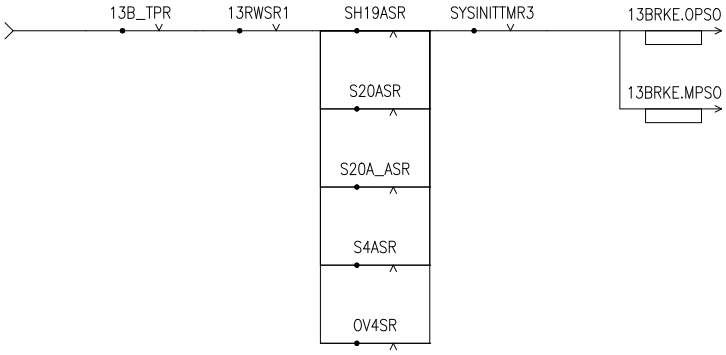
ASSIGN 13B_TPR * 13NWSR1 * (~SH19ASR + ~S20ASR + ~S20A_ASR + ~S3ASR + ~S5ASR + ~OV3SR + ~OV5SR * 14RWSR) * SYSINITTMR3 TO 13BNKE.OPS0,13BNKE.MPS0;



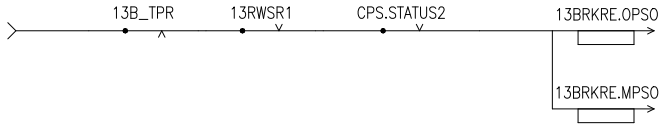
ASSIGN ~13B_TPR * 13NWSR1 * CPS.STATUS2 TO 13BNKRE.OPS0,13BNKRE.MPS0;



ASSIGN 13B_TPR * 13RWSR1 * (~SH19ASR + ~S20ASR + ~S20A_ASR + ~S4ASR + ~OV4SR) * SYSINITTMR3 TO 13BRKE.OPS0,13BRKE.MPS0;



ASSIGN ~13B_TPR * 13RWSR1 * CPS.STATUS2 TO 13BRKRE.OPS0,13BRKRE.MPS0;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross, 100 Feet Road, J.P.Nagar, 6th Phase, Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khVSHEET 00191 | dqy 'khVSHEETS 00228 | CONT'D ON: 00192

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

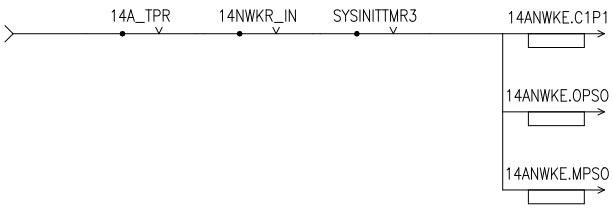
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

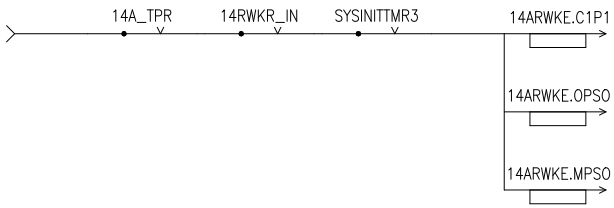
CAD FILE: 512_TAC_C1_00191_R00.DWG

(MICROLOK-II WARM STANDBY)

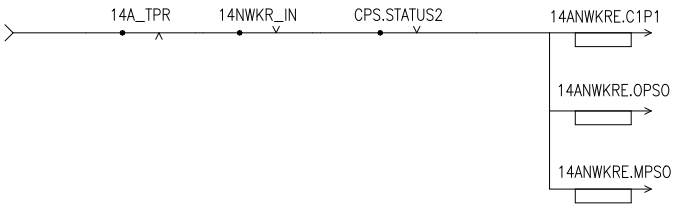
ASSIGN 14A_TPR * 14NWKR_IN * SYSINITTMR3 TO
14ANWKE.C1P1,14ANWKE.OPS0,14ANWKE.MPS0;



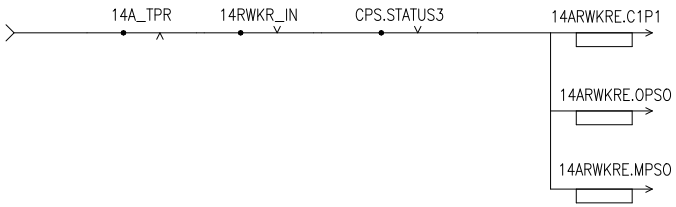
ASSIGN 14A_TPR * 14RWKR_IN * SYSINITTMR3 TO
14ARWKE.C1P1,14ARWKE.OPS0,14ARWKE.MPS0;



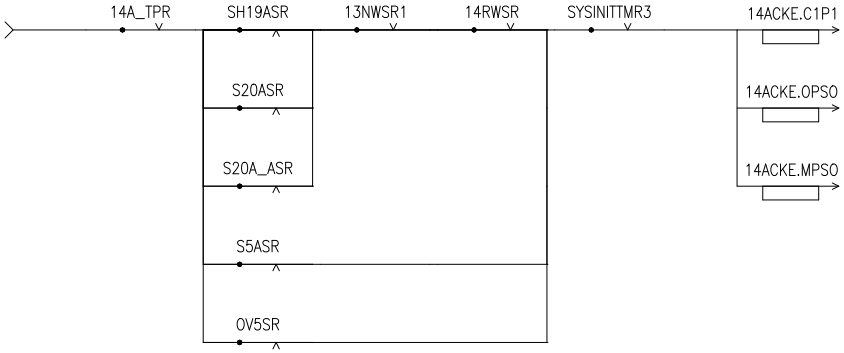
ASSIGN ~14A_TPR * 14NWKR_IN * CPS.STATUS2 TO
14ANWKRE.C1P1,14ANWKRE.OPS0,14ANWKRE.MPS0;



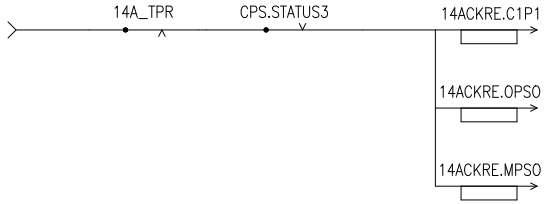
ASSIGN ~14A_TPR * 14RWKR_IN * CPS.STATUS3 TO
14ARWKRE.C1P1,14ARWKRE.OPS0,14ARWKRE.MPS0;



ASSIGN 14A_TPR * ((~SH19ASR + ~S20ASR + ~S20A_ASR) * 13NWSR1 * 14RWSR + ~S5ASR + ~OV5SR) *
SYSINITTMR3 TO 14ACKE.C1P1,14ACKE.OPS0,14ACKE.MPS0;



ASSIGN ~14A_TPR * CPS.STATUS3 TO
14ACKRE.C1P1,14ACKRE.OPS0,14ACKRE.MPS0;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khVSHEET 00192	dqy 'khVSHEETS 00228	CONT'D ON: 00193

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

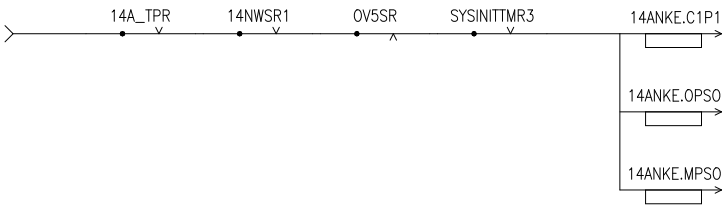
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

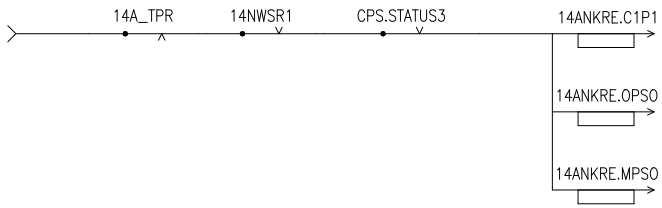
CAD FILE: 512_TAC_C1_00192_R00.DWG

(MICROLOK-II WARM STANDBY)

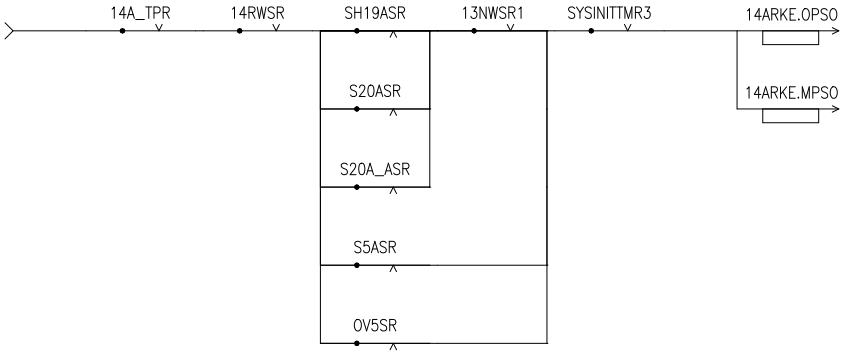
ASSIGN 14A_TPR * 14NWSR1 * ~OV5SR * SYSINITTMR3 TO
14ANKE.C1P1,14ANKE.OPS0,14ANKE.MPS0;



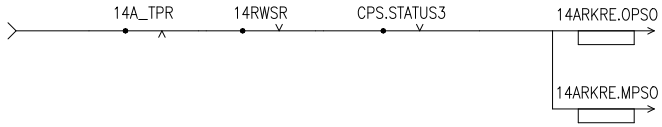
ASSIGN ~14A_TPR * 14NWSR1 * CPS.STATUS3 TO
14ANKRE.C1P1,14ANKRE.OPS0,14ANKRE.MPS0;



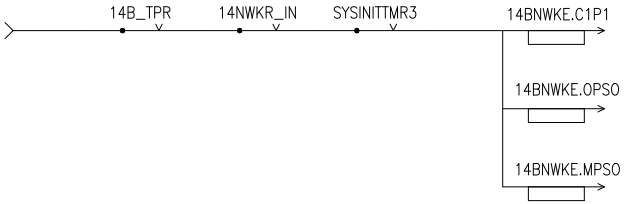
ASSIGN 14A_TPR * 14RWSR * ((~SH19ASR + ~S20ASR + ~S20A_ASR) * 13NWSR1 + ~S5ASR + ~OV5SR) *
SYSINITTMR3 TO 14ARKE.OPS0,14ARKE.MPS0;



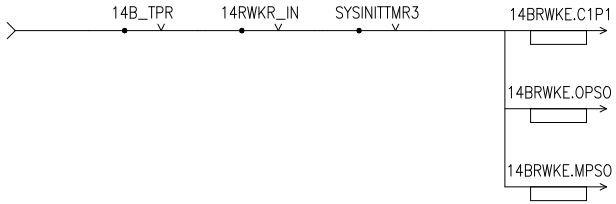
ASSIGN ~14A_TPR * 14RWSR * CPS.STATUS3 TO 14ARKRE.OPS0,14ARKRE.MPS0;



ASSIGN 14B_TPR * 14NWKR_IN * SYSINITTMR3 TO
14BNWKE.C1P1,14BNWKE.OPS0,14BNWKE.MPS0;



ASSIGN 14B_TPR * 14RWKR_IN * SYSINITTMR3 TO
14BRWKE.C1P1,14BRWKE.OPS0,14BRWKE.MPS0;



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Tel: +91 80 2654 0781,82,83
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
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APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO a fc B

'khV SHEET 00193 | dqy 'khVSHEETS 00228 | CONT'D ON: 00194

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

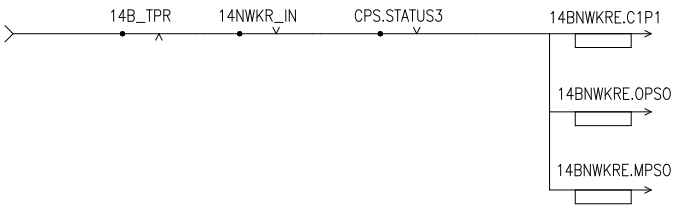
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

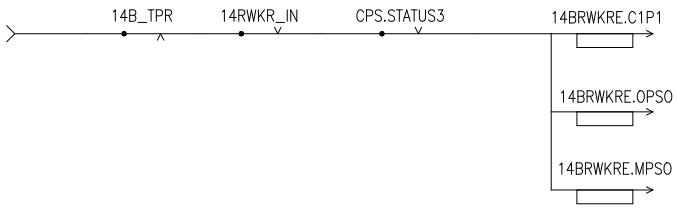
CAD FILE: 512_TAC_C1_00193_R00.DWG

(MICROLOK-II WARM STANDBY)

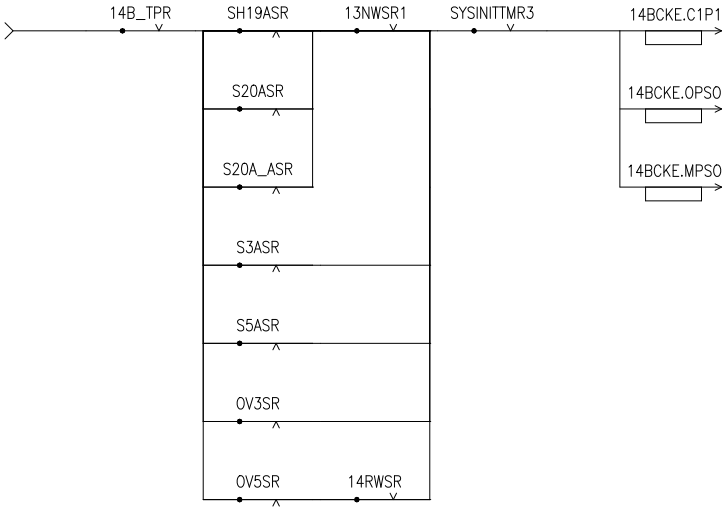
ASSIGN ~14B_TPR * 14NWKR_IN * CPS.STATUS3 TO
14BNWKRE.C1P1,14BNWKRE.OPS0,14BNWKRE.MPS0;



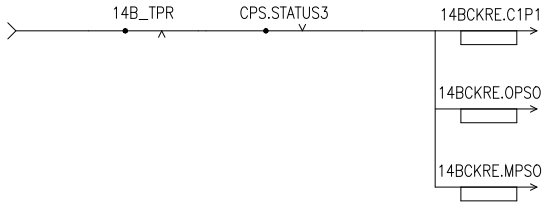
ASSIGN ~14B_TPR * 14RWKR_IN * CPS.STATUS3 TO
14BRWKRE.C1P1,14BRWKRE.OPS0,14BRWKRE.MPS0;



ASSIGN 14B_TPR * ((~SH19ASR + ~S20ASR + ~S20A_ASR) * 13NWSR1 + ~S3ASR + ~S5ASR +
~OV3SR + ~OV5SR * 14RWSR) * SYSINITTMR3 TO 14BCKE.C1P1,14BCKE.OPS0,14BCKE.MPS0;



ASSIGN ~14B_TPR * CPS.STATUS3 TO
14BCKRE.C1P1,14BCKRE.OPS0,14BCKRE.MPS0;



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REVISION	
JE-1	CHK.
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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00194	00228	00195

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

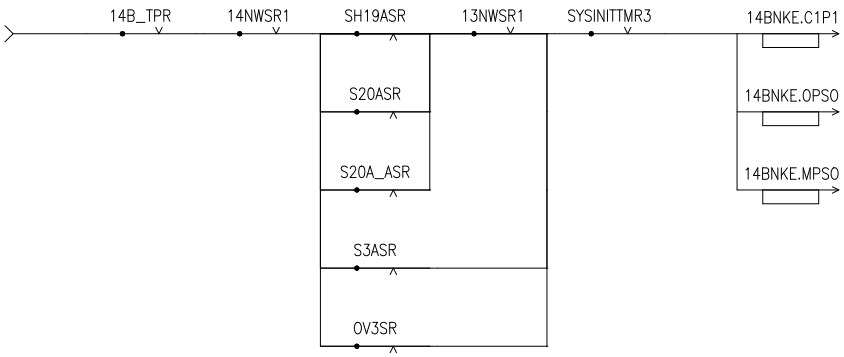
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

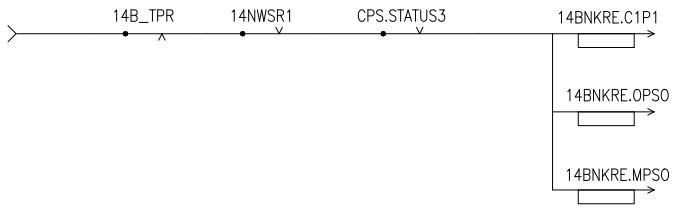
CAD FILE: 512_TAC_C1_00194_R00.DWG

(MICROLOK-II WARM STANDBY)

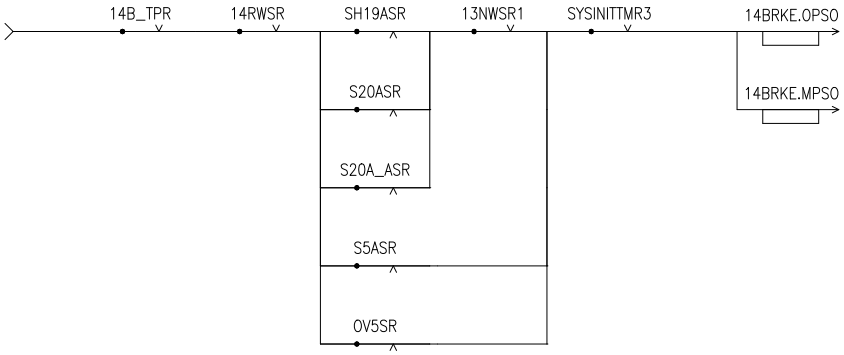
ASSIGN 14B_TPR * 14NWSR1 * ((~SH19ASR + ~S20ASR + ~S20A_ASR) * 13NWSR1 + ~S3ASR + ~OV3SR) * SYSINITTMR3 TO 14BNKE.C1P1,14BNKE.OPSO,14BNKE.MPSO;



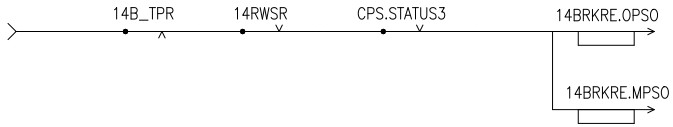
ASSIGN ~14B_TPR * 14NWSR1 * CPS.STATUS3 TO 14BNKRE.C1P1,14BNKRE.OPSO,14BNKRE.MPSO;



ASSIGN 14B_TPR * 14RWSR * ((~SH19ASR + ~S20ASR + ~S20A_ASR) * 13NWSR1 + ~S5ASR + ~OV5SR) * SYSINITTMR3 TO 14BRKE.OPSO,14BRKE.MPSO;



ASSIGN ~14B_TPR * 14RWSR * CPS.STATUS3 TO 14BRKRE.OPSO,14BRKRE.MPSO;



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JE-1	
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CHK.

APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00195	00228	00196

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

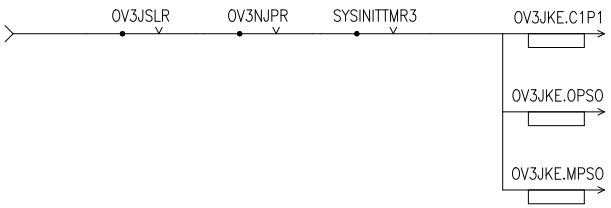
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

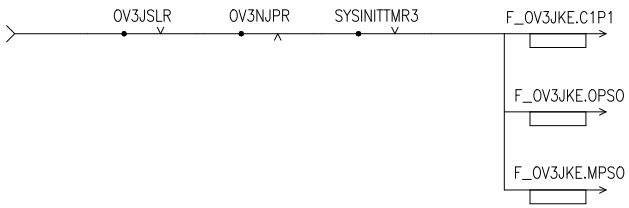
CAD FILE: 512_TAC_C1_00195_R00.DWG

(MICROLOK-II WARM STANDBY)

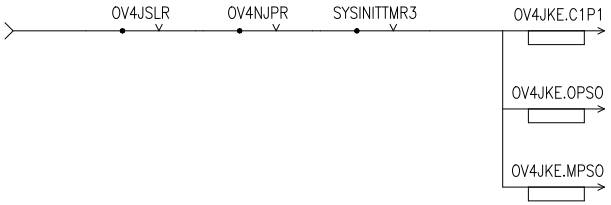
ASSIGN OV3JSLR * OV3NJPR * SYSINITTMR3 TO
OV3JKE.C1P1,OV3JKE.OPS0,OV3JKE.MPS0;



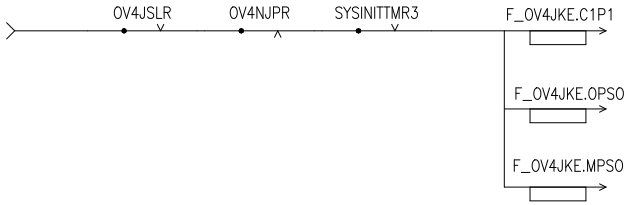
ASSIGN OV3JSLR * ~OV3NJPR * SYSINITTMR3 TO
F_OV3JKE.C1P1,F_OV3JKE.OPS0,F_OV3JKE.MPS0;



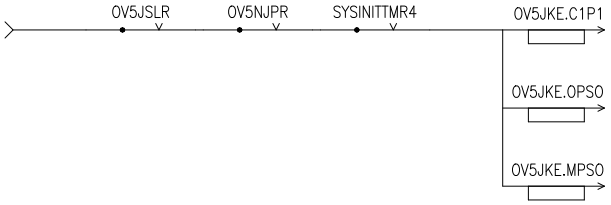
ASSIGN OV4JSLR * OV4NJPR * SYSINITTMR3 TO
OV4JKE.C1P1,OV4JKE.OPS0,OV4JKE.MPS0;



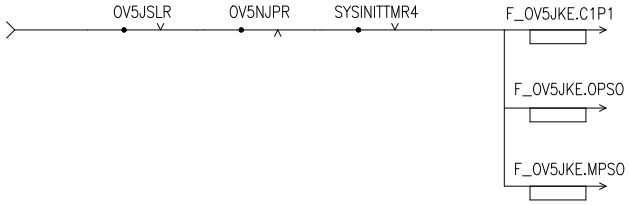
ASSIGN OV4JSLR * ~OV4NJPR * SYSINITTMR3 TO
F_OV4JKE.C1P1,F_OV4JKE.OPS0,F_OV4JKE.MPS0;



ASSIGN OV5JSLR * OV5NJPR * SYSINITTMR4 TO
OV5JKE.C1P1,OV5JKE.OPS0,OV5JKE.MPS0;



ASSIGN OV5JSLR * ~OV5NJPR * SYSINITTMR4 TO
F_OV5JKE.C1P1,F_OV5JKE.OPS0,F_OV5JKE.MPS0;



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TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00196 | dqy 'khVSHEETS 00228 | CONT'D ON: 00197

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

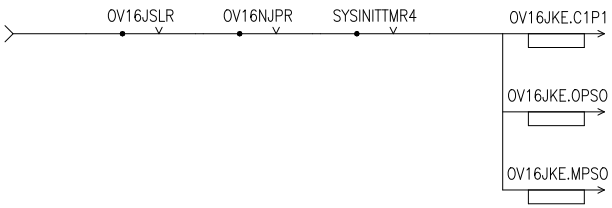
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

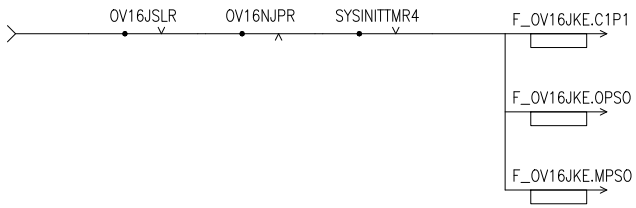
CAD FILE: 512_TAC_C1_00196_R00.DWG

(MICROLOK-II WARM STANDBY)

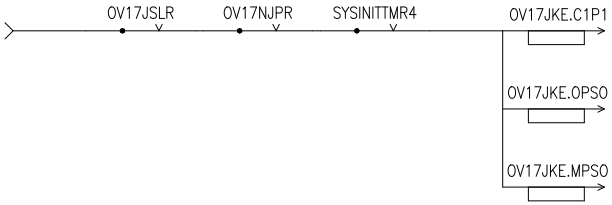
ASSIGN OV16JSLR * OV16NJPR * SYSINITTMR4 TO
OV16JKE.C1P1,OV16JKE.OPS0,OV16JKE.MPS0;



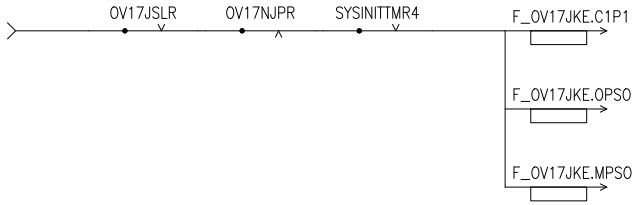
ASSIGN OV16JSLR * ~OV16NJPR * SYSINITTMR4 TO
F_OV16JKE.C1P1,F_OV16JKE.OPS0,F_OV16JKE.MPS0;



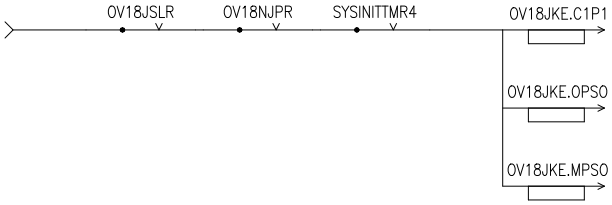
ASSIGN OV17JSLR * OV17NJPR * SYSINITTMR4 TO
OV17JKE.C1P1,OV17JKE.OPS0,OV17JKE.MPS0;



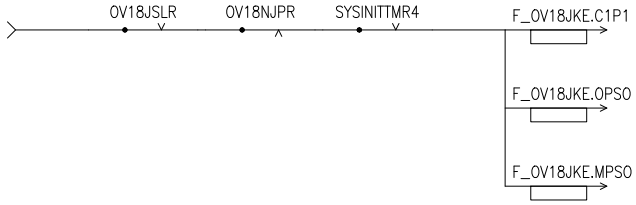
ASSIGN OV17JSLR * ~OV17NJPR * SYSINITTMR4 TO
F_OV17JKE.C1P1,F_OV17JKE.OPS0,F_OV17JKE.MPS0;



ASSIGN OV18JSLR * OV18NJPR * SYSINITTMR4 TO
OV18JKE.C1P1,OV18JKE.OPS0,OV18JKE.MPS0;



ASSIGN OV18JSLR * ~OV18NJPR * SYSINITTMR4 TO
F_OV18JKE.C1P1,F_OV18JKE.OPS0,F_OV18JKE.MPS0;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	a fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00197	00228	00198

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

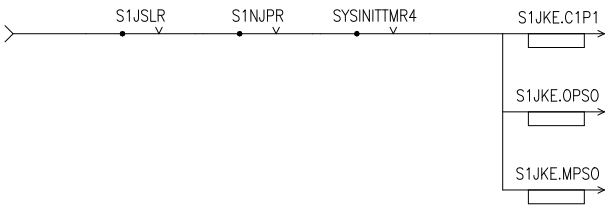
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

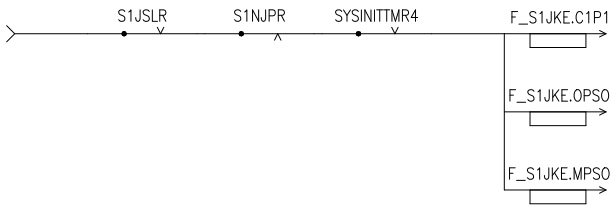
CAD FILE: 512_TAC_C1_00197_R00.DWG

(MICROLOK-II WARM STANDBY)

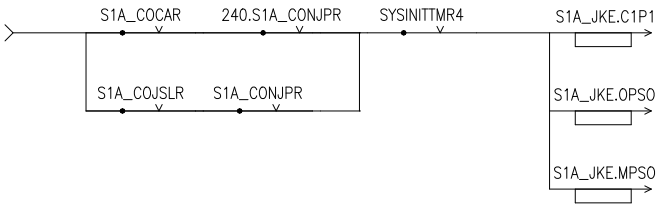
ASSIGN S1JSLR * S1NJPR * SYSINITTMR4 TO S1JKE.C1P1,S1JKE.OPSO,S1JKE.MPSO;



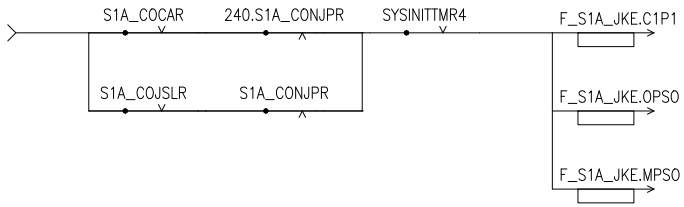
ASSIGN S1JSLR * ~S1NJPR * SYSINITTMR4 TO F_S1JKE.C1P1,F_S1JKE.OPSO,F_S1JKE.MPSO;



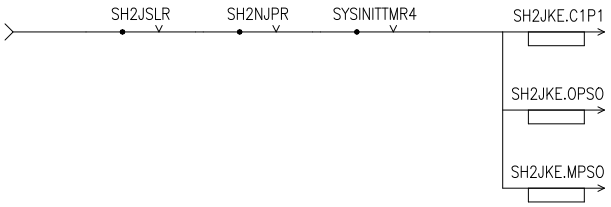
ASSIGN (S1A_COCAR * 240.S1A_CONJPR + S1A_COJSLR * S1A_CONJPR) * SYSINITTMR4 TO S1A_JKE.C1P1,S1A_JKE.OPSO,S1A_JKE.MPSO;



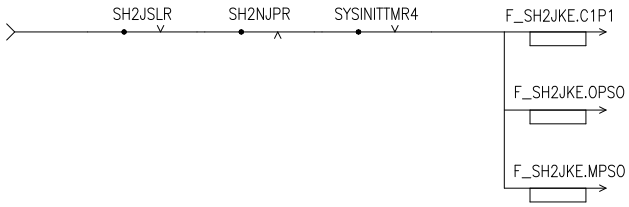
ASSIGN (S1A_COCAR * ~240.S1A_CONJPR + S1A_COJSLR * ~S1A_CONJPR) * SYSINITTMR4 TO F_S1A_JKE.C1P1,F_S1A_JKE.OPSO,F_S1A_JKE.MPSO;



ASSIGN SH2JSLR * SH2NJPR * SYSINITTMR4 TO SH2JKE.C1P1,SH2JKE.OPSO,SH2JKE.MPSO;



ASSIGN SH2JSLR * ~SH2NJPR * SYSINITTMR4 TO F_SH2JKE.C1P1,F_SH2JKE.OPSO,F_SH2JKE.MPSO;



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TVM Signalling & Transportation Systems Pvt. Ltd.
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	

CHK.

APP:

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00198 | dqy 'khVSHEETS 00228 | CONT'D ON: 00199

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

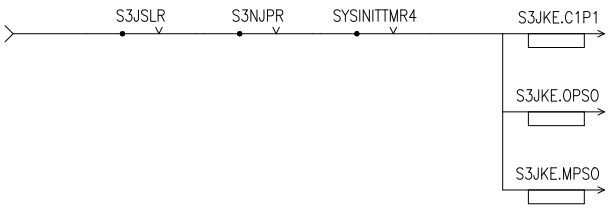
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

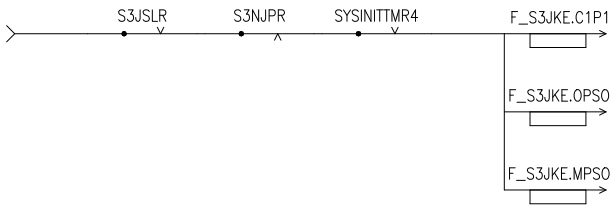
CAD FILE: 512_TAC_C1_00198_R00.DWG

(MICROLOK-II WARM STANDBY)

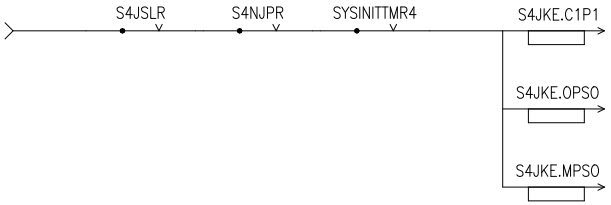
ASSIGN S3JSLR * S3NJPR * SYSINITTMR4 TO S3JKE.C1P1,S3JKE.OPS0,S3JKE.MPS0;



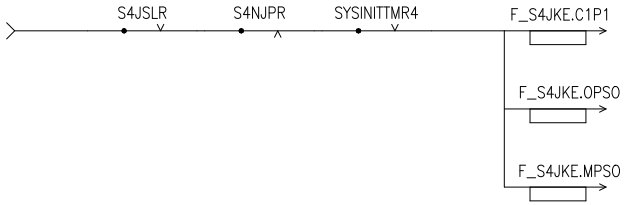
ASSIGN S3JSLR * ~S3NJPR * SYSINITTMR4 TO F_S3JKE.C1P1,F_S3JKE.OPS0,F_S3JKE.MPS0;



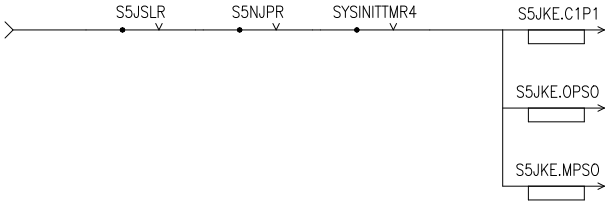
ASSIGN S4JSLR * S4NJPR * SYSINITTMR4 TO S4JKE.C1P1,S4JKE.OPS0,S4JKE.MPS0;



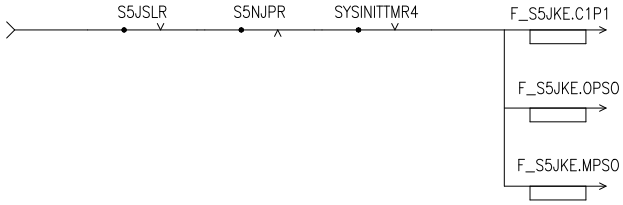
ASSIGN S4JSLR * ~S4NJPR * SYSINITTMR4 TO F_S4JKE.C1P1,F_S4JKE.OPS0,F_S4JKE.MPS0;



ASSIGN S5JSLR * S5NJPR * SYSINITTMR4 TO S5JKE.C1P1,S5JKE.OPS0,S5JKE.MPS0;



ASSIGN S5JSLR * ~S5NJPR * SYSINITTMR4 TO F_S5JKE.C1P1,F_S5JKE.OPS0,F_S5JKE.MPS0;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	

CHK.

APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00199	00228	00200

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

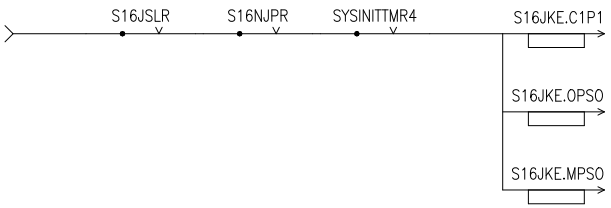
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

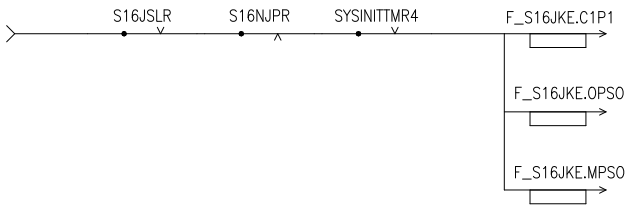
CAD FILE: 512_TAC_C1_00199_R00.DWG

(MICROLOK-II WARM STANDBY)

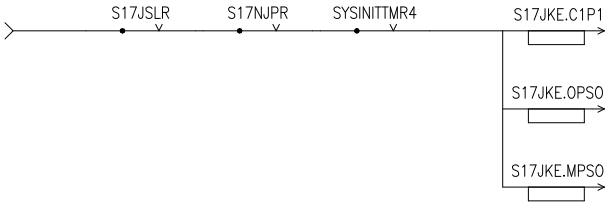
ASSIGN S16JSLR * S16NJPR * SYSINITTMR4 TO
S16JKE.C1P1,S16JKE.OPS0,S16JKE.MPS0;



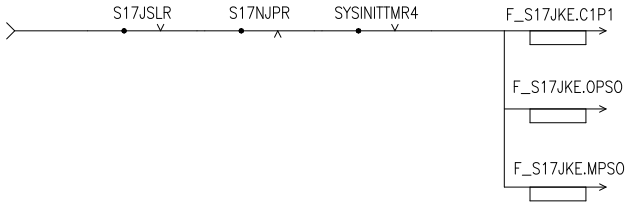
ASSIGN S16JSLR * ~S16NJPR * SYSINITTMR4 TO
F_S16JKE.C1P1,F_S16JKE.OPS0,F_S16JKE.MPS0;



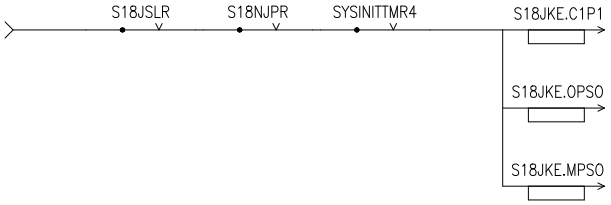
ASSIGN S17JSLR * S17NJPR * SYSINITTMR4 TO
S17JKE.C1P1,S17JKE.OPS0,S17JKE.MPS0;



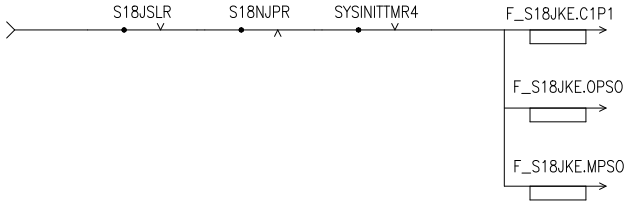
ASSIGN S17JSLR * ~S17NJPR * SYSINITTMR4 TO
F_S17JKE.C1P1,F_S17JKE.OPS0,F_S17JKE.MPS0;



ASSIGN S18JSLR * S18NJPR * SYSINITTMR4 TO
S18JKE.C1P1,S18JKE.OPS0,S18JKE.MPS0;



ASSIGN S18JSLR * ~S18NJPR * SYSINITTMR4 TO
F_S18JKE.C1P1,F_S18JKE.OPS0,F_S18JKE.MPS0;



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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
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ASTE	
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khVSHEET 00200	dqy 'khVSHEETS 00228	CONT'D ON: 00201

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

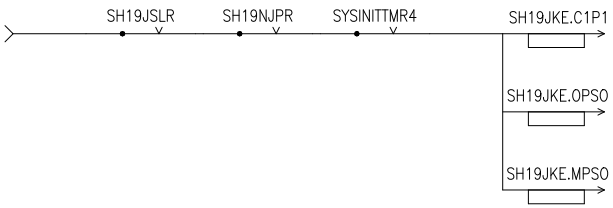
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

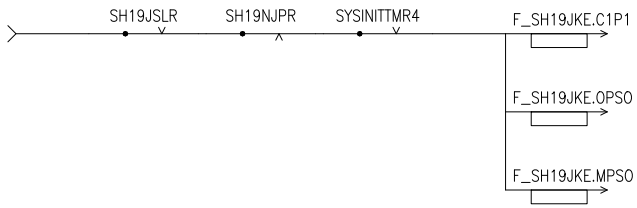
CAD FILE: 512_TAC_C1_00200_R00.DWG

(MICROLOK-II WARM STANDBY)

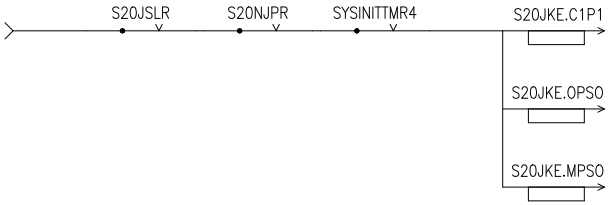
ASSIGN SH19JSLR * SH19NJPR * SYSINITTMR4 TO
SH19JKE.C1P1,SH19JKE.OPSO,SH19JKE.MPSO;



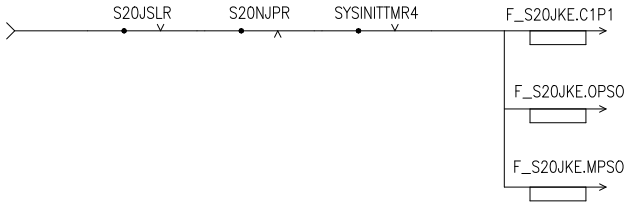
ASSIGN SH19JSLR * ~SH19NJPR * SYSINITTMR4 TO
F_SH19JKE.C1P1,F_SH19JKE.OPSO,F_SH19JKE.MPSO;



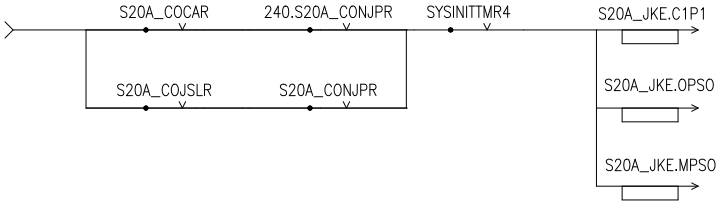
ASSIGN S20JSLR * S20NJPR * SYSINITTMR4 TO
S20JKE.C1P1,S20JKE.OPSO,S20JKE.MPSO;



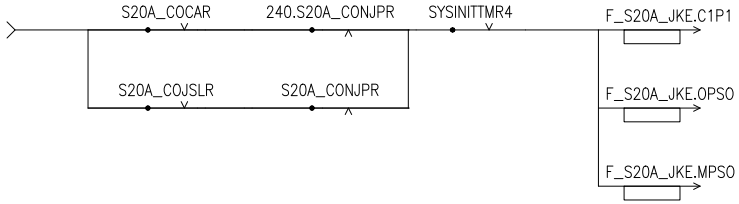
ASSIGN S20JSLR * ~S20NJPR * SYSINITTMR4 TO
F_S20JKE.C1P1,F_S20JKE.OPSO,F_S20JKE.MPSO;



ASSIGN (S20A_COCAR * 240.S20A_CONJPR + S20A_COJSLR * S20A_CONJPR) * SYSINITTMR4 TO
S20A_JKE.C1P1,S20A_JKE.OPSO,S20A_JKE.MPSO;



ASSIGN (S20A_COCAR * ~240.S20A_CONJPR + S20A_COJSLR * ~S20A_CONJPR) * SYSINITTMR4
TO F_S20A_JKE.C1P1,F_S20A_JKE.OPSO,F_S20A_JKE.MPSO;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
ASTE	
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CHK.

APP:

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00201	00228	00202

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

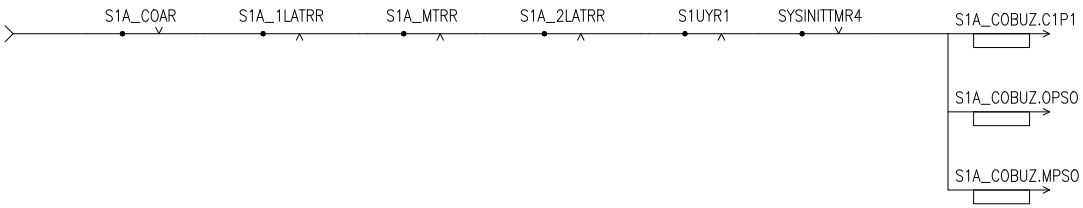
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

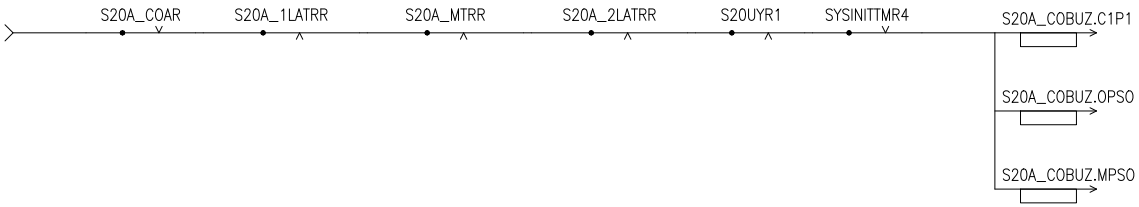
CAD FILE: 512_TAC_C1_00201_R00.DWG

(MICROLOK-II WARM STANDBY)

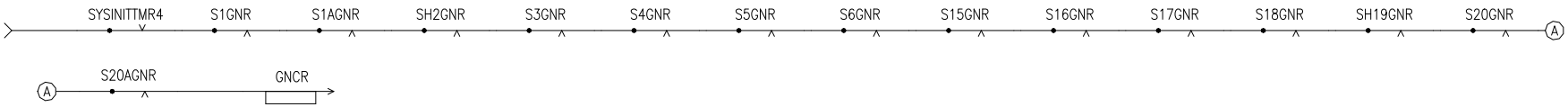
ASSIGN S1A_COAR * ~S1A_1LATRR * ~S1A_MTRR * ~S1A_2LATRR * ~S1UYR1 * SYSINITTMR4 TO
S1A_COBUZ.C1P1,S1A_COBUZ.OPSO,S1A_COBUZ.MPSO;



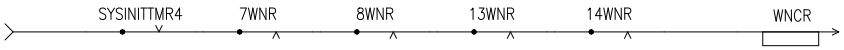
ASSIGN S20A_COAR * ~S20A_1LATRR * ~S20A_MTRR * ~S20A_2LATRR * ~S20UYR1 * SYSINITTMR4 TO
S20A_COBUZ.C1P1,S20A_COBUZ.OPSO,S20A_COBUZ.MPSO;



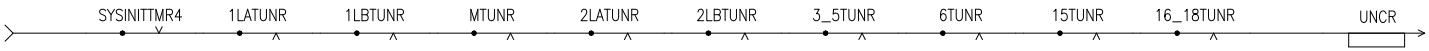
ASSIGN SYSINITTMR4 * ~S1GNR * ~S1AGNR * ~SH2GNR * ~S3GNR * ~S4GNR * ~S5GNR * ~S6GNR * ~S15GNR * ~S16GNR * ~S17GNR * ~S18GNR * ~SH19GNR * ~S20GNR * ~S20AGNR TO GNCR;



ASSIGN SYSINITTMR4 * ~7WNR * ~8WNR * ~13WNR * ~14WNR TO WNCR;



ASSIGN SYSINITTMR4 * ~1LATUNR * ~1LBTUNR * ~MTUNR * ~2LATUNR * ~2LBTUNR * ~3_5TUNR * ~6TUNR * ~15TUNR * ~16_18TUNR TO UNCR;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khVSHEET 00202 | dqy 'khVSHEETS 00228 | CONT'D ON: 00203

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

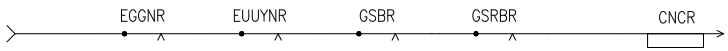
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

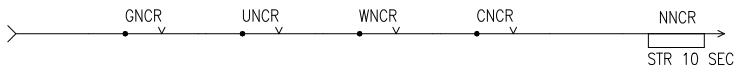
CAD FILE: 512_TAC_C1_00202_R00.DWG

(MICROLOK-II WARM STANDBY)

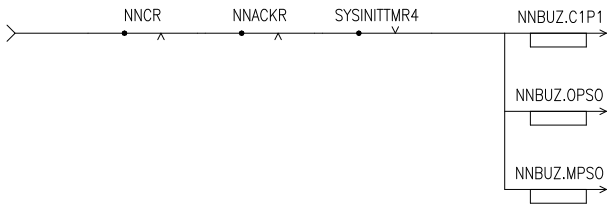
ASSIGN ~EGGNR * ~EUUYNR * ~GSBR * ~GSRBR TO CNCR;



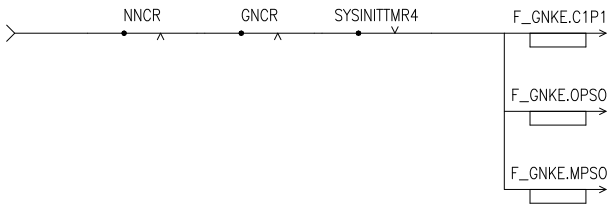
ASSIGN GNCR * UNCR * WNCR * CNCR TO NNCR;



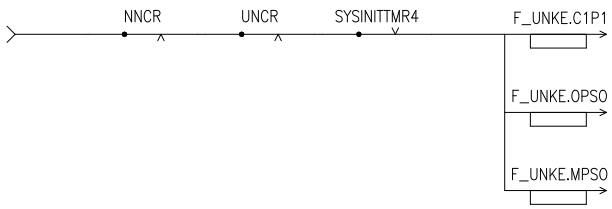
ASSIGN ~NNCR * ~NNACKR * SYSINITTMR4 TO
NNBUZ.C1P1,NNBUZ.OPSO,NNBUZ.MPSO;



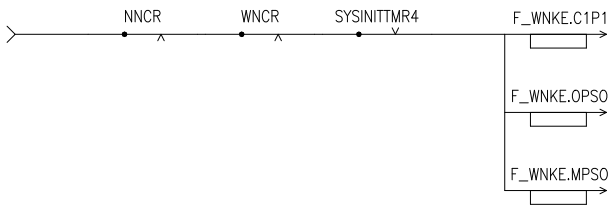
ASSIGN ~NNCR * ~GNCR * SYSINITTMR4 TO
F_GNKE.C1P1,F_GNKE.OPSO,F_GNKE.MPSO;



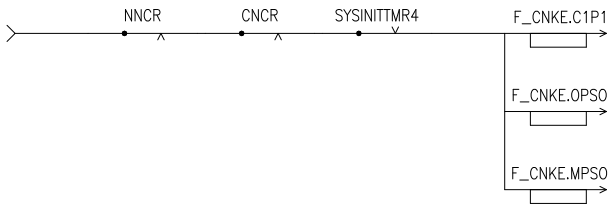
ASSIGN ~NNCR * ~UNCR * SYSINITTMR4 TO
F_UNKE.C1P1,F_UNKE.OPSO,F_UNKE.MPSO;



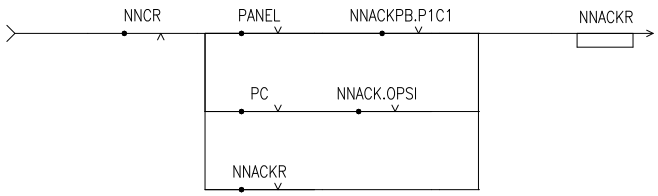
ASSIGN ~NNCR * ~WNCR * SYSINITTMR4 TO
F_WNKE.C1P1,F_WNKE.OPSO,F_WNKE.MPSO;



ASSIGN ~NNCR * ~CNCR * SYSINITTMR4 TO
F_CNKE.C1P1,F_CNKE.OPSO,F_CNKE.MPSO;



ASSIGN ~NNCR * (PANEL * NNACKPB.P1C1 + PC * NNACK.OPSI + NNACKR) TO
NNACKR;



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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
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Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00203 | dqy 'khVSHEETS 00228 | CONT'D ON: 00204

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

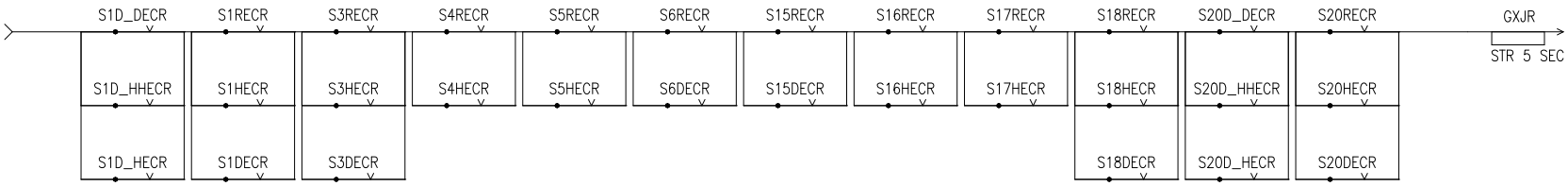
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

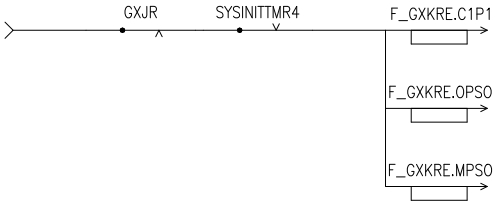
CAD FILE: 512_TAC_C1_00203_R00.DWG

(MICROLOK-II WARM STANDBY)

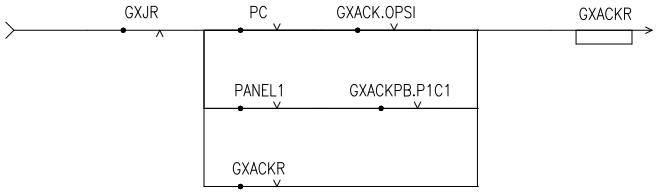
ASSIGN (S1D_DECR + S1D_HHECR + S1D_HECR) * (S1RECR + S1HECR + S1DECR) * (S3RECR + S3HECR + S3DECR) * (S4RECR + S4HECR) * (S5RECR + S5HECR) * (S6RECR + S6DECR) * (S15RECR + S15DECR) * (S16RECR + S16HECR) * (S17RECR + S17HECR) * (S18RECR + S18HECR + S18DECR) * (S20D_DECR + S20D_HHECR + S20D_HECR) * (S20RECR + S20HECR + S20DECR) TO GXJR;



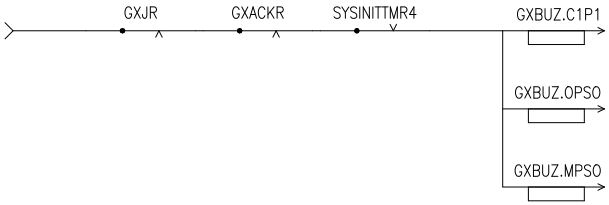
ASSIGN ~GXJR * SYSINITTMR4 TO F_GXKRE.C1P1,F_GXKRE.OPSO,F_GXKRE.MPSO;



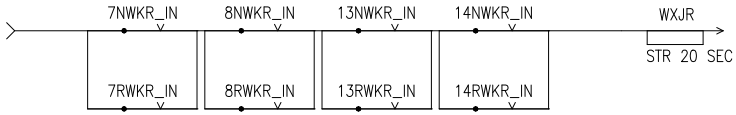
ASSIGN ~GXJR * (PC * GXACK.OPSI + PANEL1 * GXACKPB.P1C1 + GXACKR) TO GXACKR;



ASSIGN ~GXJR * ~GXACKR * SYSINITTMR4 TO GXBUZ.C1P1,GXBUZ.OPSO,GXBUZ.MPSO;



ASSIGN (7NWKR_IN + 7RWKR_IN) * (8NWKR_IN + 8RWKR_IN) * (13NWKR_IN + 13RWKR_IN) * (14NWKR_IN + 14RWKR_IN) TO WXJR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00204 | dqy 'khVSHEETS 00228 | CONT'D ON: 00205

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

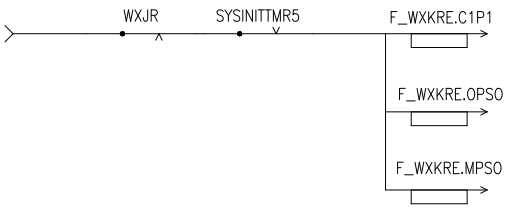
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

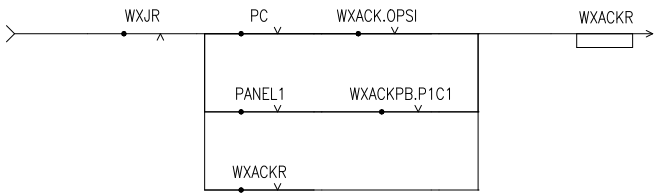
CAD FILE: 512_TAC_C1_00204_R00.DWG

(MICROLOK-II WARM STANDBY)

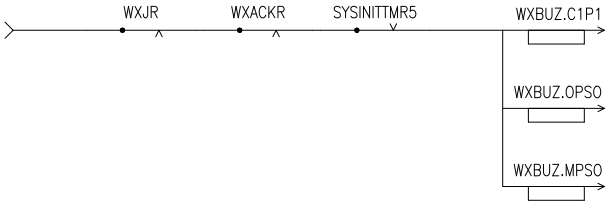
ASSIGN ~WXJR * SYSINITTMR5 TO
F_WXKRE.C1P1,F_WXKRE.OPSO,F_WXKRE.MPSO;



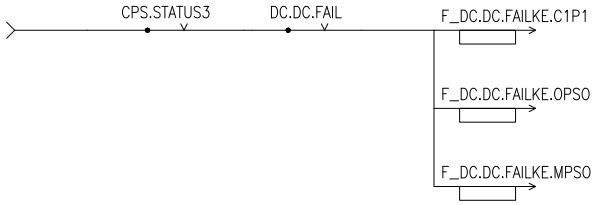
ASSIGN ~WXJR * (PC * WXACK.OPSI + PANEL1 * WXACKPB.P1C1 + WXACKR) TO
WXACKR;



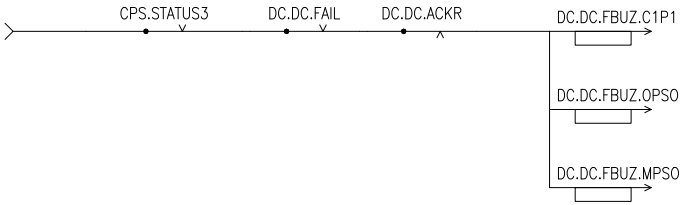
ASSIGN ~WXJR * ~WXACKR * SYSINITTMR5 TO
WXBUZ.C1P1,WXBUZ.OPSO,WXBUZ.MPSO;



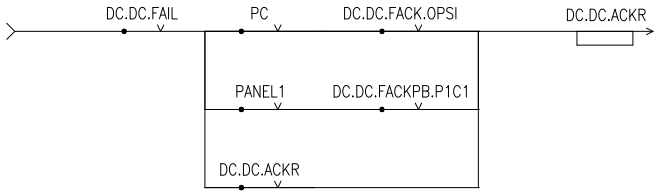
ASSIGN CPS.STATUS3 * DC.DC.FAIL TO
F_DC.DC.FAILKE.C1P1,F_DC.DC.FAILKE.OPSO,F_DC.DC.FAILKE.MPSO;



ASSIGN CPS.STATUS3 * DC.DC.FAIL * ~DC.DC.ACKR TO
DC.DC.FBUZ.C1P1,DC.DC.FBUZ.OPSO,DC.DC.FBUZ.MPSO;



ASSIGN DC.DC.FAIL * (PC * DC.DC.FACK.OPSI + PANEL1 * DC.DC.FACKPB.P1C1 +
DC.DC.ACKR) TO DC.DC.ACKR;



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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
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ASTE	
SSTE	
f CSTE	

CHK.

APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	a fc B
IPU.2107B		
'khV SHEET	dqy 'khV SHEETS	CONT'D ON:
00205	00228	00206

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

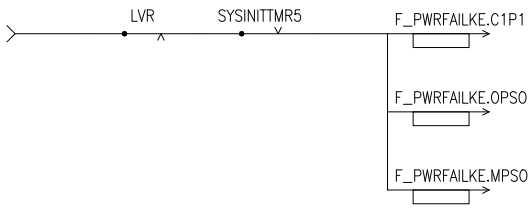
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

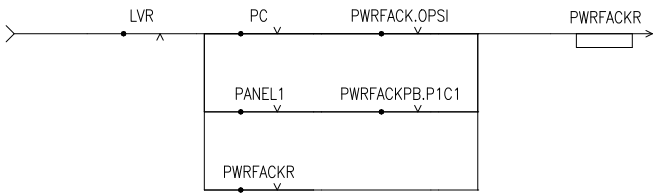
CAD FILE: 512_TAC_C1_00205_R00.DWG

(MICROLOK-II WARM STANDBY)

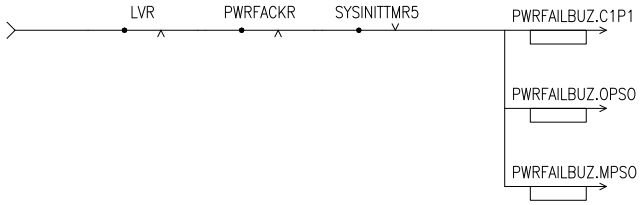
ASSIGN ~LVR * SYSINITTMR5 TO
F_PWRFAILKE.C1P1,F_PWRFAILKE.OPS0,F_PWRFAILKE.MPS0;



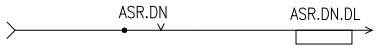
ASSIGN ~LVR * (PC * PWRFACK.OPSI + PANEL1 * PWRFACKPB.P1C1 + PWRFACKR) TO
PWRFACKR;



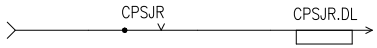
ASSIGN ~LVR * ~PWRFACKR * SYSINITTMR5 TO
PWRFAILBUZ.C1P1,PWRFAILBUZ.OPS0,PWRFAILBUZ.MPS0;



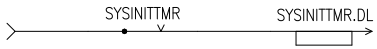
NV.ASSIGN ASR.DN TO ASR.DN.DL;



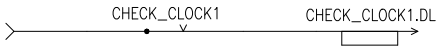
NV.ASSIGN CPSJR TO CPSJR.DL;



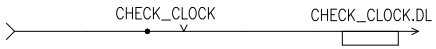
NV.ASSIGN SYSINITTMR TO SYSINITTMR.DL;



NV.ASSIGN CHECK_CLOCK1 TO CHECK_CLOCK1.DL;

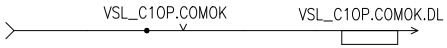


NV.ASSIGN CHECK_CLOCK TO CHECK_CLOCK.DL;

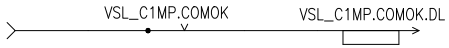


NV.ASSIGN PEER.CLOCK.SET TO PEER.CLOCK.SET.DL;

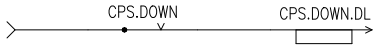
NV.ASSIGN VSL_C1OP.COMOK TO VSL_C1OP.COMOK.DL;



NV.ASSIGN VSL_C1MP.COMOK TO VSL_C1MP.COMOK.DL;

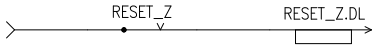


NV.ASSIGN CPS.DOWN TO CPS.DOWN.DL;

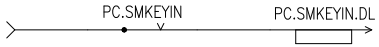


NV.ASSIGN LINK.FAIL TO LINK.FAIL.DL;

NV.ASSIGN RESET_Z TO RESET_Z.DL;



NV.ASSIGN PC.SMKEYIN TO PC.SMKEYIN.DL;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00206 | dqy 'khVSHEETS 00228 | CONT'D ON: 00207

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

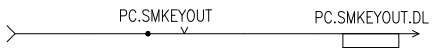
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

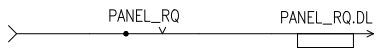
CAD FILE: 512_TAC_C1_00206_R00.DWG

(MICROLOK-II WARM STANDBY)

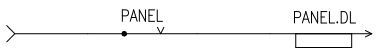
NV.ASSIGN PC.SMKEYOUT TO PC.SMKEYOUT.DL;



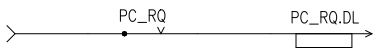
NV.ASSIGN PANEL_RQ TO PANEL_RQ.DL;



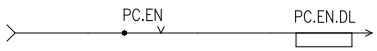
NV.ASSIGN PANEL TO PANEL.DL;



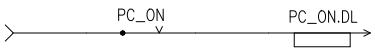
NV.ASSIGN PC_RQ TO PC_RQ.DL;



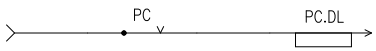
NV.ASSIGN PC.EN TO PC.EN.DL;



NV.ASSIGN PC_ON TO PC_ON.DL;

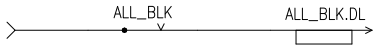


NV.ASSIGN PC TO PC.DL;



NV.ASSIGN ALL_B_INT TO ALL_B_INT.DL;

NV.ASSIGN ALL_BLK TO ALL_BLK.DL;



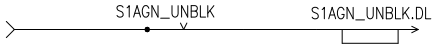
NV.ASSIGN ALL_UNBLK TO ALL_UNBLK.DL;

NV.ASSIGN S1GN_BLK TO S1GN_BLK.DL;

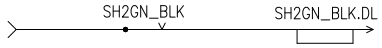
NV.ASSIGN S1GN_UNBLK TO S1GN_UNBLK.DL;

NV.ASSIGN S1AGN_BLK TO S1AGN_BLK.DL;

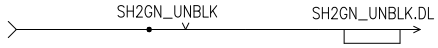
NV.ASSIGN S1AGN_UNBLK TO S1AGN_UNBLK.DL;



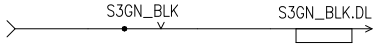
NV.ASSIGN SH2GN_BLK TO SH2GN_BLK.DL;



NV.ASSIGN SH2GN_UNBLK TO SH2GN_UNBLK.DL;



NV.ASSIGN S3GN_BLK TO S3GN_BLK.DL;



NV.ASSIGN S3GN_UNBLK TO S3GN_UNBLK.DL;

NV.ASSIGN S4GN_BLK TO S4GN_BLK.DL;

NV.ASSIGN S4GN_UNBLK TO S4GN_UNBLK.DL;

NV.ASSIGN S5GN_BLK TO S5GN_BLK.DL;

NV.ASSIGN S5GN_UNBLK TO S5GN_UNBLK.DL;

NV.ASSIGN S6GN_BLK TO S6GN_BLK.DL;

NV.ASSIGN S6GN_UNBLK TO S6GN_UNBLK.DL;

NV.ASSIGN S15GN_BLK TO S15GN_BLK.DL;

NV.ASSIGN S15GN_UNBLK TO S15GN_UNBLK.DL;

NV.ASSIGN S16GN_BLK TO S16GN_BLK.DL;

NV.ASSIGN S16GN_UNBLK TO S16GN_UNBLK.DL;

REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
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TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
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Tel: +91 80 2654 0781,82,83
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
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f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B	
IPU.2107B			
'khVSHEET	dqy 'khVSHEETS	CONT'D ON:	00208
00207	00228		

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00207_R00.DWG

(MICROLOK-II WARM STANDBY)

NV.ASSIGN S17GN_BLK TO S17GN_BLK.DL;

NV.ASSIGN S17GN_UNBLK TO S17GN_UNBLK.DL;

NV.ASSIGN S18GN_BLK TO S18GN_BLK.DL;

NV.ASSIGN S18GN_UNBLK TO S18GN_UNBLK.DL;

NV.ASSIGN SH19GN_BLK TO SH19GN_BLK.DL;

NV.ASSIGN SH19GN_UNBLK TO SH19GN_UNBLK.DL;

NV.ASSIGN S20GN_BLK TO S20GN_BLK.DL;

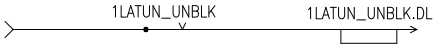
NV.ASSIGN S20GN_UNBLK TO S20GN_UNBLK.DL;

NV.ASSIGN S20AGN_BLK TO S20AGN_BLK.DL;

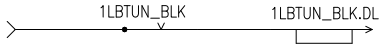
NV.ASSIGN S20AGN_UNBLK TO S20AGN_UNBLK.DL;

NV.ASSIGN 1LATUN_BLK TO 1LATUN_BLK.DL;

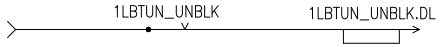
NV.ASSIGN 1LATUN_UNBLK TO 1LATUN_UNBLK.DL;



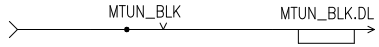
NV.ASSIGN 1LBTUN_BLK TO 1LBTUN_BLK.DL;



NV.ASSIGN 1LBTUN_UNBLK TO 1LBTUN_UNBLK.DL;



NV.ASSIGN MTUN_BLK TO MTUN_BLK.DL;



NV.ASSIGN MTUN_UNBLK TO MTUN_UNBLK.DL;

NV.ASSIGN 2LATUN_BLK TO 2LATUN_BLK.DL;

NV.ASSIGN 2LATUN_UNBLK TO 2LATUN_UNBLK.DL;

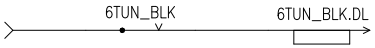
NV.ASSIGN 2LBTUN_BLK TO 2LBTUN_BLK.DL;

NV.ASSIGN 2LBTUN_UNBLK TO 2LBTUN_UNBLK.DL;

NV.ASSIGN 3_5TUN_BLK TO 3_5TUN_BLK.DL;

NV.ASSIGN 3_5TUN_UNBLK TO 3_5TUN_UNBLK.DL;

NV.ASSIGN 6TUN_BLK TO 6TUN_BLK.DL;



NV.ASSIGN 6TUN_UNBLK TO 6TUN_UNBLK.DL;

NV.ASSIGN 15TUN_BLK TO 15TUN_BLK.DL;

REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
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TVM Signalling & Transportation Systems Pvt. Ltd.
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CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khVSHEET 00208 | dqy 'khVSHEETS 00228 | CONT'D ON: 00209

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

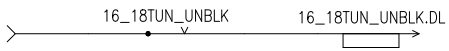
(MICROLOK-II WARM STANDBY)

CAD FILE: 512_TAC_C1_00208_R00.DWG

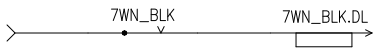
NV.ASSIGN 15TUN_UNBLK TO 15TUN_UNBLK.DL;

NV.ASSIGN 16_18TUN_BLK TO 16_18TUN_BLK.DL;

NV.ASSIGN 16_18TUN_UNBLK TO 16_18TUN_UNBLK.DL;

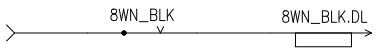


NV.ASSIGN 7WN_BLK TO 7WN_BLK.DL;



NV.ASSIGN 7WN_UNBLK TO 7WN_UNBLK.DL;

NV.ASSIGN 8WN_BLK TO 8WN_BLK.DL;



NV.ASSIGN 8WN_UNBLK TO 8WN_UNBLK.DL;

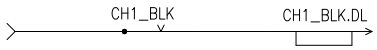
NV.ASSIGN 13WN_BLK TO 13WN_BLK.DL;

NV.ASSIGN 13WN_UNBLK TO 13WN_UNBLK.DL;

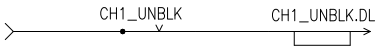
NV.ASSIGN 14WN_BLK TO 14WN_BLK.DL;

NV.ASSIGN 14WN_UNBLK TO 14WN_UNBLK.DL;

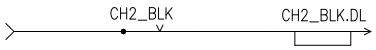
NV.ASSIGN CH1_BLK TO CH1_BLK.DL;



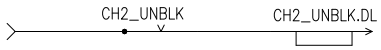
NV.ASSIGN CH1_UNBLK TO CH1_UNBLK.DL;



NV.ASSIGN CH2_BLK TO CH2_BLK.DL;



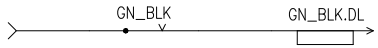
NV.ASSIGN CH2_UNBLK TO CH2_UNBLK.DL;



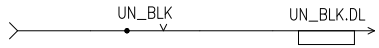
NV.ASSIGN 12LX_BLK TO 12LX_BLK.DL;

NV.ASSIGN 12LX_UNBLK TO 12LX_UNBLK.DL;

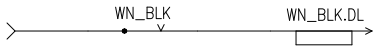
NV.ASSIGN GN_BLK TO GN_BLK.DL;



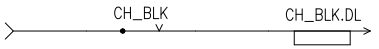
NV.ASSIGN UN_BLK TO UN_BLK.DL;



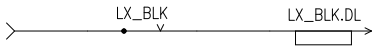
NV.ASSIGN WN_BLK TO WN_BLK.DL;



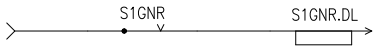
NV.ASSIGN CH_BLK TO CH_BLK.DL;



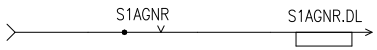
NV.ASSIGN LX_BLK TO LX_BLK.DL;



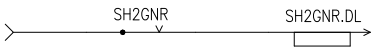
NV.ASSIGN S1GNR TO S1GNR.DL;



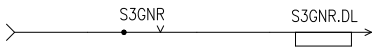
NV.ASSIGN S1AGNR TO S1AGNR.DL;



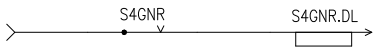
NV.ASSIGN SH2GNR TO SH2GNR.DL;



NV.ASSIGN S3GNR TO S3GNR.DL;



NV.ASSIGN S4GNR TO S4GNR.DL;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	

CHK.

APP:

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00209	00228	00210

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

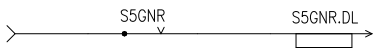
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00209_R00.DWG

(MICROLOK-II WARM STANDBY)

NV.ASSIGN S5GNR TO S5GNR.DL;



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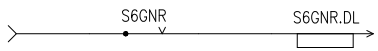

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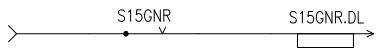

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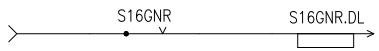

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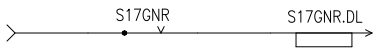
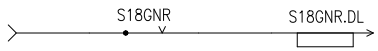
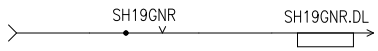


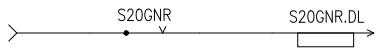
NV.ASSIGN S18GNR TO S18GNR.DL;



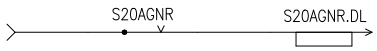
NV.ASSIGN SH19GNR TO SH19GNR.DL;



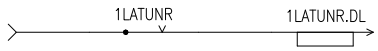
NV.ASSIGN S20GNR TO S20GNR.DL;



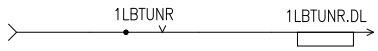
NV.ASSIGN S20AGNR TO S20AGNR.DL;



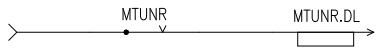
NV.ASSIGN 1LATUNR TO 1LATUNR.DL;



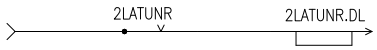
NV.ASSIGN 1LBTUNR TO 1LBTUNR.DL;



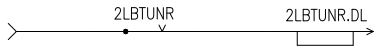
NV.ASSIGN MTUNR TO MTUNR.DL;



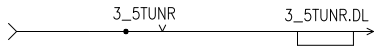
NV.ASSIGN 2LATUNR TO 2LATUNR.DL;



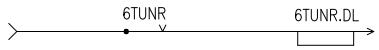
NV.ASSIGN 2LBTUNR TO 2LBTUNR.DL;



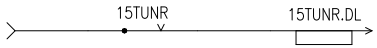
NV.ASSIGN 3_5TUNR TO 3_5TUNR.DL;



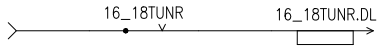
NV.ASSIGN 6TUNR TO 6TUNR.DL;



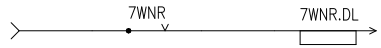
NV.ASSIGN 15TUNR TO 15TUNR.DL;



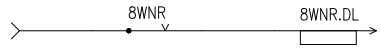
NV.ASSIGN 16_18TUNR TO 16_18TUNR.DL;



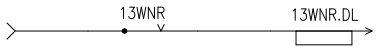
NV.ASSIGN 7WNR TO 7WNR.DL;



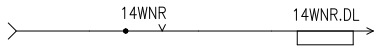
NV.ASSIGN 8WNR TO 8WNR.DL;



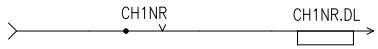
NV.ASSIGN 13WNR TO 13WNR.DL;



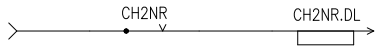
NV.ASSIGN 14WNR TO 14WNR.DL;



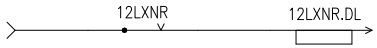
NV.ASSIGN CH1NR TO CH1NR.DL;



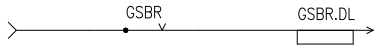
NV.ASSIGN CH2NR TO CH2NR.DL;



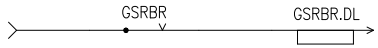
NV.ASSIGN 12LXNR TO 12LXNR.DL;



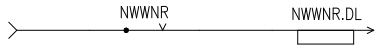
NV.ASSIGN GSBR TO GSBR.DL;



NV.ASSIGN GSRBR TO GSRBR.DL;



NV.ASSIGN NWWNR TO NWWNR.DL;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	

CHK.

APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		

'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00210	00228	00211

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

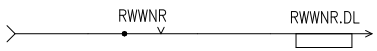
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

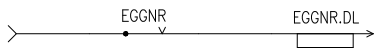
CAD FILE: 512_TAC_C1_00210_R00.DWG

(MICROLOK-II WARM STANDBY)

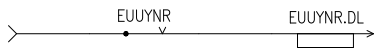
NV.ASSIGN RWWNR TO RWWNR.DL;



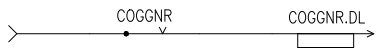
NV.ASSIGN EGGNR TO EGGNR.DL;



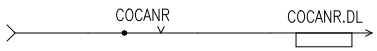
NV.ASSIGN EUUYNR TO EUUYNR.DL;



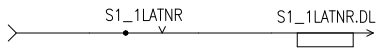
NV.ASSIGN COGGNR TO COGGNR.DL;



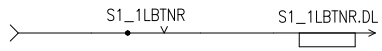
NV.ASSIGN COCANR TO COCANR.DL;



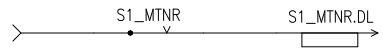
NV.ASSIGN S1_1LATNR TO S1_1LATNR.DL;



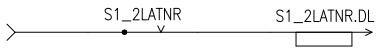
NV.ASSIGN S1_1LBTNR TO S1_1LBTNR.DL;



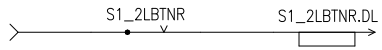
NV.ASSIGN S1_MTNR TO S1_MTNR.DL;



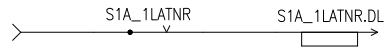
NV.ASSIGN S1_2LATNR TO S1_2LATNR.DL;



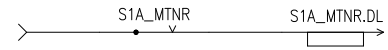
NV.ASSIGN S1_2LBTNR TO S1_2LBTNR.DL;



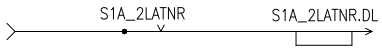
NV.ASSIGN S1A_1LATNR TO S1A_1LATNR.DL;



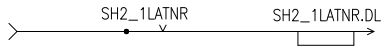
NV.ASSIGN S1A_MTNR TO S1A_MTNR.DL;



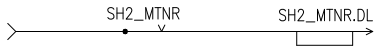
NV.ASSIGN S1A_2LATNR TO S1A_2LATNR.DL;



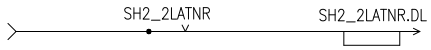
NV.ASSIGN SH2_1LATNR TO SH2_1LATNR.DL;



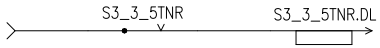
NV.ASSIGN SH2_MTNR TO SH2_MTNR.DL;



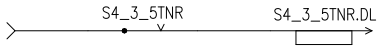
NV.ASSIGN SH2_2LATNR TO SH2_2LATNR.DL;



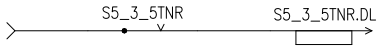
NV.ASSIGN S3_3_5TNR TO S3_3_5TNR.DL;



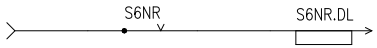
NV.ASSIGN S4_3_5TNR TO S4_3_5TNR.DL;



NV.ASSIGN S5_3_5TNR TO S5_3_5TNR.DL;

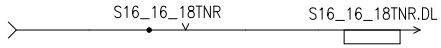


NV.ASSIGN S6NR TO S6NR.DL;

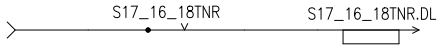


NV.ASSIGN S15NR TO S15NR.DL;

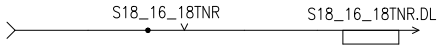
NV.ASSIGN S16_16_18TNR TO S16_16_18TNR.DL;



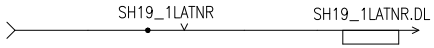
NV.ASSIGN S17_16_18TNR TO S17_16_18TNR.DL;



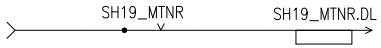
NV.ASSIGN S18_16_18TNR TO S18_16_18TNR.DL;



NV.ASSIGN SH19_1LATNR TO SH19_1LATNR.DL;



NV.ASSIGN SH19_MTNR TO SH19_MTNR.DL;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khVSHEET	dqy 'khVSHEETS	CONT'D ON: 00212
00211	00228	

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

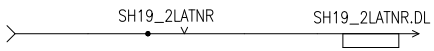
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00211_R00.DWG

(MICROLOK-II WARM STANDBY)

NV.ASSIGN SH19_2LATNR TO SH19_2LATNR.DL;



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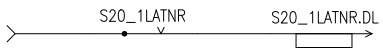
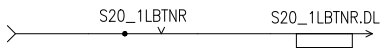


NV.ASSIGN S20_1LBTNR TO S20_1LBTNR.DL;



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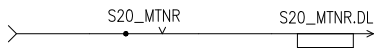
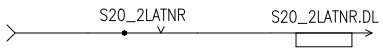
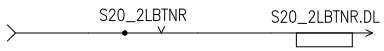


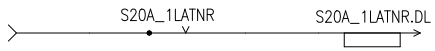
NV.ASSIGN S20_2LATNR TO S20_2LATNR.DL;



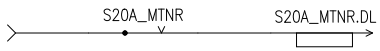
NV.ASSIGN S20_2LBTNR TO S20_2LBTNR.DL;



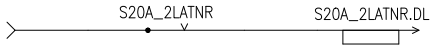
NV.ASSIGN S20A_1LATNR TO S20A_1LATNR.DL;



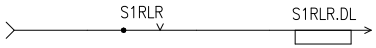
NV.ASSIGN S20A_MTNR TO S20A_MTNR.DL;



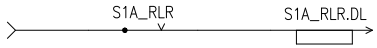
NV.ASSIGN S20A_2LATNR TO S20A_2LATNR.DL;



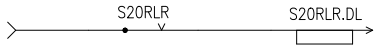
NV.ASSIGN S1RLR TO S1RLR.DL;



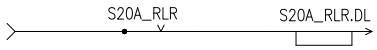
NV.ASSIGN S1A_RLR TO S1A_RLR.DL;



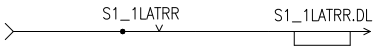
NV.ASSIGN S20RLR TO S20RLR.DL;



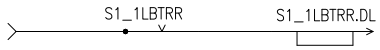
NV.ASSIGN S20A_RLR TO S20A_RLR.DL;



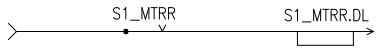
NV.ASSIGN S1_1LATRR TO S1_1LATRR.DL;



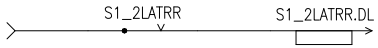
NV.ASSIGN S1_1LBTRR TO S1_1LBTRR.DL;



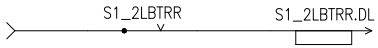
NV.ASSIGN S1_MTRR TO S1_MTRR.DL;



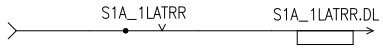
NV.ASSIGN S1_2LATRR TO S1_2LATRR.DL;



NV.ASSIGN S1_2LBTRR TO S1_2LBTRR.DL;

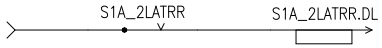


NV.ASSIGN S1A_1LATRR TO S1A_1LATRR.DL;



NV.ASSIGN S1A_MTRR TO S1A_MTRR.DL;

NV.ASSIGN S1A_2LATRR TO S1A_2LATRR.DL;



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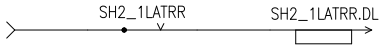
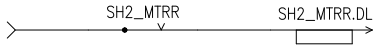
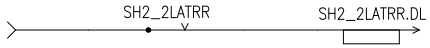


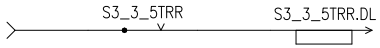
NV.ASSIGN SH2_MTRR TO SH2_MTRR.DL;



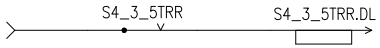
NV.ASSIGN SH2_2LATRR TO SH2_2LATRR.DL;



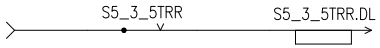
NV.ASSIGN S3_3_5TRR TO S3_3_5TRR.DL;



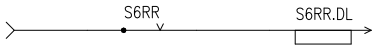
NV.ASSIGN S4_3_5TRR TO S4_3_5TRR.DL;



NV.ASSIGN S5_3_5TRR TO S5_3_5TRR.DL;



NV.ASSIGN S6RR TO S6RR.DL;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00212	00228	00213

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

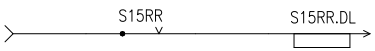
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

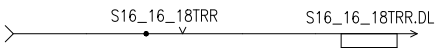
CAD FILE: 512_TAC_C1_00212_R00.DWG

(MICROLOK-II WARM STANDBY)

NV.ASSIGN S15RR TO S15RR.DL;



NV.ASSIGN S16_16_18TRR TO S16_16_18TRR.DL;



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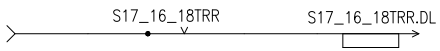

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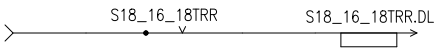

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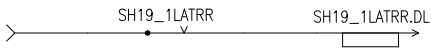
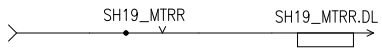


NV.ASSIGN SH19_MTRR TO SH19_MTRR.DL;



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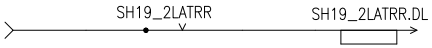
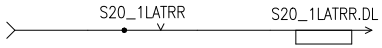


NV.ASSIGN S20_1LATRR TO S20_1LATRR.DL;



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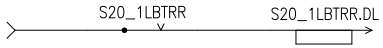
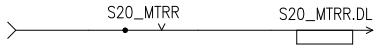
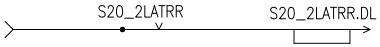


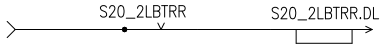
NV.ASSIGN S20_MTRR TO S20_MTRR.DL;



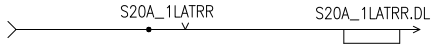
NV.ASSIGN S20_2LATRR TO S20_2LATRR.DL;



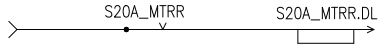
NV.ASSIGN S20_2LBTRR TO S20_2LBTRR.DL;



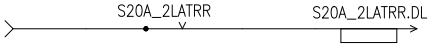
NV.ASSIGN S20A_1LATRR TO S20A_1LATRR.DL;



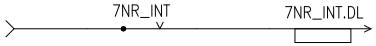
NV.ASSIGN S20A_MTRR TO S20A_MTRR.DL;



NV.ASSIGN S20A_2LATRR TO S20A_2LATRR.DL;



NV.ASSIGN 7NR_INT TO 7NR_INT.DL;



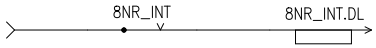
NV.ASSIGN 7NR TO 7NR.DL;

NV.ASSIGN 7RR_INT TO 7RR_INT.DL;

NV.ASSIGN 7RR TO 7RR.DL;

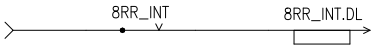
NV.ASSIGN 8ZNR TO 8ZNR.DL;

NV.ASSIGN 8NR_INT TO 8NR_INT.DL;



NV.ASSIGN 8NR TO 8NR.DL;

NV.ASSIGN 8RR_INT TO 8RR_INT.DL;



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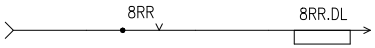

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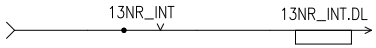
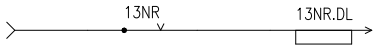


NV.ASSIGN 13NR TO 13NR.DL;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00213	00228	00214

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

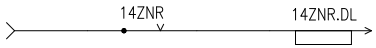
CAD FILE: 512_TAC_C1_00213_R00.DWG

(MICROLOK-II WARM STANDBY)

NV.ASSIGN 13RR_INT TO 13RR_INT.DL;

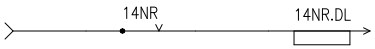
NV.ASSIGN 13RR TO 13RR.DL;

NV.ASSIGN 14ZNR TO 14ZNR.DL;



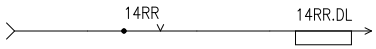
NV.ASSIGN 14NR_INT TO 14NR_INT.DL;

NV.ASSIGN 14NR TO 14NR.DL;



NV.ASSIGN 14RR_INT TO 14RR_INT.DL;

NV.ASSIGN 14RR TO 14RR.DL;



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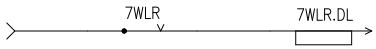
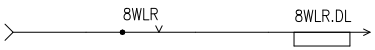


NV.ASSIGN 8WLR TO 8WLR.DL;



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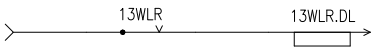

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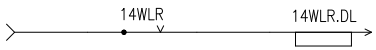

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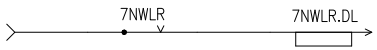

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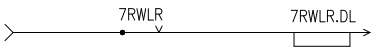

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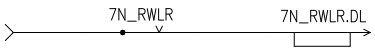

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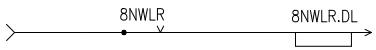

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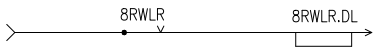

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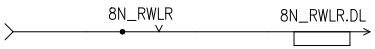
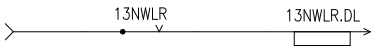


NV.ASSIGN 13NWLR TO 13NWLR.DL;



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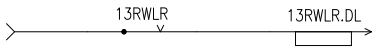
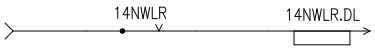


NV.ASSIGN 13N_RWLR TO 13N_RWLR.DL;

NV.ASSIGN 14NWLR TO 14NWLR.DL;



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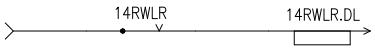

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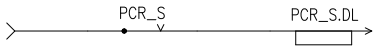
NV.ASSIGN PCR_S TO PCR_S.DL;



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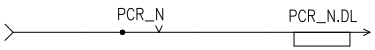
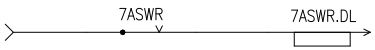
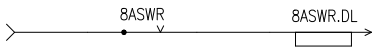


NV.ASSIGN 7ASWR TO 7ASWR.DL;



NV.ASSIGN 8ASWR TO 8ASWR.DL;



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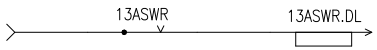


REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	

CHK.

APP:

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00214	00228	00215

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

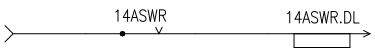
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00214_R00.DWG

(MICROLOK-II WARM STANDBY)

NV.ASSIGN 14ASWR TO 14ASWR.DL;



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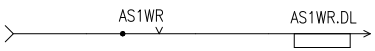
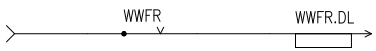


NV.ASSIGN WWFR TO WWFR.DL;



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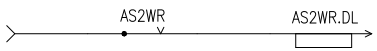
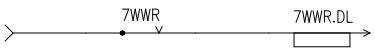
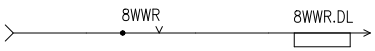


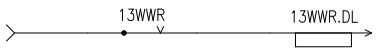
NV.ASSIGN 7WWR TO 7WWR.DL;



NV.ASSIGN 8WWR TO 8WWR.DL;



NV.ASSIGN 13WWR TO 13WWR.DL;



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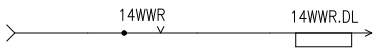
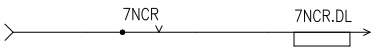
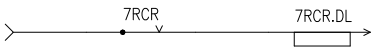


NV.ASSIGN 7NCR TO 7NCR.DL;



NV.ASSIGN 7RCR TO 7RCR.DL;



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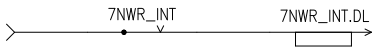
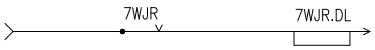


NV.ASSIGN 7RWR_INT TO 7RWR_INT.DL;

NV.ASSIGN 7WJR TO 7WJR.DL;



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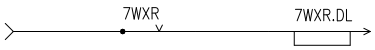

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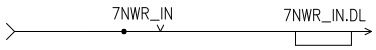

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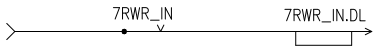


NV.ASSIGN 8NCR TO 8NCR.DL;

NV.ASSIGN 8RCR TO 8RCR.DL;

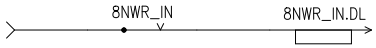
NV.ASSIGN 8NWR_INT TO 8NWR_INT.DL;

NV.ASSIGN 8RWR_INT TO 8RWR_INT.DL;

NV.ASSIGN 8WJR TO 8WJR.DL;

NV.ASSIGN 8WXR TO 8WXR.DL;

NV.ASSIGN 8NWR_IN TO 8NWR_IN.DL;



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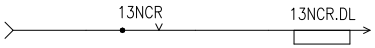
NV.ASSIGN 13NCR TO 13NCR.DL;



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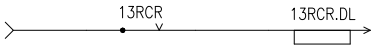

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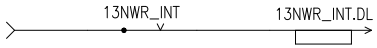

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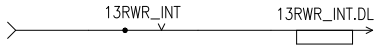


REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

	REVISION
CHK.	JE-1
	SE/SSE
	ASTE
	SSTE
APP.	f CSTE

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

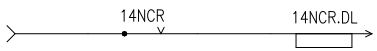
REF.SIG.PLAN.No. IPU.2107B
n e jsyos vkjsjo S.C.RLY.DRG.NO fc B
'khV SHEET 00215
dqy 'khVSHEETS 00228
CONT'D ON: 00216

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS
rkfVpøykZ
TATICHERLA
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00215_R00.DWG

(MICROLOK-II WARM STANDBY)

NV.ASSIGN 14NCR TO 14NCR.DL;



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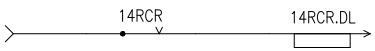

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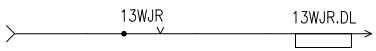

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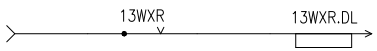

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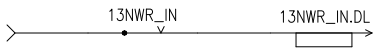

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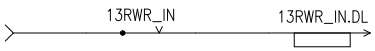

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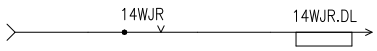
NV.ASSIGN 14RWR_INT TO 14RWR_INT.DL;

NV.ASSIGN 14WJR TO 14WJR.DL;



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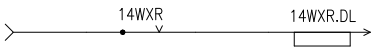

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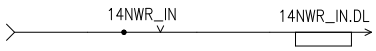

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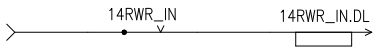

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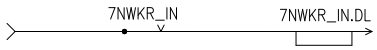

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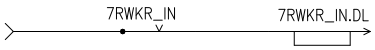

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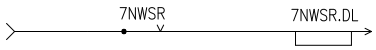

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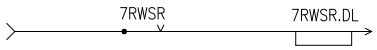

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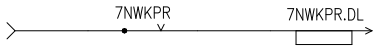

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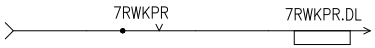
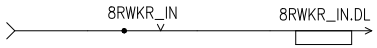


NV.ASSIGN 8NWKR_IN TO 8NWKR_IN.DL;

NV.ASSIGN 8RWKR_IN TO 8RWKR_IN.DL;



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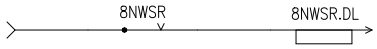

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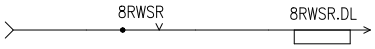

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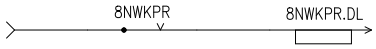

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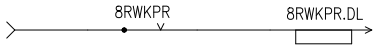

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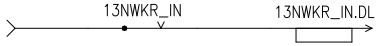

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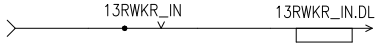

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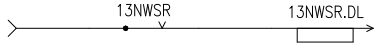

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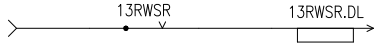


REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00216	00228	00217

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

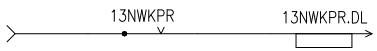
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00216_R00.DWG

(MICROLOK-II WARM STANDBY)

NV.ASSIGN 13NWKPR TO 13NWKPR.DL;



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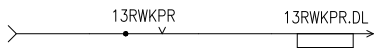

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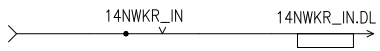

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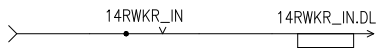

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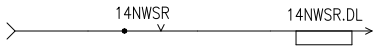

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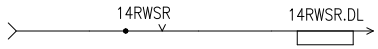

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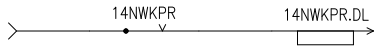

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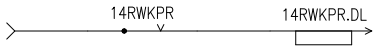

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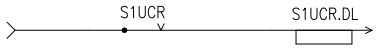

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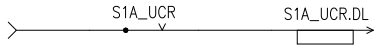

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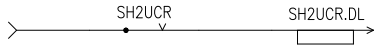

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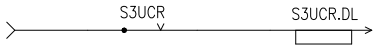

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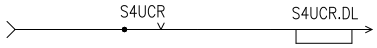

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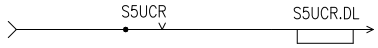

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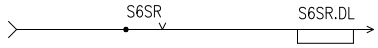

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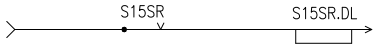

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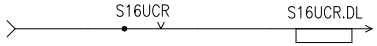

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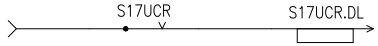

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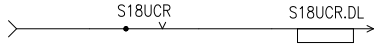

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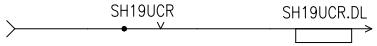

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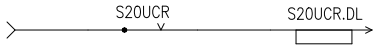

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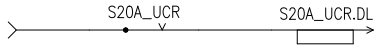

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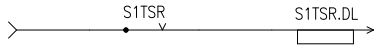

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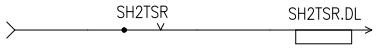

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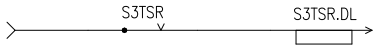

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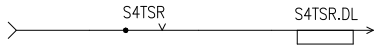

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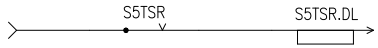

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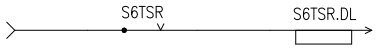


REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00217	00228	00218

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

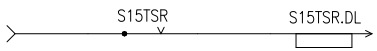
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00217_R00.DWG

(MICROLOK-II WARM STANDBY)

NV.ASSIGN S15TSR TO S15TSR.DL;



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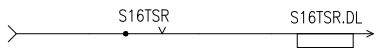

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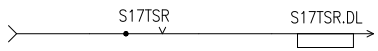

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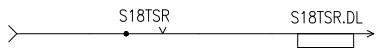

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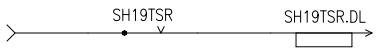

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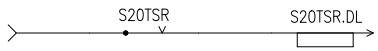

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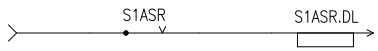

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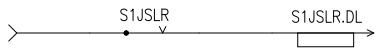

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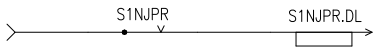

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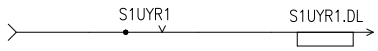
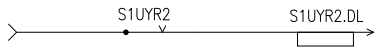


NV.ASSIGN S1UYR2 TO S1UYR2.DL;



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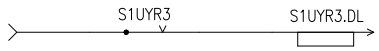
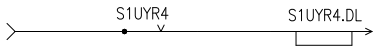


NV.ASSIGN S1UYR4 TO S1UYR4.DL;



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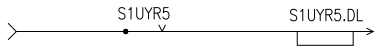
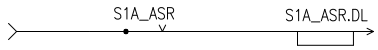


NV.ASSIGN S1A_ASR TO S1A_ASR.DL;



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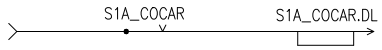
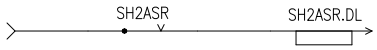


NV.ASSIGN 240.S1A_CONJPR TO 240.S1A_CONJPR.DL;

NV.ASSIGN S1A_CPSJSLR TO S1A_CPSJSLR.DL;

NV.ASSIGN S1A_CPSNJPR TO S1A_CPSNJPR.DL;

NV.ASSIGN SH2ASR TO SH2ASR.DL;



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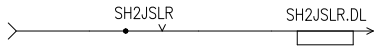

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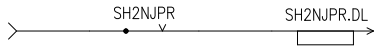

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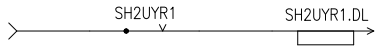

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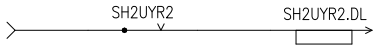

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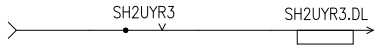

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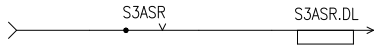
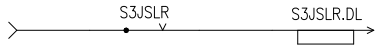


NV.ASSIGN S3JSLR TO S3JSLR.DL;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00218	00228	00219

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

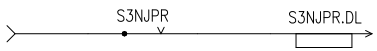
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

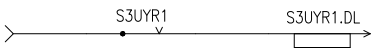
CAD FILE: 512_TAC_C1_00218_R00.DWG

(MICROLOK-II WARM STANDBY)

NV.ASSIGN S3NJPR TO S3NJPR.DL;



NV.ASSIGN S3UYR1 TO S3UYR1.DL;



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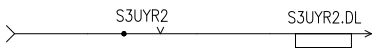

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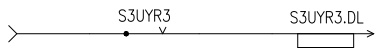

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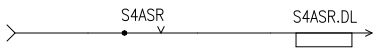

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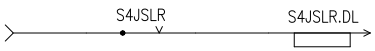

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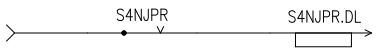

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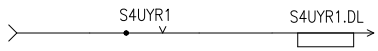

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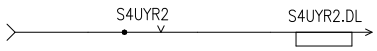

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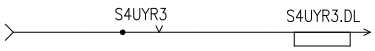

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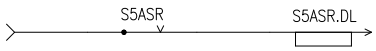

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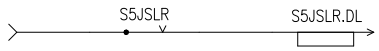

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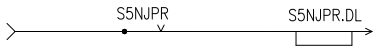

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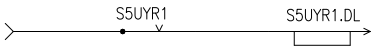

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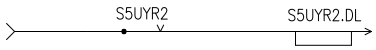

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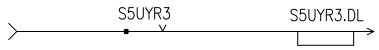

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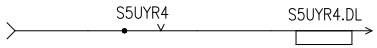
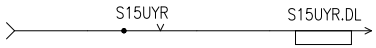


NV.ASSIGN S6UYR TO S6UYR.DL;

NV.ASSIGN S15UYR TO S15UYR.DL;



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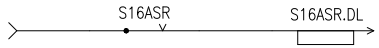

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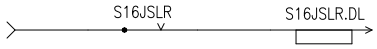

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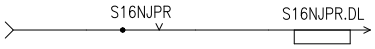

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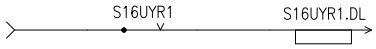

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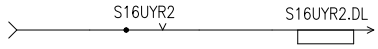

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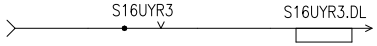

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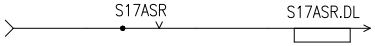

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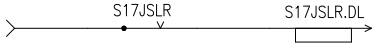

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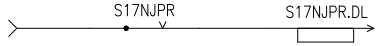

REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	CHK.
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE: MICROLOK II APPLICATION LOGIC EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00219	00228	00220

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00219_R00.DWG

(MICROLOK-II WARM STANDBY)

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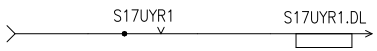

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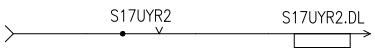

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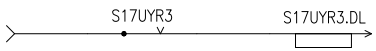

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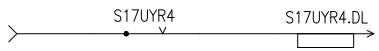

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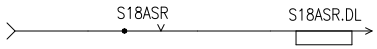

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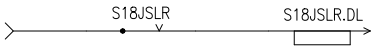

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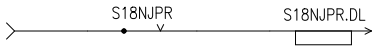

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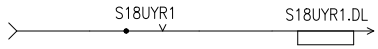

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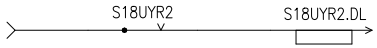

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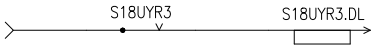

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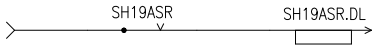

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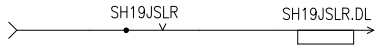

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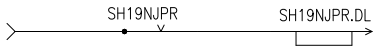

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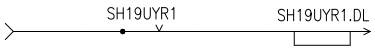

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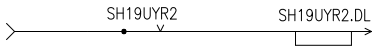

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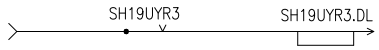

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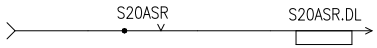

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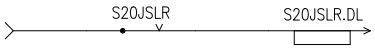

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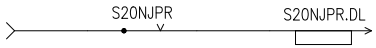

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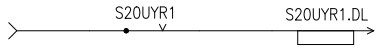
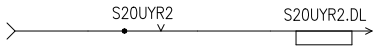
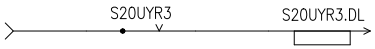


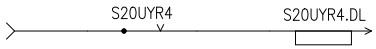
NV.ASSIGN S20UYR2 TO S20UYR2.DL;



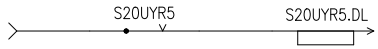
NV.ASSIGN S20UYR3 TO S20UYR3.DL;



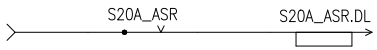
NV.ASSIGN S20UYR4 TO S20UYR4.DL;



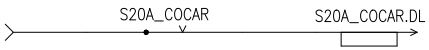
NV.ASSIGN S20UYR5 TO S20UYR5.DL;



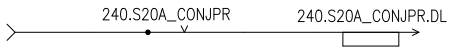
NV.ASSIGN S20A_ASR TO S20A_ASR.DL;



NV.ASSIGN S20A_COCAR TO S20A_COCAR.DL;



NV.ASSIGN 240.S20A_CONJPR TO 240.S20A_CONJPR.DL;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET 00220	dqy 'khVSHEETS 00228	CONT'D ON: 00221

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

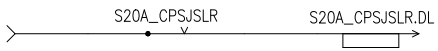
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

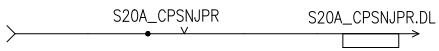
CAD FILE: 512_TAC_C1_00220_R00.DWG

(MICROLOK-II WARM STANDBY)

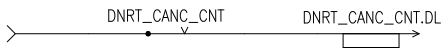
NV.ASSIGN S20A_CPSJSLR TO S20A_CPSJSLR.DL;



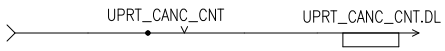
NV.ASSIGN S20A_CPSNJPR TO S20A_CPSNJPR.DL;



NV.ASSIGN DNRT_CANC_CNT TO DNRT_CANC_CNT.DL;

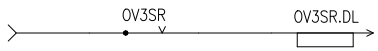


NV.ASSIGN UPRT_CANC_CNT TO UPRT_CANC_CNT.DL;

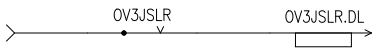


NV.ASSIGN CORT_CANC_CNT TO CORT_CANC_CNT.DL;

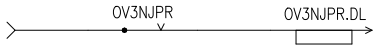
NV.ASSIGN OV3SR TO OV3SR.DL;



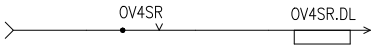
NV.ASSIGN OV3JSLR TO OV3JSLR.DL;



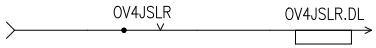
NV.ASSIGN OV3NJPR TO OV3NJPR.DL;



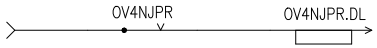
NV.ASSIGN OV4SR TO OV4SR.DL;



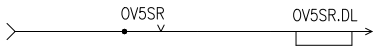
NV.ASSIGN OV4JSLR TO OV4JSLR.DL;



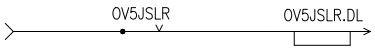
NV.ASSIGN OV4NJPR TO OV4NJPR.DL;



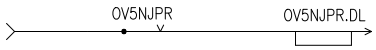
NV.ASSIGN OV5SR TO OV5SR.DL;



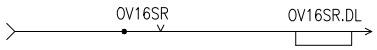
NV.ASSIGN OV5JSLR TO OV5JSLR.DL;



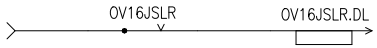
NV.ASSIGN OV5NJPR TO OV5NJPR.DL;



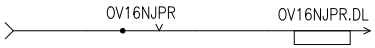
NV.ASSIGN OV16SR TO OV16SR.DL;



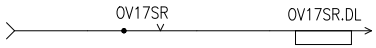
NV.ASSIGN OV16JSLR TO OV16JSLR.DL;



NV.ASSIGN OV16NJPR TO OV16NJPR.DL;

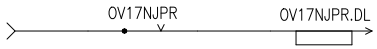


NV.ASSIGN OV17SR TO OV17SR.DL;

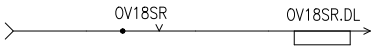


NV.ASSIGN OV17JSLR TO OV17JSLR.DL;

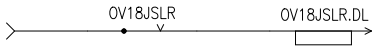
NV.ASSIGN OV17NJPR TO OV17NJPR.DL;



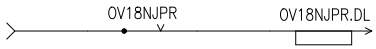
NV.ASSIGN OV18SR TO OV18SR.DL;



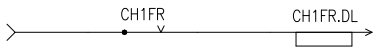
NV.ASSIGN OV18JSLR TO OV18JSLR.DL;



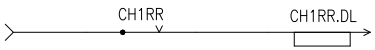
NV.ASSIGN OV18NJPR TO OV18NJPR.DL;



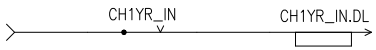
NV.ASSIGN CH1FR TO CH1FR.DL;



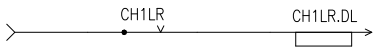
NV.ASSIGN CH1RR TO CH1RR.DL;



NV.ASSIGN CH1YR_IN TO CH1YR_IN.DL;



NV.ASSIGN CH1LR TO CH1LR.DL;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00221	00228	00222

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

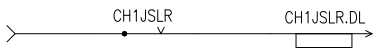
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

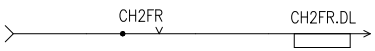
CAD FILE: 512_TAC_C1_00221_R00.DWG

(MICROLOK-II WARM STANDBY)

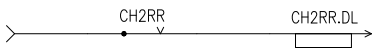
NV.ASSIGN CH1JSLR TO CH1JSLR.DL;



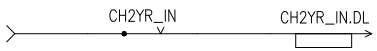
NV.ASSIGN CH2FR TO CH2FR.DL;



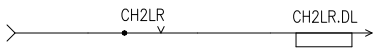
NV.ASSIGN CH2RR TO CH2RR.DL;



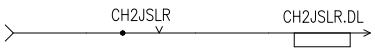
NV.ASSIGN CH2YR_IN TO CH2YR_IN.DL;



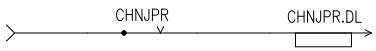
NV.ASSIGN CH2LR TO CH2LR.DL;



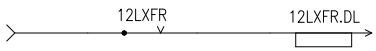
NV.ASSIGN CH2JSLR TO CH2JSLR.DL;



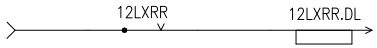
NV.ASSIGN CHNJPR TO CHNJPR.DL;



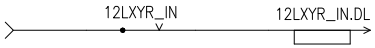
NV.ASSIGN 12LXFR TO 12LXFR.DL;



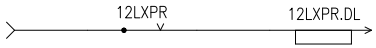
NV.ASSIGN 12LXRR TO 12LXRR.DL;



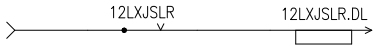
NV.ASSIGN 12LXYR_IN TO 12LXYR_IN.DL;



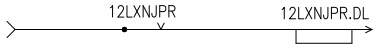
NV.ASSIGN 12LXPR TO 12LXPR.DL;



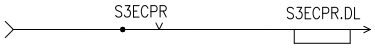
NV.ASSIGN 12LXJSLR TO 12LXJSLR.DL;



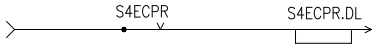
NV.ASSIGN 12LXNJPR TO 12LXNJPR.DL;



NV.ASSIGN S3ECPR TO S3ECPR.DL;



NV.ASSIGN S4ECPR TO S4ECPR.DL;



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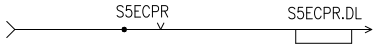
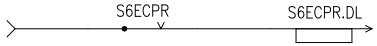
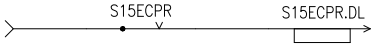


NV.ASSIGN S6ECPR TO S6ECPR.DL;



NV.ASSIGN S15ECPR TO S15ECPR.DL;



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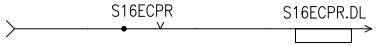
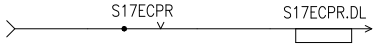


NV.ASSIGN S17ECPR TO S17ECPR.DL;



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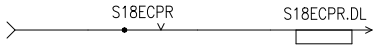
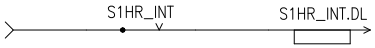
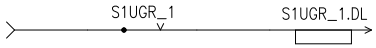


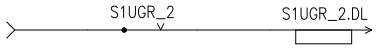
NV.ASSIGN S1HR_INT TO S1HR_INT.DL;



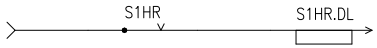
NV.ASSIGN S1UGR_1 TO S1UGR_1.DL;



NV.ASSIGN S1UGR_2 TO S1UGR_2.DL;



NV.ASSIGN S1HR TO S1HR.DL;



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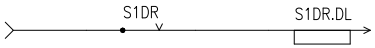
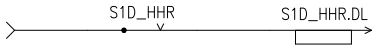
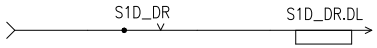


NV.ASSIGN S1D_HHR TO S1D_HHR.DL;



NV.ASSIGN S1D_DR TO S1D_DR.DL;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	

CHK.

APP.

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00222	00228	00223

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

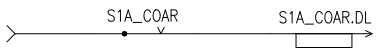
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

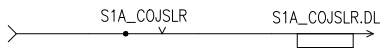
CAD FILE: 512_TAC_C1_00222_R00.DWG

(MICROLOK-II WARM STANDBY)

NV.ASSIGN S1A_COAR TO S1A_COAR.DL;

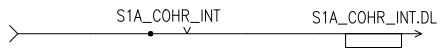


NV.ASSIGN S1A_COJSLR TO S1A_COJSLR.DL;

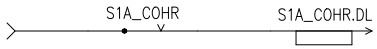


NV.ASSIGN S1A_CONJPR TO S1A_CONJPR.DL;

NV.ASSIGN S1A_COHR_INT TO S1A_COHR_INT.DL;

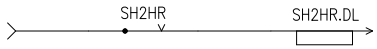


NV.ASSIGN S1A_COHR TO S1A_COHR.DL;

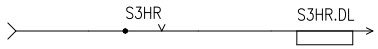


NV.ASSIGN SH2HR_INT TO SH2HR_INT.DL;

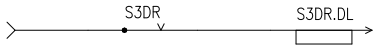
NV.ASSIGN SH2HR TO SH2HR.DL;



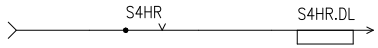
NV.ASSIGN S3HR TO S3HR.DL;



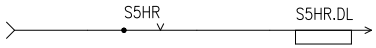
NV.ASSIGN S3DR TO S3DR.DL;



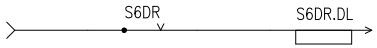
NV.ASSIGN S4HR TO S4HR.DL;



NV.ASSIGN S5HR TO S5HR.DL;



NV.ASSIGN S6DR TO S6DR.DL;



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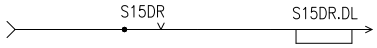

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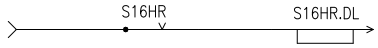
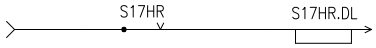
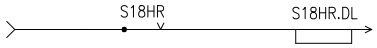


NV.ASSIGN S17HR TO S17HR.DL;



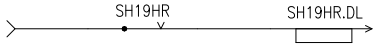
NV.ASSIGN S18HR TO S18HR.DL;



NV.ASSIGN S18DR TO S18DR.DL;

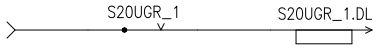
NV.ASSIGN SH19HR_INT TO SH19HR_INT.DL;

NV.ASSIGN SH19HR TO SH19HR.DL;



NV.ASSIGN S20HR_INT TO S20HR_INT.DL;

NV.ASSIGN S20UGR_1 TO S20UGR_1.DL;



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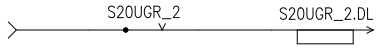
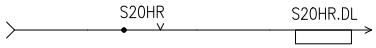
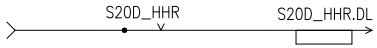


NV.ASSIGN S20HR TO S20HR.DL;



NV.ASSIGN S20DR TO S20DR.DL;

NV.ASSIGN S20D_HHR TO S20D_HHR.DL;



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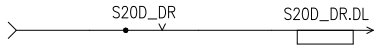
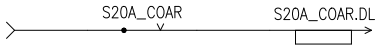


NV.ASSIGN S20A_COAR TO S20A_COAR.DL;



NV.ASSIGN S20A_COJSLR TO S20A_COJSLR.DL;

REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
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ASTE	
SSTE	
f CSTE	

CHK.

APP:

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO fc B

'khV SHEET 00223 | dqy 'khVSHEETS 00228 | CONT'D ON: 00224

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

rkfVpøykZ
TATICHERLA

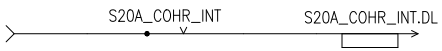
SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

(MICROLOK-II WARM STANDBY)

CAD FILE: 512_TAC_C1_00223_R00.DWG

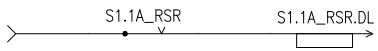
NV.ASSIGN S20A_CONJPR TO S20A_CONJPR.DL;

NV.ASSIGN S20A_COHR_INT TO S20A_COHR_INT.DL;



NV.ASSIGN S20A_COHR TO S20A_COHR.DL;

NV.ASSIGN S1.1A_RSR TO S1.1A_RSR.DL;



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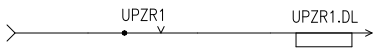
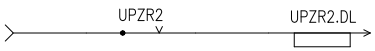
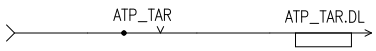


NV.ASSIGN UPZR2 TO UPZR2.DL;



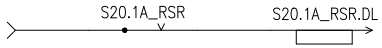
NV.ASSIGN ATP_TAR TO ATP_TAR.DL;



NV.ASSIGN ATP_SNR TO ATP_SNR.DL;

NV.ASSIGN ATP_TABELL TO ATP_TABELL.DL;

NV.ASSIGN S20.1A_RSR TO S20.1A_RSR.DL;



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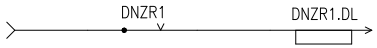
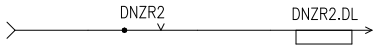
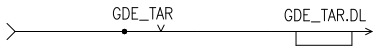


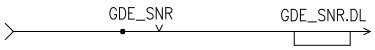
NV.ASSIGN DNZR2 TO DNZR2.DL;



NV.ASSIGN GDE_TAR TO GDE_TAR.DL;

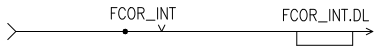


NV.ASSIGN GDE_SNR TO GDE_SNR.DL;

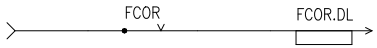


NV.ASSIGN GDE_TABELL TO GDE_TABELL.DL;

NV.ASSIGN FCOR_INT TO FCOR_INT.DL;

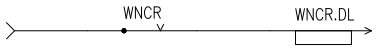


NV.ASSIGN FCOR TO FCOR.DL;



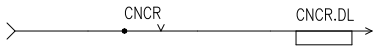
NV.ASSIGN GNCR TO GNCR.DL;

NV.ASSIGN WNCR TO WNCR.DL;

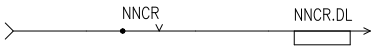


NV.ASSIGN UNCR TO UNCR.DL;

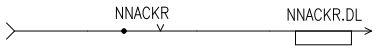
NV.ASSIGN CNCR TO CNCR.DL;



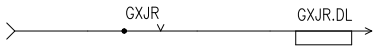
NV.ASSIGN NNCR TO NNCR.DL;



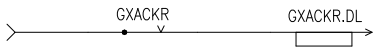
NV.ASSIGN NNACKR TO NNACKR.DL;



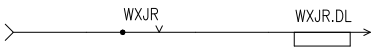
NV.ASSIGN GXJR TO GXJR.DL;



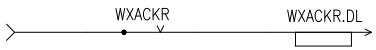
NV.ASSIGN GXACKR TO GXACKR.DL;



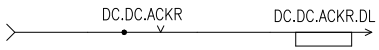
NV.ASSIGN WXJR TO WXJR.DL;



NV.ASSIGN WXACKR TO WXACKR.DL;



NV.ASSIGN DC.DC.ACKR TO DC.DC.ACKR.DL;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No. IPU.2107B | n e jsyos vkjsjo S.C.RLY.DRG.NO | a fc B

'khV SHEET 00224 | dqy 'khVSHEETS 00228 | CONT'D ON: 00225

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

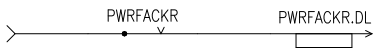
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

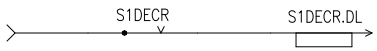
CAD FILE: 512_TAC_C1_00224_R00.DWG

(MICROLOK-II WARM STANDBY)

NV.ASSIGN PWRFACKR TO PWRFACKR.DL;

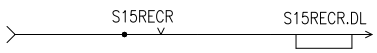


NV.ASSIGN S1DECR TO S1DECR.DL;

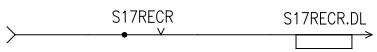


NV.ASSIGN S1A_COHECR TO S1A_COHECR.DL;

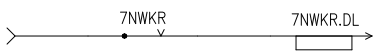
NV.ASSIGN S15RECR TO S15RECR.DL;



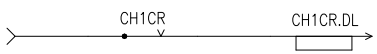
NV.ASSIGN S17RECR TO S17RECR.DL;



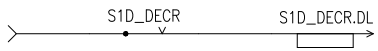
NV.ASSIGN 7NWKR TO 7NWKR.DL;



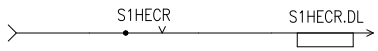
NV.ASSIGN CH1CR TO CH1CR.DL;



NV.ASSIGN S1D_DECR TO S1D_DECR.DL;

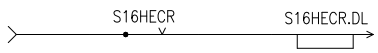


NV.ASSIGN S1HECR TO S1HECR.DL;

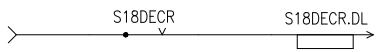


NV.ASSIGN SH2_OFFECR TO SH2_OFFECR.DL;

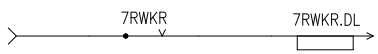
NV.ASSIGN S16HECR TO S16HECR.DL;



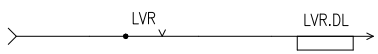
NV.ASSIGN S18DECR TO S18DECR.DL;



NV.ASSIGN 7RWKR TO 7RWKR.DL;

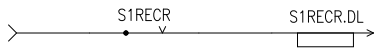


NV.ASSIGN LVR TO LVR.DL;



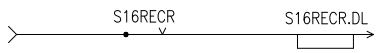
NV.ASSIGN S1D_HHECR TO S1D_HHECR.DL;

NV.ASSIGN S1RECR TO S1RECR.DL;

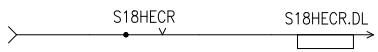


NV.ASSIGN SH2_ONECR TO SH2_ONECR.DL;

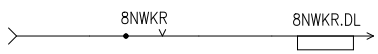
NV.ASSIGN S16RECR TO S16RECR.DL;



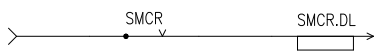
NV.ASSIGN S18HECR TO S18HECR.DL;



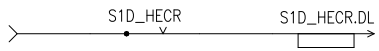
NV.ASSIGN 8NWKR TO 8NWKR.DL;



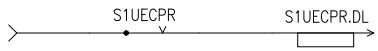
NV.ASSIGN SMCR TO SMCR.DL;



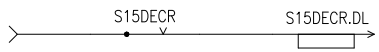
NV.ASSIGN S1D_HECR TO S1D_HECR.DL;



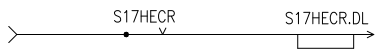
NV.ASSIGN S1UECPR TO S1UECPR.DL;



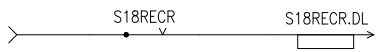
NV.ASSIGN S15DECR TO S15DECR.DL;



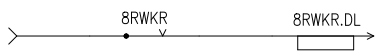
NV.ASSIGN S17HECR TO S17HECR.DL;



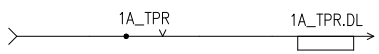
NV.ASSIGN S18RECR TO S18RECR.DL;



NV.ASSIGN 8RWKR TO 8RWKR.DL;



NV.ASSIGN 1A_TPR TO 1A_TPR.DL;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET 00225	dqy 'khVSHEETS 00228	CONT'D ON: 00226

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

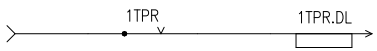
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

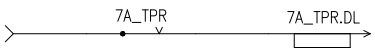
CAD FILE: 512_TAC_C1_00225_R00.DWG

(MICROLOK-II WARM STANDBY)

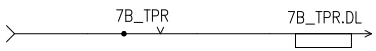
NV.ASSIGN 1TPR TO 1TPR.DL;



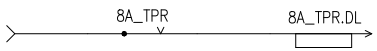
NV.ASSIGN 7A_TPR TO 7A_TPR.DL;



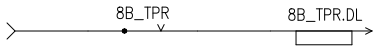
NV.ASSIGN 7B_TPR TO 7B_TPR.DL;



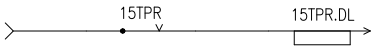
NV.ASSIGN 8A_TPR TO 8A_TPR.DL;



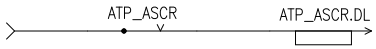
NV.ASSIGN 8B_TPR TO 8B_TPR.DL;



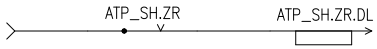
NV.ASSIGN 15TPR TO 15TPR.DL;



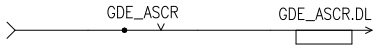
NV.ASSIGN ATP_ASCR TO ATP_ASCR.DL;



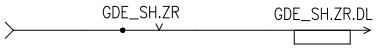
NV.ASSIGN ATP_SH.ZR TO ATP_SH.ZR.DL;



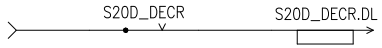
NV.ASSIGN GDE_ASCR TO GDE_ASCR.DL;



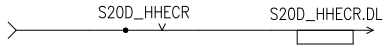
NV.ASSIGN GDE_SH.ZR TO GDE_SH.ZR.DL;



NV.ASSIGN S20D_DECR TO S20D_DECR.DL;



NV.ASSIGN S20D_HHECR TO S20D_HHECR.DL;



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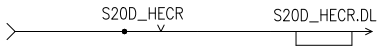

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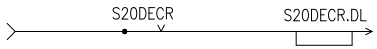

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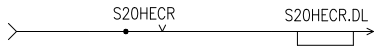

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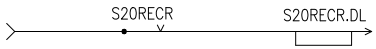
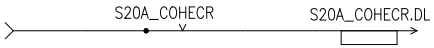


NV.ASSIGN S20UECPR TO S20UECPR.DL;

NV.ASSIGN S20A_COHECR TO S20A_COHECR.DL;



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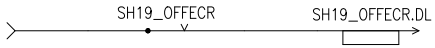
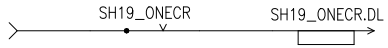
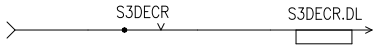


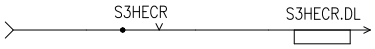
NV.ASSIGN SH19_ONECR TO SH19_ONECR.DL;



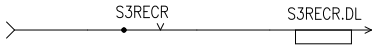
NV.ASSIGN S3DECR TO S3DECR.DL;



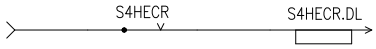
NV.ASSIGN S3HECR TO S3HECR.DL;



NV.ASSIGN S3RECR TO S3RECR.DL;



NV.ASSIGN S4HECR TO S4HECR.DL;



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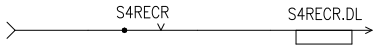

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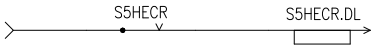
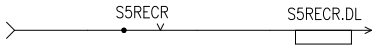
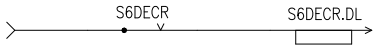


NV.ASSIGN S5RECR TO S5RECR.DL;



NV.ASSIGN S6DECR TO S6DECR.DL;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	

CHK.

APP.

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00226	00228	00227

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

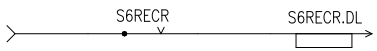
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00226_R00.DWG

(MICROLOK-II WARM STANDBY)

NV.ASSIGN S6RECR TO S6RECR.DL;



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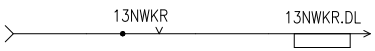

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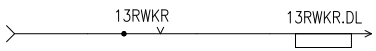

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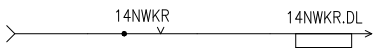

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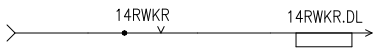
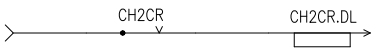
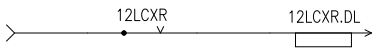


NV.ASSIGN CH2CR TO CH2CR.DL;



NV.ASSIGN 12LCXR TO 12LCXR.DL;



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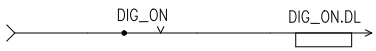
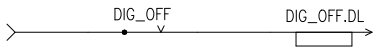
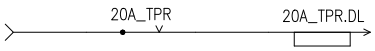


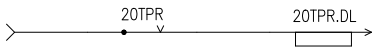
NV.ASSIGN DIG_OFF TO DIG_OFF.DL;



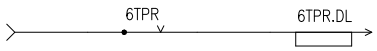
NV.ASSIGN 20A_TPR TO 20A_TPR.DL;



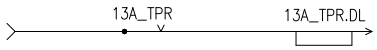
NV.ASSIGN 20TPR TO 20TPR.DL;



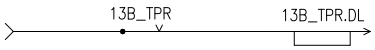
NV.ASSIGN 6TPR TO 6TPR.DL;



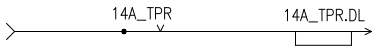
NV.ASSIGN 13A_TPR TO 13A_TPR.DL;



NV.ASSIGN 13B_TPR TO 13B_TPR.DL;



NV.ASSIGN 14A_TPR TO 14A_TPR.DL;



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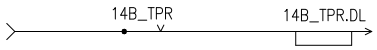
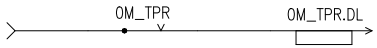


NV.ASSIGN 0M_TPR TO 0M_TPR.DL;



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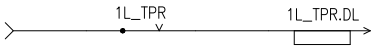
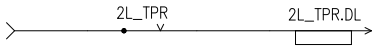
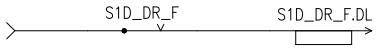


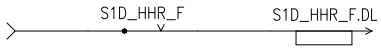
NV.ASSIGN 2L_TPR TO 2L_TPR.DL;



NV.ASSIGN S1D_DR_F TO S1D_DR_F.DL;



NV.ASSIGN S1D_HHR_F TO S1D_HHR_F.DL;



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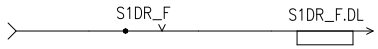

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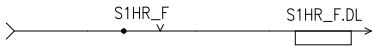
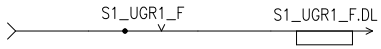
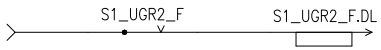


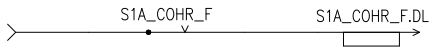
NV.ASSIGN S1_UGR1_F TO S1_UGR1_F.DL;



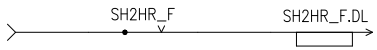
NV.ASSIGN S1_UGR2_F TO S1_UGR2_F.DL;



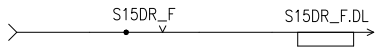
NV.ASSIGN S1A_COHR_F TO S1A_COHR_F.DL;



NV.ASSIGN SH2HR_F TO SH2HR_F.DL;



NV.ASSIGN S15DR_F TO S15DR_F.DL;



REV	DATE	DESIGNED	CHECKED	APPROVED	MODIFICATION
00	05.02.14	ANS	PM	VSH	INITIAL ISSUE

TVM Signalling & Transportation Systems Pvt. Ltd.
#610, "NARAYANA", 1st Floor, 15th Cross,
100 Feet Road, J.P.Nagar, 6th Phase,
Bengaluru – 560 078
Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	CHK.
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	APP.

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khVSHEET	dqy 'khVSHEETS	CONT'D ON:
00227	00228	00228

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

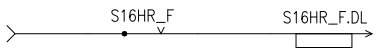
rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00227_R00.DWG

(MICROLOK-II WARM STANDBY)

NV.ASSIGN S16HR_F TO S16HR_F.DL;



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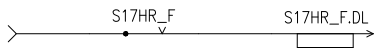

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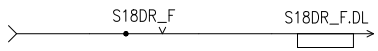

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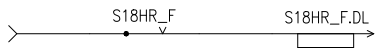
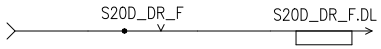
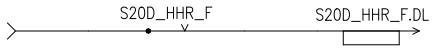


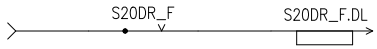
NV.ASSIGN S20D_DR_F TO S20D_DR_F.DL;



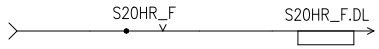
NV.ASSIGN S20D_HHR_F TO S20D_HHR_F.DL;



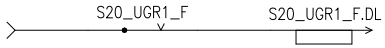
NV.ASSIGN S20DR_F TO S20DR_F.DL;



NV.ASSIGN S20HR_F TO S20HR_F.DL;



NV.ASSIGN S20_UGR1_F TO S20_UGR1_F.DL;



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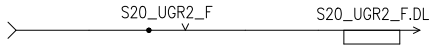
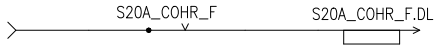
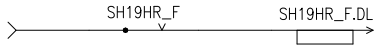


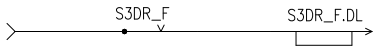
NV.ASSIGN S20A_COHR_F TO S20A_COHR_F.DL;



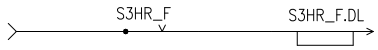
NV.ASSIGN SH19HR_F TO SH19HR_F.DL;



NV.ASSIGN S3DR_F TO S3DR_F.DL;



NV.ASSIGN S3HR_F TO S3HR_F.DL;



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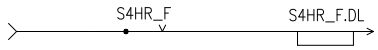

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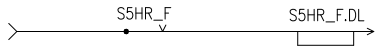
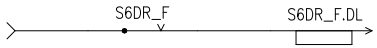
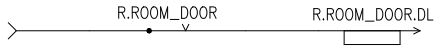


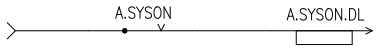
NV.ASSIGN S6DR_F TO S6DR_F.DL;



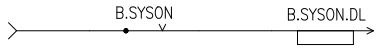
NV.ASSIGN R.ROOM_DOOR TO R.ROOM_DOOR.DL;



NV.ASSIGN A.SYSON TO A.SYSON.DL;

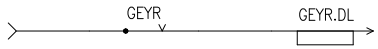


NV.ASSIGN B.SYSON TO B.SYSON.DL;



NV.ASSIGN DC.DC.FAIL TO DC.DC.FAIL.DL;

NV.ASSIGN GEYR TO GEYR.DL;



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TVM Signalling & Transportation Systems Pvt. Ltd.
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Tel: +91 80 2654 0781,82,83
E-mail : info@tsts.in

CONTRACT No:06/S&T/01/2012 Dtd:23.04.2012

REVISION	
JE-1	
SE/SSE	
ASTE	
SSTE	
f CSTE	

CHK.

APP:

TITLE:
MICROLOK II APPLICATION LOGIC
EQUIVALENT CIRCUITS - C1

REF.SIG.PLAN.No.	n e jsyos vkjsjo S.C.RLY.DRG.NO	fc B
IPU.2107B		
'khV SHEET	dqy 'khVSHEETS	CONT'D ON:
00228	00228	00229

flxuYk o nwj lapkj
SIGNAL & TELECOMMUNICATIONS

rkfVpøykZ
TATICHERLA

SOUTH CENTRAL RAILWAY
DIVISION: GUNTAKAL

CAD FILE: 512_TAC_C1_00228_R00.DWG

(MICROLOK-II WARM STANDBY)