MUKESH PATEL SCHOOL OF TECHNOLOGY MANAGEMENT AND ENGINEERING

(Affiliated to NMIMS Deemed to be University, Mumbai)



MPMC PROJECT

On

Ultrasonic Distance Calculator using 8051 Microcontroller

Submitted by

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MUKESH PATEL SCHOOL OF TECHNOLOGY MANAGEMENT & ENGINEERING

CERTIFICATE OF SUBMISSION

This is to certify that <u>Reneeka Nadkarni</u> (Roll Number: <u>C034</u>, SAP ID: <u>70322000186</u>), <u>Amishi Desai</u> (Roll Number: <u>C044</u>, SAP ID: <u>70322000023</u>), and <u>Chahel Gupta</u> (Roll Number: <u>C049</u>, SAP ID: <u>70322000047</u>), students of B.Tech Integrated (Computers), Division B, Batch B2, Semester VIII, Year IV at MPSTME, Mumbai, have successfully completed and submitted the project titled "*Ultrasonic Distance Calculator using 8051 Microcontroller*" as a part of the Microprocessors and Microcontrollers course.

Prof. Vipul Gohil Head of Department MPMC Faculty (Computers)

ULTRASONIC DISTANCE CALCULATOR USING 8051 MICROCONTROLLER

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I. INTRODUCTION

The Ultrasonic Distance Calculator using 8051 is a sophisticated electronic device that utilizes ultrasonic technology to accurately measure distances. This report provides an in-depth understanding of the principles behind ultrasonic rangefinders, the circuitry involved, and how the 8051 microcontroller plays a crucial role in making distance calculations.

II. DESIGN & PRINCIPLES

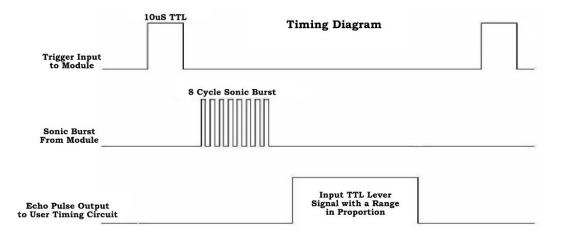
Ultrasonic rangefinders operate on the pulse-echo method, similar to RADAR systems. The basic principle involves transmitting an ultrasonic signal towards an object, receiving the echo signal from the object, and calculating the distance based on the time taken for the signal to travel to and from the object.

2.1 Component List

Name	Quantity	Component
LM016L	1	LCD Display
HC - SR04	1	Ultrasonic Sensor
AT89C51	1	8051 Microcontroller

2.2 How does the circuit work?

- 1. The HC-SR04 module has ultrasonic transmitter, receiver and control circuit on a single board. The module has 4 pins, Vcc, Gnd, Trig and Echo.
- 2. When a pulse of 10µs or more is given to the Trig pin, 8 pulses of 40 kHz are generated. After this, the Echo pin is made high by the control circuit in the module.



- 3. Echo pin remains high till it gets echo signal of the transmitted pulses back.
- 4. The time for which the echo pin remains high, i.e. the width of the Echo pin gives the time taken for generated ultrasonic sound to travel towards the object and return.
- 5. Using this time and the speed of sound in air, we can find the distance of the object using a simple formula for distance using speed and time.

```
Object Distance(in cm) = (Sound Velocity * Time)/2,
Where, Sound Velocity = 34300 (in cm per second)
```

Here, oscillator frequency of AT89C51 (8051) is 11.0592 MHz, then timer frequency of 8051 will be 921.6 kHz.

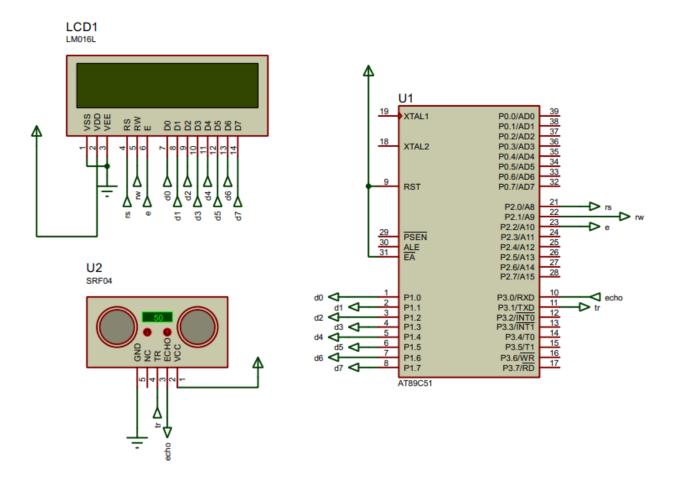
So, Time required to execute 1 instruction is 1.085 us. Thus, timer gets incremented after 1.085 us time elapse.

: Distance =
$$(34300 * TimerCount * 1.085 * 10^{-6}) / 2$$

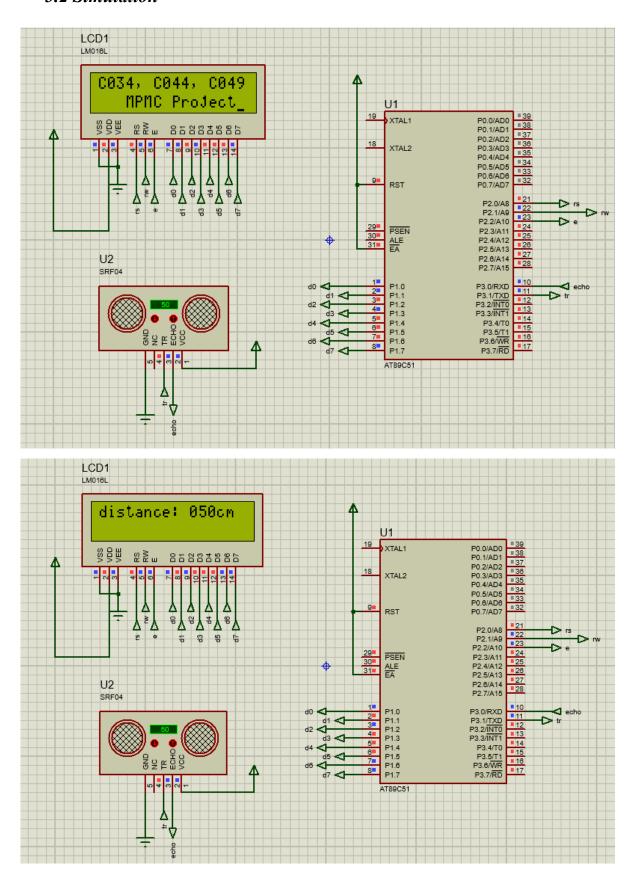
= TimerCount / 54

III. SIMULATION & TESTING

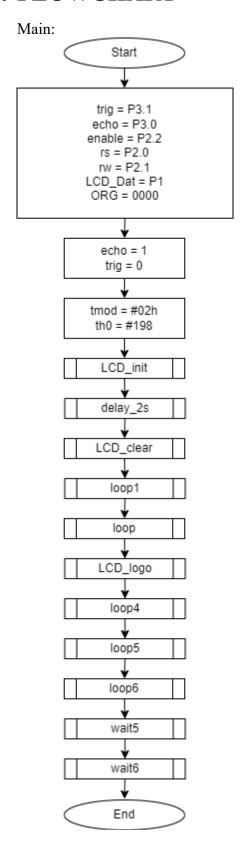
3.1 Circuit Diagram



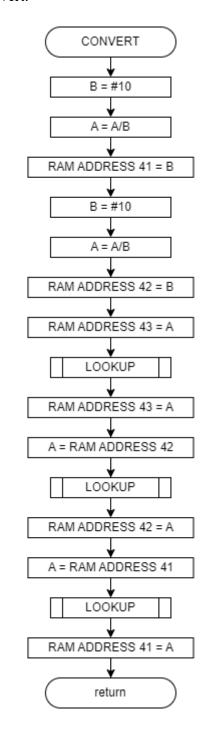
3.2 Simulation



IV. FLOWCHART



convert:



loop1: loop: loop loop1 a = 0 get_level a = @a+dptr CONVERT cursr_home yes if a == 0? LCD_logo display enable = 1 continue loop? LCD_dat = a Νo enable = 0 end delay1ms delay_10us: delay_10us dptr = dptr + 1 delay_2s: r7 = #18 delay_2s r7 = r7 - 1 R3 = #50 R2 = #10 R1 = #250 if r7 == 0?

TT3

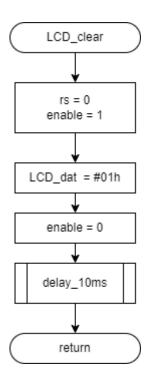
end

8 M P M C P R O J E C T

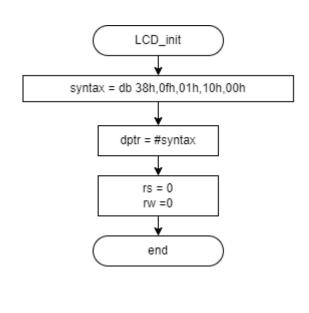
yes **↓**

return

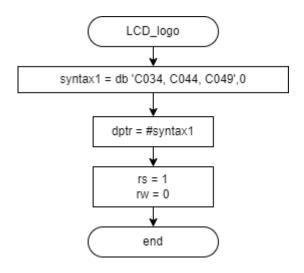
LCD_clear:



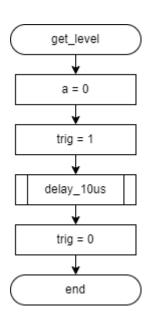
LCD init:

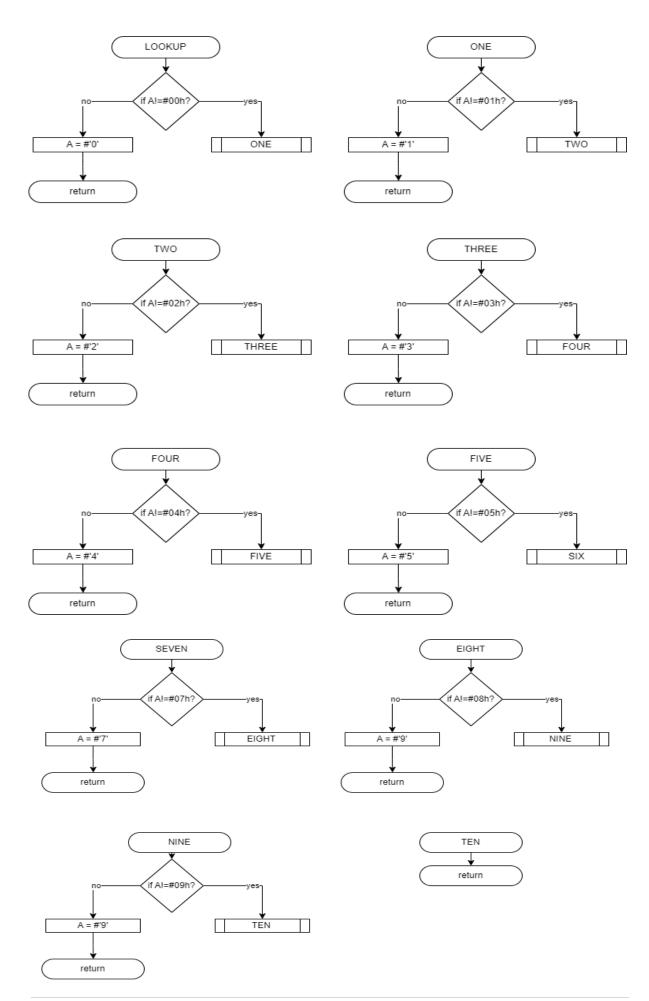


LCD_logo:

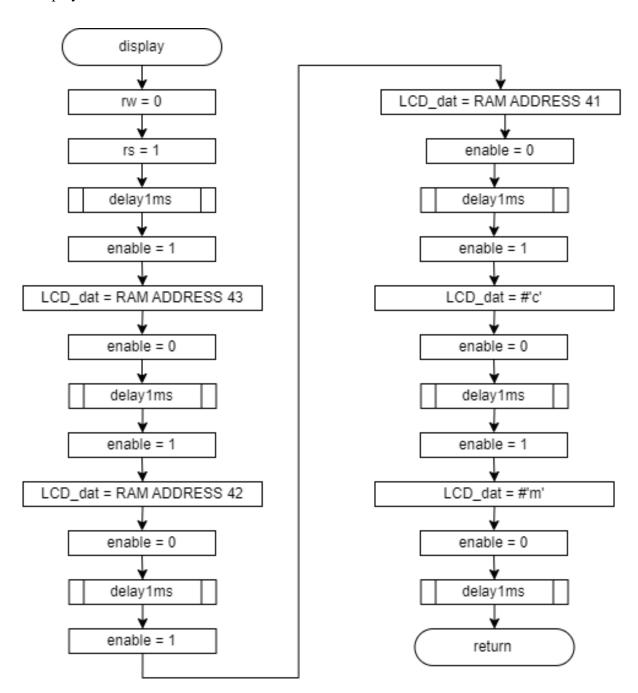


get_level:





display:



V. CODE

```
trig EQU P3.1
                                               loop4:
echo EQU P3.0
                                                       clr a
enable EQU p2.2
                                                       movc a, @a+dptr
rs EQU p2.0
                                                       jz new_command
rw EQU p2.1
                                                       setb enable
LCD_dat EQU p1
                                                       mov LCD_dat, a
ORG 0000
                                                       clr enable
                                                       acall delay1ms
setb echo
                                                       inc dptr
clr trig
                                                       sjmp loop4
mov tmod, #02h
mov th0, #198
                                               syntax1: db 'C034, C044, C049',0
acall LCD_init
acall delay_2s
                                               new_command:
acall LCD_clear
                                                       mov dptr, #syntax2
                                                       clr rs
loop1:
                                                       clr rw
       acall get_level
       acall CONVERT
                                               loop5:
       acall cursr_home
                                                       clr a
       acall display
                                                       movc a, @a+dptr
       SJMP loop1
                                                       jz LCD_logo_2
                                                       setb enable
LCD_init:
                                                       mov LCD_dat, a
       mov dptr, #syntax
                                                       clr enable
       clr rs
                                                       acall delay1ms
       clr rw
                                                       inc dptr
                                                       sjmp loop5
loop:
                                               syntax2: db 0c0h,14h,14h,14h,00h
       clr a
       movc a, @a+dptr
       jz LCD_logo
                                               LCD_logo_2:
       setb enable
                                                       mov dptr, #syntax3
       mov LCD dat, a
                                                       setb rs
       clr enable
                                                       clr rw
       acall delay1ms
                                               loop6:
       inc dptr
       sjmp loop
                                                       clr a
                                                       movc a, @a+dptr
syntax: db 38h,0fh,01h,10h,00h
                                                       jz return
                                                       setb enable
LCD_logo:
                                                       mov LCD_dat, a
       mov dptr, #syntax1
                                                       clr enable
       setb rs
                                                       acall delay1ms
       clr rw
                                                       inc dptr
                                                       sjmp loop6
```

syntax3: db "MPMC Project",0 stay: djnz r7, stay return:ret ret CONVERT: cursr_home: MOV B,#10 clr rs setb enable DIV AB mov LCD_dat,#80h MOV 41,B MOV B,#10 clr enable DIV AB acall delay10ms setb enable MOV 42,B mov LCD_dat,#0Ch MOV 43,A clr enable ret CALL LOOKUP LCD_clear: MOV 43,A **MOV A,42** clr rs setb enable mov LCD_dat,#01h CALL LOOKUP clr enable MOV 42,A acall delay10ms MOV A,41 ret CALL LOOKUP get_level: MOV 41,A clr A RET setb trig acall delay_10us LOOKUP: clr trig CJNE A,#00H,ONE MOV A,#'0' wait5: RET jnb echo, wait5 ONE: setb tr0 CJNE A,#01H,TWO wait6: MOV A,#'1' jnb tf0, wait6 RET inc A clr tf0 TWO: CJNE A,#02H,THREE jz return MOV A,#'2' RET jb echo, wait6 clr tr0 THREE: ret CJNE A,#03H,FOUR MOV A,#'3' delay_10us: mov r7, #18 RET

FOUR: clr enable acall delay1ms

CJNE A,#04H,FIVE

MOV A,#4' SETB enable RET MOV LCD_dat,#'s' clr enable

FIVE: acall delay1ms

CJNE A,#05H,SIX

MOV A,#5'

RET

MOV LCD_dat,#'t'

clr enable

acall delay1ms

CJNE A,#06H,SEVEN

MOV A,#6'

RET

MOV LCD_dat,#a'

SEVEN: clr enable acall delay1ms
CJNE A,#07H,EIGHT

MOV A,#'7'
RET
MOV LCD_dat,#'n'
clr enable
EIGHT: acall delay1ms

CJNE A,#08H,NINE
MOV A,#8'
RET
MOV LCD_dat,#'c'

ET MOV LCD_dat,#'c' clr enable acall delay1ms

CJNE A,#09H,TEN

MOV A,#'9'

RET

MOV LCD_dat,#e'
clr enable

TEN: clr enable acall delay1ms

RET

SETB enable

display: MOV LCD_dat,#':'
clr rw clr enable
setb rs acall delay1ms

SETB enable
SETB enable
MOV LCD_dat,#' '
MOV LCD_dat,#'d'
clr enable
acall delay1ms

acall delay1ms

SETB enable

SETB enable

MOV LCD_dat,43

MOV LCD_dat,#i'

clr enable

SIX:

NINE:

acall delay1ms

```
MOV R1,#250
      acall delay1ms
      SETB enable
                                            TT3:
      MOV LCD_dat,42
                                                 DJNZ R1,TT1
      clr enable
                                                 DJNZ R2,TT1
      acall delay1ms
                                                 DJNZ R3,TT3
                                            RET
      SETB enable
      MOV LCD_dat,41
                                         END
      clr enable
      acall delay1ms
      SETB enable
      MOV LCD_dat,#'c'
      clr enable
      acall delay1ms
      SETB enable
      MOV LCD_dat,#'m'
      clr enable
      acall delay1ms
      RET
delay10ms:
   MOV R3,#1
   MOV R2,#1
   MOV R1,#19
  TT1:
       DJNZ R1,TT1
       DJNZ R2,TT1
       DJNZ R3,TT1
   RET
delay1ms:
   MOV R2,#04
   MOV R1,#18
  TT2:
       DJNZ R1,TT2
       DJNZ R2,TT2
   RET
delay_2s:
  MOV R3,#50
   MOV R2,#10
```

VI. APPLICATIONS

- Parking distance sensors in automobiles.
- Industrial automation for object detection and measurement.
- Robotics for obstacle avoidance and navigation.
- Security systems for detecting intrusions.
- Used in terrain monitoring robots.

VII. CITATIONS

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