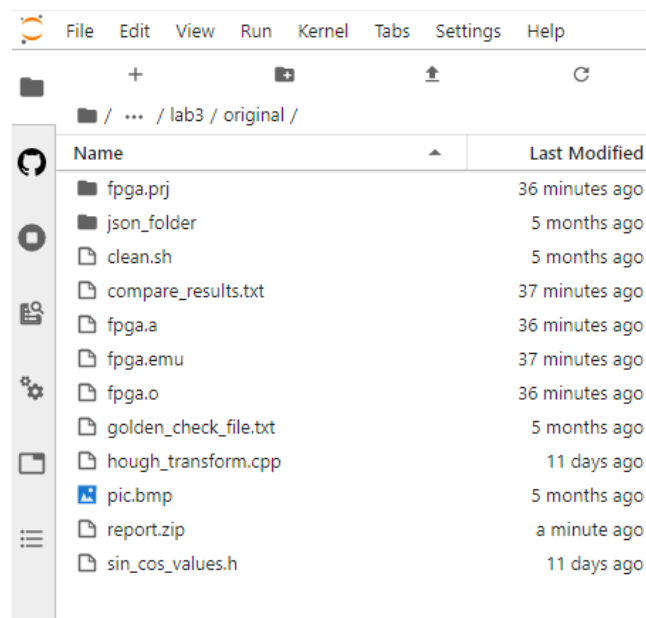


## Appendix. How to Transfer Report Files to Your Local Computer

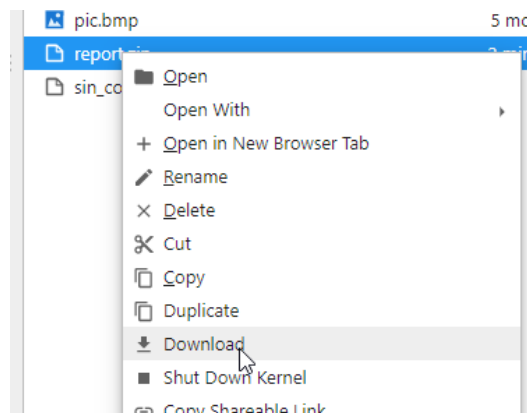
1. The original (Part C) subdirectory was used for these instructions. Replace with the subdirectory you are working from. In the terminal, navigate to the subdirectory you are working from. You will need to transfer the entire `fpga.prj/reports/` subdirectory to your local machine. The first step is to zip this directory into a zip file. Execute the following command (this command was done from `~/labs/lab3/original/`)

```
$ zip -r report.zip fpga.prj/reports
```

2. A file called `report.zip` will be created in the current directory. Navigate to the directory you are in inside of the terminal using the file browser on the left side of Jupyter, as shown in the screenshot below.



3. Right-click the `report.zip` file, and select Download.



- \_\_\_\_\_ 4. Unzip the report.zip file on your local machine, navigate to fpga.prj/reports/ within the unzipped directory, and open the file report.html.

Report: fpga\_bae426

File | C:/work/developer/oneAPI/class\_instances/class\_1013/labs\_work/report/fpga.prj/reports/report.html#view1

Phoenix Buy CV G Family Phoenix Virtual Learning Animation Career Health SoC Griffin Learning Shutdown

Reports [Summary](#) [Throughput Analysis](#) [Area Analysis](#) [System Viewers](#)

Summary Content

- Compile Info
- Kernels Summary
- Clock Frequency Summary
- System Resource Utilization Summary
- Quartus Fitter Resource Utilization Summary
- Compile Estimated Kernel Resource Utilization S
- Warnings Summary

Summary

[Compile Info](#)

Project Name	fpga_bae426
Target Family, Device, Board	Arria 10, 10AX115S2F45I25GES, intel_a10gx_pac_pac_a10
SYCL Version	20.3.0 Build 72
Quartus Version	
Reports Generated At	Tue Oct 13 07:12:27 2020

[Kernels Summary](#)

Name	Source Location	Kernel Type	Autorun	Workgroup Size	# Compute Units	Target Frequency (MHz)
Hough_Transform_kernel	:0	Single work-item	No	1,1,1	1	Not specified

[Clock Frequency Summary](#)

[System Resource Utilization Summary](#)

[Quartus Fitter Resource Utilization Summary](#)

hough\_transfo

```
1 //=====
2 // Copyri
3 //
4 // SPDX-L
5 // =====
6
7 #include
8 #include
9 #include
10 #include
11 #include
12
13 // This f
14 #include
15
16 #define W
17 #define H
18 #define I
19 #define T
20 #define R
21 #define N
22
23 using nam
24
25 // This f
26 void read
27
28 class Hou
29
30 int main(
31
32 //Decla
33 char pi
34 short a
35
36 //Initi
37 std::fi
38
```

Details

---

Intel Corporation. All rights reserved.

Intel, the Intel logo, Altera, Arria, Cyclone, Enpirion, MAX, Nios, Quartus and Stratix words and logos are trademarks of Intel Corporation or its subsidiaries in the U.S. and/or other countries.

Intel warrants performance of its FPGA and semiconductor products to current specifications in accordance with Intel's standard warranty, but reserves the right to make changes to any products and services at any time without notice. Intel assumes no responsibility or liability arising out of the application or use of any information, product, or service described herein except as expressly agreed to in writing by Intel. Intel customers are advised to obtain the latest version of device specifications before relying on any published information and before placing orders for products or services.

\*Other names and brands may be claimed as the property of others.