

Systolic Symmetric Architecture

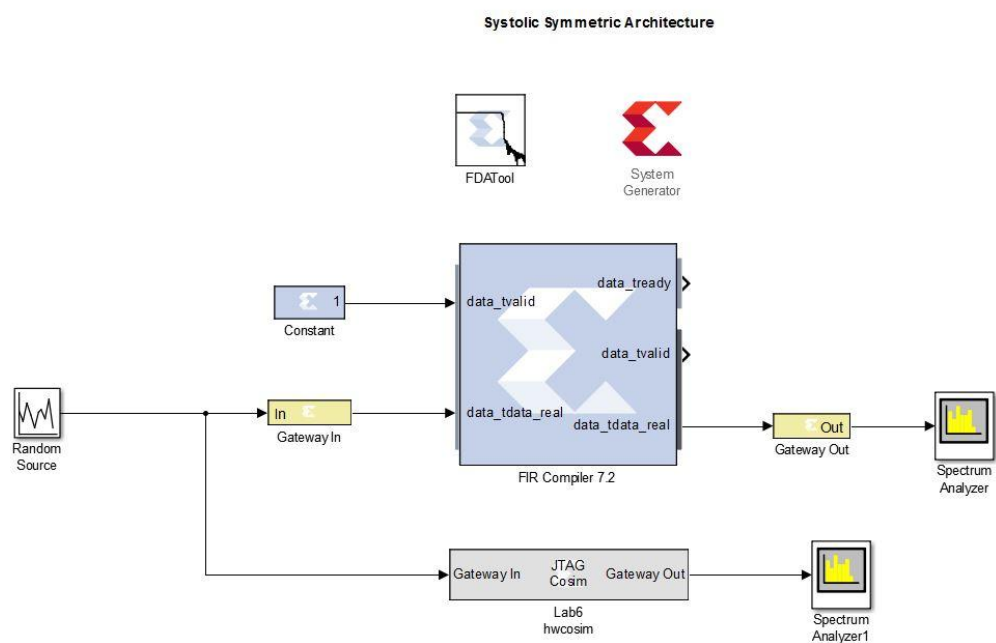


Figure 1: LPF Design

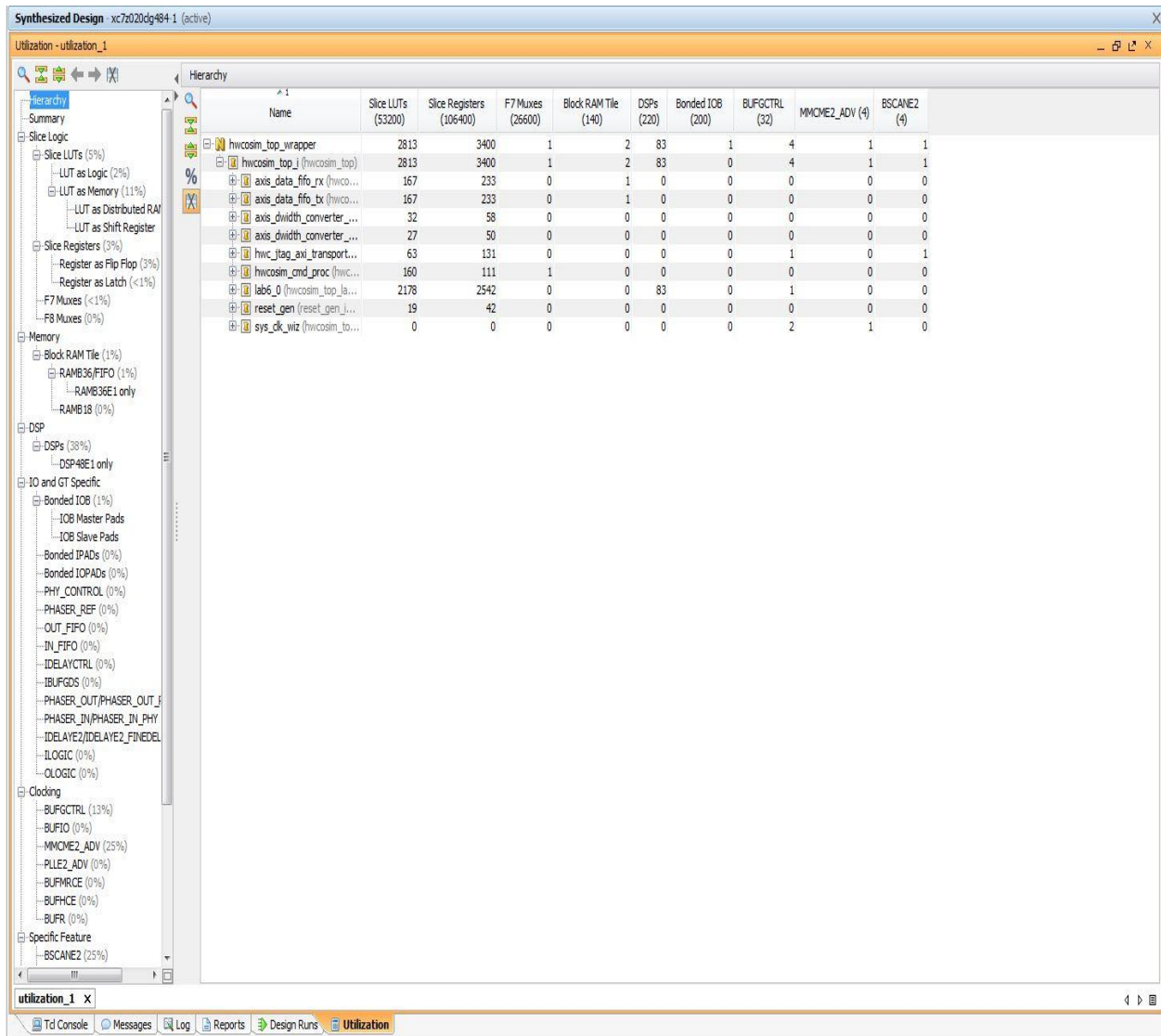


Figure 2: Hardware Utilization

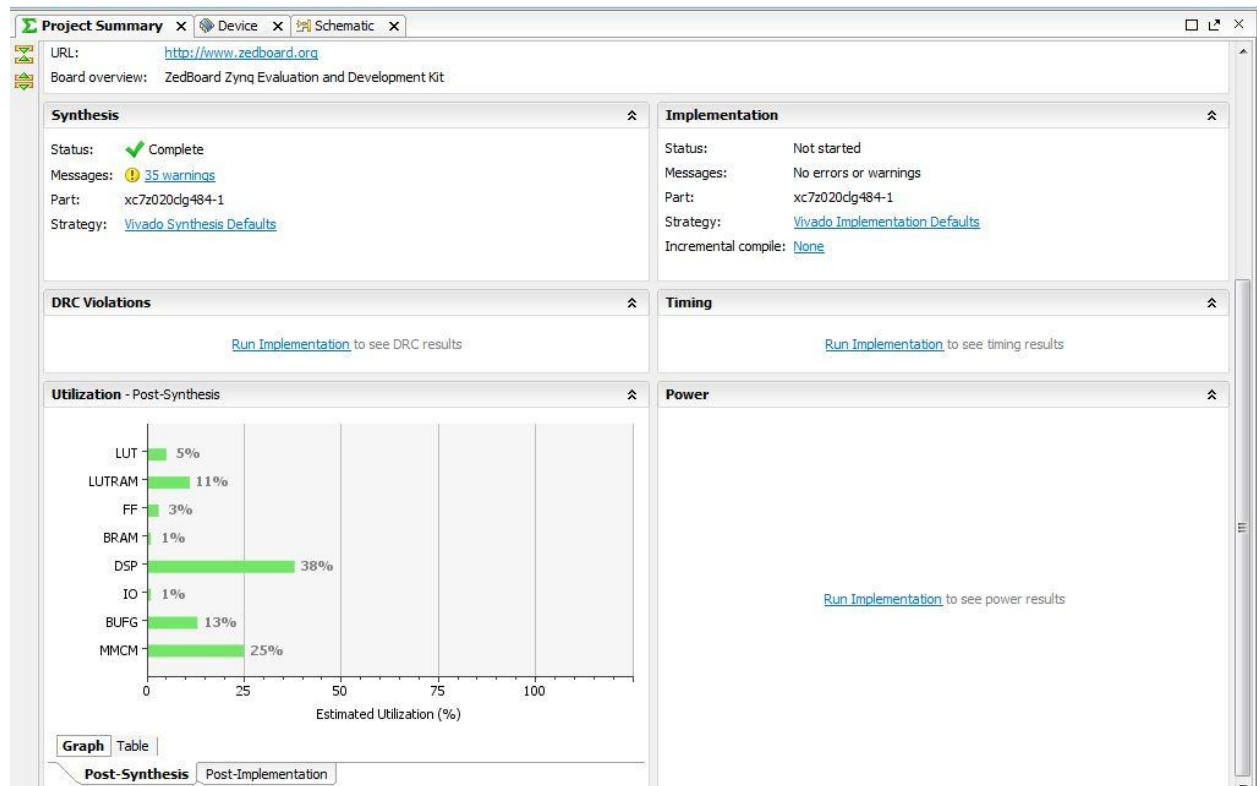


Figure 3: Post Synthesis Utilization Report (Graph)

Utilization - Post-Synthesis				⤴
Resource	Estimation	Available	Utilization %	
LUT	2813	53200	5.29	
LUTRAM	1956	17400	11.24	
FF	3395	106400	3.19	
BRAM	2	140	1.43	
DSP	83	220	37.73	
IO	1	200	0.50	
BUFG	4	32	12.50	
MMCM	1	4	25.00	

Graph
Table

Post-Synthesis
Post-Implementation

Figure 4: Post Synthesis Utilization Report (Table)

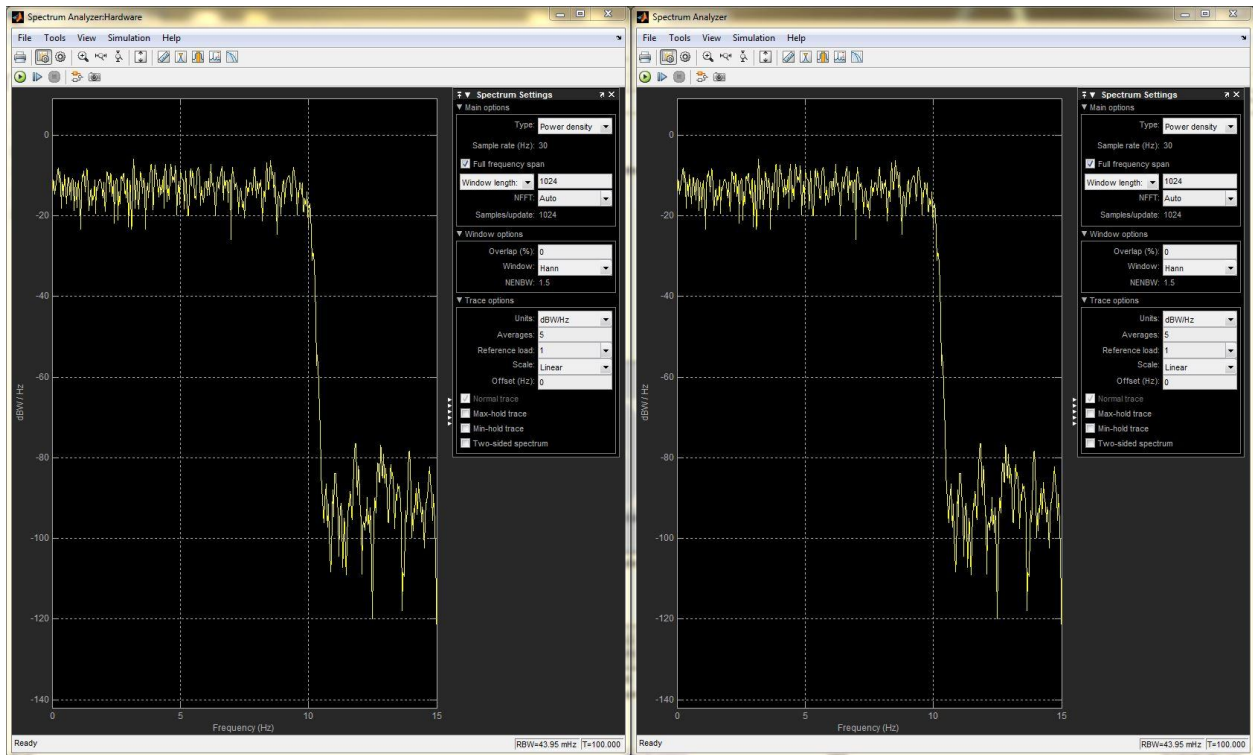


Figure 5: Spectrum Analyzer Comparison

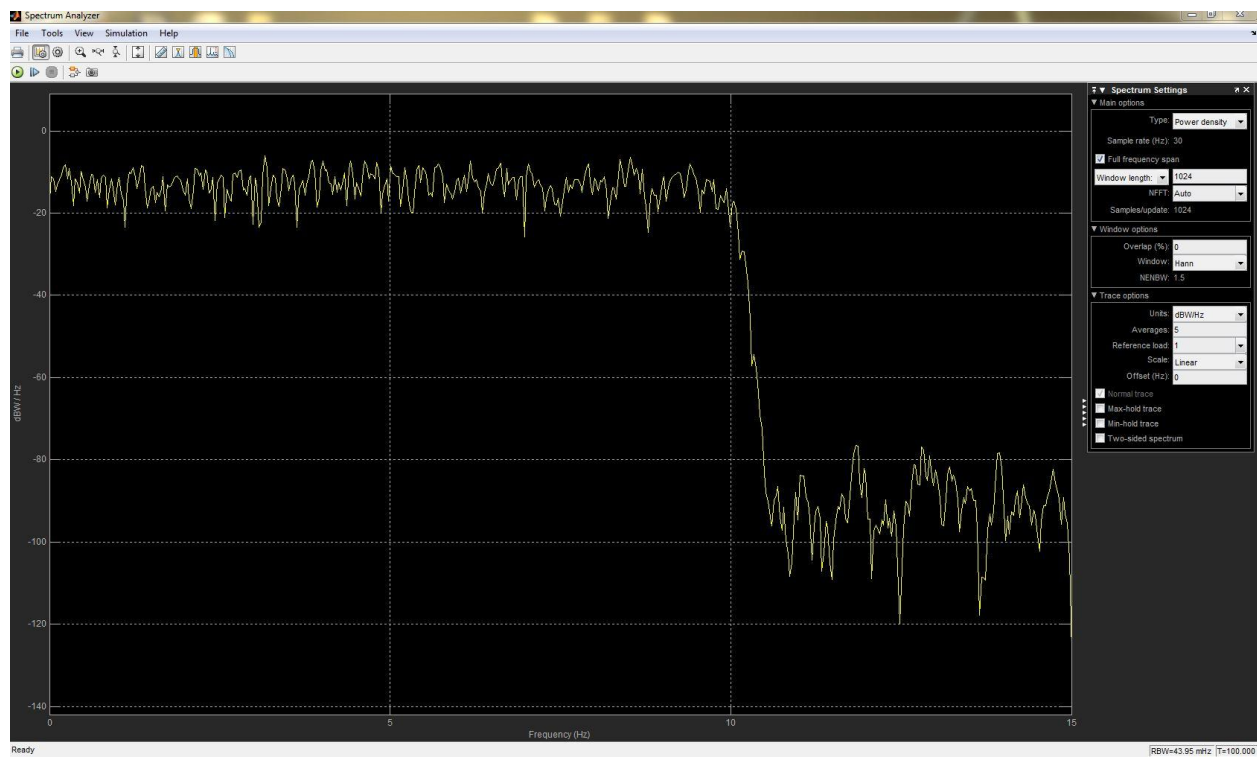


Figure 6: Spectrum Analyzer: Soft Mode

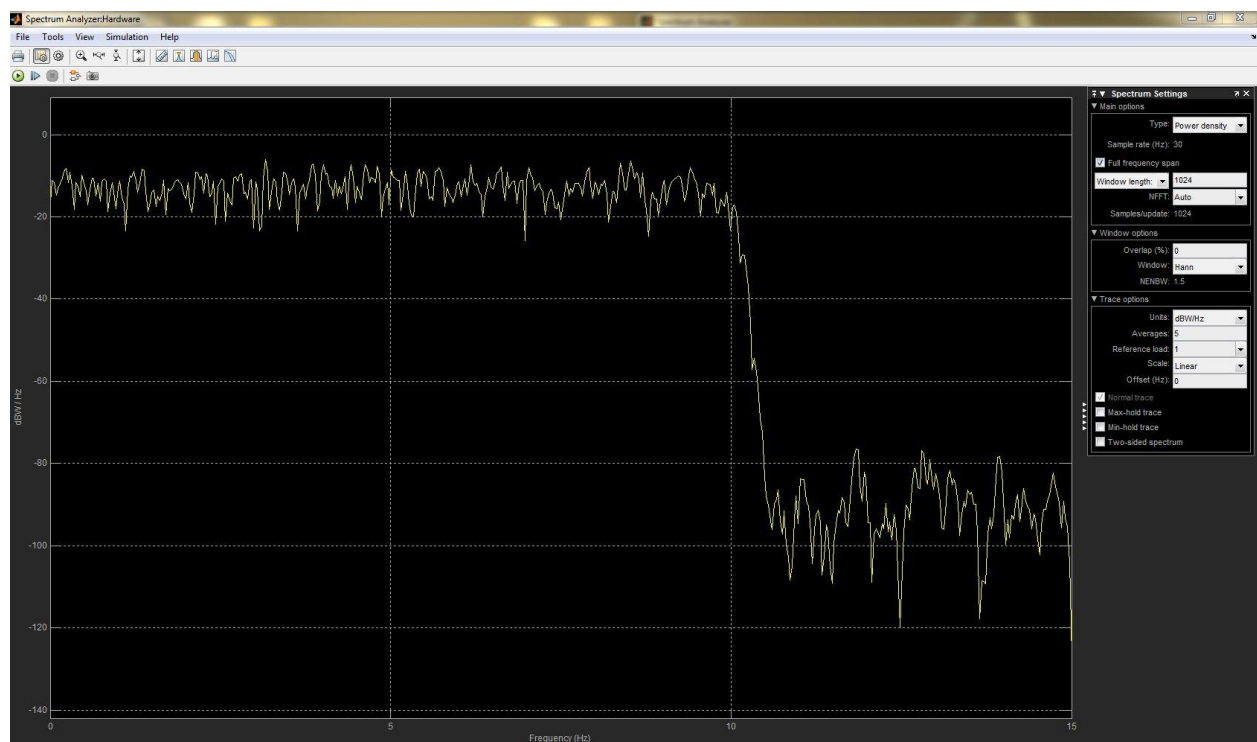


Figure 7: Spectrum Analyzer: Hardware