Bryan Orabutt

bryan@bryanorabutt.com | github.com/BryanOrabutt | bryanorabutt.com | 217-299-8623

SKILLS

PROGRAMMING
C • Python • MATLAB • C++
Assembly • Verilog • VerilogA

TOOLS
Cadence IC Tools • KiCAD
Calibre DRC/LVS/PEX • Git

EMBEDDED PLATFORMS
Arduino • PSoC • Raspberry Pi
Beaglebone • ESP32

EDUCATION

WASHINGTON UNIVERSITY IN ST. LOUIS
MS in Computer Engineering
May 2022| St. Louis, MO

SOUTHERN ILLINOIS UNIVERSITY EDWARDSVILLE
MS in Electrical Engineering
August 2018 | Edwardsville, IL

BS in Computer Engineering
December 2015 | Edwardsville, IL

INTERESTS

I am an engineer at heart and love to make things. I enjoy embedded programming, circuit design, and 3D printing.

You can also often find me playing board games and table top RPGs with friends, or out foraging for wild mushrooms.

EXPERIENCE

INTEL | Product Development Engineer
Jan 2022 - Present | Hillsboro, OR

SOUTHERN ILLINOIS UNIVERSITY EDWARDSVILLE | Lecturer
August 2018 – December 2018 | Edwardsville, IL
- Stuff

RESEARCH

WUSTL RADIOCHEMISTRY LAB | Doctoral Candidate
Jan 2019 – Jan 2022 | St. Louis, MO
TODO: fill out stuff

SIUE VLSI DESIGN LAB | Graduate Researcher
Jan 2016 – August 2018 | Edwardsville, IL
TODO: fill out stuff

ACADEMIC PROJECTS

MACHINE LEARNING ACCELERATOR | Synopsys DC, Verilog, C
Washington University in St. Louis
TODO: fill out stuff

REAL-TIME GPIO INTERRUPT | Raspberry Pi, C, Linux, PSoC
Southern Illinois University Edwardsville
TODO: fill out stuff

PUBLICATIONS

[1] B. Orabutt, R. D. Chamberlain, J. Elson, G. Engel, F. Delaunay, and L. G. Sobotka. Design of mixed-mode systems for pulse-shape discrimination. In *2021 IEEE International Midwest Symposium on Circuits and Systems (MWSCAS)*, pages 990–994, 2021.