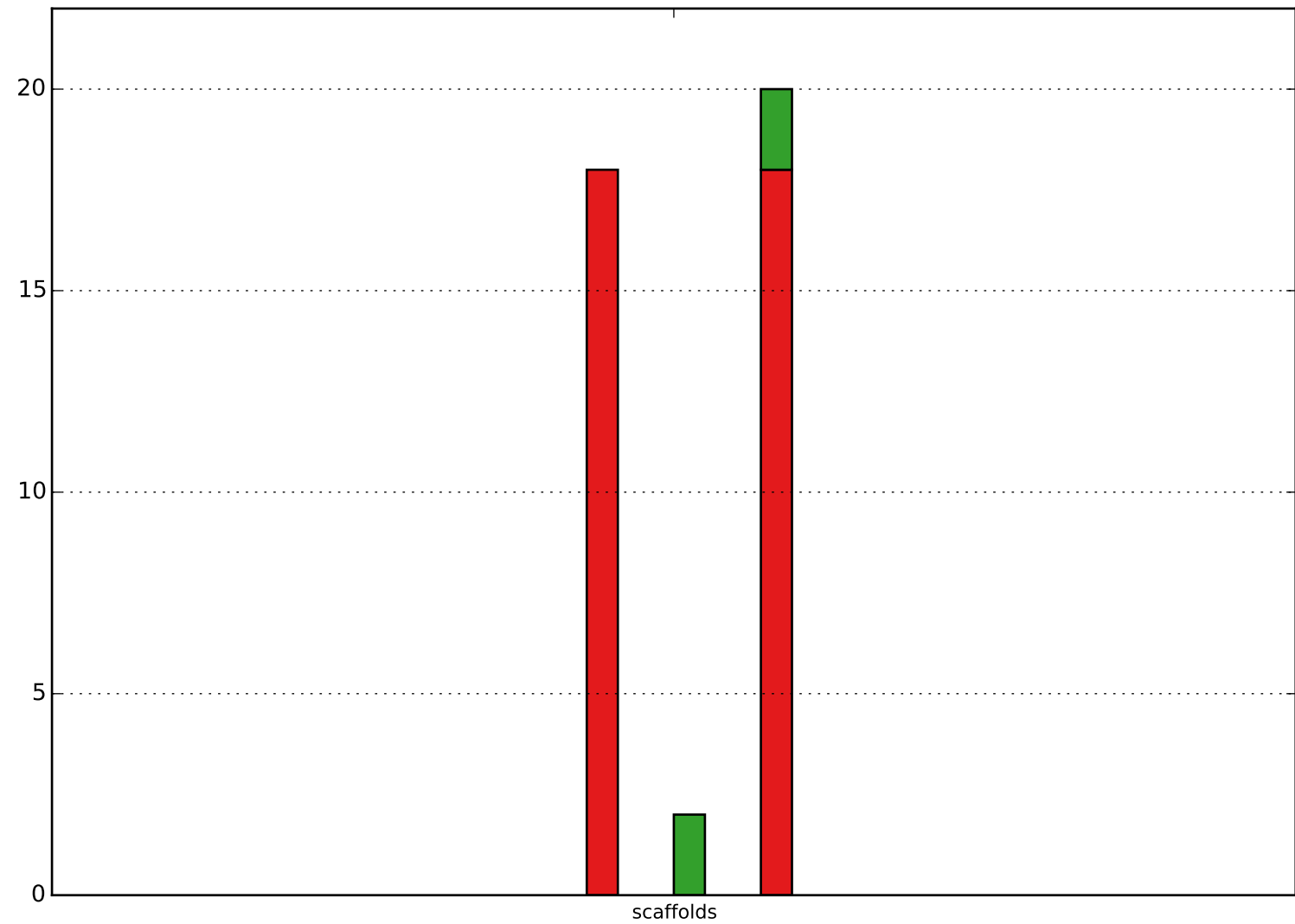


Misassemblies



relocations



inversions