

## Ca11me1ce

### Local Development Platform:

Dell XPS-13-9350

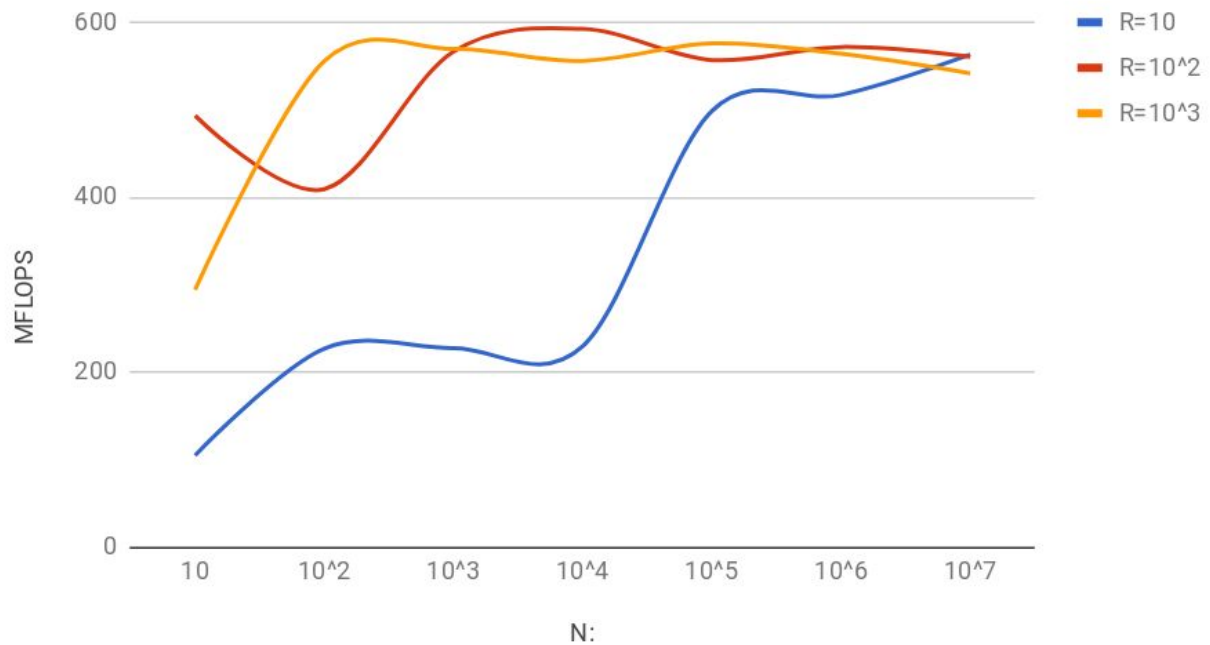
Processor: 3.1 GHz Intel Skylake Core i7-6560U

R A M: 16GB LPDDR3 1866MHz

OS: Ubuntu 16.04.3 LTS(Xenial Xerus)

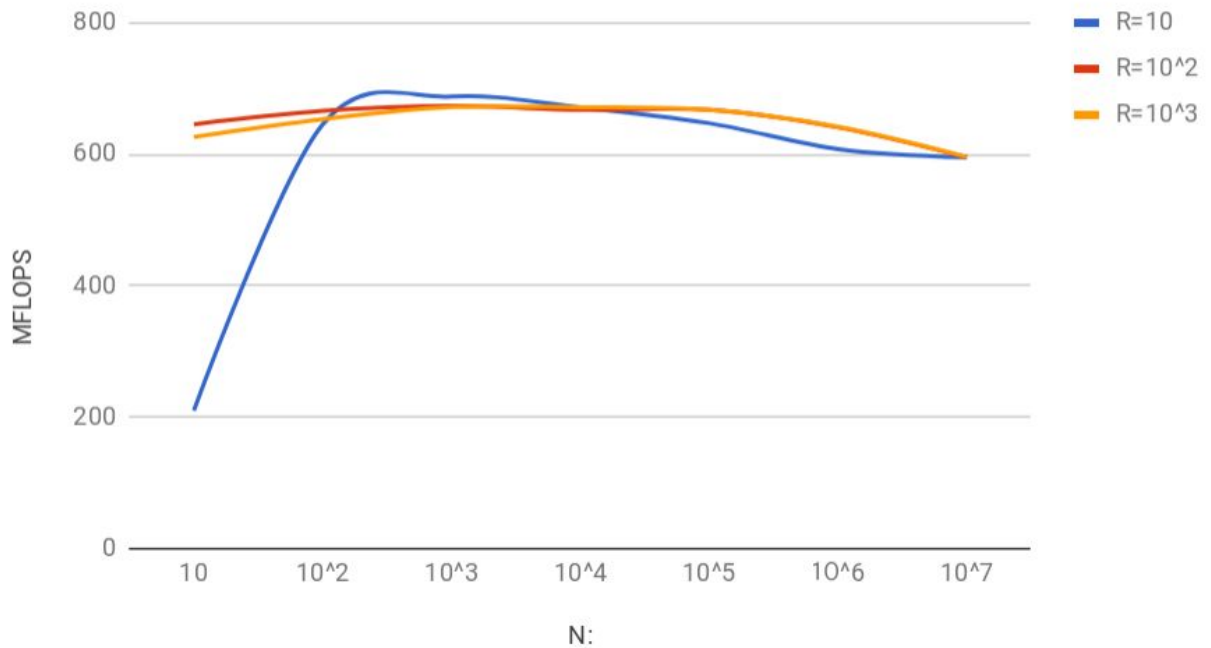
Compiler: GNU Compiler Collection

### Local Platform: MFLOPS vs. N



ICS SSH

### ICS SSH: MFLOPS vs. N:



**Summary:** For very small loop lengths we see poor performance no matter which type of CPU or architecture is used. Performance stays constant for very large loops. On standard microprocessors, performance grows with N until some maximum is reached, followed by several sudden breakdowns. Compare these two graphs, the low-performance region extends much farther than on cache-bashed microprocessors, but there are no breakdowns at all. We can get that the vector systems are somewhat complementary to standard CPUs in that they meet different platforms. It may be possible to optimize codes in a way that circumvents low-performance regions.