Solutions to Tutorial Sheet -11 IEC103 (91) Find the Q points (VCEI, ICI) and (VCE2, IC2) of transfers

81 and Q2 prespectively in the amplifier circuit shown in

Fig. Q1

Take Ic ~ IE, NBE = 0.7 V

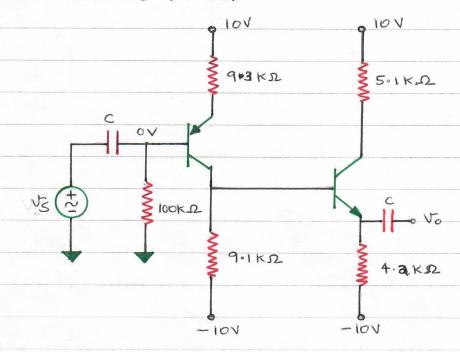


Fig. Q1

Sol.

i)
$$I_{C1} = \frac{10 - 0.7}{9.3 \text{ k}} = \frac{9.3}{9.3 \text{ k}} = 1 \text{ mA} \approx I_{E1}$$

 $V_{E1} = 10 - 1 \times 10^{3} \times 9.3 \times 10^{3} = 10 - 9.3 = 0.7 V$ $V_{C1} = -10 + 9.1 \times 10^{3} \times 1 \times 10^{3} = -10 + 9.1 = -0.9 V$ $V_{CE1} = V_{C1} - V_{E1} = -0.9 - 0.7 = -1.6 V$

9 point of transister 9, is (-1.64, IMA)

11)
$$V_{E2} = V_{C1} - V_{BE} = -0.9 - 0.7 = -1.6 V$$

$$I_{E2} = \frac{V_{E2} - (-10)}{4.2 K} = \frac{-1.6 + 10}{4.2 K} = \frac{8.4}{4.2 K} = 2 \text{mA} \times I_{C2}$$

 $V_{C2} = 10 - I_{C2} \times 5 \cdot 1K = 10 - 5 \cdot 1 \times 10^{3} \times 2 \times 10^{3} = -0.2$ $V_{CE2} = V_{C2} - V_{E2} = -0.2 - (-1.6) = 1.4 \text{ V}$

Q point of transistor Q2 is (1.4V, 2MA)

shown in Fig. Q2. Assume that the base currents are megligible and take VBE = 0.7 V for both Q, and Q2.

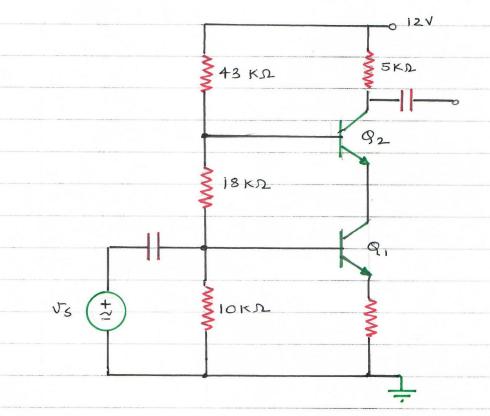
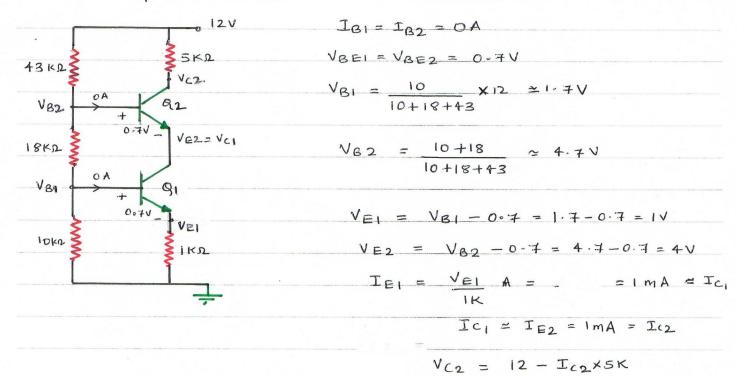


Fig. Q2

Sol. De equivalent ascuit



$$V_{CE1} = V_{C1} - V_{E1} = V_{E2} - V_{E1}$$

= $4 - 1 = 3V$

VCE2 = VC2 - VE2 = -

- . Q point for Q, and Q2 are

 $VCE_1 = 3V$, $IC_1 = 1mA$ and

 $V(E_2 = 3V)$ E(2 = IMA)

(93 For the amplified circuit shown below, calculate the voltage gain $Av\left(\frac{v_o}{v_s}\right)$, current gain $\left(\frac{v_o}{v_s}\right)$; input resistance (Rin), and output resistance (Ro). Given that the transistor is of silicon and $\beta = 100$.

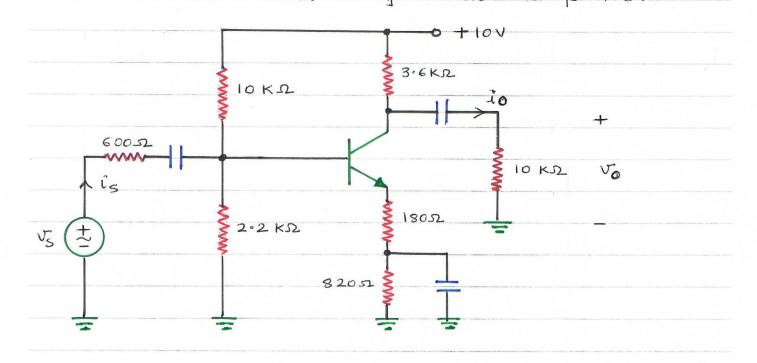
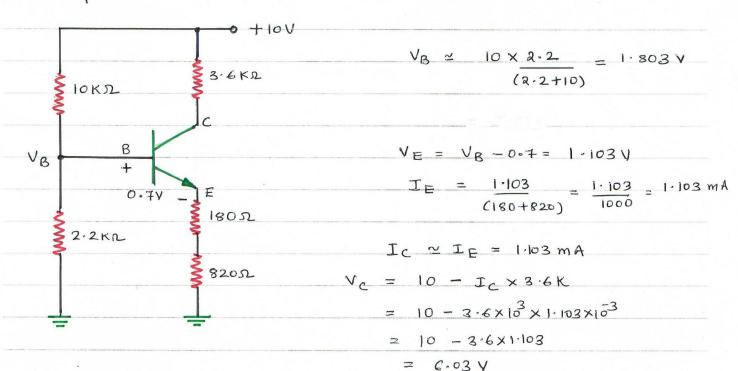


Fig. Q3

Sol.

Capacitors act as open circuit to DC and short circuit to AC.

De equivalent ascuit

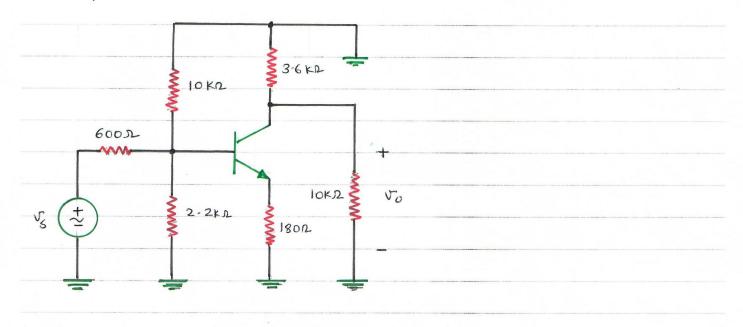


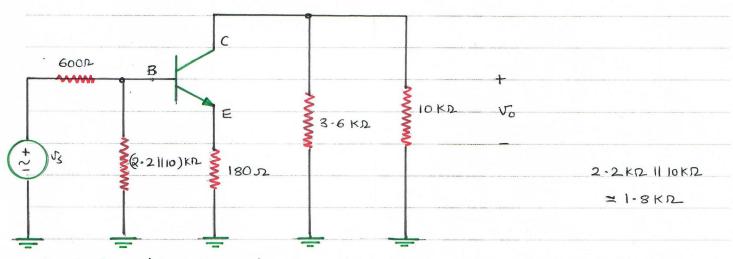
Free Lined Graph Paper from http://incompetech.com/graphpaper/lined/

 $V_{CB} = V_{C} - V_{B} = 6.03 - 1.803 = 4.227 V$

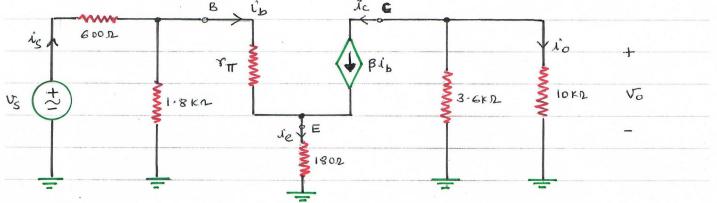
So, collecter base junction is reverse biased. So, the transister is in active negion.

AC equivalent Circuit





Replacing the transistor by its small-signal model (or hybrid-IT model)



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$$r_{\Pi} = \frac{25 \times 10^{-3}}{I_{B}} = \frac{25 \times 10^{-3}}{(I_{C}/\beta)} = \frac{25 \times 10^{-3} \times 100}{I_{C}}$$

Voltage gain

$$= \frac{1}{15} + \frac{20.27 \text{ Kib}}{1.8 \text{ K}} = \frac{1}{15} + \frac{20.27 \text{ ib}}{1.8}$$

$$V_0 = -i_C \times 3.6 \text{K II 10 K}$$

$$= -2.65 \text{K } \text{A}_C$$

$$= -2.65 \text{K } \text{K } \text{B} \text{A}_b$$

$$= -2.65 \text{K} \times 100 \text{A}_b = -265000 \text{A}_b$$

Voltage gain =
$$\frac{V_0}{V_S}$$
 = $\frac{-265000}{27626}$

$$\hat{\lambda}_0 = -\lambda_c \times \frac{3.6}{10+3.6}$$

current gain =
$$\frac{10}{15}$$
 = $\frac{-26.5 \, \text{lb}}{12.261 \, \text{b}}$ = -2.16 = A_i^2

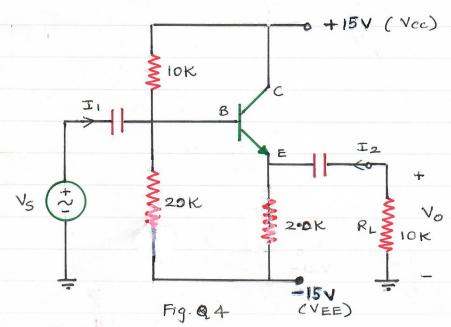
Input resistance

$$Rin = \frac{V_S}{i_S} = \frac{27626i_b}{12 \cdot 26i_b} = \frac{2253.3}{12 \cdot 26i_b} = \frac{2253$$

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output resistance

For the CC amplifier shown in Fig. 4. shown below draw the small signal equivalent of the circuit and compute the voltage gain = $A_V = \frac{V_O}{V_S}$



Assume that capacities act as short circuit in the frequency range of interest. Take $\beta = 100$, VBE = 0.7VSol.

Calculating the gussent values

$$V_{B} = 15 \times \frac{20}{20+10} = 15 \times \frac{10}{20+10} = 5V$$

$$V_{E} = V_{B} - V_{BE} = 5 - 0.7 = 4.3V$$

$$I_{E} = \frac{V_{E}}{2K} = \frac{4.3}{2K} = 2.15 \text{ mA}$$

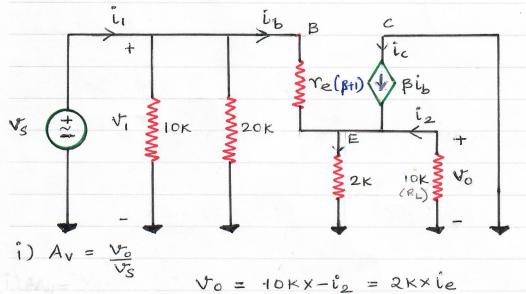
$$I_{C} = \frac{\beta}{\beta+1} I_{E} = \frac{100}{101} I_{E} \approx I_{E} = 2.15 \text{ mA}$$

$$V_{CE} = V_{C} - V_{E} = 15 - 4.3 = 11.7V$$

$$Y_{C} = \frac{25}{I_{C}(\text{in mA})} = \frac{25}{2.15} = 11.6 \Omega$$

1.6.0

Small signal equivalent of the circuit is as given below.



= 2K x (B+1) ib

$$V_1 = V_S = (\beta + i) \Upsilon e \mathring{l}_b + (\beta + i) \times 2K \times \mathring{l}_b$$

$$= (\beta + i) \times (\Upsilon e + 2K) \mathring{l}_b$$

$$A_{V} = \frac{V_{0}}{V_{5}} = \frac{2K \times (\beta + i) l_{b}}{(\beta + i) (\gamma_{e} + 2k) l_{b}} \sim 1$$