



The schematic diagram illustrates the connection of the LCD module to the microcontroller. It features two 74LVC244 ICs, IC2A and IC2B, which act as buffers or drivers for the LCD module's signals.

**IC2A (74LVC244) Connections:**

- Inputs:** A1 (A-LCD\_CS), A2 (A-SDI), A3 (A-LCD\_LED), A4 (A-LCD\_RST).
- Outputs:** Y1 (LCD\_CS), Y2 (SDI), Y3 (LCD\_LED), Y4 (LCD\_RST).
- Power:** Pin 1 (G) is connected to GND. Pin 18 is connected to +3V3.

**IC2B (74LVC244) Connections:**

- Inputs:** A1 (A-SCL), A2 (A-ADS\_CS), A3 (A-ADS\_BUSY), A4 (A-ADS\_IRQ).
- Outputs:** Y1 (SCL), Y2 (ADS\_CS), Y3 (ADS\_BUSY), Y4 (ADS\_IRQ).
- Power:** Pin 1 (G) is connected to GND. Pin 18 is connected to +3V3.

The microcontroller pins are connected to the module pins as follows:

- Microcontroller Pin 18 to Module Pin 18 (+3V3)
- Microcontroller Pin 17 to Module Pin 17 (GND)
- Microcontroller Pin 16 to Module Pin 16 (A-LCD\_CS)
- Microcontroller Pin 15 to Module Pin 15 (A-SDI)
- Microcontroller Pin 14 to Module Pin 14 (A-LCD\_LED)
- Microcontroller Pin 13 to Module Pin 13 (A-LCD\_RST)
- Microcontroller Pin 12 to Module Pin 12 (Y1)
- Microcontroller Pin 11 to Module Pin 11 (Y2)
- Microcontroller Pin 10 to Module Pin 10 (Y3)
- Microcontroller Pin 9 to Module Pin 9 (Y4)

Pin connection diagram for the LCD module. The diagram shows two rows of pins. The top row (CON2) has pins 1-8 connected to GND, A-LCD\_LED, A-LCD\_CS, A-LCD\_RST, A-LCD\_SDI, A-SCL, ADS\_BUSY, ADS\_AUX, and ADS\_VREF. The bottom row (CON1) has pins 1-16 connected to GND, VCC, A-LCD\_LED, A-LCD\_CS, SDO, A-ADS\_CS, ADS\_IRQ, ADS\_VBAT, and GND. Red arrows indicate connections between the two rows: from pin 3 to pin 4, and from pin 15 to pin 16.



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