LLVM support for kernel architectures (2020)

- arm
- arm64
- x86
- powerpc
- mips
- arc
- hexagon
- riscv
- s390
- sparc

- alpha
- c6x
- csky (WIP)
- h8300
- ia64
- m68k
- microblaze
- nds32
- nios2
- openrisc
- parisc
- sh
- xtensa

- Relatively well tested
- Not relatively well tested
- Kernel builds broken / known issues
- No LLVM Backend

LLVM=1

Via https://www.kernel.org/doc/html/latest/kbuild/llvm.html :

```
$ make CC=clang LD=ld.lld AR=llvm-ar NM=llvm-nm \
STRIP=llvm-strip OBJCOPY=llvm-objcopy OBJDUMP=llvm-objdump \
OBJSIZE=llvm-size READELF=llvm-readelf HOSTCC=clang \
HOSTCXX=clang++ HOSTAR=llvm-ar HOSTLD=ld.lld
```

or:

```
$ make LLVM=1
```

Goals for 2020+: Move CI systems to test with **LLVM=1** rather than CC=clang.