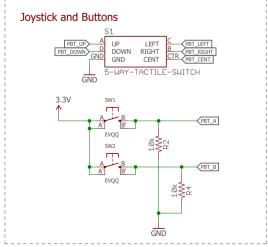
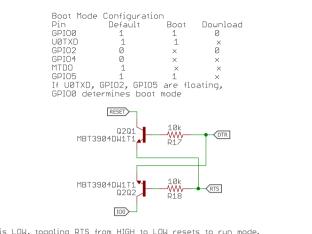


RTC GPIOs: 0,2,4,12–15,25–27,32–39
usable by ULP, internal Pullups /Pulldowns
ext0 for one pin as wakeup source
ext1 for multiple pins as wakeup source
only for GPIOs: 32–39?
no internal Pullups /Pulldowns





If DTR is LOW, toggling RTS from HIGH to LOW resets to run mode. If RTS is HIGH, toggling DTR from LOW to HIGH resets to bootloader.

