

**NASA Research and Technology Program and Project
Management Requirements
NASA Procedural Requirements 7120.8**

**Appendix J. Technology Readiness Levels
(TRLs)**

Effective Date: February 05, 2008
Expiration Date: February 05, 2018

J.1 - TRL Readiness Levels (TRLs) Definitions:

| TRL | Definition | Hardware Description | Software Description | Exit Criteria |
|------------|---|--|---|---|
| 1 | Basic principles observed and reported. | Scientific knowledge generated underpinning hardware technology concepts/applications. | Scientific knowledge generated underpinning basic properties of software architecture and mathematical formulation. | Peer reviewed publication of research underlying the proposed concept/application. |
| 2 | Technology concept and/or application formulated. | Invention begins, practical application is identified but is speculative, no experimental proof or detailed analysis is available to support the conjecture. | Practical application is identified but is speculative, no experimental proof or detailed analysis is available to support the conjecture. Basic properties of algorithms, representations and concepts defined. Basic principles coded. Experiments performed with | Documented description of the application/concept that addresses feasibility and benefit. |

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| | | | coded. Experiments performed with synthetic data. | |
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| TRL | Definition | Hardware Description | Software Description | Exit Criteria |
|------------|---|--|--|---|
| 3 | Analytical and experimental critical function and/or characteristic proof of concept. | Analytical studies place the technology in an appropriate context and laboratory demonstrations, modeling and simulation validate analytical prediction. | Development of limited functionality to validate critical properties and predictions using non-integrated software components. | Documented analytical/experimental results validating predictions of key parameters. |
| 4 | Component and/or breadboard validation in laboratory environment. | A low fidelity system/component breadboard is built and operated to demonstrate basic functionality and critical test environments, and associated performance predictions are defined relative to the final operating environment. | Key, functionally critical, software components are integrated, and functionally validated, to establish interoperability and begin architecture development. Relevant Environments defined and performance in this environment predicted. | Documented test performance demonstrating agreement with analytical predictions. Documented definition of relevant environment. |
| 5 | Component and/or breadboard validation in relevant environment. | A medium fidelity system/component breadboard is built and operated to demonstrate overall performance in a simulated operational environment with realistic support elements that demonstrates overall performance in critical areas. Performance predictions are made for subsequent development phases. | End-to-end software elements implemented and interfaced with existing systems/simulations conforming to target environment. End-to-end software system, tested in relevant environment, meeting predicted performance. Operational environment performance predicted. Prototype implementations developed. | Documented test performance demonstrating agreement with analytical predictions. Documented definition of scaling requirements. |

| TRL | Definition | Hardware | Software | Exit Criteria |
|------------|-------------------|-----------------|-----------------|----------------------|
|------------|-------------------|-----------------|-----------------|----------------------|

| TRL | Definition | Hardware Description | Software Description | Exit Criteria |
|------------|---|--|---|--|
| 6 | System/sub-system model or prototype demonstration in an operational environment. | A high fidelity system/component prototype that adequately addresses all critical scaling issues is built and operated in a relevant environment to demonstrate operations under critical environmental conditions. | Prototype implementations of the software demonstrated on full-scale realistic problems. Partially integrate with existing hardware/software systems. Limited documentation available. Engineering feasibility fully demonstrated. | Documented test performance demonstrating agreement with analytical predictions. |
| 7 | System prototype demonstration in an operational environment. | A high fidelity engineering unit that adequately addresses all critical scaling issues is built and operated in a relevant environment to demonstrate performance in the actual operational environment and platform (ground, airborne, or space). | Prototype software exists having all key functionality available for demonstration and test. Well integrated with operational hardware/software systems demonstrating operational feasibility. Most software bugs removed. Limited documentation available. | Documented test performance demonstrating agreement with analytical predictions. |

| TRL | Definition | Hardware Description | Software Description | Exit Criteria |
|------------|--|---|--|---|
| 8 | Actual system completed and "flight qualified" through test and demonstration. | The final product in its final configuration is successfully demonstrated through test and analysis for its intended operational environment and platform (ground, airborne, or space). | All software has been thoroughly debugged and fully integrated with all operational hardware and software systems. All user documentation, training documentation, and maintenance documentation | Documented test performance verifying analytical predictions. |

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|---|--|--|---|---|
| | | (ground, airborne, or space). | documentation, and maintenance documentation completed. All functionality successfully demonstrated in simulated operational scenarios. Verification and Validation (V&V) completed. | |
| 9 | Actual system flight proven through successful mission operations. | The final product is successfully operated in an actual mission. | All software has been thoroughly debugged and fully integrated with all operational hardware/software systems. All documentation has been completed. Sustaining software engineering support is in place. System has been successfully operated in the operational environment. | Documented mission operational results. |

J.2 Technology Development Terminology

Proof of Concept:

Analytical and experimental demonstration of hardware/software concepts that may or may not be incorporated into subsequent development and/or operational units.

Breadboard:

A low fidelity unit that demonstrates function only, without respect to form or fit in the case of hardware, or platform in the case of software. It often uses commercial and/or ad hoc components and is not intended to provide definitive information regarding operational performance.

Brassboard:

A medium fidelity functional unit that typically tries to make use of as much operational hardware/software as possible and begins to address scaling issues associated with the operational system. It does not have the engineering pedigree in all aspects, but is structured to be able to operate in simulated operational environments in order to assess performance of critical functions.

Proto-type Unit:

The proto-type unit demonstrates form, fit, and function at a scale deemed to be representative of the final product operating in its

Proto-type Unit:

The proto-type unit demonstrates form, fit, and function at a scale deemed to be representative of the final product operating in its operational environment. A subscale test article provides fidelity sufficient to permit validation of analytical models capable of predicting the behavior of full-scale systems in an operational environment.

Engineering Unit:

A high fidelity unit that demonstrates critical aspects of the engineering processes involved in the development of the operational unit. Engineering test units are intended to closely resemble the final product (hardware/software) to the maximum extent possible and are built and tested so as to establish confidence that the design will function in the expected environments. In some cases, the engineering unit will become the final product, assuming proper traceability has been exercised over the components and hardware handling.

Mission Configuration:

The final architecture/system design of the product that will be used in the operational environment. If the product is a subsystem/component, then it is embedded in the actual system in the actual configuration used in operation. Laboratory Environment:

An environment that does not address in any manner the environment to be encountered by the system, subsystem, or component (hardware or software) during its intended operation. Tests in a laboratory environment are solely for the purpose of demonstrating the underlying principles of technical performance (functions), without respect to the impact of environment.

Relevant Environment:

Not all systems, subsystems, and/or components need to be operated in the operational environment in order to satisfactorily address performance margin requirements. Consequently, the relevant environment is the specific subset of the operational environment that is required to demonstrate critical "at risk" aspects of the final product performance in an operational environment. It is an environment that focuses specifically on "stressing" the technology advance in question.

Operational Environment:

The environment in which the final product will be operated. In the case of space flight hardware/software, it is space. In the case of ground-based or airborne systems that are not directed toward space flight, it will be the environments defined by the scope of operations. For software, the environment will be defined by the operational platform.

Laboratory Environment:

An environment that does not address in any manner the environment to be encountered by the system, subsystem, or component (hardware or software) during its intended operation tests in a laboratory environment are solely for the purpose of demonstrating the underlying principles of technical performance (functions) without respect to the impact of environment.

