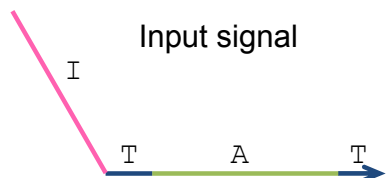


$I_1$	$I_0$	$O$
0	0	0/4
0	1	1/4
1	0	2/4
1	1	3/4



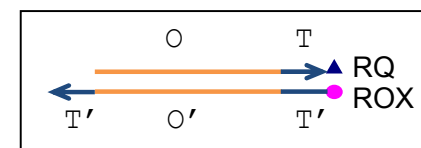
$I_0$  Switching signal



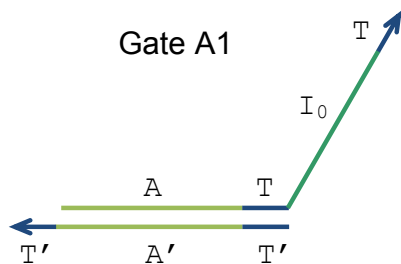
$I_1$  Switching signal



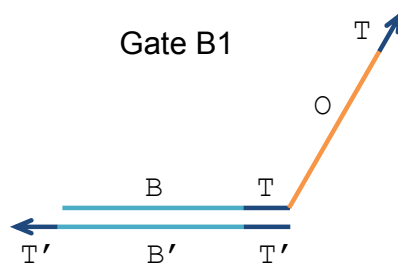
Output reporter



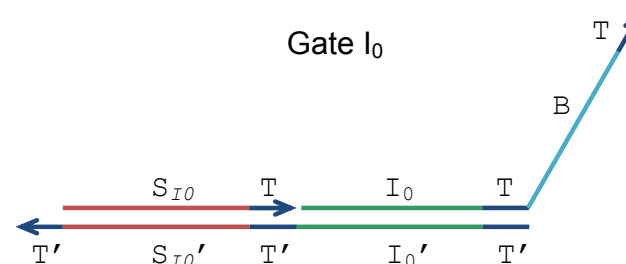
Gate A1



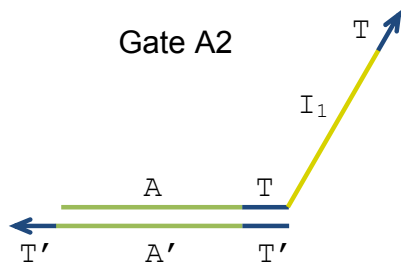
Gate B1



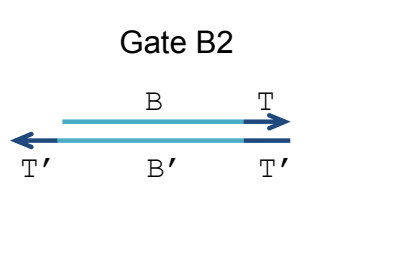
Gate  $I_0$



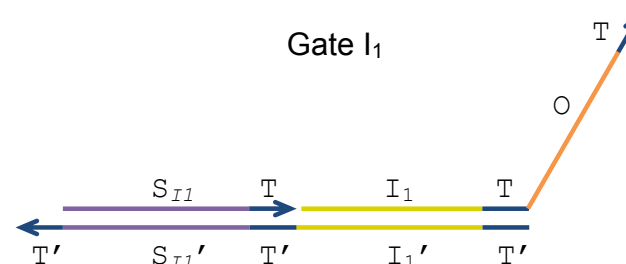
Gate A2



Gate B2



Gate  $I_1$



$[A1] = [A2]$

$[B1] = [B2]$