

FEMB 7m Cable	Net Name	# of Wires	R_RT [Ω]	R_LAr [Ω]	XSection [mm^2]
Analog Mother Board	FE-ANA-IN	3	0.078	0.025	1.554
	REGR_BIAS	1	0.233	0.074	0.518
FPGA Mezzanine	P1.5V	1	0.233	0.074	0.518
	P2.8V	1	0.233	0.074	0.518
	P3.6V	1	0.233	0.074	0.518
	BIAS	1	0.233	0.074	0.518
Summary		8	0.029	0.009	4.144