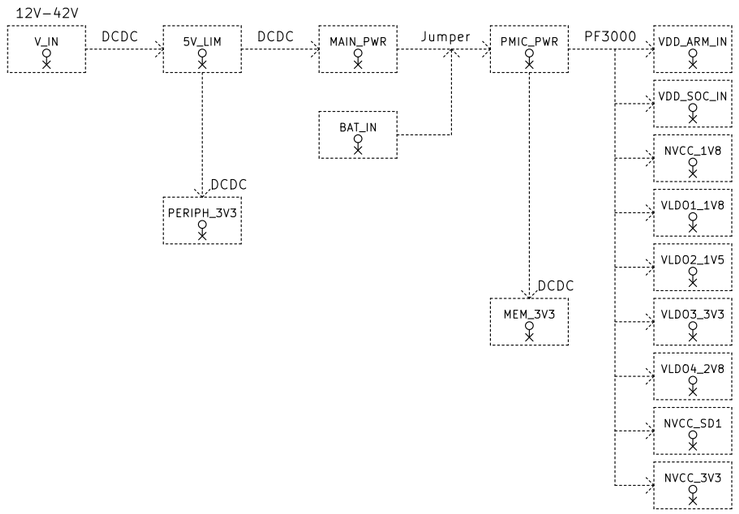
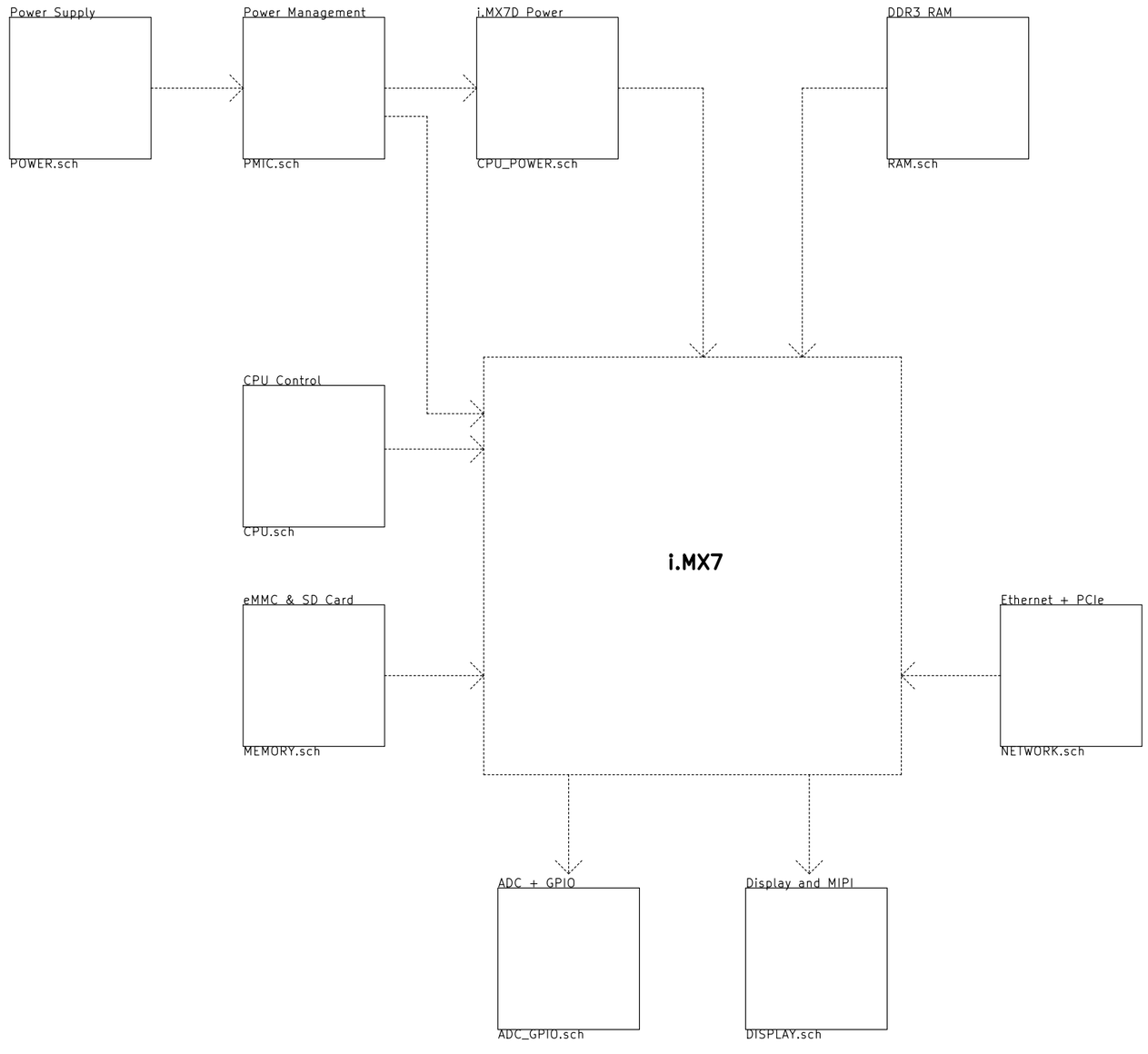
Power tree





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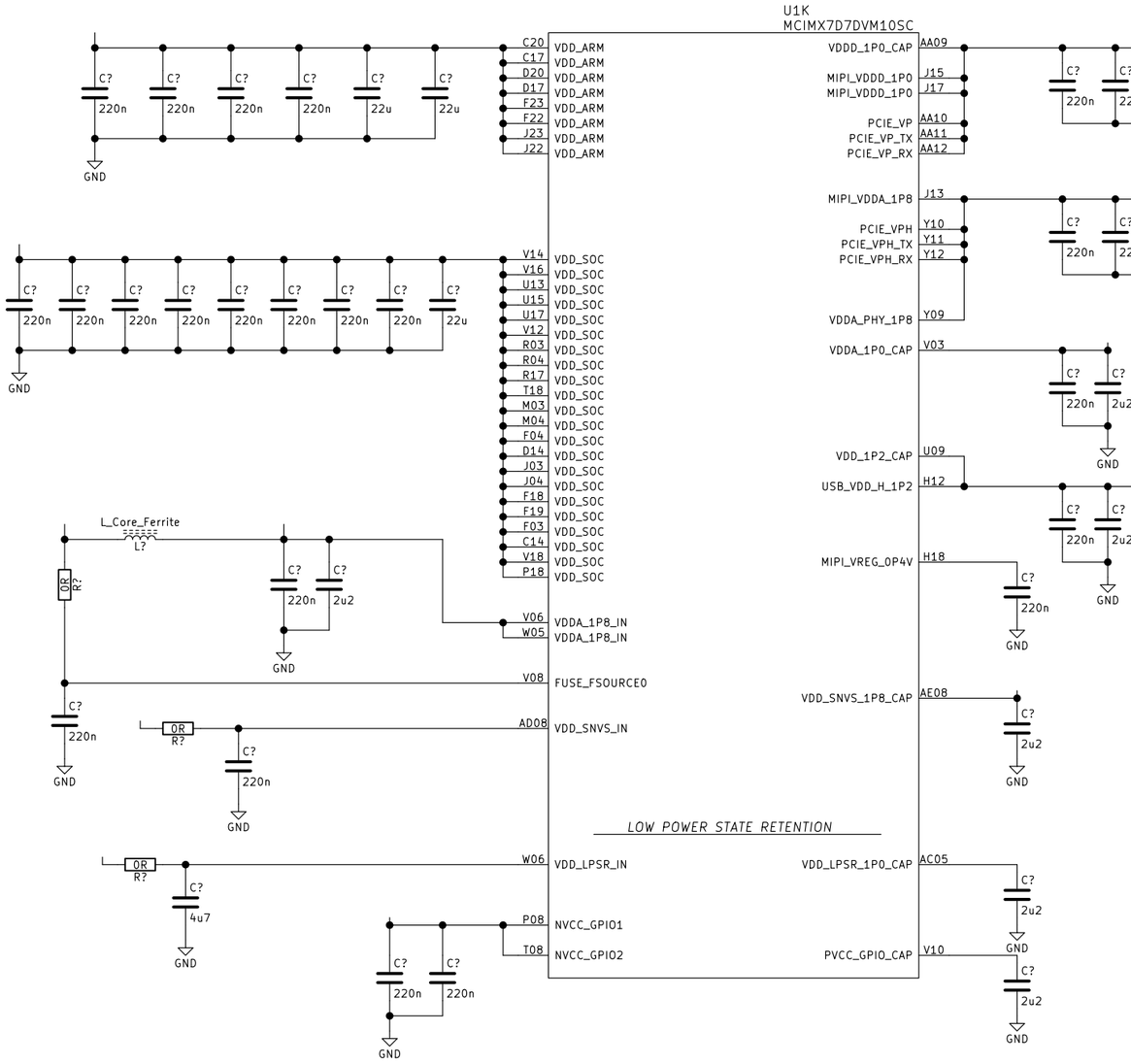
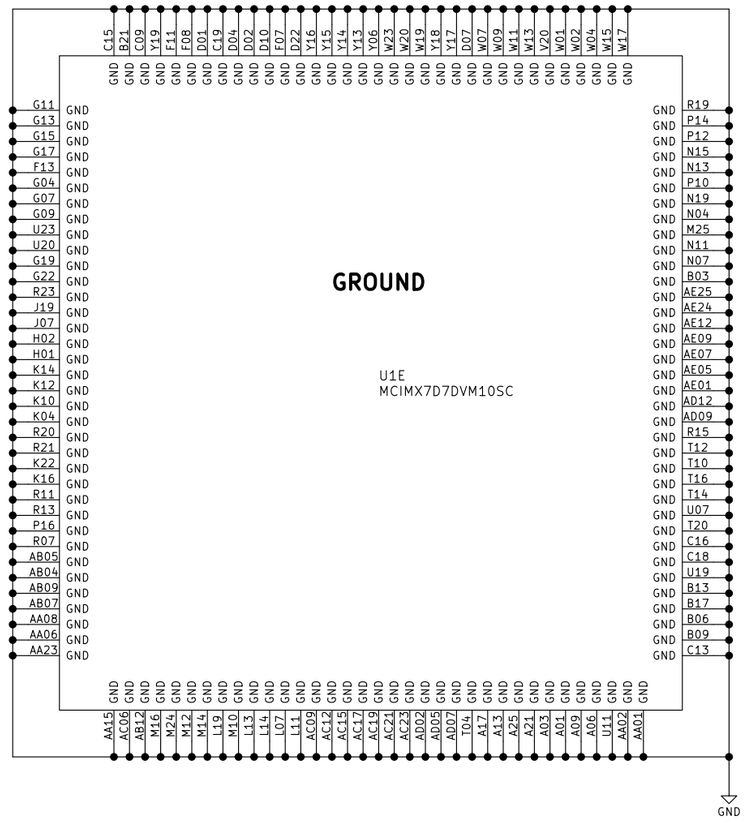
File: i.MX7_Test.sch	
Title:	
Size: A3	Date:
	Rev:
	Id: 1/10





Sheet: /i.MX7D Power/ File: CPU_POWER.sch		
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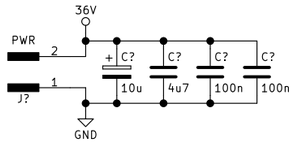
Size: A3	Date:	**Rev:** Id: 2/10

Sheet: /Power Management/
File: PMIC.sch

Title:

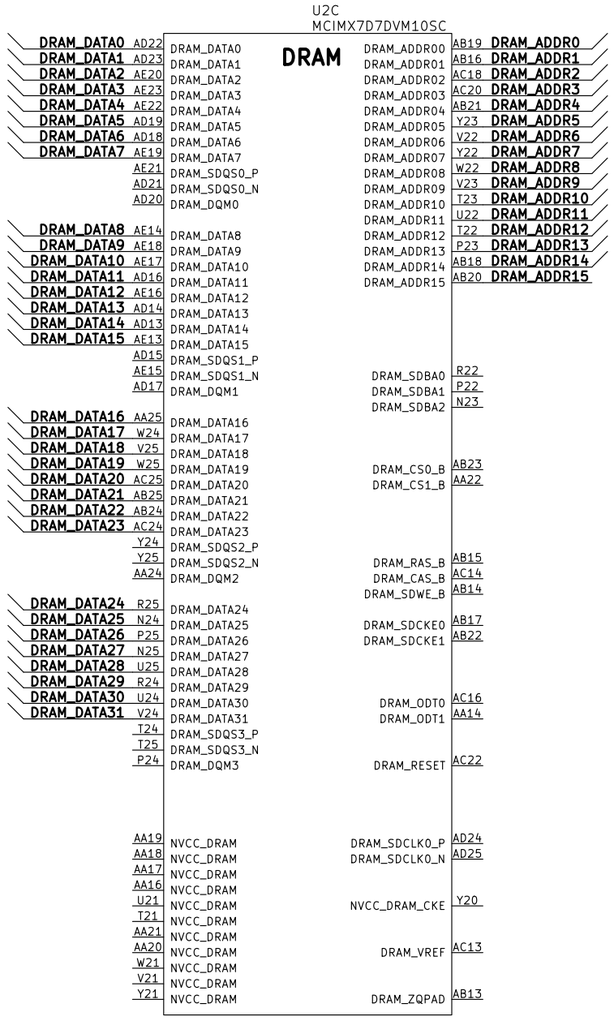
Size: A3	Date:	**Rev:**
		Id: 3/10

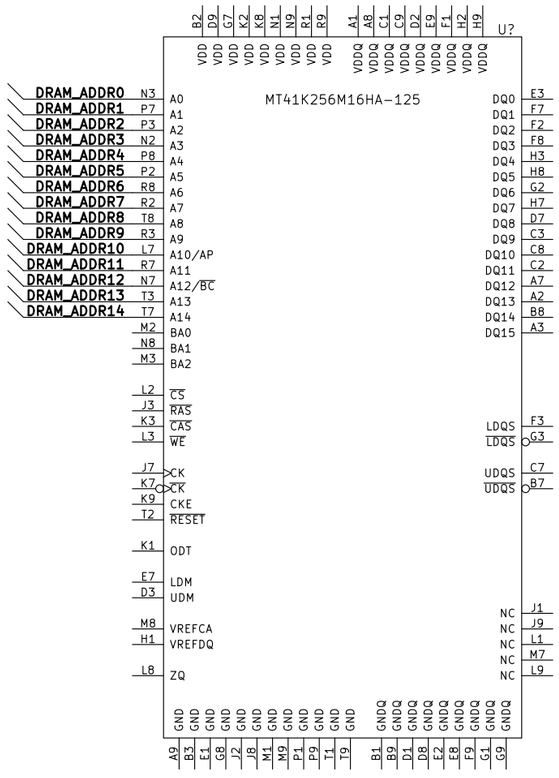


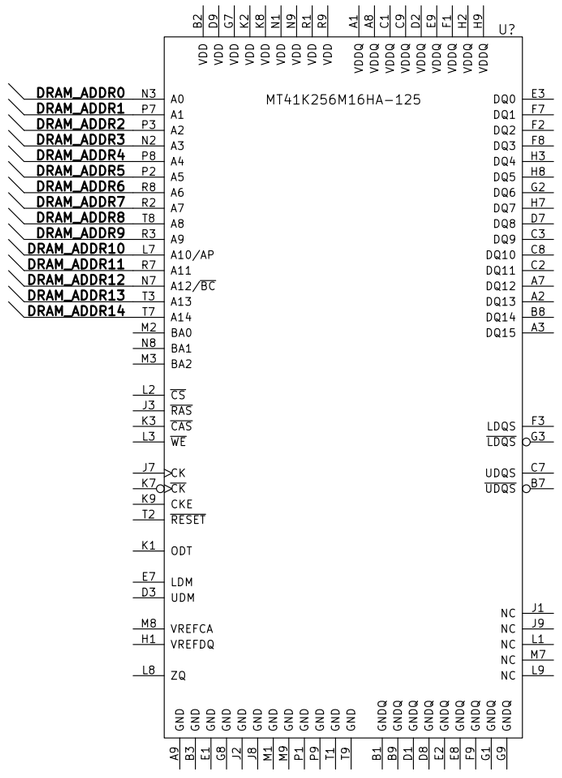
Sheet: /Power Supply/
File: POWER.sch

Title:

Size: A3 | Date: | **Rev:** Id: 4/10



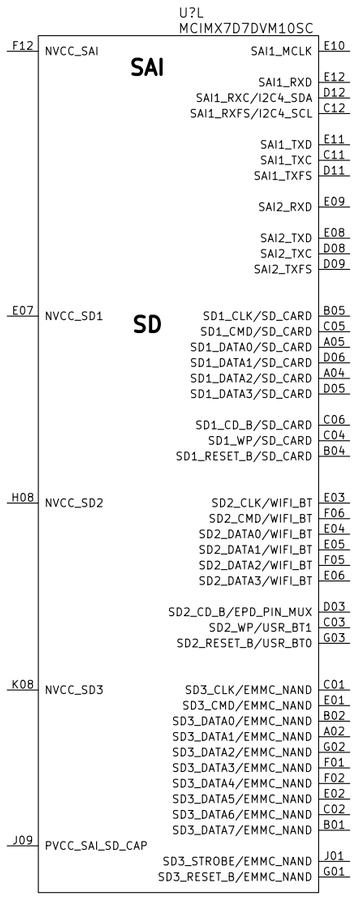




Sheet: /DDR3 RAM/
File: RAM.sch

Title:

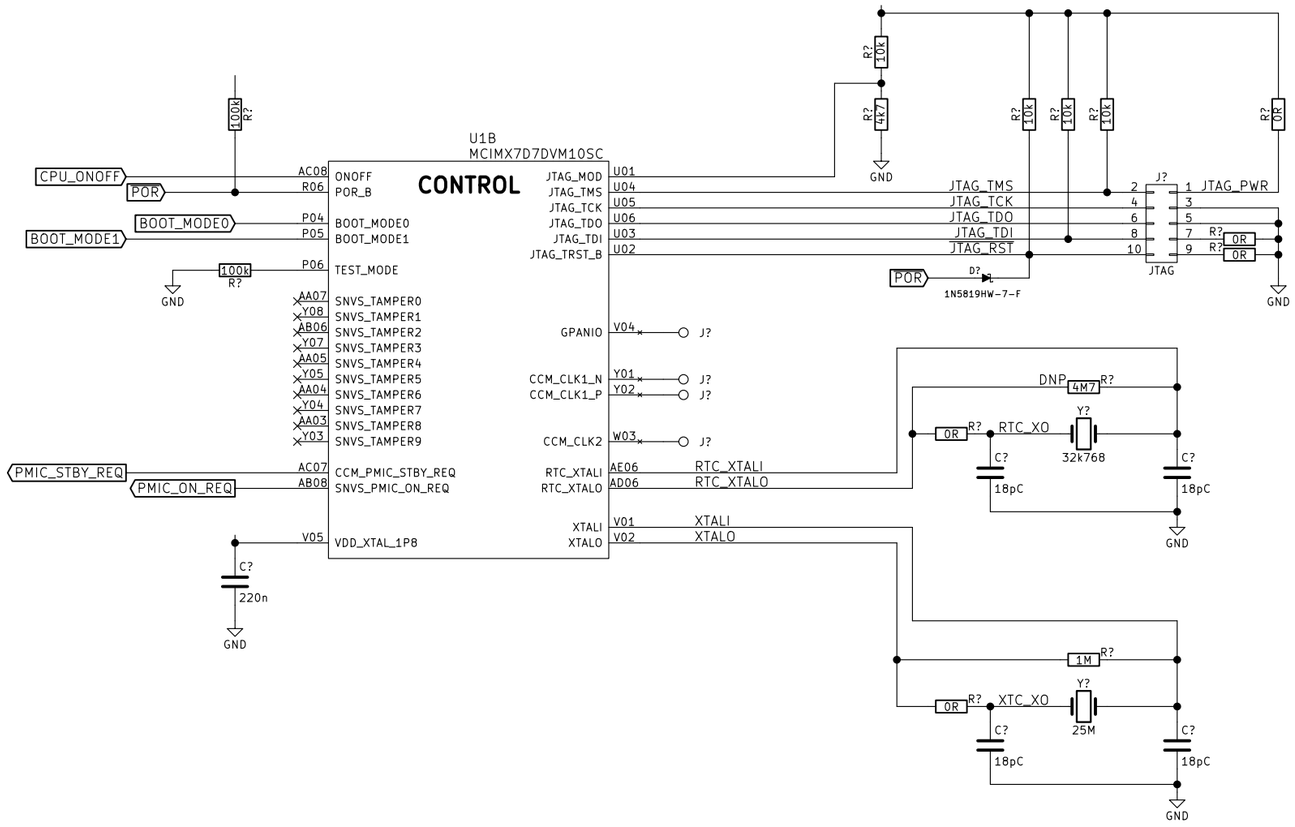
Size: A3	Date:	**Rev:**
		Id: 5/10

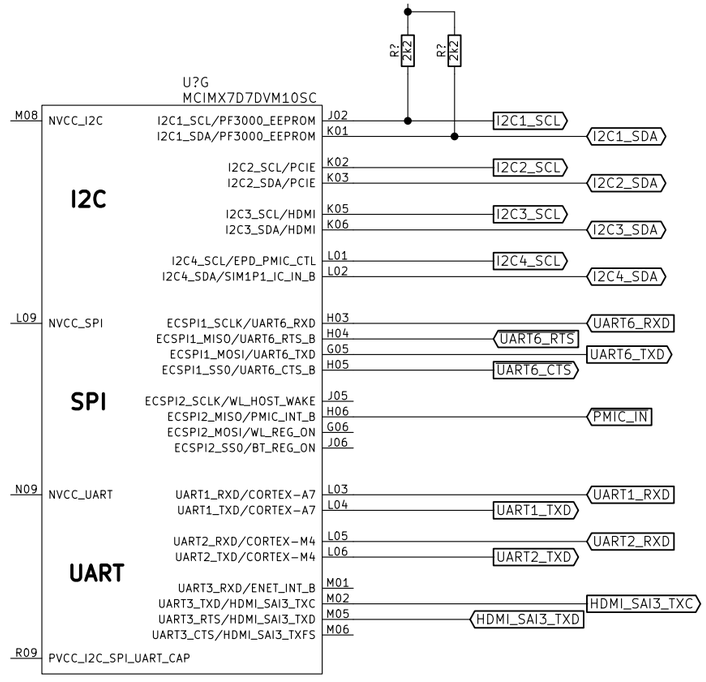


Sheet: /eMMC & SD Card/
File: MEMORY.sch

Title:

Size: A3	Date:	**Rev:**
		Id: 6/10





Sheet: /CPU Control/
File: CPU.sch

Title:

Size: A3 | Date: | **Rev:** Id: 7/10

U?H
MCIMX7D7DVM10SC

LCD

K18	NVCC_LCD	
		LCD_CLK E20
		LCD_ENABLE F25
		LCD_HSYNC E25
		LCD_VSYNC F24
		LCD_RESET C21
		LCD_DATA00 D21
		LCD_DATA01 A22
		LCD_DATA02 B22
		LCD_DATA03 A23
		LCD_DATA04 C22
		LCD_DATA05 B23
		LCD_DATA06 A24
		LCD_DATA07 F20
		LCD_DATA08 E21
		LCD_DATA09 C23
		LCD_DATA10 B24
		LCD_DATA11 G20
		LCD_DATA12 F21
		LCD_DATA13 E22
		LCD_DATA14 D23
		LCD_DATA15 C24
		LCD_DATA16 B25
		LCD_DATA17 G21
		LCD_DATA18 E23
		LCD_DATA19 D24
		LCD_DATA20 C25
		LCD_DATA21 E24
		LCD_DATA22 D25
		LCD_DATA23 G23

EPDC

M18	NVCC_EPDC1	
		EPDC_D00 P20
		EPDC_D01 P21
		EPDC_D02 N20
		EPDC_D03 N21
		EPDC_D04 N22
		EPDC_D05 M20
		EPDC_D06 M21
		EPDC_D07 M22
		EPDC_D08 M23
		EPDC_D09 L25
		EPDC_D10 L24
		EPDC_D11 L23
		EPDC_D12 L22
		EPDC_D13 L21
		EPDC_D14 L20
		EPDC_D15 K25
L17	NVCC_EPDC2	
		EPDC_SDCE0 G25
		EPDC_SDCE1 G24
		EPDC_SDCE2 H23
		EPDC_SDCE3 H22
		EPDC_SDCLK J21
		EPDC_SDLE J20
		EPDC_SDOE H21
		EPDC_SDSHR H20
		EPDC_GDCLK J25
		EPDC_GDOE J24
		EPDC_GDRL K21
		EPDC_GDSP H25
		EPDC_PWRCOM H24
N17	PVCC_EPDC_LCD_CAP	EPDC_PWRSTAT K20
		EPDC_BDR0 K24
		EPDC_BDR1 K23

U?I
MCIMX7D7DVM10SC

MIPI CSI

MIPI_CSI_CLK_N	A15
MIPI_CSI_CLK_P	B15
MIPI_CSI_D0_N	A16
MIPI_CSI_D0_P	B16
MIPI_CSI_D1_N	A14
MIPI_CSI_D1_P	B14

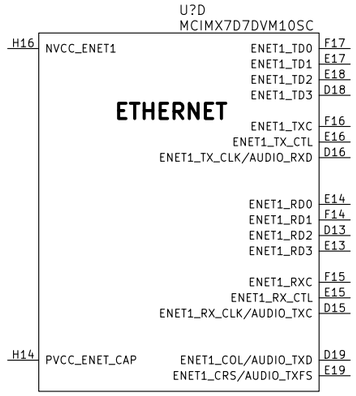
MIPI DSI

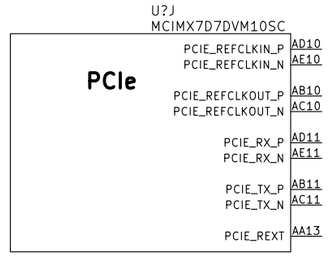
MIPI_DSI_CLK_N	A19
MIPI_DSI_CLK_P	B19
MIPI_DSI_D0_N	A20
MIPI_DSI_D0_P	B20
MIPI_DSI_D1_N	A18
MIPI_DSI_D1_P	B18

Sheet: /Display and MIPI/
File: DISPLAY.sch

Title:

Size: A3	Date:	**Rev:**
		Id: 8/10

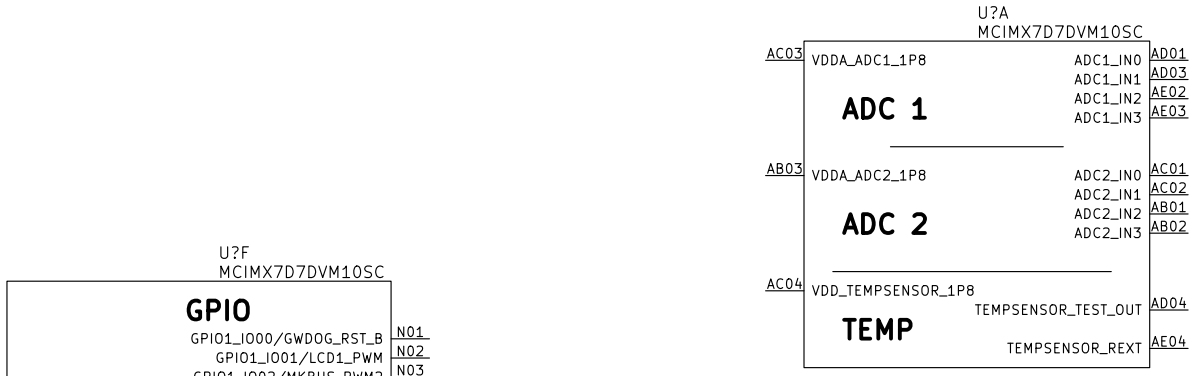




Sheet: /Ethernet + PCIe/
File: NETWORK.sch

Title:

Size: A3 | Date: | **Rev:** Id: 9/10



Sheet: /ADC + GPIO/
File: ADC_GPIO.sch

Title:

Size: A4	Date:	**Rev:**
		Id: 10/10