

MCIMX7D7DVM10SC

X7D7DVM10SC

DRAM_ADDR00
DRAM_ADDR00
DRAM_ADDR01
DRAM_ADDR02
DRAM_ADDR03
DRAM_ADDR03
DRAM_ADDR05
DRAM_ADDR05
DRAM_ADDR06
DRAM_ADDR06
DRAM_ADDR07
DRAM_ADDR09
DRAM_ADDR09
DRAM_ADDR09
DRAM_ADDR010
DRAM_ADDR10
DRAM_ADDR11
DRAM_ADDR112
DRAM_ADDR13
DRAM_ADDR13
DRAM_ADDR13
DRAM_ADDR144
DRAM_ADDR15
DRAM_ADDR15
DRAM_ADDR15
DRAM_ADDR15
DRAM_ADDR15
DRAM_ADDR16
DRAM_ADDR16
DRAM_ADDR17
DRAM_ADDR17
DRAM_ADDR17
DRAM_ADDR18
DRAM_ADDR19
DRAM_ADDR19
DRAM_ADDR19
DRAM_ADDR19
DRAM_ADDR19
DRAM_ADDR115
DRAM_ADDR15
 DRAM_DATA0
 AD22
 DRAM_DATA1
 DRAM_DATA1

 DRAM_DATA2
 AE20
 DRAM_DATA1

 DRAM_DATA3
 AE23
 DRAM_DATA2

 DRAM_DATA4
 AE23
 DRAM_DATA3
 DRAM | DRAM_DATA4 | AE22 | DRAM_DATA3 | DRAM_DATA4 | AE22 | DRAM_DATA5 | DRAM_DATA6 | DRAM_DATA6 | DRAM_DATA7 | AE12 | DRAM_DATA6 | DRAM_DATA7 | DRAM_DAT AE21 DRAM_SDQS0_P DRAM_SDQS0_N AD20 DRAM_DQM0 DRAM_DATA8 AE14 DRAM_DATA8 DRAM_DATA9 AE14 DRAM_DATA8
DRAM_DATA10 AE17 DRAM_DATA10
DRAM_DATA11 AD16
DRAM_DATA11 AD16
DRAM_DATA12 AE16
DRAM_DATA13 AD14
DRAM_DATA13 AD14
DRAM_DATA14 AD13
DRAM_DATA14 AD13
DRAM_DATA14 AD13
DRAM_DATA14 AD13 DRAM_DATA14 AD13 DRAM_DATA14 DRAM_DATA15 AE13 AD15 DRAM_SDQS1_P
AE15 DRAM_SDQS1_N
DRAM_DQM1 DRAM_SDBA0
DRAM_SDBA1
DRAM_SDBA2
R22
P22
N23 DRAM_DATA16 AA25
DRAM_DATA17 W24
DRAM_DATA18 V25
DRAM_DATA19 W25
DRAM_DATA20 AC25
DRAM_DATA21 AB25
DRAM_DATA22 AB24
DRAM_DATA23 AC24
DRAM_DATA23 AC24
DRAM_DATA22 DRAM_DATA22
DRAM_DATA23 DRAM_DATA23
DRAM_DATA24
DRAM_DATA25 DRAM_DATA25
DRAM_DATA26 DRAM_DATA27
DRAM_DATA27
DRAM_DATA27
DRAM_DATA27
DRAM_DATA28 DRAM_DATA28 DRAM_CSO_B AB23 DRAM_CS1_B AA22 Y24 Y25 DRAM_SDQS2_P DRAM_SDQS2_N DRAM_DQM2 DRAM_RAS_B AC14 DRAM_CAS_B AC14 DRAM_SDWE_B DRAM_DATA24 R25
DRAM_DATA25 N24
DRAM_DATA25 N24
DRAM_DATA26 P25
DRAM_DATA27 N25
DRAM_DATA27 N25
DRAM_DATA28 U25
DRAM_DATA29 R24
DRAM_DATA31 U24
DRAM_DATA31 U24
DRAM_DATA31 V24
DRAM_DATA31 V24 DRAM_SDCKE0 AB17 DRAM_SDCKE1 AB22 DRAM_ODTO AC16 DRAM_DATA31 V24 DRAM_DATA31 DRAM_ODT1 AA14 T24 DRAM_SDQS3_P T25 DRAM_SDQS3_N P24 DRAM_DQM3 DRAM_RESET AC22 AA19 AA18 NVCC_DRAM AA17 NVCC_DRAM AA16 NVCC_DRAM DRAM_SDCLKO_P AD24 DRAM_SDCLKO_N AD25 U21 NVCC_DRAM NVCC_DRAM NVCC_DRAM_CKE Y20 AA21 NVCC DRAM AA20 NVCC_DRAM DRAM_VREF AC13 W21 NVCC_DRAM NVCC_DRAM NVCC_DRAM NVCC_DRAM NVCC_DRAM DRAM_ZQPAD AB13

 DRAM_ADDRO
 N3

 DRAM_ADDR1
 P7

 DRAM_ADDR2
 P3

 DRAM_ADDR3
 N2

 DRAM_ADDR4
 P8

 DRAM_ADDR4
 P8
 DQ0 E3 F7 MT41K256M16HA-125 DQ2 F8 DQ4 H3 DQ4 H8 G2 H7 DQ7 DQ8 D7 DQ9 C3
DQ10 C8 DQ10 C2 A7 DQ12 DQ13 B8 DQ13 B8 B8 A3 N8 BA1 BA2 L2 J3 K3 CAS LDQS OG3 L3 WE <u>C7</u> K70 CK K9 CKE T2 RESET UDQS 0B7 K1 ODT E7 LDM D3 UDM NC J1 J9 NC L1 M7 M8 VREFCA H1 VREFDQ L8 ZQ NC L9

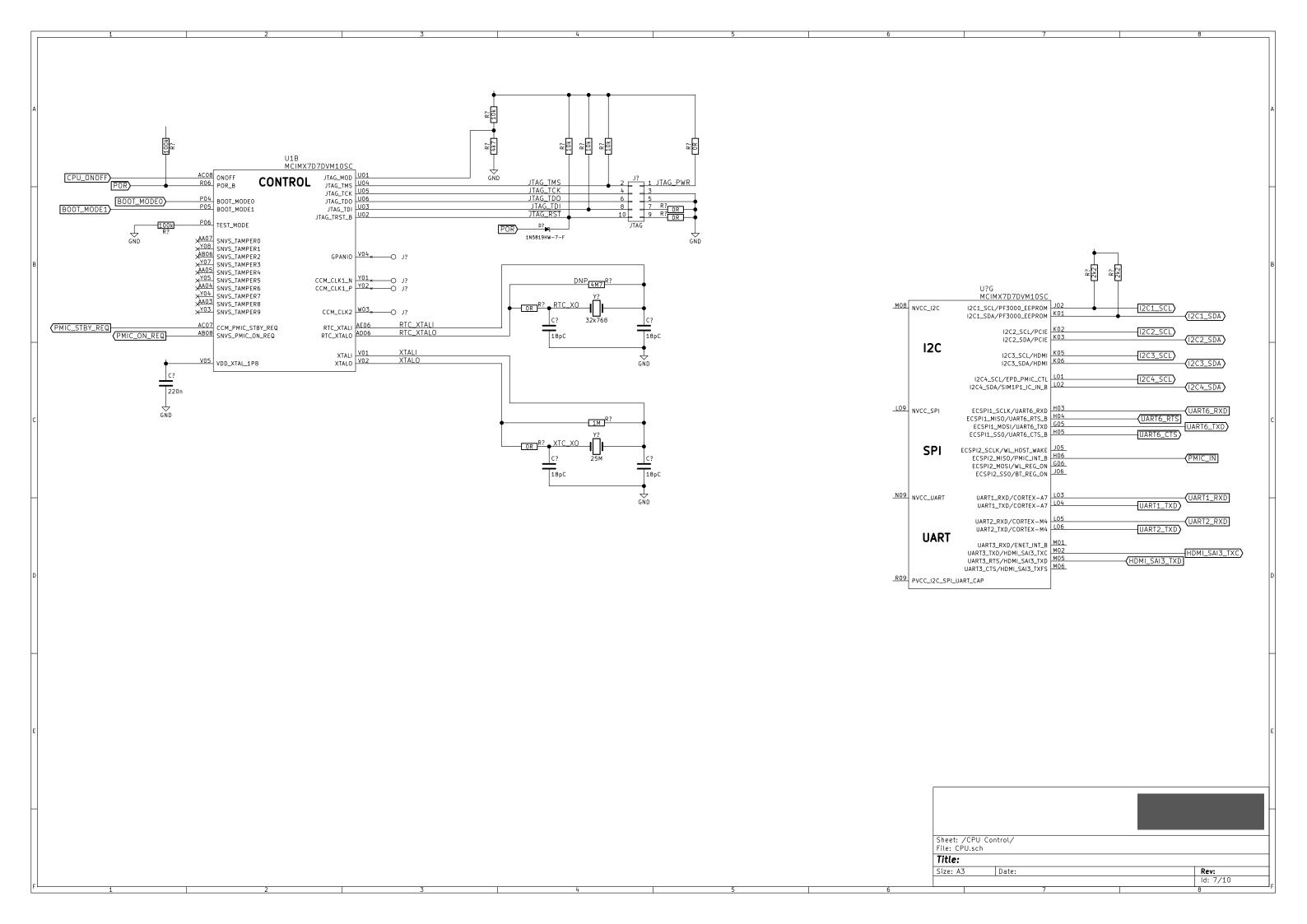
DQ0 E3 F7 MT41K256M16HA-125 DQ1 F7
DQ2 F2
DQ3 F8
DQ4 H3
DQ5 H8
DQ6 G2
DQ7 D7
DQ8 C3 DRAM_ADDR3 N2
DRAM_ADDR4 P8
DRAM_ADDR5 P2
DRAM_ADDR6 R8
DRAM_ADDR7 R2
DRAM_ADDR8 T8
DRAM_ADDR9 R3
DRAM_ADDR11 R7
DRAM_ADDR11 R7
DRAM_ADDR11 R7
DRAM_ADDR12 N7
DRAM_ADDR14 T7
DRAM_ADDR14 T7
DRAM_ADDR14 T7
DRAM_ADDR14 T7
M14
BA0
BA0
BA1 DQ8 DQ9 C3 C8 C2 DQ11 A7 DQ13 DQ14 A2 DQ15 B8 A3 N8 BA1 BA2 L2 J3 K3 CAS LDQS G3 L3 WE J7 CK
K7 CK
K9 CKE
T2 RESET UDQS C7 K1 ODT E7 LDM D3 UDM NC J9 NC L1 NC M7 M8 VREFCA H1 VREFDQ L8 ZQ NC L9

> Sheet: /DDR3 RAM/ File: RAM.sch Title: Size: A3 Date: ld: 5/10

U?L MCIMX7D7DVM10SC SAI1_MCLK E10 F12 NVCC_SAI SAI SAI1_RXD
SAI1_RXC/I2C4_SDA
SAI1_RXF5/I2C4_SCL

C12 SAI1_TXD
SAI1_TXC
SAI1_TXFS

E11
C11
D11 SAI2_RXD E09 SD1_CLK/SD_CARD SD1_DATAO/SD_CARD SD1_DATA1/SD_CARD SD1_DATA2/SD_CARD SD1_DATA2/SD_CARD SD1_DATA3/SD_CARD SD1_DATA3/SD_C NVCC_SD1 SD SD1_CD_B/SD_CARD CO4 SD1_WP/SD_CARD CO4 SD1_RESET_B/SD_CARD B04 H08 NVCC_SD2 SD2_CD_B/EPD_PIN_MUX SD2_WP/USR_BT1 SD2_RESET_B/USR_BT0 K08 NVCC_SD3 J09 PVCC_SAI_SD_CAP SD3_STROBE/EMMC_NAND SD3_RESET_B/EMMC_NAND GO1 Sheet: /eMMC & SD Card/ File: MEMORY.sch Title: Rev: Id: 6/10 Date: Size: A3



U?H MCIMX7D7DVM10SC K18 NVCC_LCD LCD_CLK E20 LCD LCD_DATA00 D21
LCD_DATA01 A22
LCD_DATA02 B22
LCD_DATA04 C22
LCD_DATA05 B23
LCD_DATA06 A24
LCD_DATA06 E21
LCD_DATA09 E21
LCD_DATA10 B24
LCD_DATA11 G20
LCD_DATA11 G20
LCD_DATA12 F21
LCD_DATA13 C22
LCD_DATA15 C24
LCD_DATA16 B25
LCD_DATA17 C24
LCD_DATA17 G21
LCD_DATA17 G21
LCD_DATA17 G21
LCD_DATA18 E23
LCD_DATA19 C24
LCD_DATA19 C24
LCD_DATA19 C25
LCD_DATA19 D24
LCD_DATA12 C25
LCD_DATA22 C25
LCD_DATA23 C23 EPDC_D00
EPDC_D01
EPDC_D02
EPDC_D03
EPDC_D04
EPDC_D04
EPDC_D06
EPDC_D07
EPDC_D06
EPDC_D07
EPDC_D08
EPDC_D09
EPDC_D09
EPDC_D10
EPDC_D11
EPDC_D11
EPDC_D11
EPDC_D12
EPDC_D14
EPDC_D14
EPDC_D14
EPDC_D15 NVCC_EPDC1 EPDC U?I MCIMX7D7DVM10SC EPDC_SDCE0 G25 EPDC_SDCE1 G24 EPDC_SDCE2 H23 EPDC_SDCE3 H22 L17 NVCC_EPDC2 MIPI_CSI_CLK_N MIPI_CSI_CLK_P MIPI CSI EPDC_SDCLK EPDC_SDLE EPDC_SDSHR MIPI_CSI_DO_N A16 MIPI_CSI_DO_P B16 MIPI_CSI_D1_N MIPI_CSI_D1_P EPDC_GDCLK J25 EPDC_GDOE J24 EPDC_GDRL K21 EPDC_GDSP H25 MIPI_DSI_CLK_N A19 MIPI_DSI_CLK_P B19 MIPI DSI MIPI_DSI_DO_N MIPI_DSI_DO_P B20 EPDC_PWRCOM H24 EPDC_PWRSTAT K20 N17 PVCC_EPDC_LCD_CAP EPDC_BDR0 K24 EPDC_BDR1 K23 MIPI_DSI_D1_N A18 MIPI_DSI_D1_P B18 Sheet: /Display and MIPI/ File: DISPLAY.sch Title: Rev: Id: 8/10 Size: A3 Date:

