

A

B

C

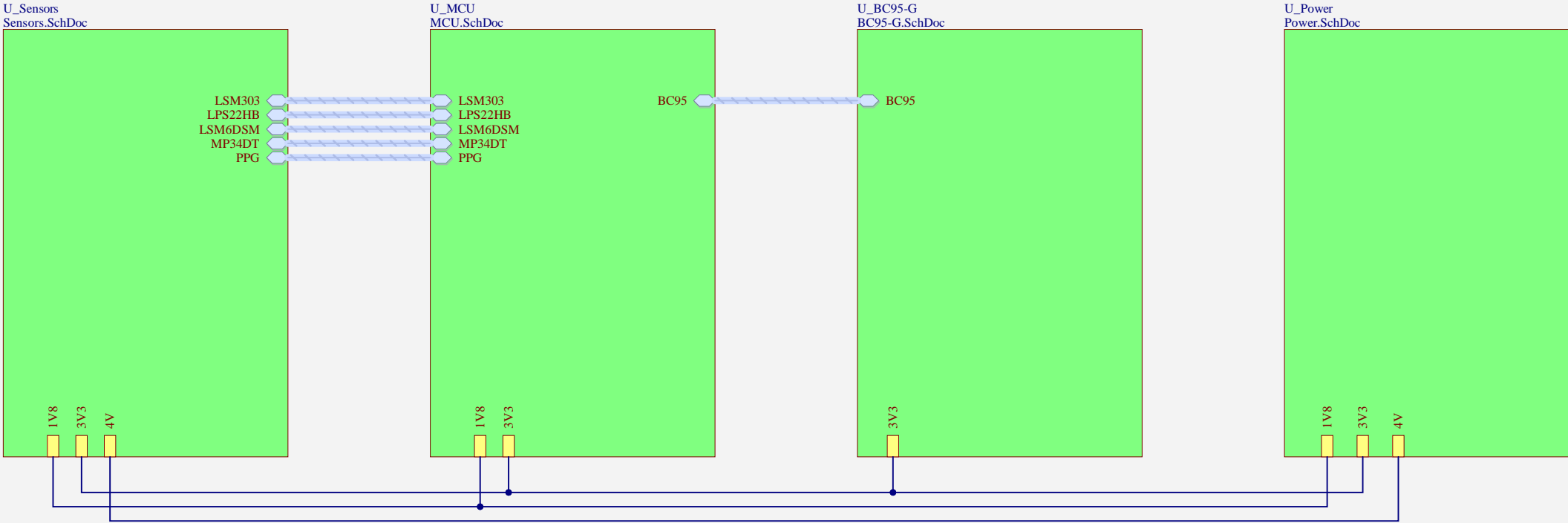
D

A

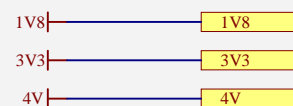
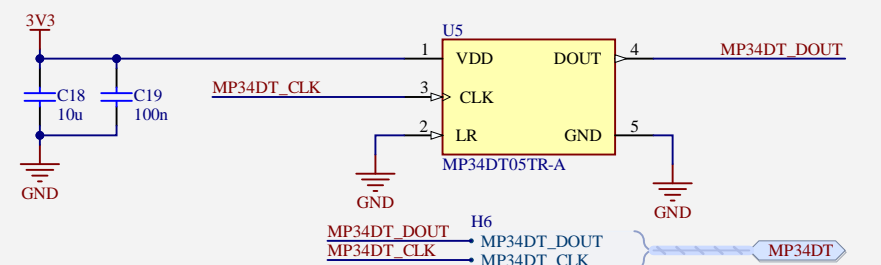
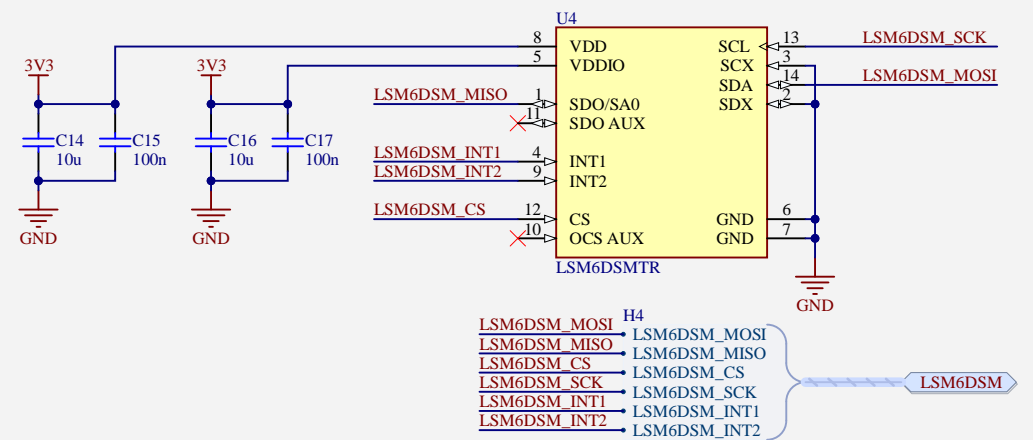
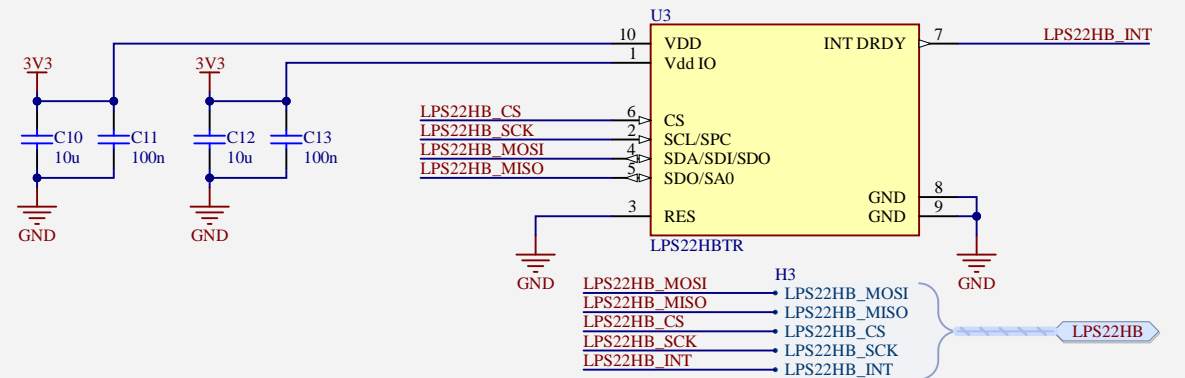
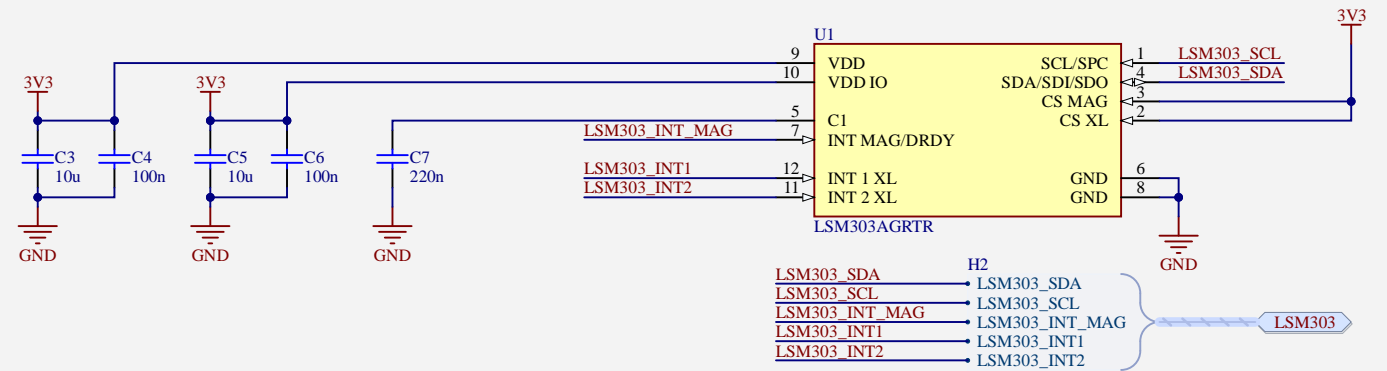
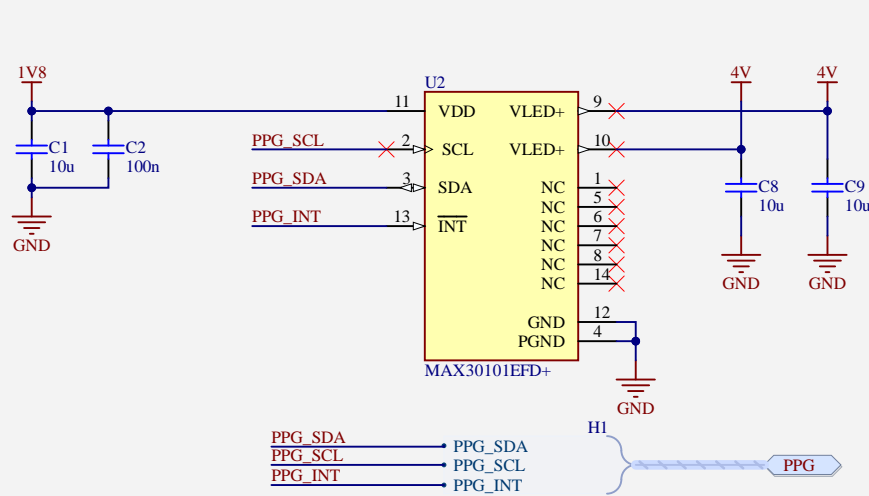
B

C

D



ETH Eidgenössische Technische Hochschule Zürich Swiss Federal Institute of Technology Zurich		Project: H-Watch Main	
Drawing number: SCH1V0.2	Rev: V0.2	Format: A3	Laboratory: ETH-PBL
Date: 18/03/2021 11:25:06			Drawn by: schuluka
File: C:\Users\schuluka\Git\Github\H-Watch\Hardware\H-Watch_V0.2\H-Watch.SchDoc			
Sheet: H-Watch.SchDoc		Page 1 of 5	



A

B

C

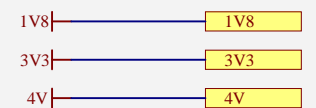
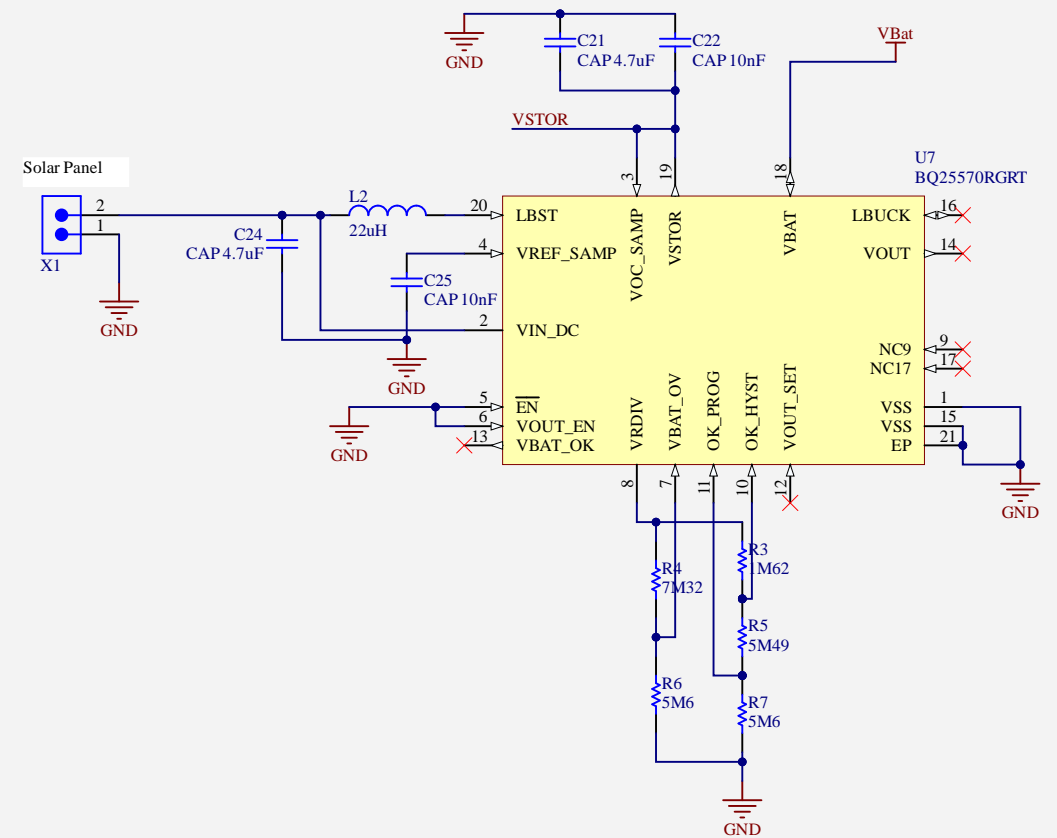
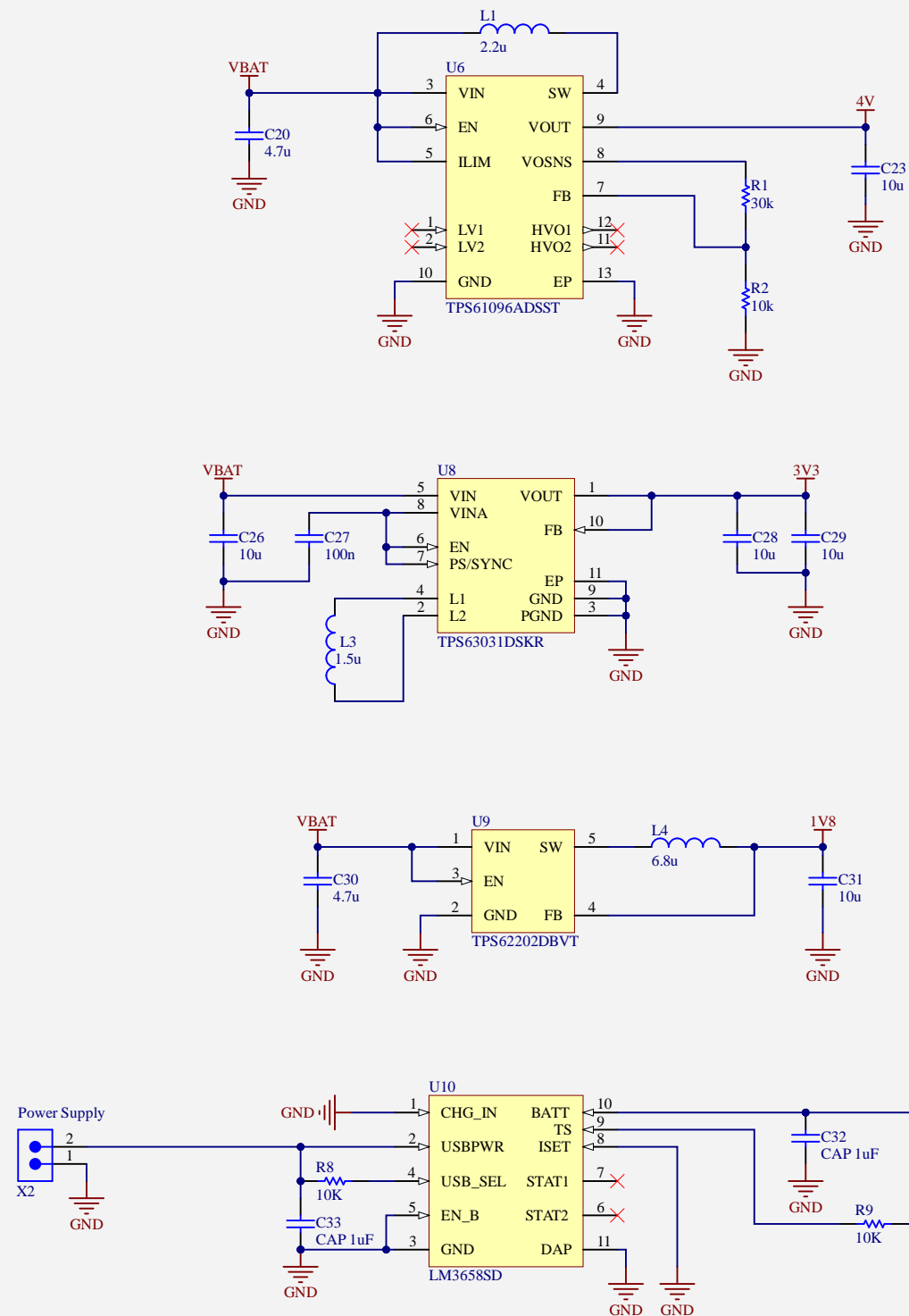
D

A

B

C

D



ETH Eidgenössische Technische Hochschule Zürich Swiss Federal Institute of Technology Zurich		Project: H-Watch Power	
Drawing number: SCH3V0.2	Rev: V0.2	Format: A3	Laboratory: ETH-PBL
Date: 18/03/2021 11:25:06		Drawn by: schuluka	Sheet: Power.SchDoc
File: C:\Users\schuluka\Git\Github\H-Watch\Hardware\H-Watch_V0.2\Power.SchDoc			

1

2

3

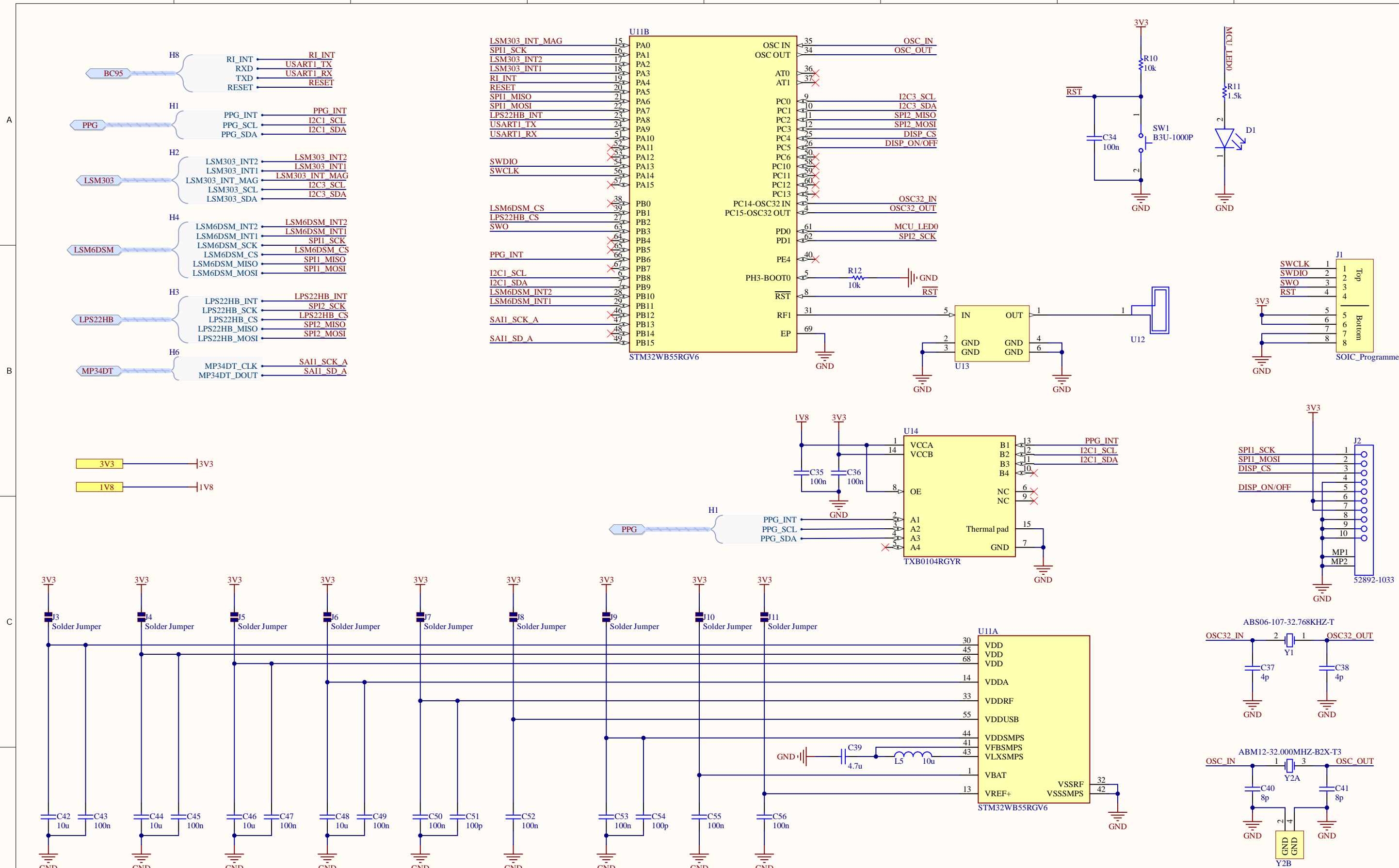
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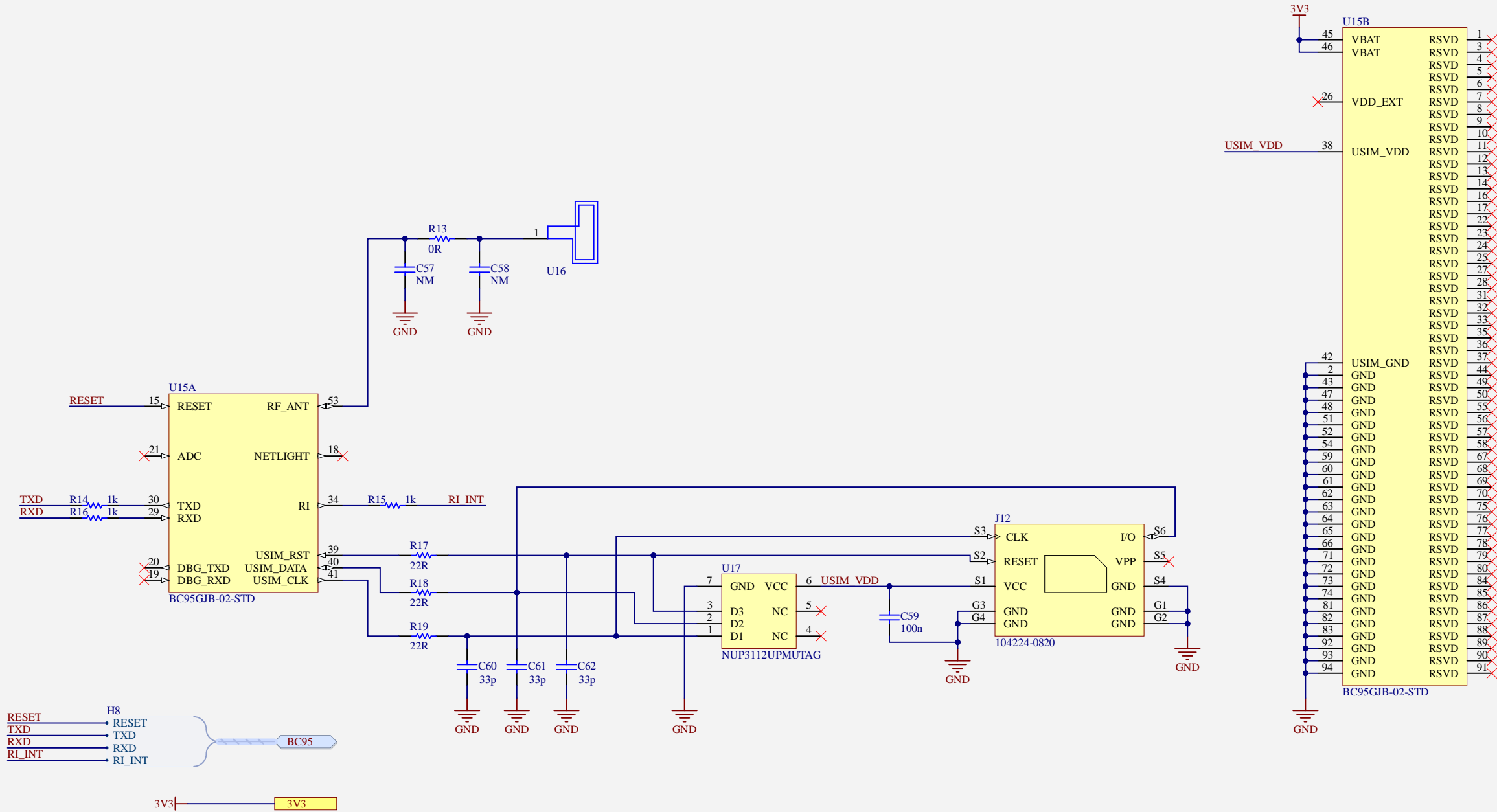
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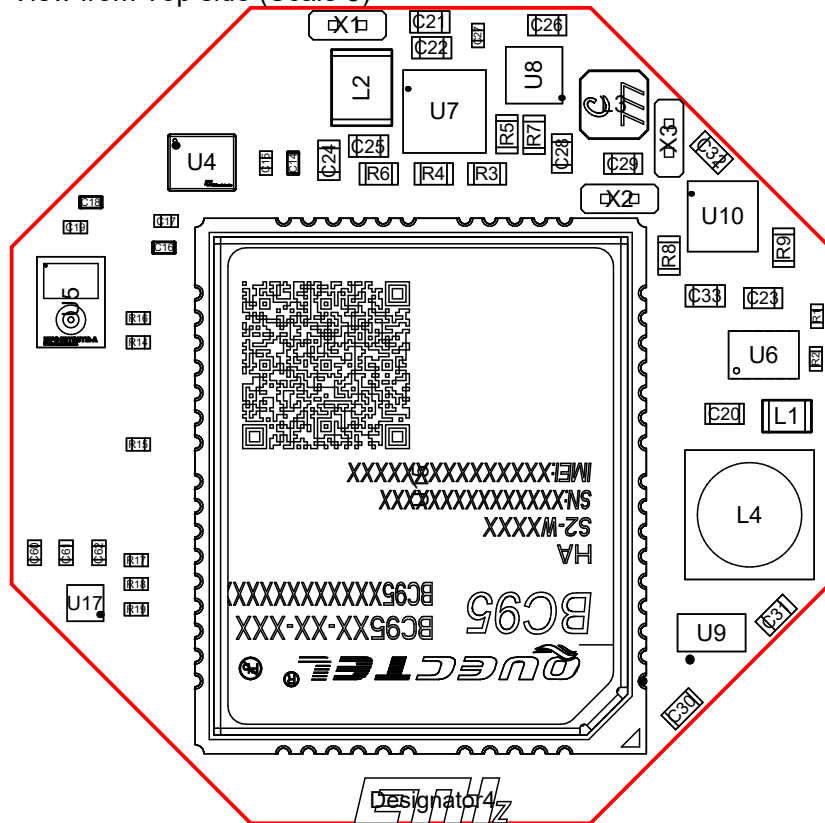
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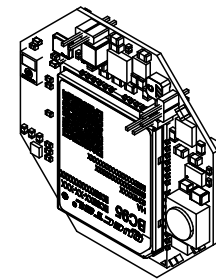
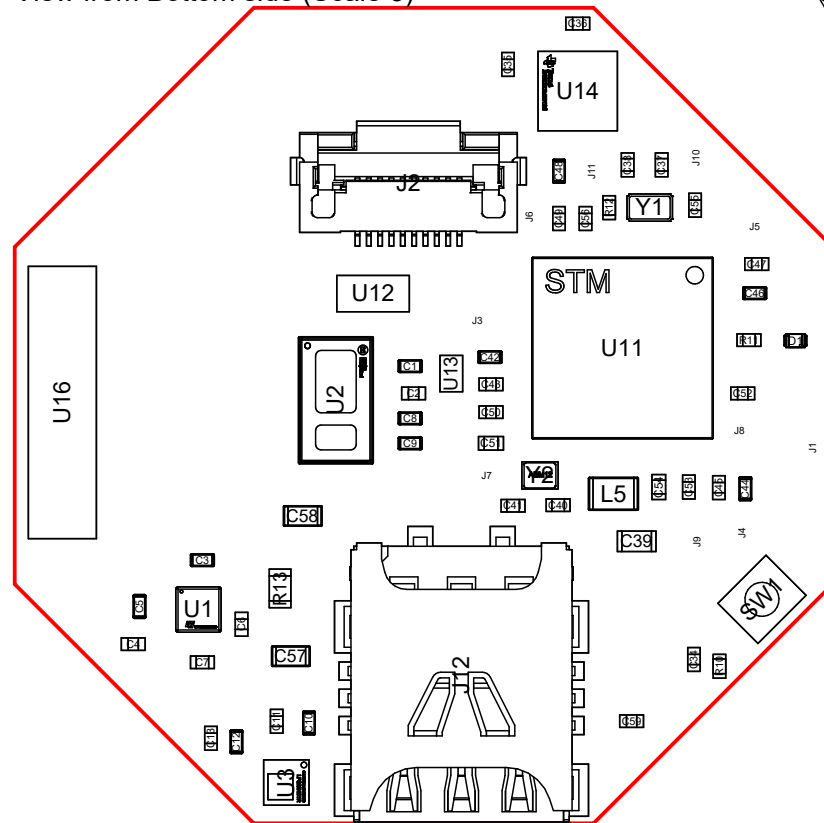




View from Top side (Scale 3)



View from Bottom side (Scale 3)



ETH

Eidgenössische Technische Hochschule Zürich
Swiss Federal Institute of Technology Zurich

Project:

H-Watch Assembly Drawing

Drawing number: [ASMV0.2](#)

Rev: [0.2](#)

Format: Laboratory: [ETH-PBL](#)

Sheet: [H-Watch_assembly.PCB](#)

Date: [18/03/2021 11:25](#)

Drawn by: [schuluka, polonelli](#)

Page [1](#) of [1](#)

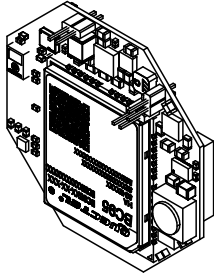
File: [C:\Users\schuluka\Git\Github\H-Watch\Hardware\H-Watch_V0.2\H-Watch_assembly.PCBdwf](#)

A

Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Overlay			Legend	GTO
Surface Material	Top Solder	0.03mm	SM-001	Solder Mask	GTS
PbSn	Top Surface Finish	0.02mm		Surface Finish	
CF-004	Top Layer	0.04mm		Signal	GTL
Prepreg		0.07mm	PP-006	Dielectric	
CF-004	Int1 (GND)	0.04mm		Signal	G1
Core		0.12mm	Core-035	Dielectric	
CF-004	Int2 (Sign)	0.04mm		Signal	G2
Prepreg		0.07mm	PP-006	Dielectric	
Prepreg		0.07mm	PP-006	Dielectric	
CF-004	Int3 (GND)	0.04mm		Signal	G3
Core		0.12mm	Core-035	Dielectric	
CF-004	Int4 (PWR)	0.04mm		Signal	G4
Prepreg		0.07mm	PP-006	Dielectric	
CF-004	Bottom Layer	0.04mm		Signal	GBL
PbSn	Bottom Surface Finish	0.02mm		Surface Finish	
Surface Material	Bottom Solder	0.03mm	SM-001	Solder Mask	GBS
	Bottom Overlay			Legend	GBO

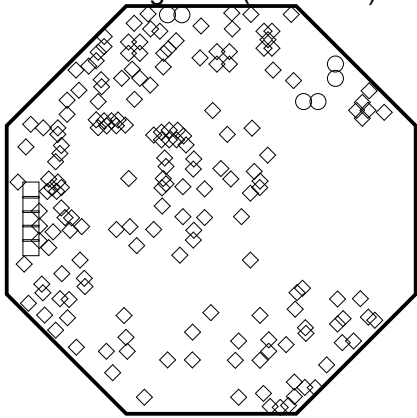
Total thickness: 0.83mm



Drill Table

Symbol	Count	Hole Size	Plated	Via / Pad
◇	180	0.20mm	Plated	Via
○	6	0.60mm	Plated	Pad
□	5	0.90mm	Plated	Pad
191 Total				

Drill Drawing View (Scale 3:2)



B

C

D



Eidgenössische Technische Hochschule Zürich
Swiss Federal Institute of Technology Zurich

Project:

H-Watch Manufacturing

Drawing number: MANV0.2

Rev: V0.2

Format: Laboratory: ETH-PBL

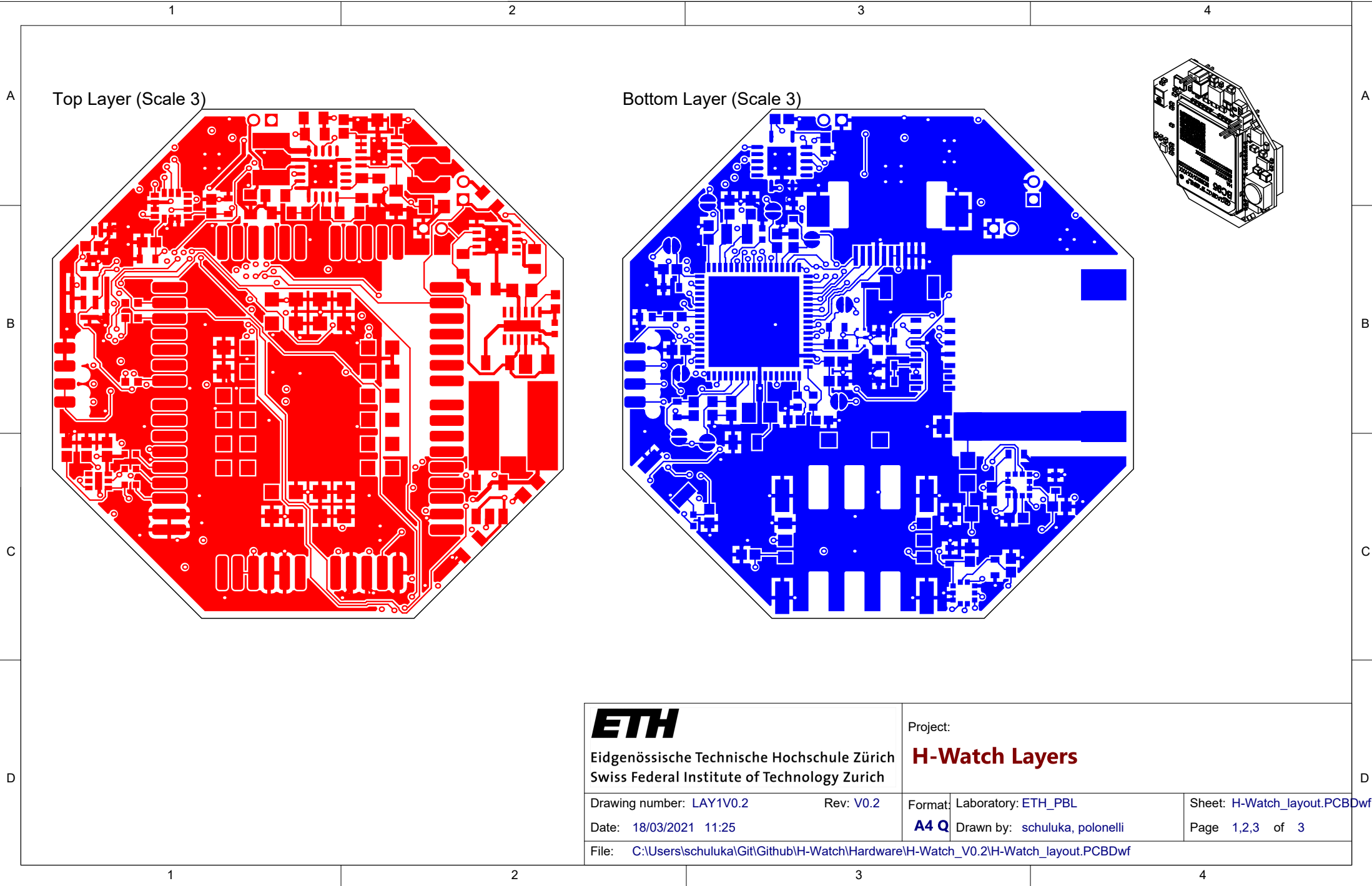
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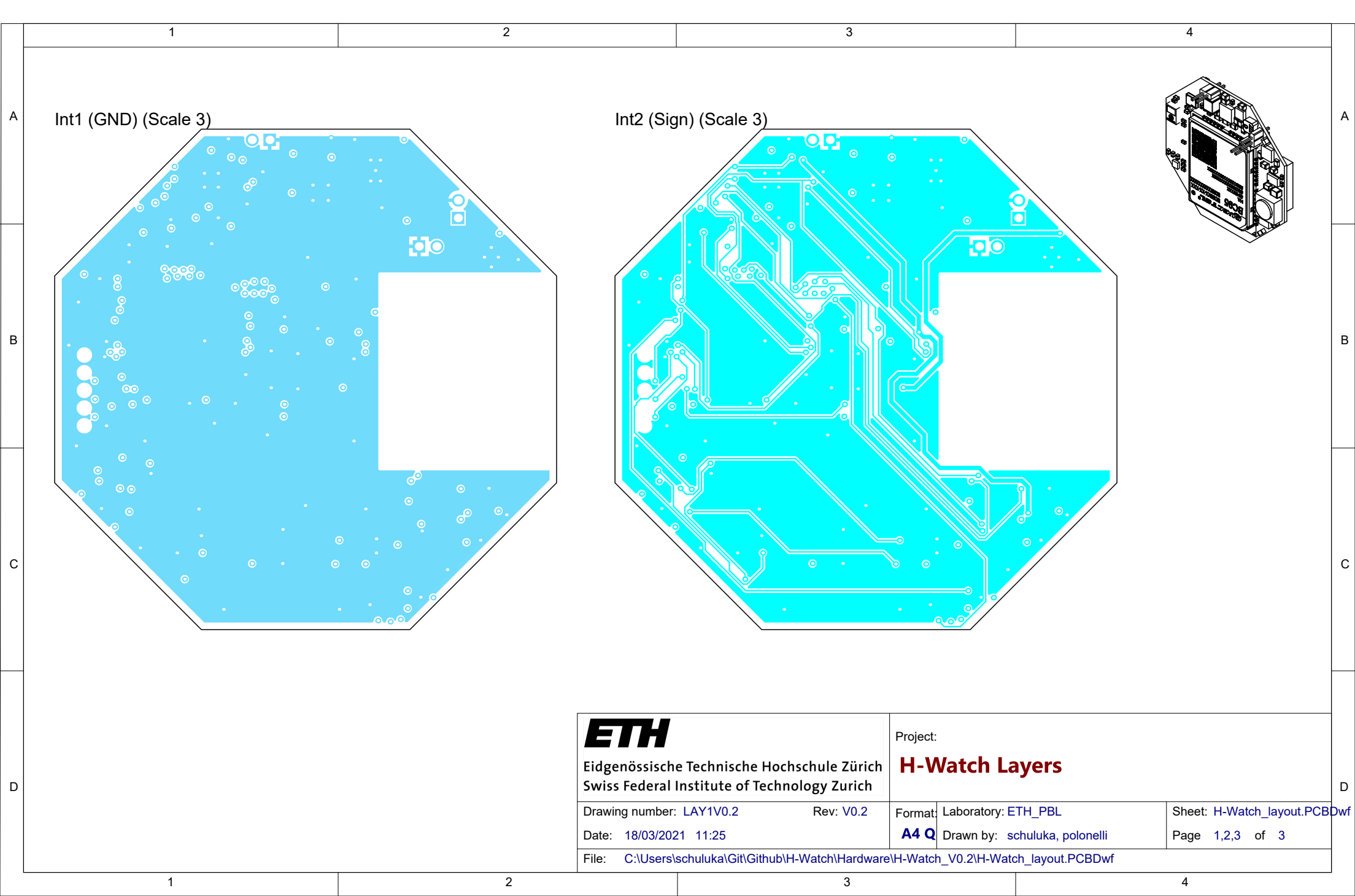
Date: 18/03/2021 11:25

A4 Q Drawn by: schuluka, polonelli

Page 1 of 1

File: C:\Users\schuluka\Git\Github\H-Watch\Hardware\H-Watch_V0.2\H-Watch_manufacturing.PCBDwf





ETH

Eidgenössische Technische Hochschule Zürich
Swiss Federal Institute of Technology Zurich

Project:

H-Watch Layers

Drawing number: LAY1V0.2

Rev: V0.2

Format: Laboratory: ETH_PBL

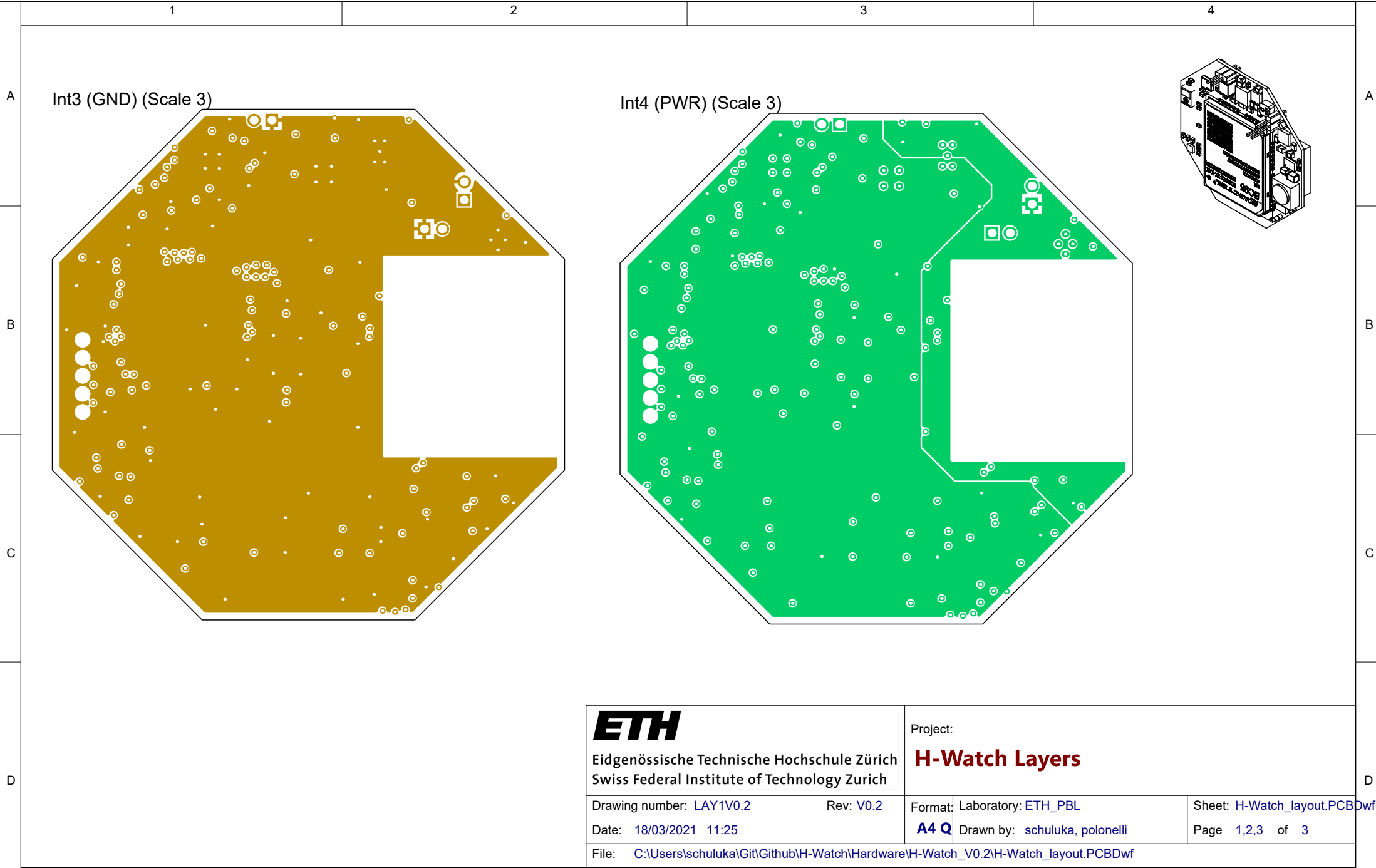
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Date: 18/03/2021 11:25

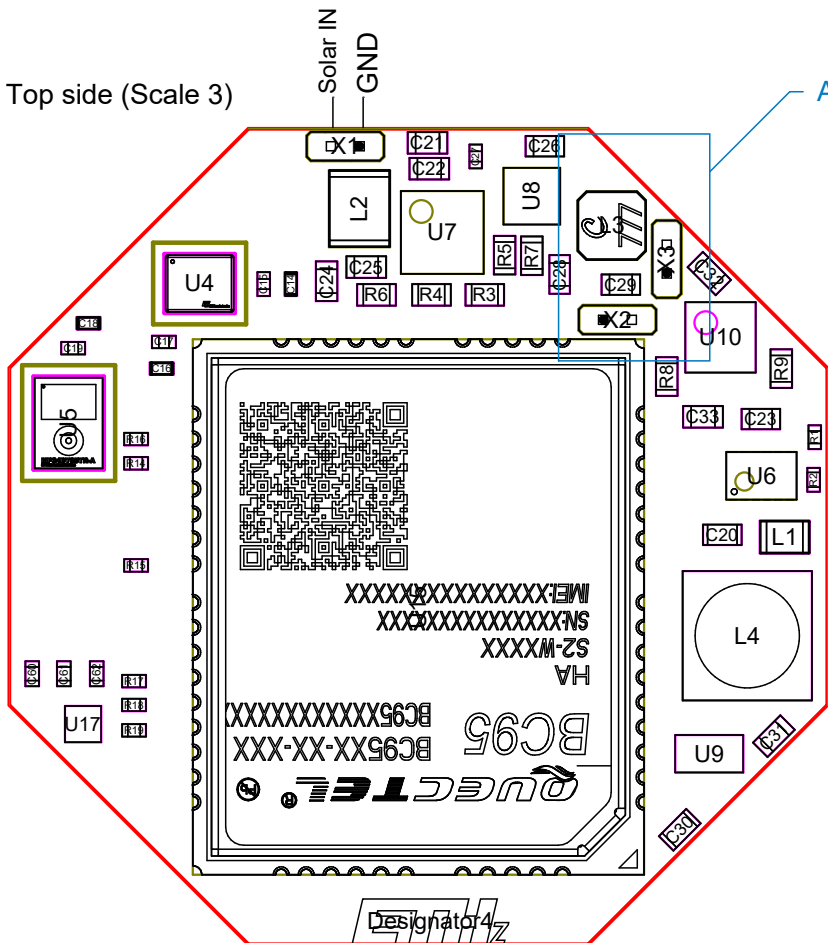
Drawn by: schuluka, polonelli

Page 1,2,3 of 3

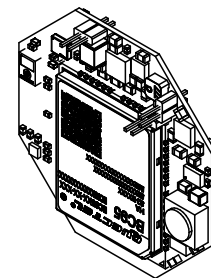
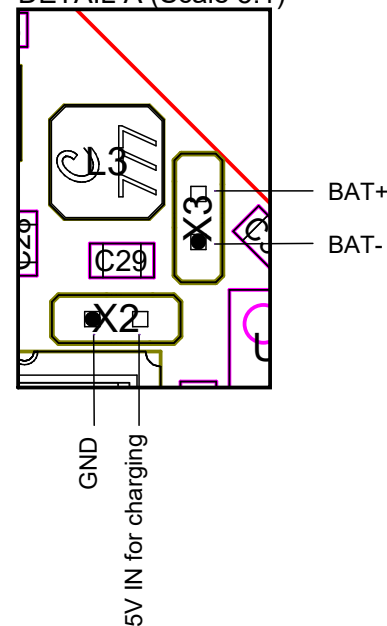
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View from Top side (Scale 3)



DETAIL A (Scale 5:1)



ETH

Eidgenössische Technische Hochschule Zürich
Swiss Federal Institute of Technology Zurich

Project:

H-Watch Connectors

Drawing number: CONV0.2

Rev: V0.2

Format: Laboratory: ETH-PBL

Sheet: H-Watch_connectors.PCB

Date: 18/03/2021 11:25

A4 Q Drawn by: schuluka, polonelli

Page 1 of 1

File: C:\Users\schuluka\Git\Github\H-Watch\Hardware\H-Watch_V0.2\H-Watch_connectors.PCBdwf