

Command	Code											Description	Execution Time
	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0			
Clear Display	0	0	0	0	0	0	0	0	0	1	Clears the display and returns the cursor to the home position (address 0).	82μs~1.64ms	
Return Home	0	0	0	0	0	0	0	0	1	*	Returns the cursor to the home position (address 0). Also returns a shifted display to the home position. DD RAM contents remain unchanged.	40μs~1.64ms	
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	S	Sets the cursor move direction and enables/disables the display.	40μs	
Display ON/OFF Control	0	0	0	0	0	0	1	D	C	B	Turns the display ON/OFF (D), or the cursor ON/OFF (C), and blink of the character at the cursor position (B).	40μs	
Cursor & Display Shift	0	0	0	0	0	1	S/C	R/L	*	*	Moves the cursor and shifts the display without changing the DD RAM contents.	40μs	
Function Set	0	0	0	0	1	DL	N\$	F	*	#	Sets the data width (DL), the number of lines in the display (L), and the character font (F).	40μs	
Set CG RAM Address	0	0	0	1	A _{CG}						Sets the CG RAM address. CG RAM data can be read or altered after making this setting.	40μs	
Set DD RAM Address	0	0	1	A _{DD}						Sets the DD RAM address. Data may be written or read after making this setting.	40μs		
Read Busy Flag & Address	0	1	BF	AC						Reads the BUSY flag (BF) indicating that an internal operation is being performed and reads the address counter contents.	1μs		
Write Data to CG or DD RAM	1	0	Write Data						Writes data into DD RAM or CG RAM.	46μs			
Read Data from CG or DD RAM	1	1	Read Data						Reads data from DD RAM or CG RAM.	46μs			
	I/D = 1: Increment I/D = 0: Decrement S = 1: Accompanies display shift. S/C = 1: Display shift S/C = 0: cursor move R/L = 1: Shift to the right. R/L = 0: Shift to the left. DL = 1: 8 bits DL = 0: 4 bits N = 1: 2 lines N = 0: 1 line F = 1: 5x10 dots F = 0: 5 x 7 dots BF = 1: Busy BF = 0: Can accept data # Set to 1 on 24x4 modules \$ With KS0072 is Address Mode.										DD RAM: Display data RAM CG RAM: Character generator RAM A _{CG} : CG RAM Address A _{DD} : DD RAM Address Corresponds to cursor address. AC: Address counter Used for both DD and CG RAM address.	Execution times are typical. If transfers are timed by software and the busy flag is not used, add 10% to the above times.	