McuLink Power and USB

File: mculink_kicad_power_usb.kicad_sch

LPC55S69 IO Ports

File: mculink_kicad_ioports.kicad_sch

Debug interface

File: mculink_kicad_debug.kicad_sch

UART Interface to Target

File: mculink_kicad_uart.kicad_sch

Open points:
- Solder Jumper: different footprint?

Changes v0.3:
- added UART CTS/RTS to NavQPlus connector, including level shifter

Changes v0.2:
- improved silkscreen, marks for pin numbers on connectors
- all UART connections through SN74LVC1T45GW buffers to prevent backfeeding
- added VTARGET to tracking voltage

FID1 Fiducial

FID2 Fiducial

FID3 Fiducial

H1 MountingHole_Pad — GND

H2 MountingHole_Pad — GND

H3 MountingHole_Pad — GND

H4 MountingHole_Pad — GND

L6 Logo HSLU

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Hochschule Luzern, Technik & Architektur

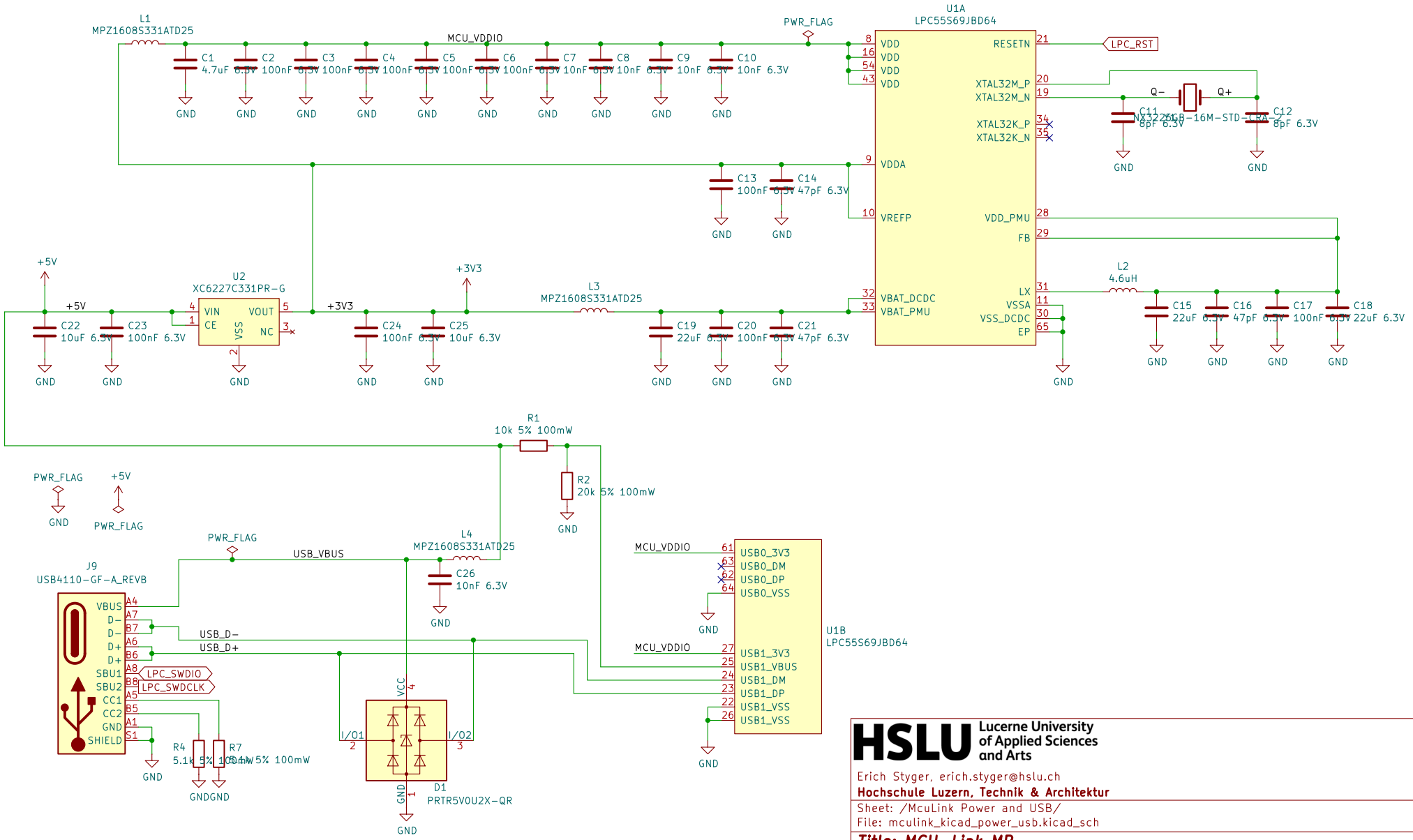
Sheet: /

File: MCU-Link_MR.kicad_sch

Title: MCU-Link MR

Size: A4	Date: 2024-04-28	**Rev: v0.2**
KiCad E.D.A. 8.0.2		Id: 1/5

LPC55S69 MCU Powering and USB



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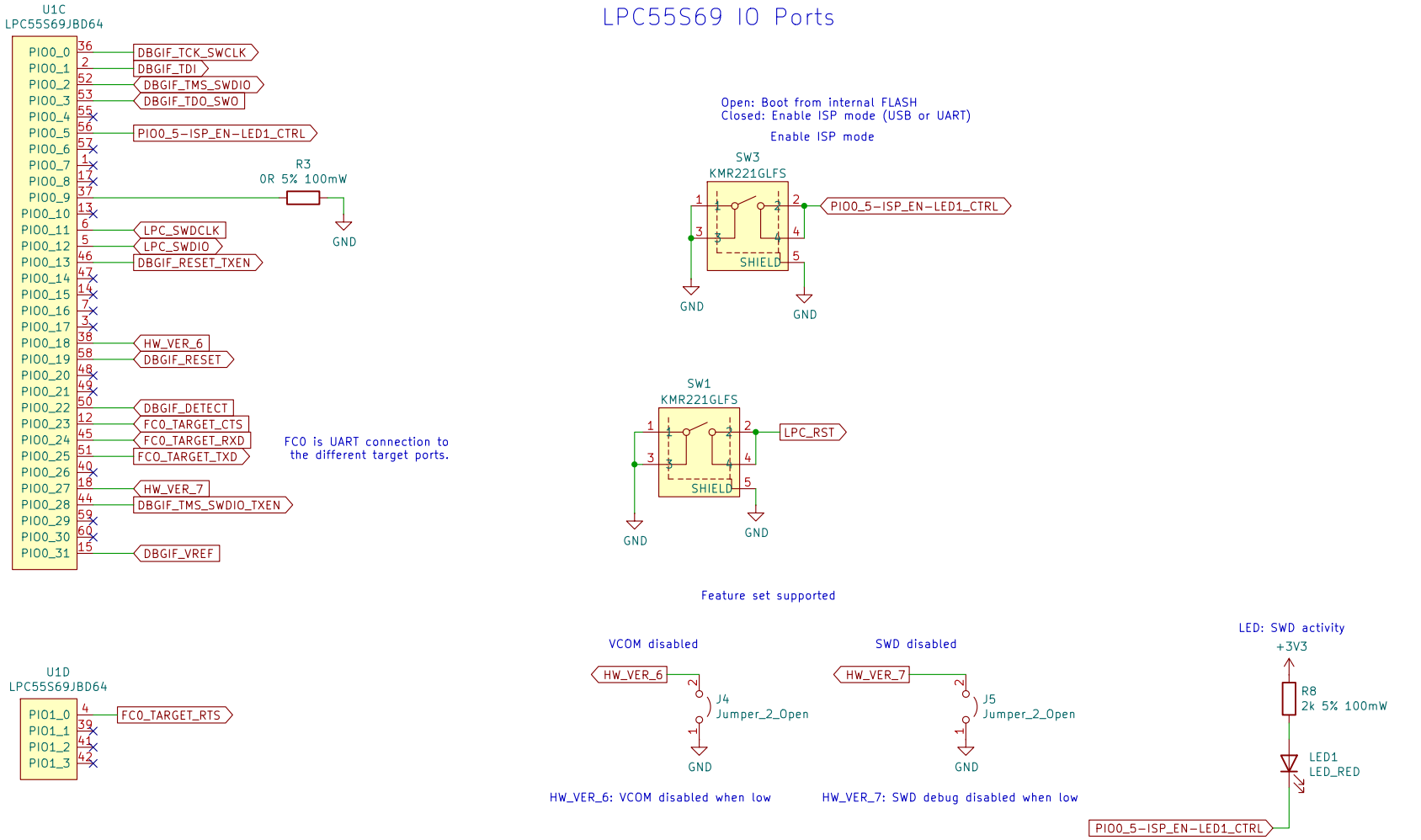
Sheet: /McuLink Power and USB/
File: mculink_kicad_power_usb.kicad_sch

Title: MCU-Link MR

Size: A4	Date: 2024-04-28	**Rev: v0.2**
KiCad E.D.A. 8.0.2		Id: 2/5

LPC55S69 IO Ports



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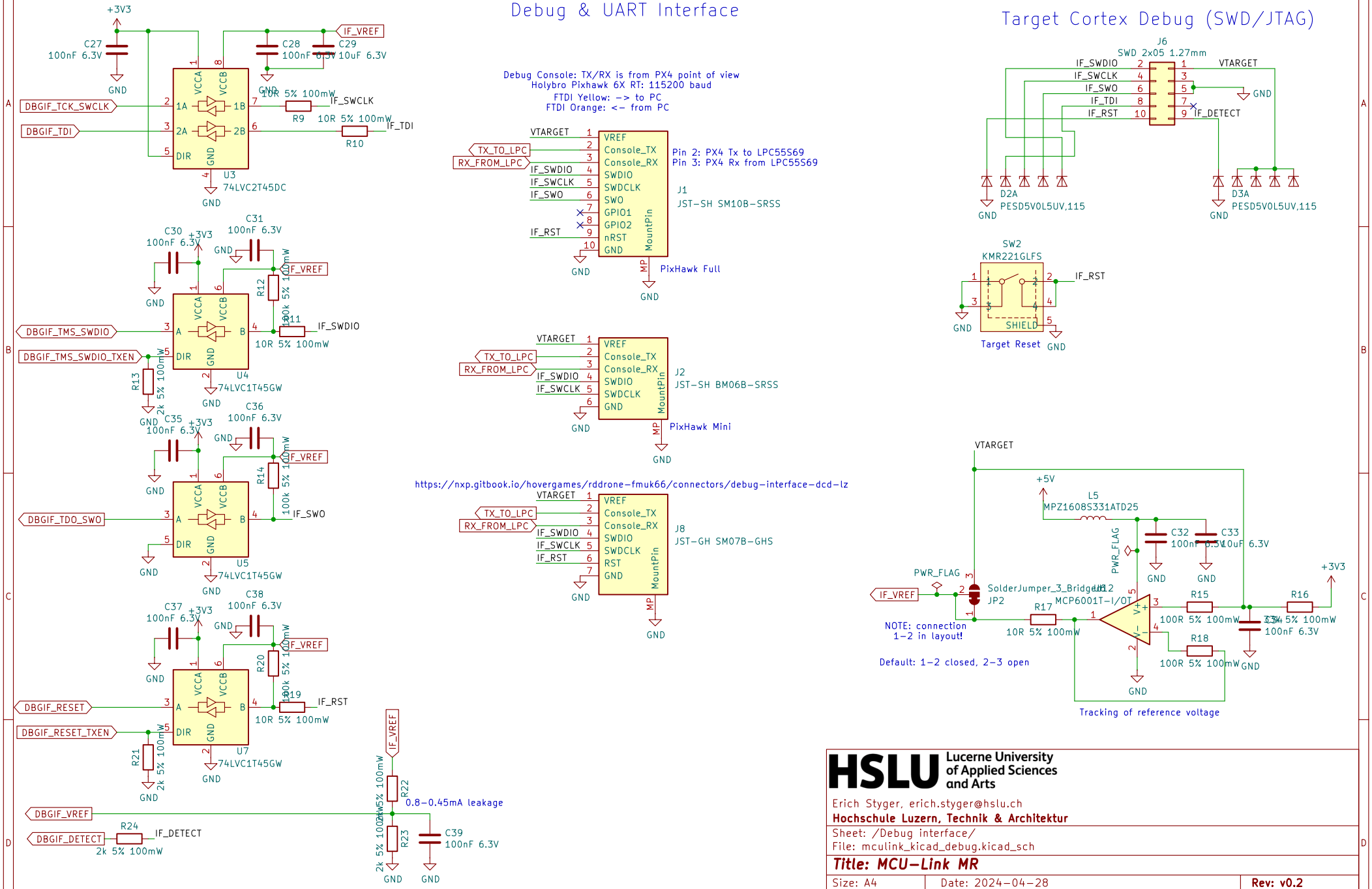
Sheet	/LPC55S69 IO Ports/
File	mculink_kicad_ioports.kicad_sch
Title	***MCU-Link MR***
Size	A4
Date	2024-04-28
Rev	**v0.2**
KiCad	E.D.A. 8.0.2
Id	3/5

Debug & UART Interface



Target Cortex Debug (SWD/JTAG)

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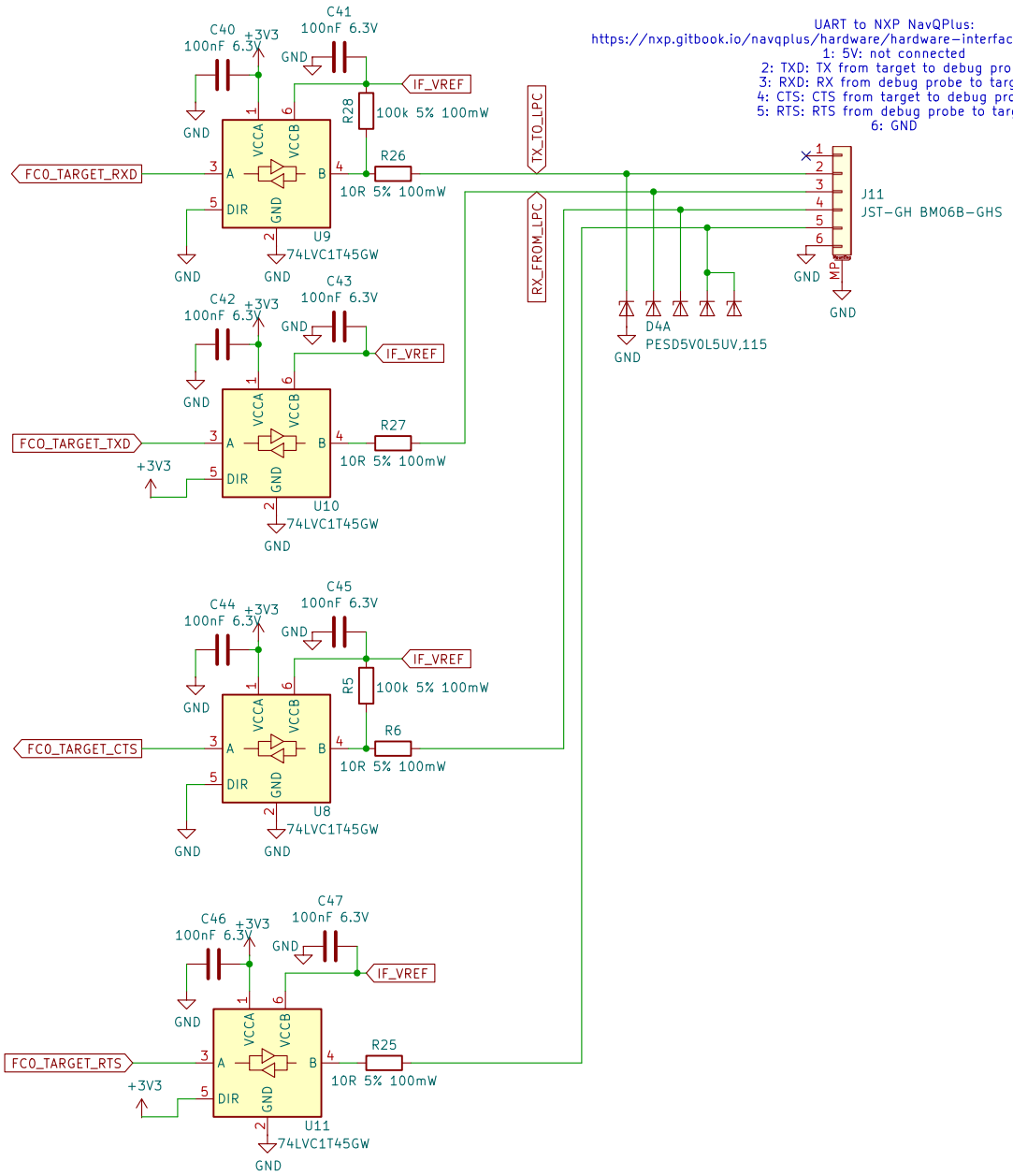
Sheet: /Debug interface/

File: mculink_kicad_debug.kicad_sch

Title: MCU-Link MR

Size: A4	Date: 2024-04-28	**Rev: v0.2**
KiCad E.D.A. 8.0.2		Id: 4/5

UART Interface to Target (NavQPlus)



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Sheet: /UART Interface to Target/		
File: mculink_kicad_uart.kicad_sch		
Title: MCU-Link MR		
Size: A4	Date: 2024-07-02	**Rev: v0.3**
KiCad E.D.A. 8.0.2		Id: 5/5