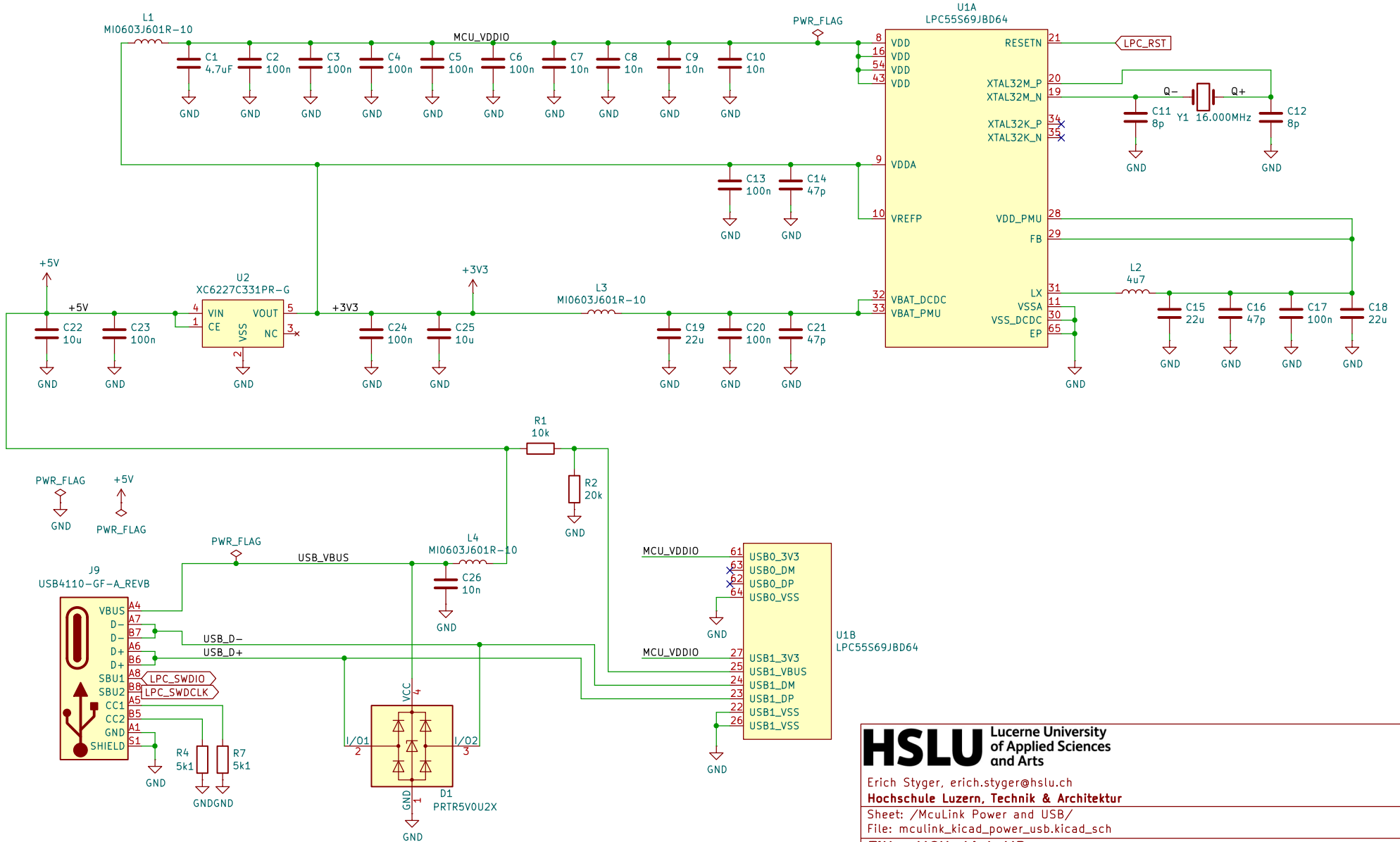


LPC55S69 MCU Powering and USB



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and Arts

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Hochschule Luzern, Technik & Architektur

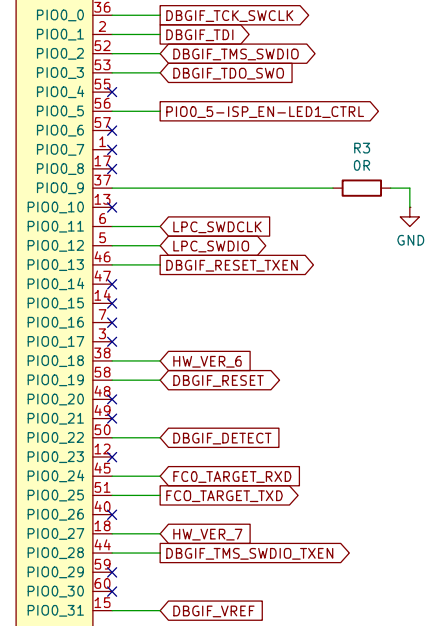
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Title: MCU-Link MR

Size: A4 Date: 2024-04-28
KiCad E.D.A. 8.0.1

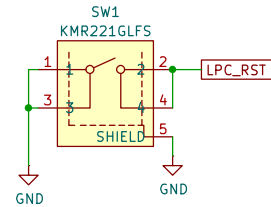
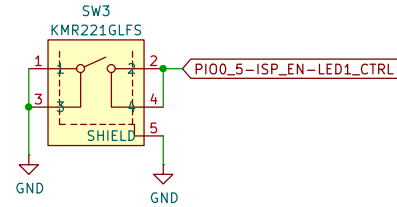
Rev: v0.2
Id: 1/5

U1C
LPC55569JBD64



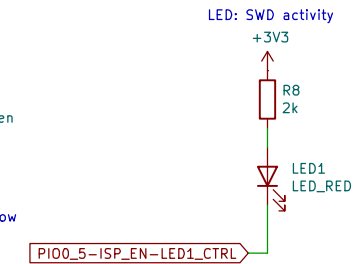
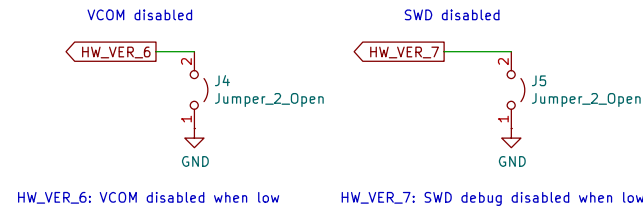
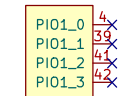
LPC55569 IO Ports

Open: Boot from internal FLASH
Closed: Enable ISP mode (USB or UART)
Enable ISP mode



Feature set supported

U1D
LPC55569JBD64



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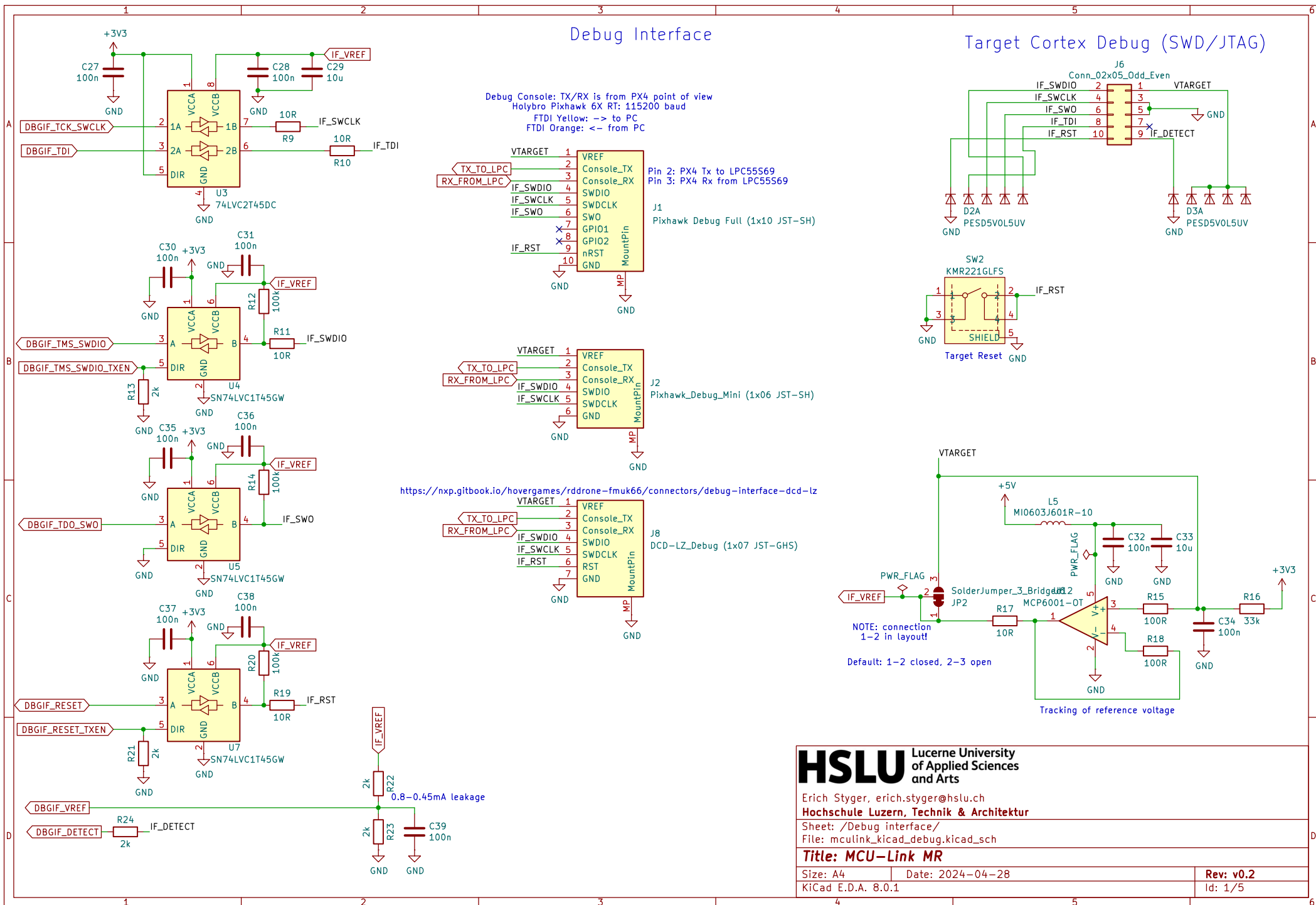
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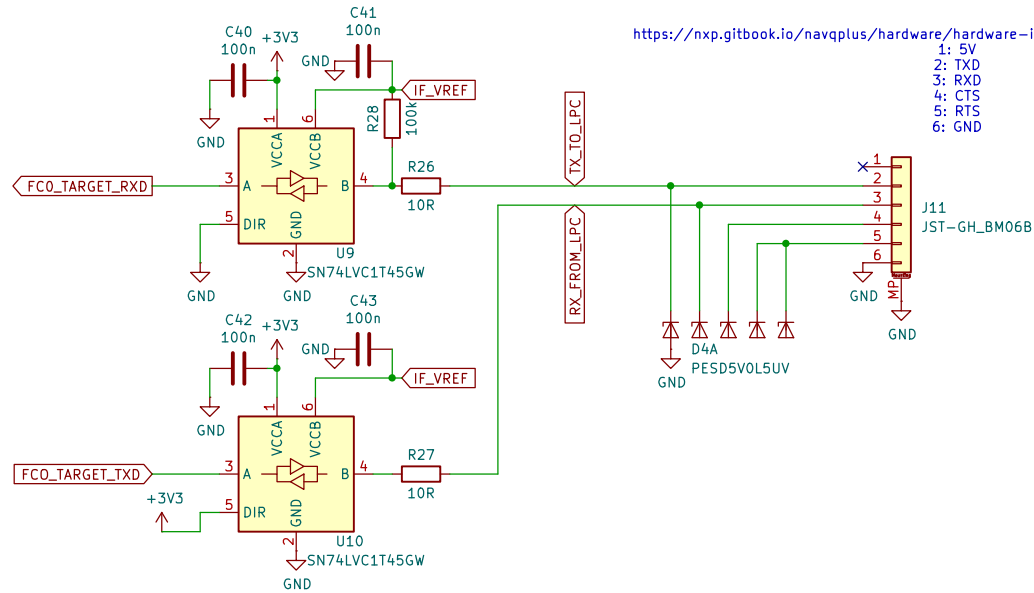
Title: MCU-Link MR

Size: A4 Date: 2024-04-28
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UART Interface to Target



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Sheet: /UART Interface to Target/

File: mcmlink_kicad_uart.kicad_sch

Title: MCU-Link MR

Size: A4

Date: 2024-04-28

Rev: v0.2

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