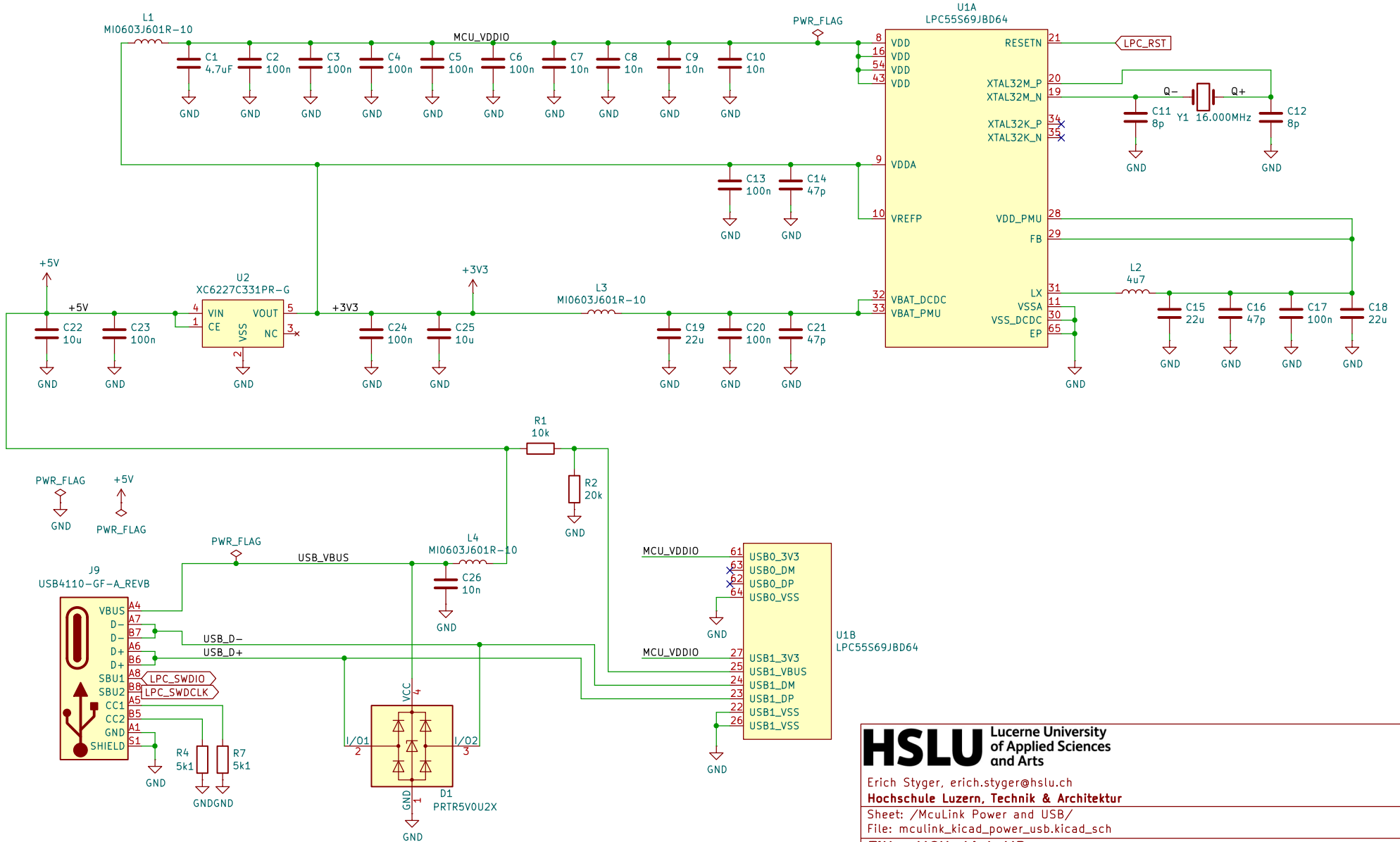


LPC55S69 MCU Powering and USB



HSLU Lucerne University
of Applied Sciences
and Arts

Erich Styger, erich.styger@hslu.ch
Hochschule Luzern, Technik & Architektur

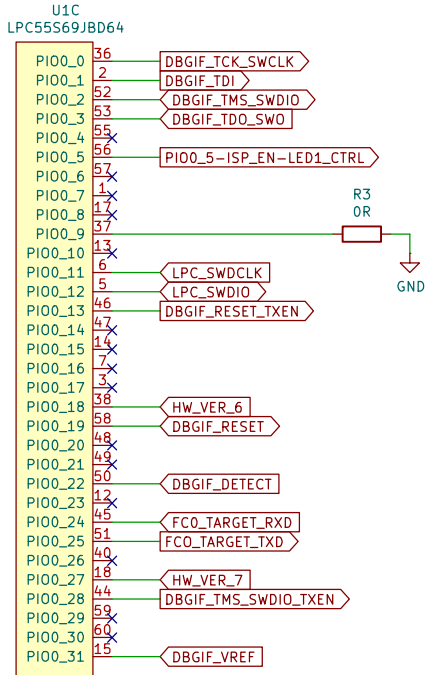
Sheet: /McuLink Power and USB/
File: mcuLink_kicad_power_usb.kicad_sch

Title: MCU-Link MR

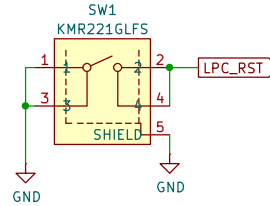
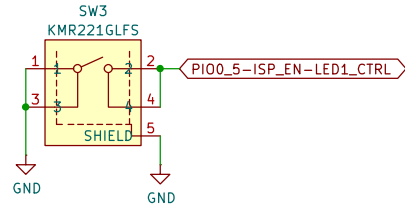
Size: A4 Date: 2024-04-28
KiCad E.D.A. 8.0.1

Rev: v0.2
Id: 1/5

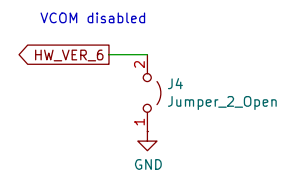
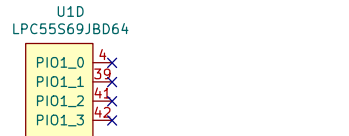
LPC55S69 IO Ports



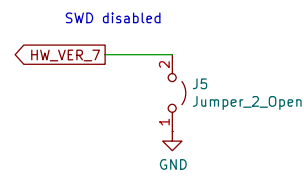
Open: Boot from internal FLASH
Closed: Enable ISP mode (USB or UART)
Enable ISP mode



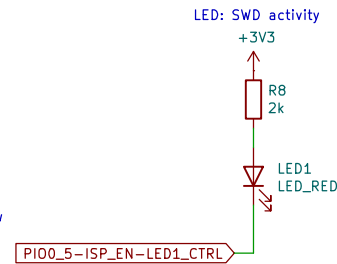
Feature set supported



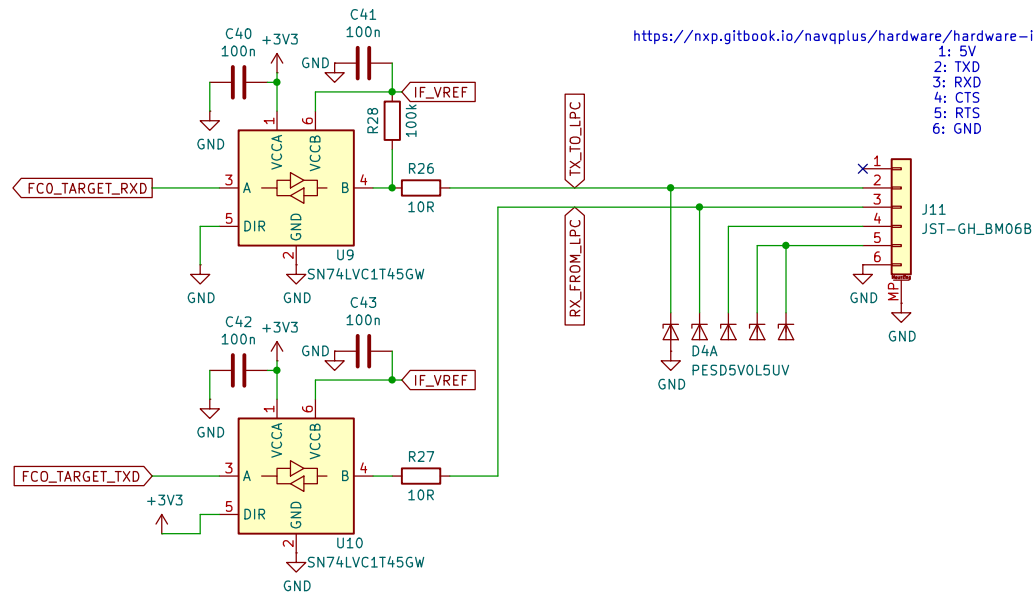
HW_VER_6: VCOM disabled when low



HW_VER_7: SWD debug disabled when low



UART Interface to Target



HSLU Lucerne University
of Applied Sciences
and Arts

Erich Styger, erich.styger@hslu.ch

Hochschule Luzern, Technik & Architektur

Sheet: /UART Interface to Target/

File: mcmlink_kicad_uart.kicad_sch

Title: MCU-Link MR

Size: A4

Date: 2024-04-28

Rev: v0.2

KiCad E.D.A. 8.0.1

Id: 1/5