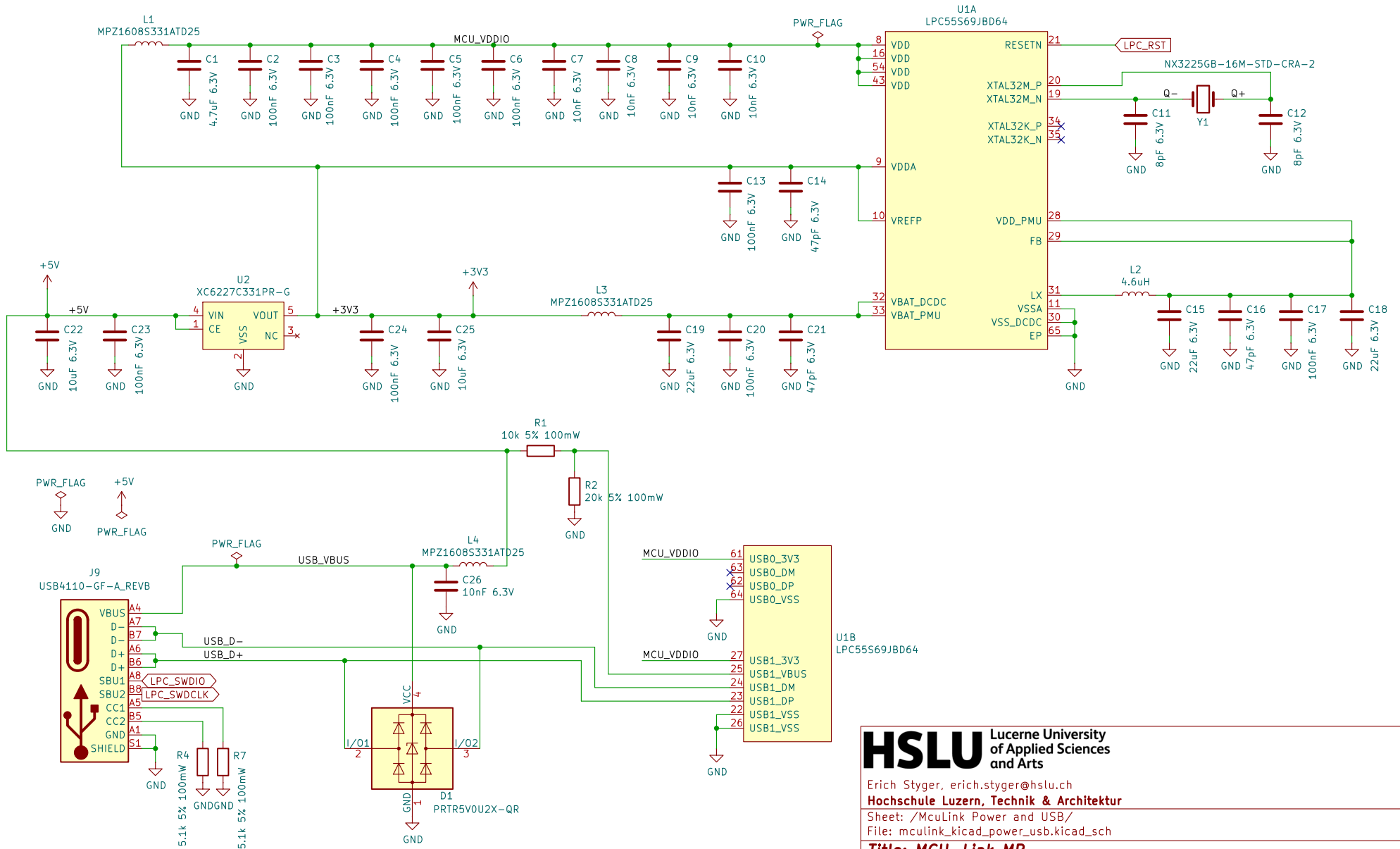


LPC55S69 MCU Powering and USB



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Sheet: /McuLink Power and USB/

File: mcmlink_kicad_power_usb.kicad_sch

Title: MCU-Link MR

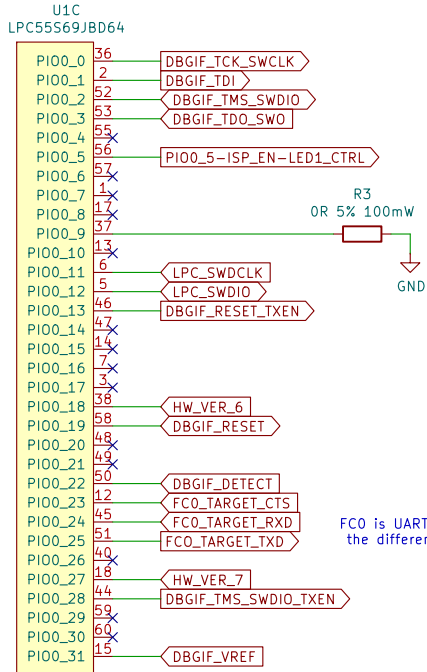
Size: A4 Date: 2024-04-28

KiCad E.D.A. 8.0.6

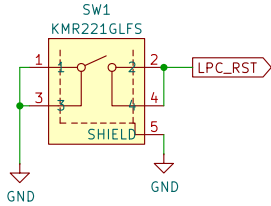
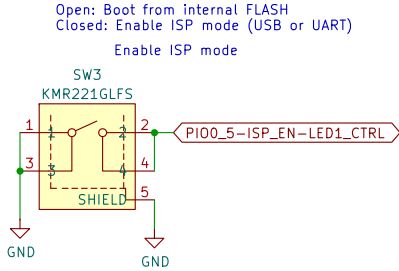
Rev: v0.2

Id: 2/5

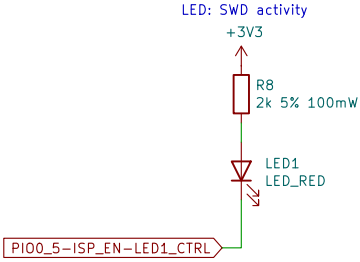
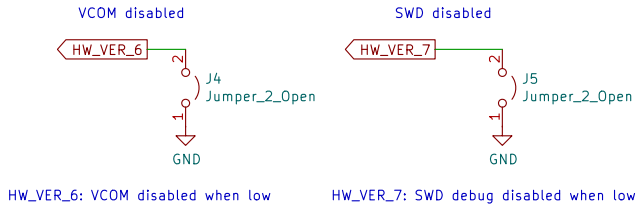
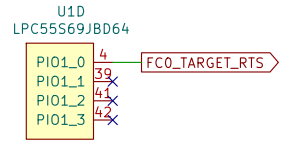
LPC55S69 IO Ports



FC0 is UART connection to the different target ports.

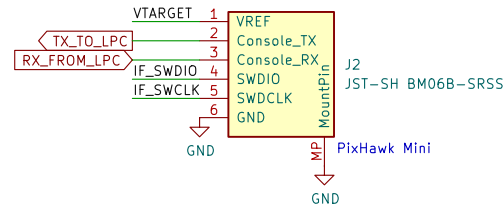
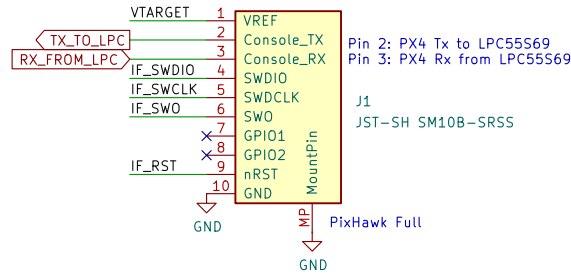


Feature set supported

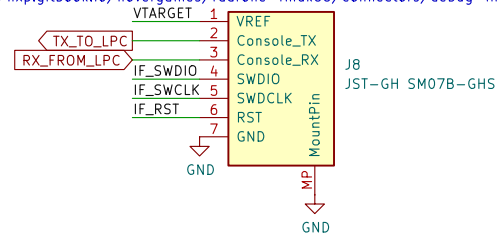


Debug & UART Interface

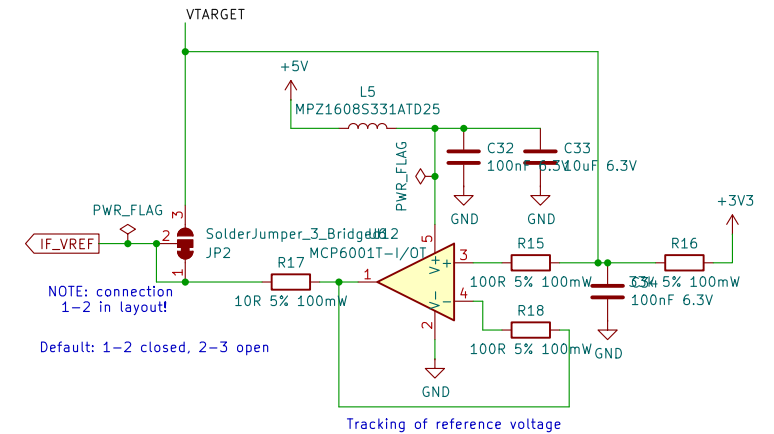
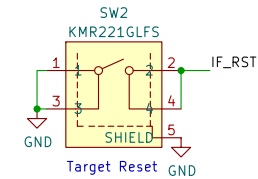
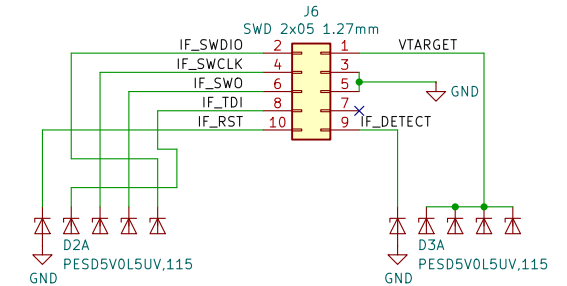
Debug Console: TX/RX is from PX4 point of view
Holybro Pixhawk 6X RT: 115200 baud
FTDI Yellow: -> to PC
FTDI Orange: <- from PC



<https://nxp.gitbook.io/hovergames/rddrone-fmuk66/connectors/debug-interface-dcd-lz>



Target Cortex Debug (SWD/JTAG)



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Sheet: /Debug interface/

File: mcmlink_kicad_debug.kicad_sch

Title: MCU-Link MR

Size: A4 Date: 2024-04-28

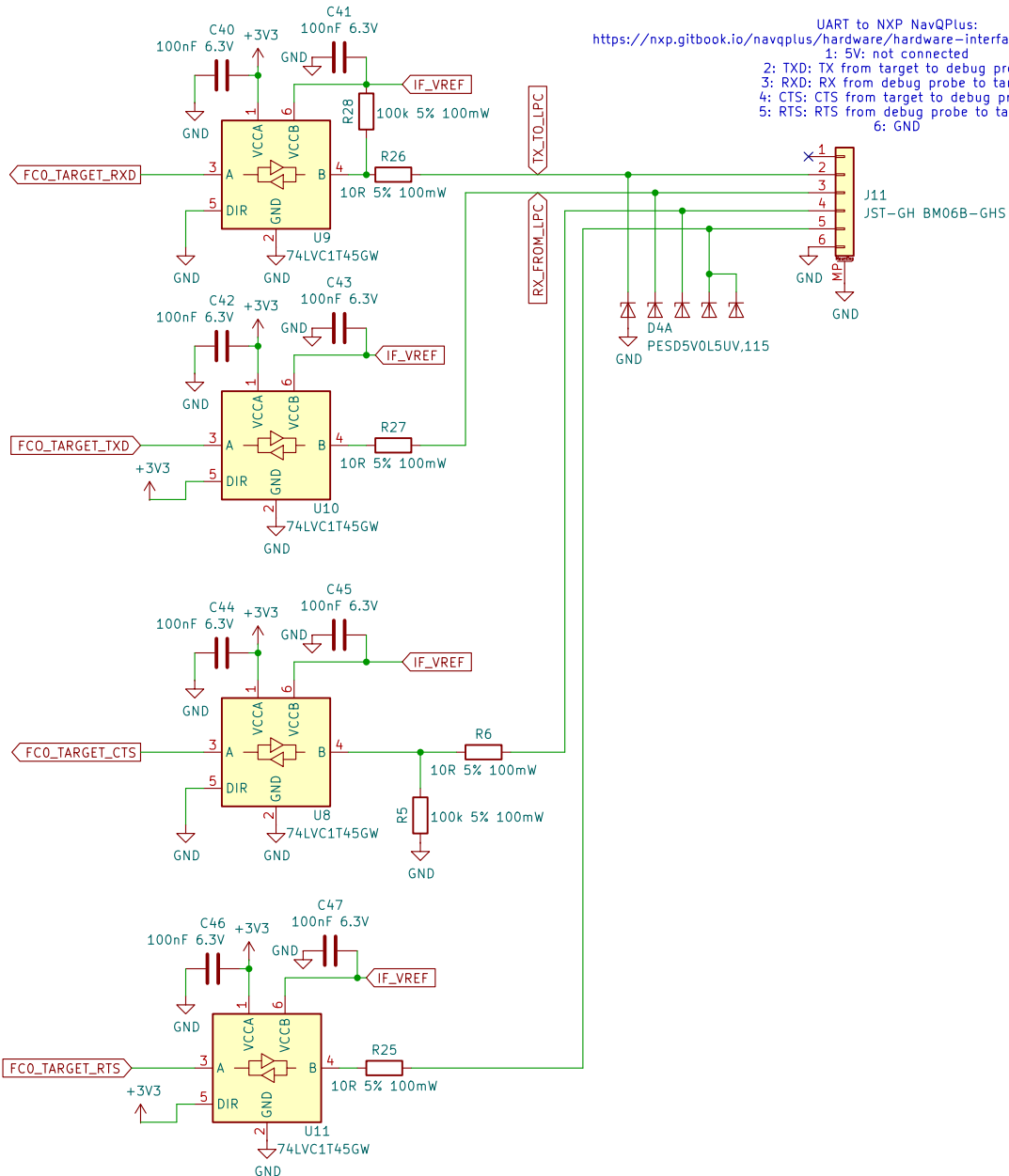
KiCad E.D.A. 8.0.6

Rev: v0.2

Id: 4/5

UART Interface to Target (NavQPlus)

UART to NXP NavQPlus:
<https://nxp.gitbook.io/navqplus/hardware/hardware-interfaces/uart/uart2-a53-debug>
 1: 5V: not connected
 2: TXD: TX from target to debug probe
 3: RXD: RX from debug probe to target
 4: CTS: CTS from target to debug probe
 5: RTS: RTS from debug probe to target
 6: GND



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Sheet: /UART Interface to Target/

File: mcmlink_kicad_uart.kicad_sch

Title: MCU-Link MR

Size: A4

Date: 2024-07-02

Rev: v0.3

KiCad E.D.A. 8.0.6

Id: 5/5