

Signal Generation Using the TMS320F243 DSK / TMS320LF2407 DSK

The schematic on the following page gives a simple circuit for using the TMS320F243 DSK or the TMS320LF2407 DSK for generating waveforms.

Application for the TMS320LF2407 DSK: Sine

Uses tables to generate a sine and cosine waveforms.

Digital to analog conversion is implemented using pulse-width modulation (PWM). The outputs of General Purpose Timers GPT1 and GPT2 are used for this purpose.

The resistor/capacitor networks provide a 1 kHz low-pass filter. This means we can generate output waveforms up 1 kHz.

For experimentation, the following modifications could be made to the code:

1. Generate the sum of 2 waveforms e.g. $(\text{sine} + \text{cosine})/2$
2. Generate difference between two waveforms e.g. $(\text{sine} - \text{cosine})/2$
3. Change the values in the table to generate a triangular output.
4. Generate irregular waveforms – put some random values into the table.

The sample C code is to be found in the directory [sine](#).

References

SLAA116 Application Report on using PWM from a timer as a DAC.

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[Template](#).....A basic project for the TMS320LF2407 DSK. Configures an analog-to-digital converter and generates a pulse width modulation (PWM) output.

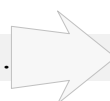
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TMS320LF2407DSK: IIR
TMS320LF2407 DSK: FFT

[Motor Control](#).....TMS320LF2407 DSK: Stepper Motor
TMS320LF2407 DSK: DC Motor

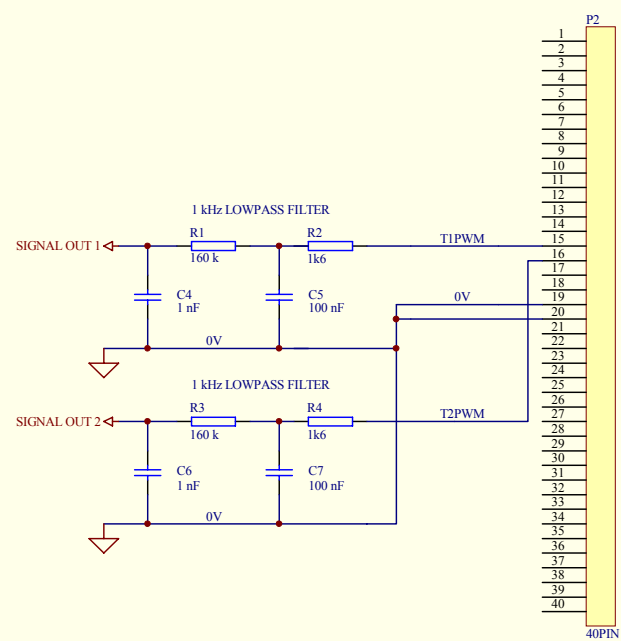
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[Route Map](#)

Schematic 1 Below ↓

SIGNAL GENERATION USING THE TMS320F243 DSK / TMS320LF2407 DSK



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|---|---------------------------|--------------------------|
| Title | | |
| SIGNAL GENERATION USING THE TMS320F243 DSK / TMS320LF2407 DSK | | |
| Size | Number | Revision |
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