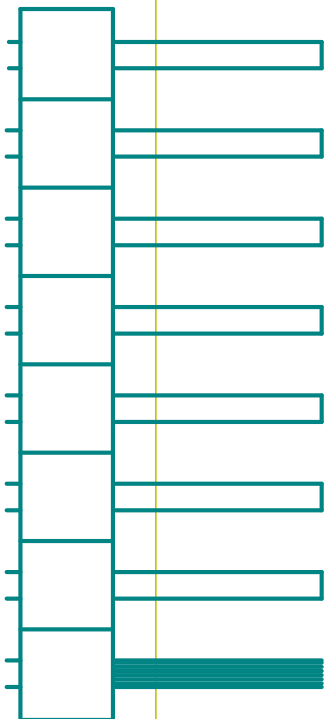


ARM & SAM  
RESET Source



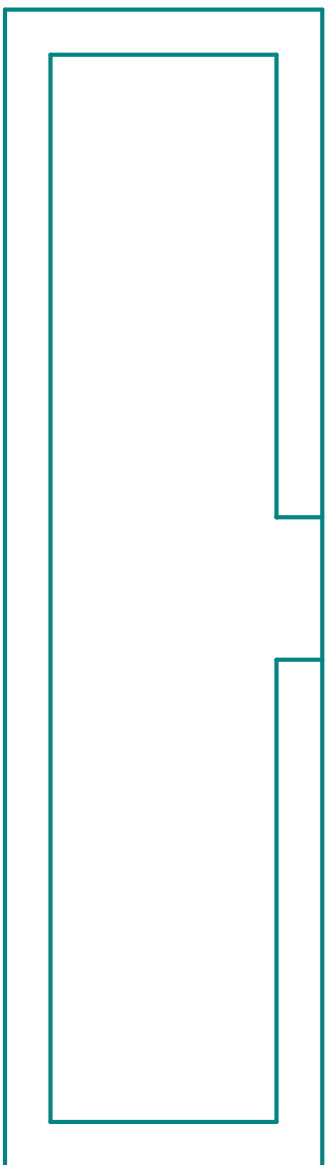
JP1



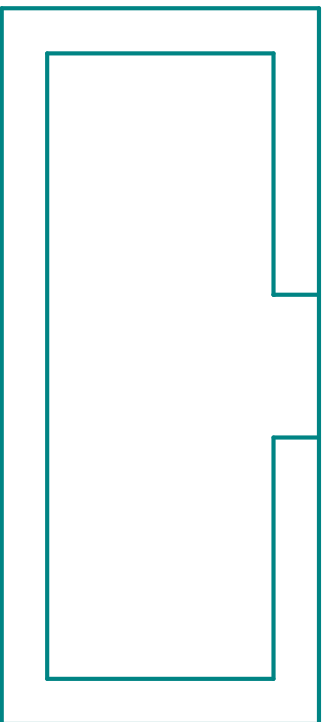
J1

R1

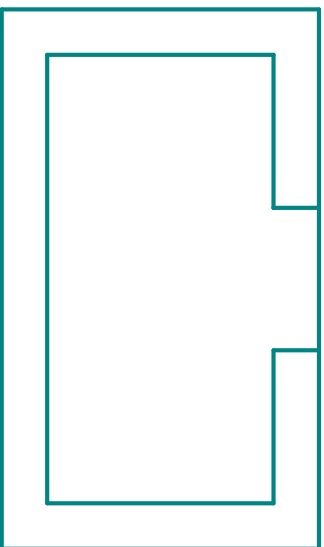
D1



J3

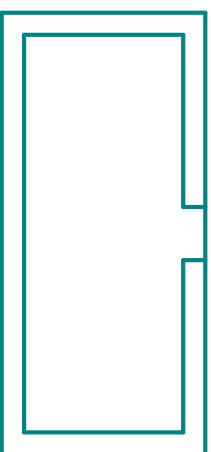


J4



J6

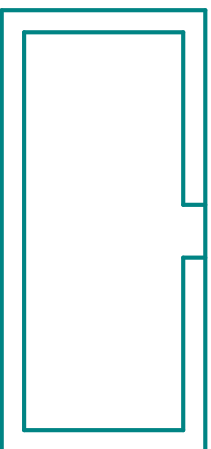
ARM



J5



J7  
VDD  
GND  
RX  
TX



J2

SAM

UART

AVR

AVR

AVR

Uchiyama

UNO  
JPG  
1993  
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