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FAN7621

PFM Controller for Half-Bridge Resonant Converters

Features

- Variable Frequency Control with 50% Duty Cycle for Half-bridge Resonant Converter Topology
- High Efficiency through Zero Voltage Switching (ZVS)
- Fixed Dead Time (350ns)
- Up to 300kHz Operating Frequency
- Pulse Skipping for Frequency Limit (Programmable) at Light-Load Condition
- Remote On/Off Control Using CON Pin
- Protection Functions: Over-Voltage Protection (OVP), Overload Protection (OLP), Over-Current Protection (OCP), Abnormal Over-Current Protection (AOCP), Internal Thermal Shutdown (TSD)

Applications

- PDP and LCD TVs
- Desktop PCs and Servers
- Adapters
- Telecom Power Supplies
- Video Game Consoles

Description

The FAN7621 is a pulse frequency modulation controller for high-efficiency half-bridge resonant converters. Offering everything necessary to build a reliable and robust resonant converter, the FAN7621 simplifies designs and improves productivity, while improving performance. The FAN7621 includes a high-side gate-drive circuit, an accurate current controlled oscillator, frequency limit circuit, soft-start, and built-in protection functions. The high-side gate-drive circuit has a common-mode noise cancellation capability, which guarantees stable operation with excellent noise immunity. Using the zero-voltage-switching (ZVS) technique dramatically reduces the switching losses and efficiency is significantly improved. The ZVS also reduces the switching noise noticeably, which allows a small-sized Electromagnetic Interference (EMI) filter.

The FAN7621 can be applied to various resonant converter topologies; such as series resonant, parallel resonant, and LLC resonant converters.

Related Resources

[AN4151 — Half-bridge LLC Resonant Converter Design using FSFR-series Fairchild Power Switch \(FPS™\)](#)

Ordering Information

| Part Number | Operating Junction Temperature | Package | Packaging Method |
|-------------|--------------------------------|--------------------------------------|------------------|
| FAN7621N | -40°C ~ 130°C | 16-Lead, Dual Inline Package (DIP) | Tube |
| FAN7621SJ | | 16-Lead, Small-Outline Package (SOP) | Tube |
| FAN7621SJX | | 16-Lead, Small-Outline Package (SOP) | Tape & Reel |

Application Circuit Diagram

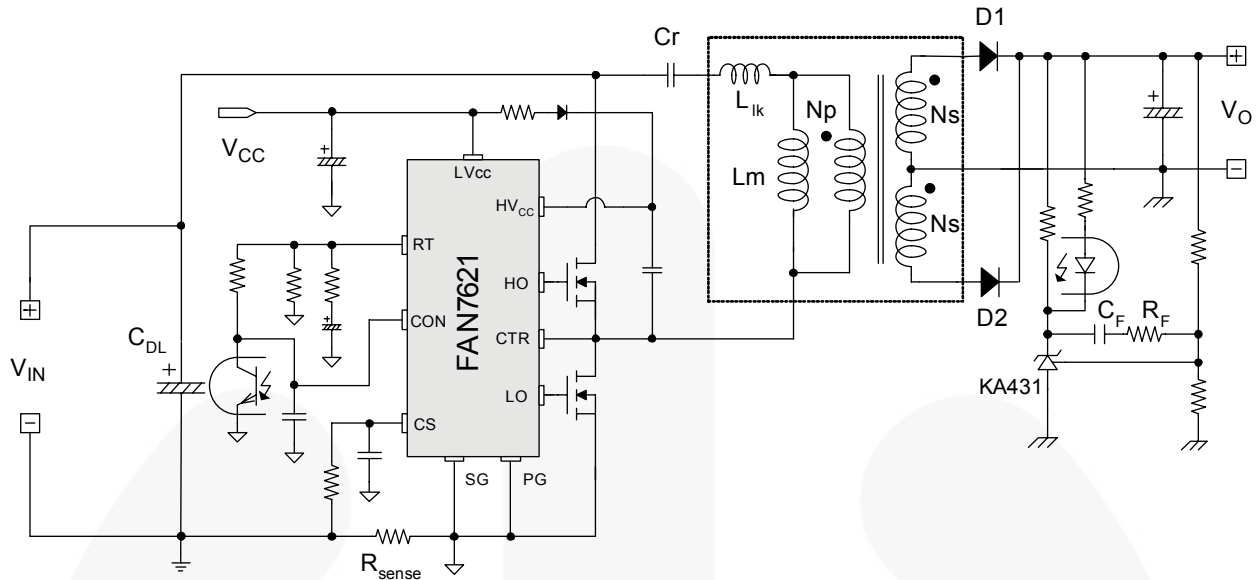


Figure 1. Typical Application Circuit (LLC Resonant Half-Bridge Converter)

Block Diagram

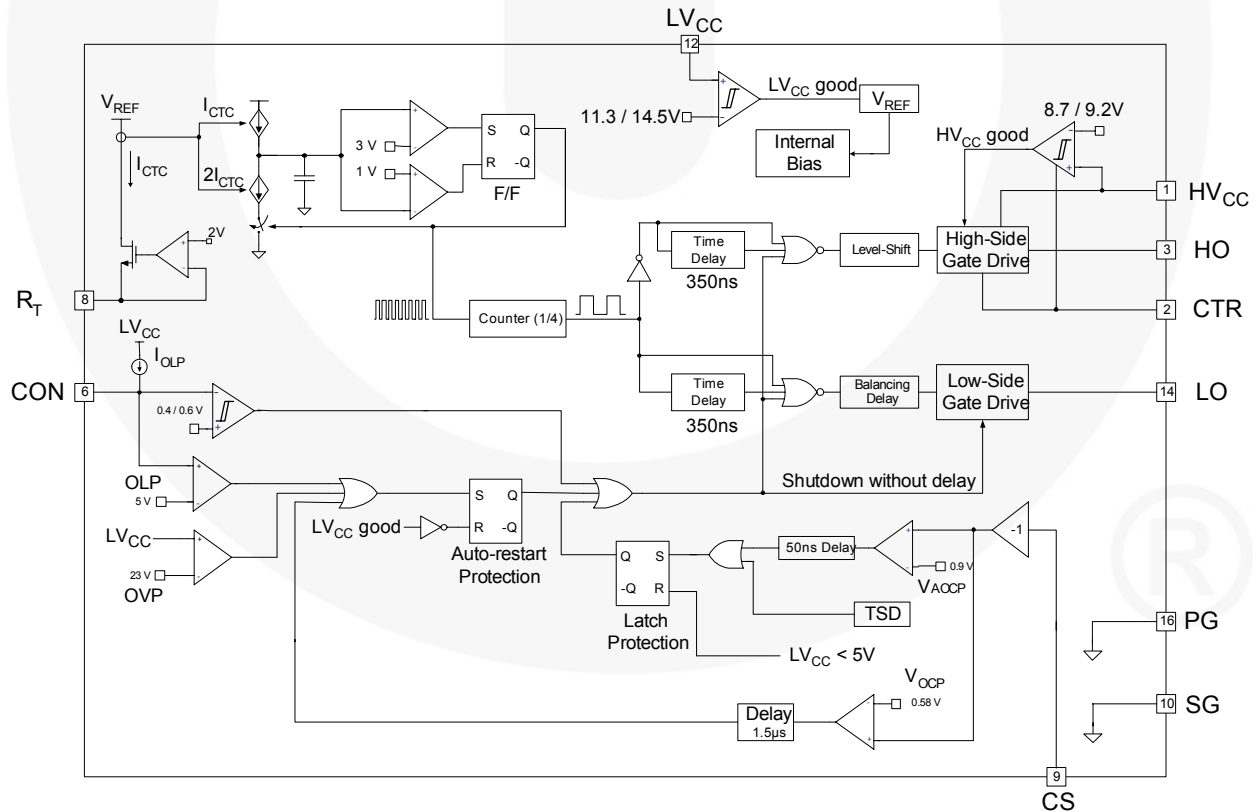


Figure 2. Internal Block Diagram

Pin Configuration

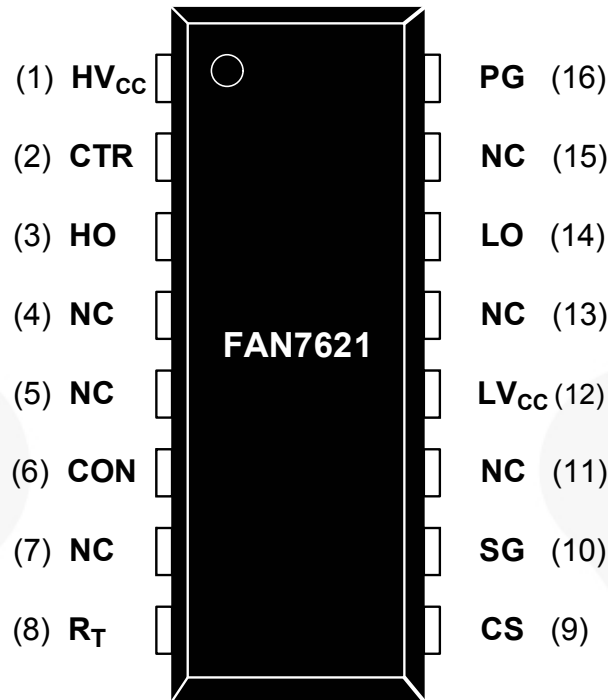


Figure 3. Package Diagram

Pin Definitions

| Pin # | Name | Description |
|-------|------------------|---|
| 1 | HV _{CC} | This is the supply voltage of the high-side gate-drive circuit IC. |
| 2 | CTR | This is the drain of the low-side MOSFET. Typically, a transformer is connected to this pin. |
| 3 | HO | This is the high-side gate driving signal. |
| 4 | NC | No connection. |
| 5 | NC | No connection. |
| 6 | CON | This pin is for a protection and enabling/disabling the controller. When the voltage of this pin is above 0.6V, the IC operation is enabled. When the voltage of this pin drops below 0.4V, gate drive signals for both MOSFETs are disabled. When the voltage of this pin increases above 5V, protection is triggered. |
| 7 | NC | No connection. |
| 8 | R _T | This pin programs the switching frequency. Typically, an opto-coupler is connected to control the switching frequency for the output voltage regulation. |
| 9 | CS | This pin senses the current flowing through the low-side MOSFET. Typically, negative voltage is applied on this pin. |
| 10 | SG | This pin is the control ground. |
| 11 | NC | No connection. |
| 12 | LV _{CC} | This pin is the supply voltage of the control IC. |
| 13 | NC | No connection. |
| 14 | LO | This is the low-side gate driving signal. |
| 15 | NC | No connection. |
| 16 | PG | This pin is the power ground. This pin is connected to the source of the low-side MOSFET. |

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. $T_A=25^{\circ}\text{C}$ unless otherwise specified.

| Symbol | Parameter | Min. | Max. | Unit |
|------------------------|---|---------------|-----------|--------------------|
| V_{HO} | High-Side Gate Driving Voltage | $V_{CTR}-0.3$ | HV_{CC} | V |
| V_{LO} | Low-Side Gate Driving Voltage | -0.3 | LV_{CC} | |
| LV_{CC} | Low-Side Supply Voltage | -0.3 | 25.0 | V |
| HV_{CC} to V_{CTR} | High-Side V_{CC} Pin to Center Voltage | -0.3 | 25.0 | V |
| V_{CTR} | Center Voltage | -0.3 | 600.0 | V |
| V_{CON} | Control Pin Input Voltage | -0.3 | LV_{CC} | V |
| V_{CS} | Current Sense (CS) Pin Input Voltage | -5.0 | 1.0 | V |
| V_{RT} | R_T Pin Input Voltage | -0.3 | 5.0 | V |
| dV_{CTR}/dt | Allowable Center Voltage Slew Rate | | 50 | V/ns |
| P_D | Total Power Dissipation | 16-DIP | 1.56 | W |
| | | 16-SOP | 1.13 | W |
| T_J | Maximum Junction Temperature ⁽¹⁾ | | +150 | $^{\circ}\text{C}$ |
| | Recommended Operating Junction Temperature ⁽¹⁾ | -40 | +130 | |
| T_{STG} | Storage Temperature Range | -55 | +150 | $^{\circ}\text{C}$ |

Note:

- The maximum value of the recommended operating junction temperature is limited by thermal shutdown.

Thermal Impedance

| Symbol | Parameter | Value | Unit |
|---------------|---------------------------------------|--------|------|
| θ_{JA} | Junction-to-Ambient Thermal Impedance | 16-DIP | 80 |
| | | 16-SOP | 110 |

Electrical Characteristics

$T_A=25^{\circ}\text{C}$ and $\text{LV}_{\text{CC}}=17\text{V}$ unless otherwise specified.

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|---------------------------------|---|---|------|------|------|------|
| Supply Section | | | | | | |
| I _{LK} | Offset Supply Leakage Current | HV _{CC} =V _{CTR} | | | 50 | μA |
| I _Q HV _{CC} | Quiescent HV _{CC} Supply Current | (HV _{CC} UV+) - 0.1V | | 50 | 120 | μA |
| I _Q LV _{CC} | Quiescent LV _{CC} Supply Current | (LV _{CC} UV+) - 0.1V | | 100 | 200 | μA |
| I _O HV _{CC} | Operating HV _{CC} Supply Current (RMS Value) | f _{OSC} =100kHz, V _{CON} > 0.6V, C _{Load} =1nF | | 5 | 8 | mA |
| | | No Switching, V _{CON} < 0.4V | | 100 | 200 | μA |
| I _O LV _{CC} | Operating LV _{CC} Supply Current (RMS Value) | f _{OSC} =100kHz, V _{CON} > 0.6V, C _{Load} =1nF | | 6 | 9 | mA |
| | | No Switching, V _{CON} < 0.4V | | 2 | 4 | mA |
| UVLO Section | | | | | | |
| LV _{CC} UV+ | LV _{CC} Supply Under-Voltage Positive Going Threshold (LV _{CC} Start) | | 13.0 | 14.5 | 16.0 | V |
| LV _{CC} UV- | LV _{CC} Supply Under-Voltage Negative Going Threshold (LV _{CC} Stop) | | 10.2 | 11.3 | 12.4 | V |
| LV _{CC} UVH | LV _{CC} Supply Under-Voltage Hysteresis | | | 3.2 | | V |
| HV _{CC} UV+ | HV _{CC} Supply Under-Voltage Positive Going Threshold (HV _{CC} Start) | | 8.2 | 9.2 | 10.2 | V |
| HV _{CC} UV- | HV _{CC} Supply Under-Voltage Negative Going Threshold (HV _{CC} Stop) | | 7.8 | 8.7 | 9.6 | V |
| HV _{CC} UVH | HV _{CC} Supply Under-Voltage Hysteresis | | | 0.5 | | V |
| Oscillator & Feedback Section | | | | | | |
| V _{CONDIS} | Control Pin Disable Threshold Voltage | | 0.36 | 0.40 | 0.44 | V |
| V _{CONEN} | Control Pin Enable Threshold Voltage | | 0.54 | 0.60 | 0.66 | V |
| V _{RT} | V-I Converter Threshold Voltage | R _T =5.2kΩ | 1.5 | 2.0 | 2.5 | V |
| f _{OSC} | Output Oscillation Frequency | | 94 | 100 | 106 | kHz |
| DC | Output Duty Cycle | | 48 | 50 | 52 | % |
| f _{SS} | Internal Soft-Start Initial Frequency | f _{SS} =f _{OSC} +40kHz, R _T =5.2kΩ | | 140 | | kHz |
| t _{SS} | Internal Soft-Start Time | | 2 | 3 | 4 | ms |
| Output Section | | | | | | |
| I _{source} | Peak Sourcing Current | HV _{CC} =17V | 250 | 360 | | mA |
| I _{sink} | Peak Sinking Current | HV _{CC} =17V | 460 | 600 | | mA |
| t _r | Rising Time | C _{Load} =1nF, HV _{CC} =17V | | 65 | | ns |
| t _f | Falling Time | | | 35 | | ns |
| V _{HOH} | High Level of High-Side Gate Driving Signal (V _{HVCC} -V _{HO}) | I _O =20mA | | | 1.0 | V |
| V _{HOL} | Low Level of High-Side Gate Driving Signal | | | | 0.6 | V |
| V _{LOH} | High Level of High-Side Gate Driving Signal (V _{LVCC} -V _{LO}) | | | | 1.0 | V |
| V _{LOL} | Low Level of High-Side Gate Driving Signal | | | | 0.6 | V |

Electrical Characteristics (Continued)T_A=25°C and LV_{CC}=17V unless otherwise specified.

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|----------------------------------|---|-------------------------|-------|-------|-------|------|
| Protection Section | | | | | | |
| I _{OLP} | OLP Delay Current | V _{CON} =4V | 3.8 | 5.0 | 6.2 | μA |
| V _{OLP} | OLP Protection Voltage | V _{CON} > 3.5V | 4.5 | 5.0 | 5.5 | V |
| V _{OVP} | LV _{CC} Over-Voltage Protection | LV _{CC} > 21V | 21 | 23 | 25 | V |
| V _{AOCP} | AOCP Threshold Voltage | | -1.0 | -0.9 | -0.8 | V |
| t _{BAO} | AOCP Blanking Time | | | 50 | | ns |
| V _{OCP} | OCP Threshold Voltage | | -0.64 | -0.58 | -0.52 | V |
| t _{BO} | OCP Blanking Time ⁽²⁾ | | 1.0 | 1.5 | 2.0 | μs |
| t _{DA} | Delay Time (Low-Side) Detecting from V _{AOCP} to Switch Off ⁽²⁾ | | | 250 | 400 | ns |
| T _{SD} | Thermal Shutdown Temperature ⁽²⁾ | | 110 | 130 | 150 | °C |
| I _{SU} | Protection Latch Sustain LV _{CC} Supply Current | LV _{CC} =7.5V | | 100 | 150 | μA |
| V _{PRSET} | Protection Latch Reset LV _{CC} Supply Voltage | | 5 | | | V |
| Dead-Time Control Section | | | | | | |
| D _T | Dead Time | | | 350 | | ns |

Note:

2. These parameters, although guaranteed, are not tested in production.

Typical Performance Characteristics

These characteristic graphs are normalized at $T_A=25^\circ\text{C}$.

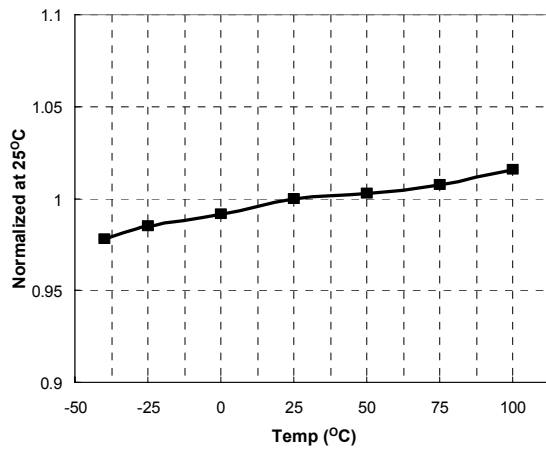


Figure 4. Low-Side MOSFET Duty Cycle vs. Temperature

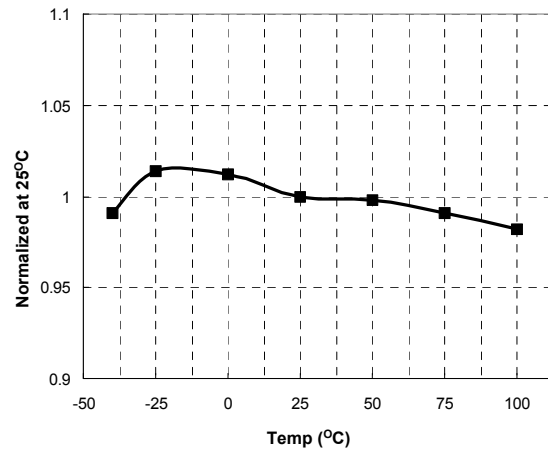


Figure 5. Switching Frequency vs. Temperature

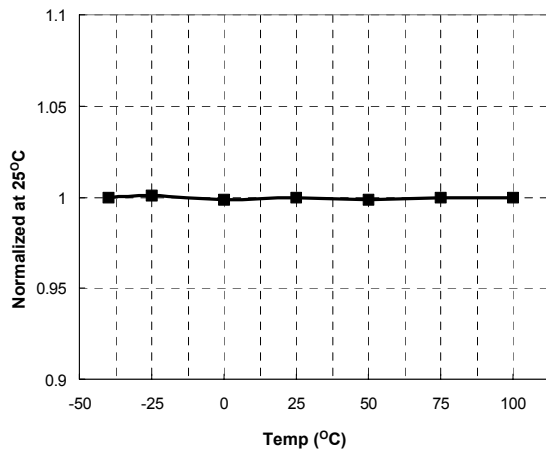


Figure 6. High-Side V_{CC} (HV_{CC}) Start vs. Temperature

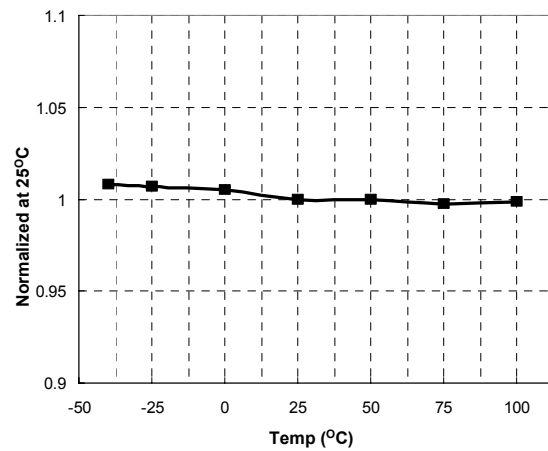


Figure 7. High-Side V_{CC} (HV_{CC}) Stop vs. Temperature

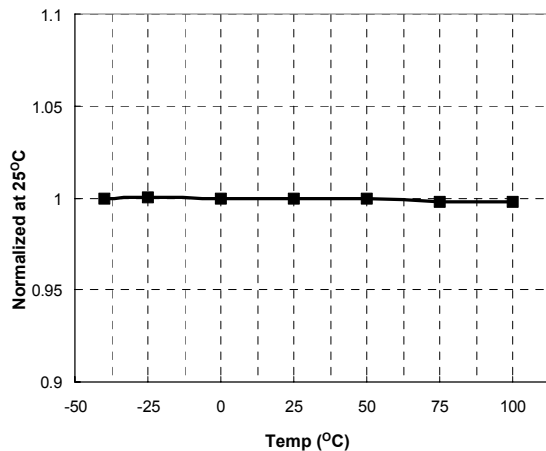


Figure 8. Low-Side V_{CC} (LV_{CC}) Start vs. Temperature

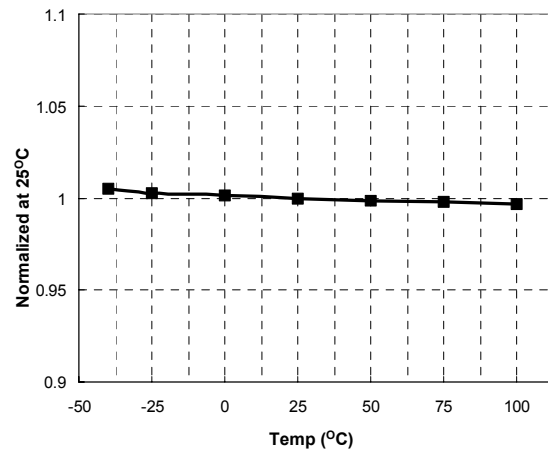


Figure 9. Low-Side V_{CC} (LV_{CC}) Stop vs. Temperature

Typical Performance Characteristics (Continued)

These characteristic graphs are normalized at $T_A=25^\circ\text{C}$.

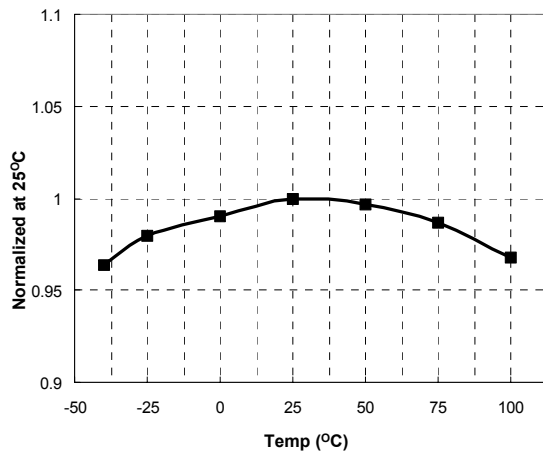


Figure 10. OLP Delay Current vs. Temperature

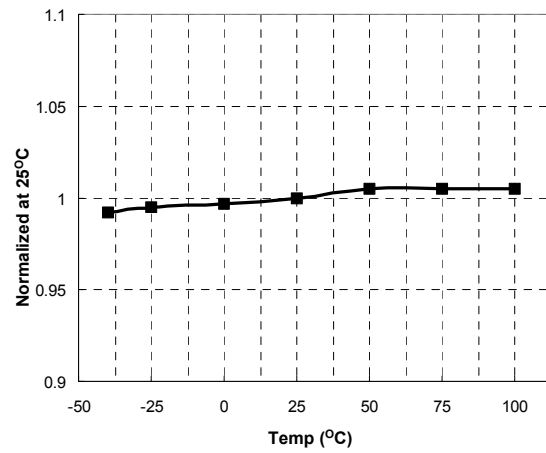


Figure 11. OLP Protection Voltage vs. Temperature

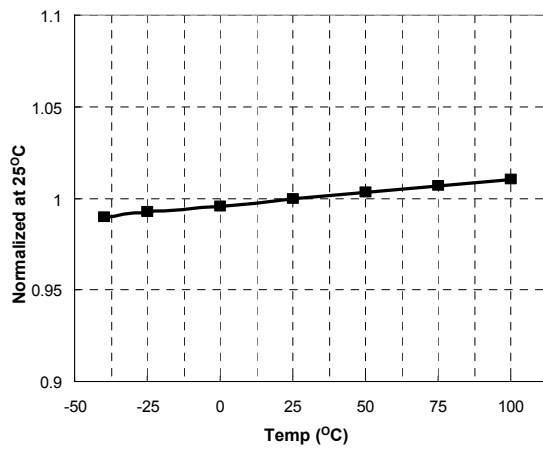


Figure 12. LVCC OVP Voltage vs. Temperature

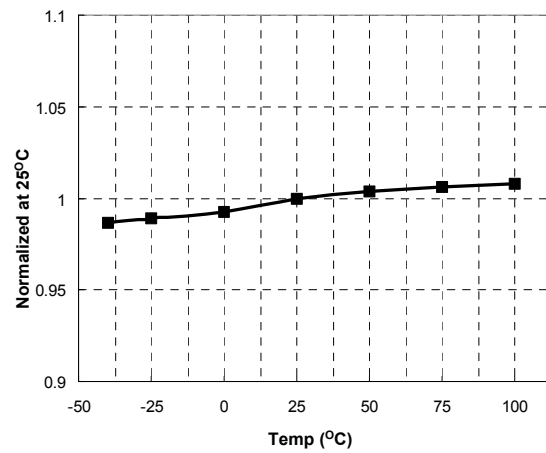


Figure 13. R_T Voltage vs. Temperature

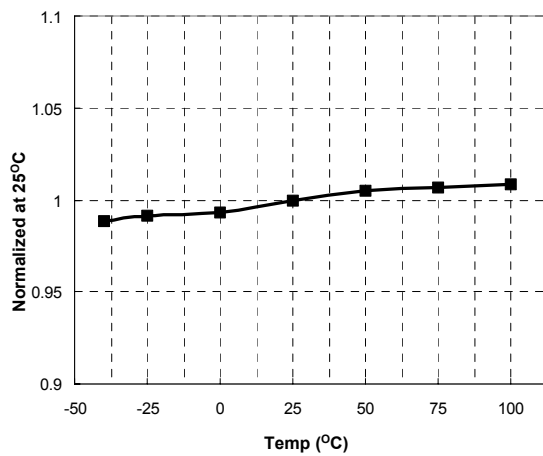


Figure 14. CON Pin Enable Voltage vs. Temperature

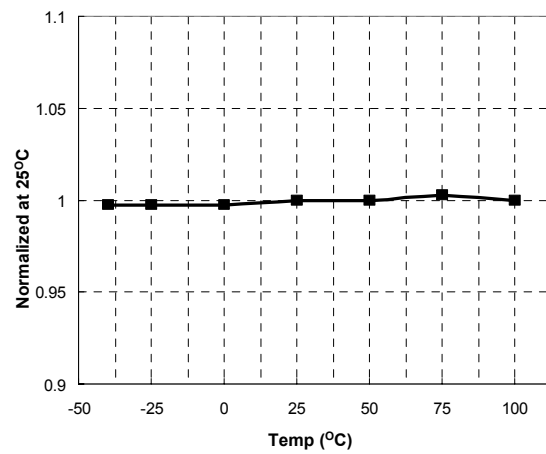


Figure 15. OCP Voltage vs. Temperature

Functional Description

1. Basic Operation

FAN7621 is designed to drive high-side and low-side MOSFETs complementarily with 50% duty cycle. A fixed dead time of 350ns is introduced between consecutive transitions, as shown in Figure 16.

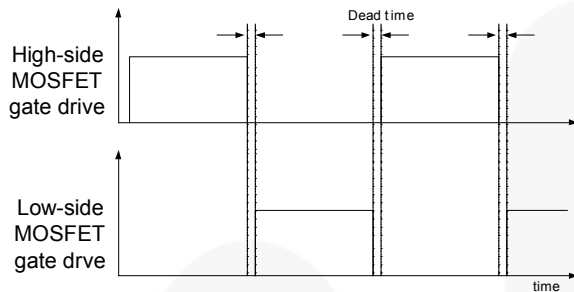


Figure 16. MOSFETs Gate Drive Signal

2. Internal Oscillator

FAN7621 employs a current-controlled oscillator, as shown in Figure 17. Internally, the voltage of R_T pin is regulated at 2V and the charging / discharging current for the oscillator capacitor, C_T , is obtained by copying the current flowing out of R_T pin (I_{CTC}) using a current mirror. Therefore, the switching frequency increases as I_{CTC} increases.

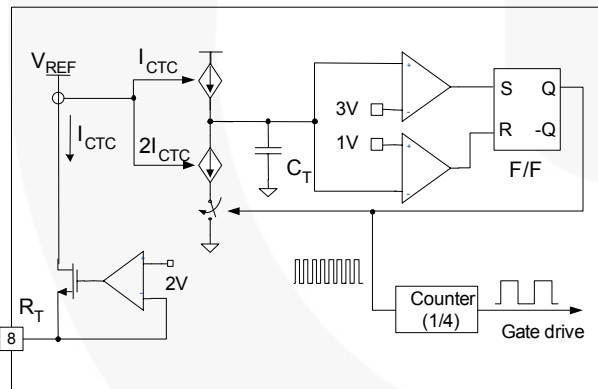


Figure 17. Current Controlled Oscillator

3. Frequency Setting

Figure 18 shows the typical voltage gain curve of a resonant converter, where the gain is inversely proportional to the switching frequency in the ZVS region. The output voltage can be regulated by modulating the switching frequency. Figure 19 shows the typical circuit configuration for R_T pin, where the opto-coupler transistor is connected to the R_T pin to modulate the switching frequency.

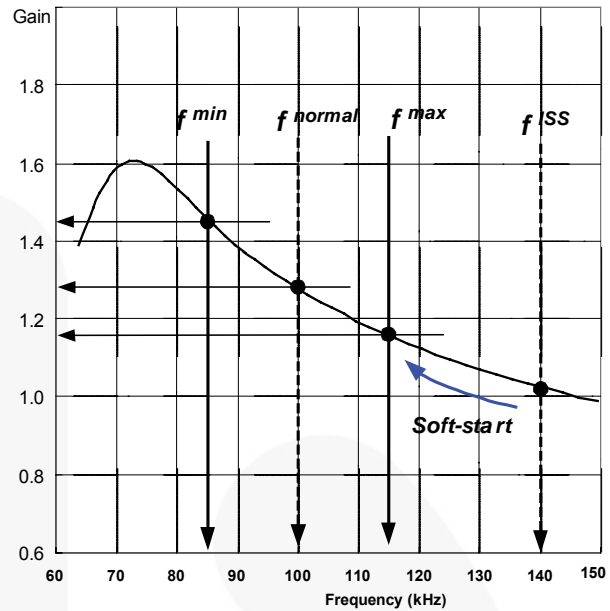


Figure 18. Resonant Converter Typical Gain Curve

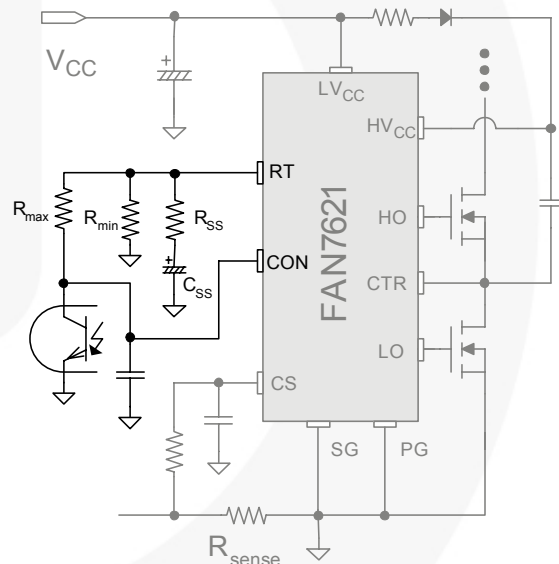


Figure 19. Frequency Control Circuit

The minimum switching frequency is determined as:

$$f_{\min} = \frac{5.2k\Omega}{R_{\min}} \times 100(kHz) \quad (1)$$

Assuming the saturation voltage of opto-coupler transistor is 0.2V, the maximum switching frequency is determined as:

$$f_{\max} = \left(\frac{5.2k\Omega}{R_{\min}} + \frac{4.68k\Omega}{R_{\max}} \right) \times 100(kHz) \quad (2)$$

To prevent excessive inrush current and overshoot of output voltage during startup, increase the voltage gain of the resonant converter progressively. Since the voltage gain of the resonant converter is inversely proportional to the switching frequency, the soft-start is

implemented by sweeping down the switching frequency from an initial high frequency (f^{ISS}) until the output voltage is established. The soft-start circuit is made by connecting R-C series network on the R_T pin, as shown in Figure 19. FAN7621 also has an internal soft-start for 3ms to reduce the current overshoot during the initial cycles, which adds 40kHz to the initial frequency of the external soft-start circuit, as shown in Figure 20. The initial frequency of the soft-start is given as:

$$f^{ISS} = \left(\frac{5.2k\Omega}{R_{min}} + \frac{5.2k\Omega}{R_{SS}} \right) \times 100 + 40 \text{ (kHz)} \quad (3)$$

It is typical to set the initial (soft-start) frequency of two ~ three times the resonant frequency (f_o) of the resonant network.

The soft-start time is three to four times the RC time constant. The RC time constant is as follows:

$$T_{SS} = R_{SS} \cdot C_{SS} \quad (4)$$

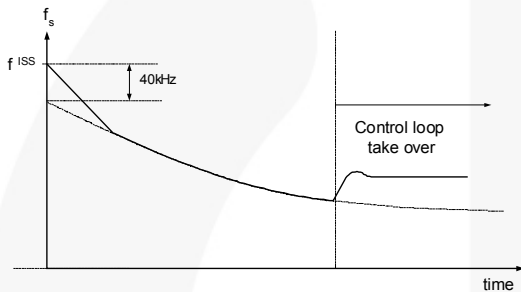


Figure 20. Frequency Sweeping of Soft-Start

4. Control Pin

The FAN7621 has a control pin for protection, cycle skipping, and remote on/off. Figure 21 shows the internal block diagram for control pin.

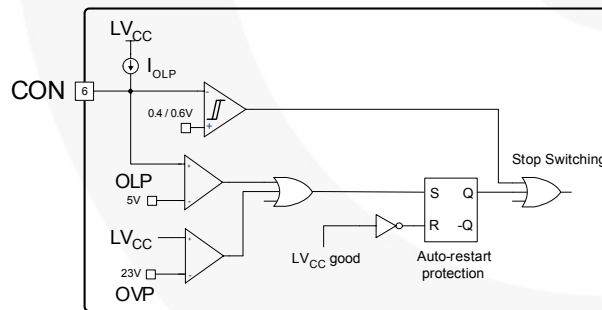


Figure 21. Internal Block of Control Pin

Protection: When the control pin voltage exceeds 5V, protection is triggered. Detailed applications are described in the protection section.

Pulse Skipping: FAN7621 stops switching when the control pin voltage drops below 0.4V and resumes switching when the control pin voltage rises above 0.6V. To use pulse-skipping, the control pin should be connected to the opto-coupler collector pin. The frequency that causes pulse skipping is given as:

$$f^{SKIP} = \left(\frac{5.2k\Omega}{R_{min}} + \frac{4.16k\Omega}{R_{max}} \right) \times 100 \text{ (kHz)} \quad (5)$$

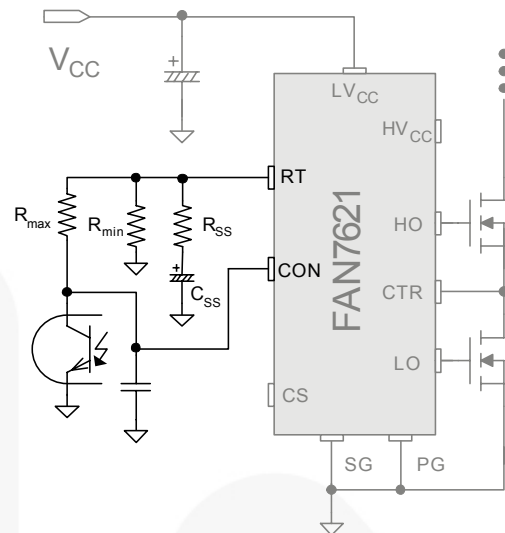


Figure 22. Control Pin Configuration for Pulse Skipping

Remote On / Off: When an auxiliary power supply is used for standby, the main power stage using FAN7621 can be shut down by pulling down the control pin voltage, as shown in Figure 23. R1 and C1 are used to ensure soft-start when switching resumes.

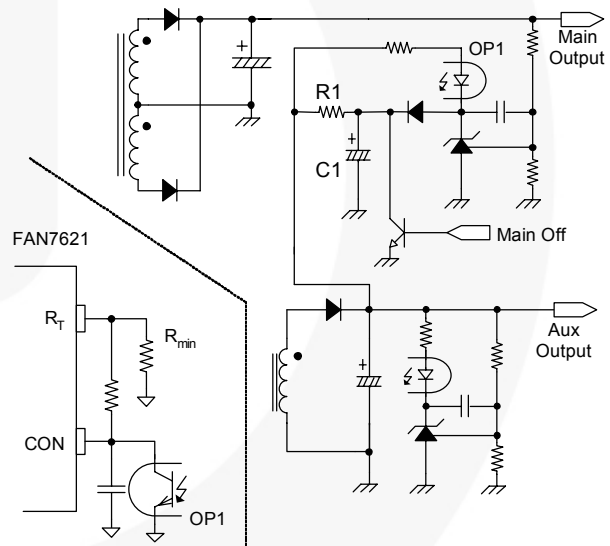


Figure 23. Remote On / Off Circuit

5. Protection Circuits

The FAN7621 has several self-protective functions, such as Overload Protection (OLP), Over-Current Protection (OCP), Abnormal Over-Current Protection (AOCP), Over-Voltage Protection (OVP), and Thermal Shutdown (TSD). OLP, OCP, and OVP are auto-restart mode protections; while AOCP and TSD are latch-mode protections, as shown in Figure 24.

5.1 Auto-Restart Mode Protection: Once a fault condition is detected, switching is terminated and the MOSFETs remain off. When LV_{CC} falls to the LV_{CC} stop voltage of 11.3V, the protection is reset. FAN7621

5.7 Overload Protection (OLP): Overload is defined as the load current exceeding its normal level due to an unexpected abnormal event. In this situation, the protection circuit should trigger to protect the power supply. However, even when the power supply is in the normal condition, the overload situation can occur during the load transition. To avoid premature triggering of protection, the overload protection circuit should be designed to trigger only after a specified time to determine whether it is a transient situation or a true overload situation. Figure 27 shows a typical overload protection circuit. By sensing the resonant capacitor voltage on the control pin, the overload protection can be implemented. Using RC time constant, shutdown delay can be also introduced. The voltage obtained on the control pin is given as:

$$V_{CON} = \frac{C_B}{2(C_B + C_{sense})} V_{Cr}^{p-p} \quad (7)$$

where V_{Cr}^{p-p} is the amplitude of the resonant capacitor voltage.

5.8 Over-Voltage Protection (OVP): When the LV_{CC} reaches 23V, OVP is triggered. This protection is used when auxiliary winding of the transformer to supply V_{CC} to the controller is utilized.

5.9 Thermal Shutdown (TSD): If the temperature of the junction exceeds approximately 130°C, the thermal shutdown triggers.

6. PCB Layout Guideline

Duty imbalance problems may occur due to the radiated noise from main transformer, the inequality of the secondary-side leakage inductances of main transformer, and so on. Among them, it is one of the dominant reasons that the control components in the vicinity of R_T pin are enclosed by the primary current flow pattern on PCB layout. The direction of the magnetic field on the components caused by the primary current flow is changed when the high-and-low side MOSFET turns on by turns. The magnetic fields with opposite direction from each other induce a current through, into, or out of the R_T pin, which makes the turn-on duration of each MOSFET different. It is strongly recommended to separate the control components in the vicinity of R_T pin from the primary current flow pattern on PCB layout. Figure 28 shows an example for the duty-balanced case. The yellow and blue lines show the primary current flows when the lower-side and higher-side MOSFETs turns on, respectively. The primary current does not enclose any component of controller.

It is helpful to reduce the duty imbalance to make the loop configured between CON pin and opto-coupler as small as possible, as shown in the red line in Figure 28.

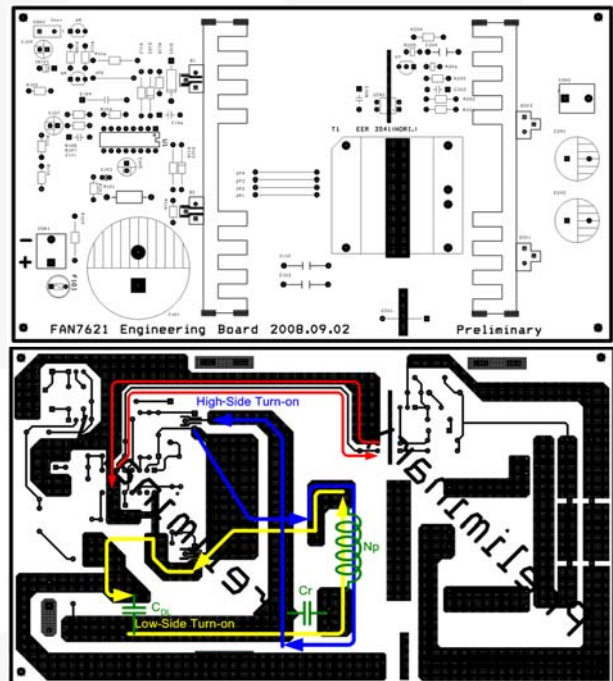


Figure 28. Example for Duty Balancing

| Application | Device | Input Voltage Range | Rated Output Power | Output Voltage (Rated Current) |
|-------------|---------|---|--------------------|-----------------------------------|
| LCD TV | FAN7621 | 390V _{DC} (340~400V _{DC}) | 200W | 24V-8.3A |

Features

- High efficiency (>94% at 400V_{DC} input)
- Reduced EMI noise through zero-voltage-switching (ZVS)
- Enhanced system reliability with various protection functions

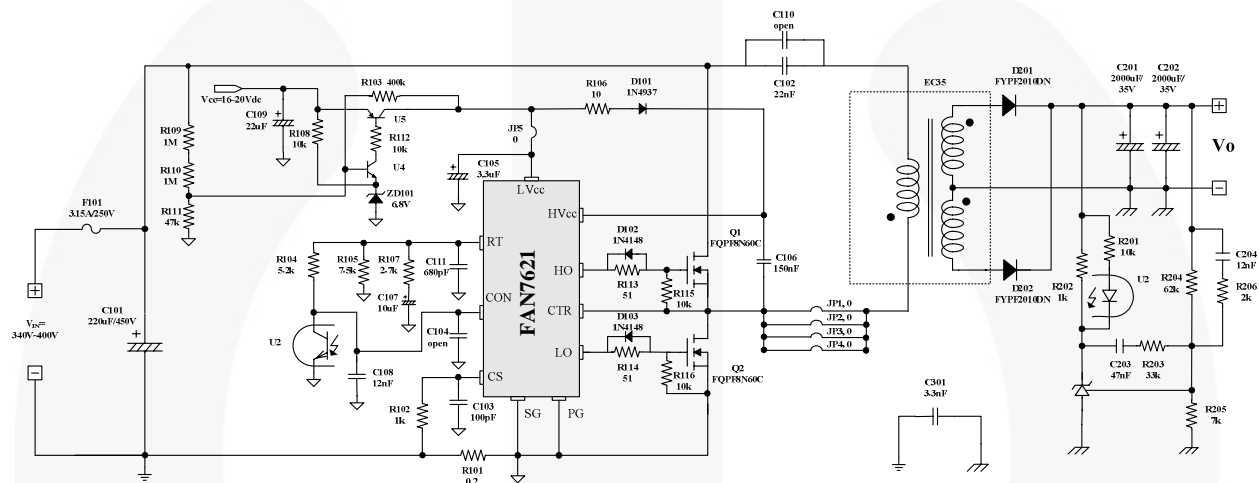


Figure 29. Typical Application Circuit

Typical Application Circuit (Continued)

Usually, LLC resonant converters require large leakage inductance value. To obtain a large leakage inductance, sectional winding method is used.

- Core: EC35 ($A_e=106 \text{ mm}^2$)
- Bobbin: EC35 (Horizontal)
- Transformer Model Number: SNX-2468-1

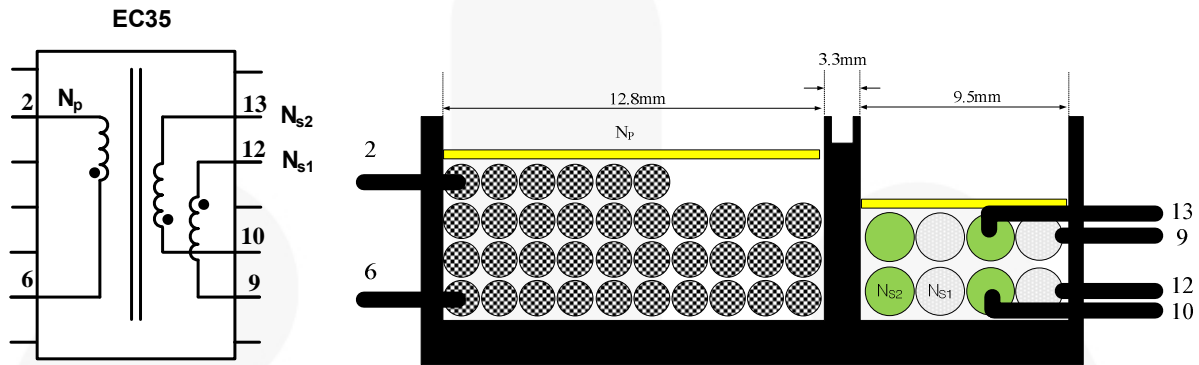
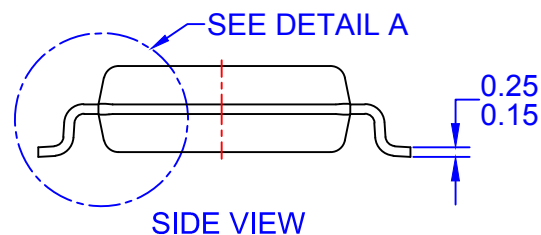
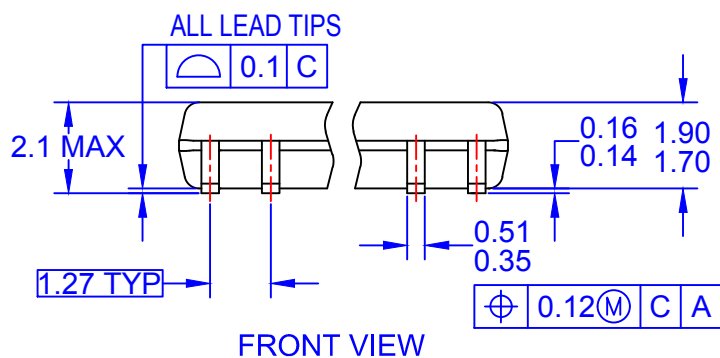
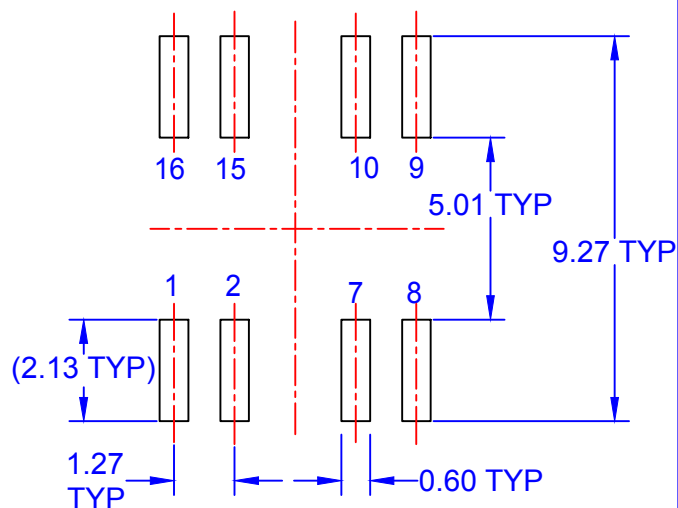
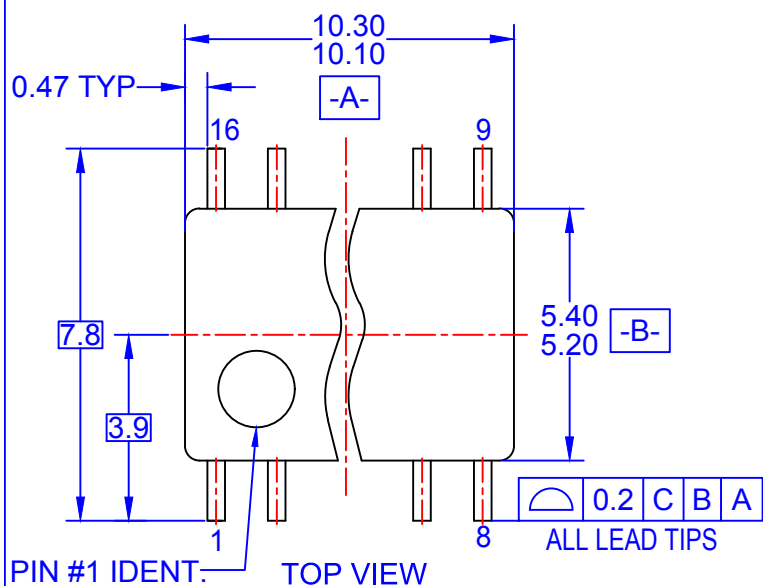


Figure 30. Transformer Construction

| | Pins (S → F) | Wire | Turns | Note |
|----------|--------------|-----------------------|-------|-----------------|
| N_p | 6 → 2 | 0.08φ×88 (Litz Wire) | 36 | |
| N_{s1} | 12 → 9 | 0.08φ×234 (Litz Wire) | 4 | Bifilar Winding |
| N_{s2} | 10 → 13 | 0.08φ×234 (Litz Wire) | 4 | Bifilar Winding |

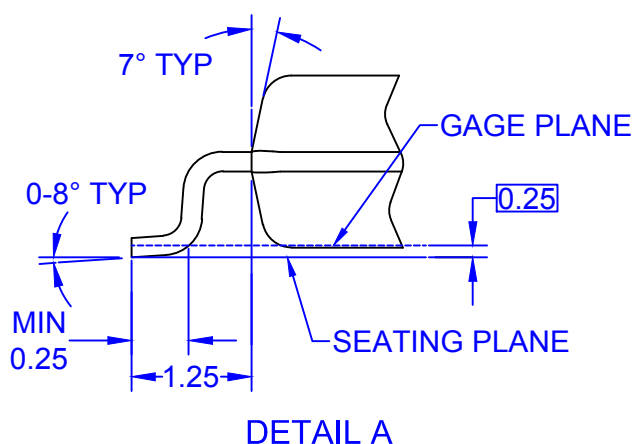
| | Pins | Specifications | Remark |
|--|------|---------------------------|-------------------------------------|
| Primary-Side Inductance (L_p) | 2–6 | $550\mu\text{H} \pm 10\%$ | 100kHz, 1V |
| Primary-Side Effective Leakage (L_r) | 2–6 | $110\mu\text{H} \pm 10\%$ | Short one of the secondary windings |

For more detailed information regarding the transformer, visit <http://www.santronics-usa.com/documents.html> or contact sales@santronics-usa.com or +1-408-734-1878 (Sunnyvale, California USA).



NOTES:

- CONFORMS TO EIAJ EDR-7320 REGISTRATION, ESTABLISHED IN DECEMBER, 1998.
- DIMENSIONS ARE IN MILLIMETERS.
- DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- DRAWING FILENAME: MKT-M16Drev5





A. CONFORMS TO JEDEC MS-001, VARIATION BB
B. ALL DIMENSIONS ARE IN MILLIMETERS
C. DIMENSIONS ARE EXCLUSIVE OF BURRS,
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