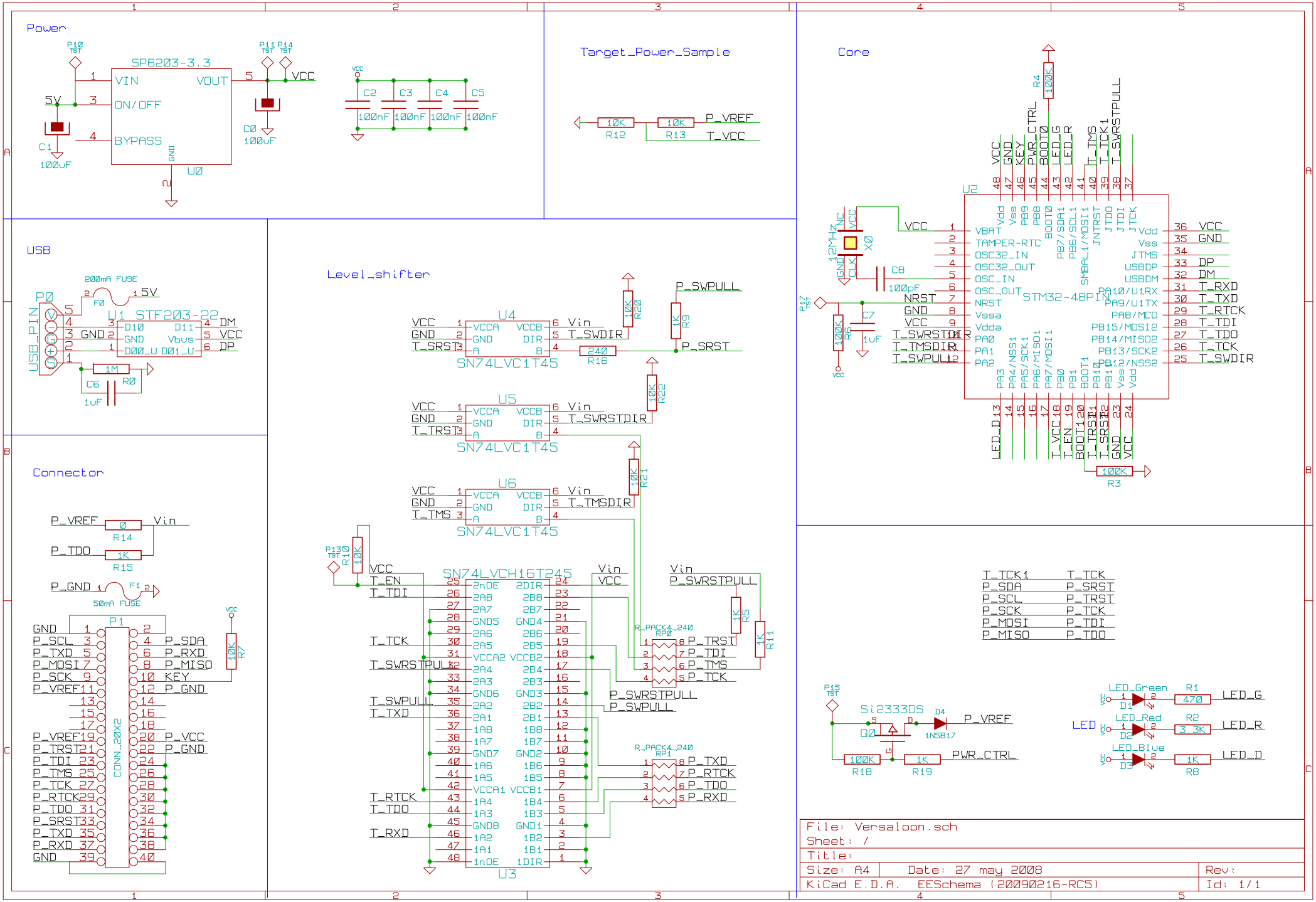
Power
SP6203-3.3
VIN
VOUT
ON/OFF
BYPASS
U0
5V
VCC
C1 100uF
C0 100uF
C2 C3 C4 C5 100nF
Target_Power_Sample
10K R12
10K R13
P_VREF
T_VCC
Core
STM32-48PIN
U2
X0 12MHz
C8 100pF
C7 1uF
R3 100K
R4 100K
USB
U1 STF203-22
200mA FUSE
F0
P0
USB_PIN
DM
DP
VCC
GND
C6 1uF
R0 1M
Level_shifter
U4 SN74LVC1T45
U5 SN74LVC1T45
U6 SN74LVC1T45
U3 SN74LVCH16T245
R_PACK4_240
Connector
P1
CONN_20X2
P_VREF
P_TDO
P_GND
R14 0
R15 1K
50mA FUSE
Si2333DS
Q0
D4 1N5817
R18 100K
R19 1K
PWR_CTRL
LED_Green
LED_Red
LED_Blue
R1 470
R2 3.3K
R8 1K
LED_G
LED_R
LED_D
T_TCK1 T_TCK
P_SDA P_SRST
P_SCL P_TRST
P_SCK P_TCK
P_MOSI P_TDI
P_MISO P_TDO
File: Versaloon.sch
Sheet: /
Title:
Size: A4
Date: 27 may 2008
Rev:
KiCad E.D.A. EESchema (20090216-RC5)
Id: 1/1