Introduction

In 2011, we (Weixin Technology Studio) launched an EP2C5/EP2C8 cost-effective development board with a price of about 300-400 yuan. Due to the practical design, rich resources, and moderate price, it was warmly welcomed and received the support of many buyers and friends once launched. I would like to express my deep gratitude to the friends who supported us and all the good buyers!

At the same time, we noticed many friends, especially students, who hope to have an entry-level development board with the lowest price possible, rich supporting materials, and suitable for beginners. Based on this, we would like to develop a student development board with a suitable hardware configuration. However, it must be available at a lower price, which can effectively popularize FPGA knowledge and help friends who are eager to learn FPGA knowledge! Therefore, after careful selection, planning, and design, we officially selected Altera's IV generation main chip and designed the FPGA Student Development Board (hereinafter referred to as the Student Development Board).

So, finally, the FPGA Student Development Board was launched!

The main chip of the student aid board is the latest Altera IV EP4CE6E22C8N. All IO is drawn out to facilitate testing and self-expansion. At the same time, the peripherals have added basic and more classic peripherals, which are very suitable for getting started with FPGA learning. Learning the hardware description language Verilog or VHDL or doing NIOSII is completely easy.

At the same time, we will write a series of supporting documents and try to keep abreast of the student development board itself, and the language will be as popular as possible. The ultimate goal is to allow beginners to master basic FPGA knowledge and easily enter the FPGA world as soon as possible. Everyone is welcome to criticize and correct! There is no perfect development board in the world. There are still many areas of the student development board that need to be improved, improved, and updated. We will also make further efforts and do a good job seriously, and thank you all, Friends!

Author's Note：

For friends who are not familiar with FPGAs, it is recommended to patiently follow the documentation to learn the operation and practice it repeatedly. Friends who have a certain foundation can choose to read or skip reading according to the specific content.

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