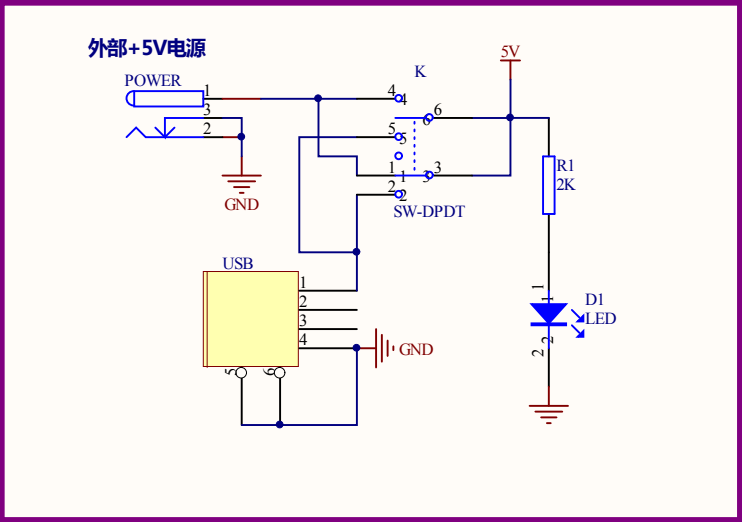
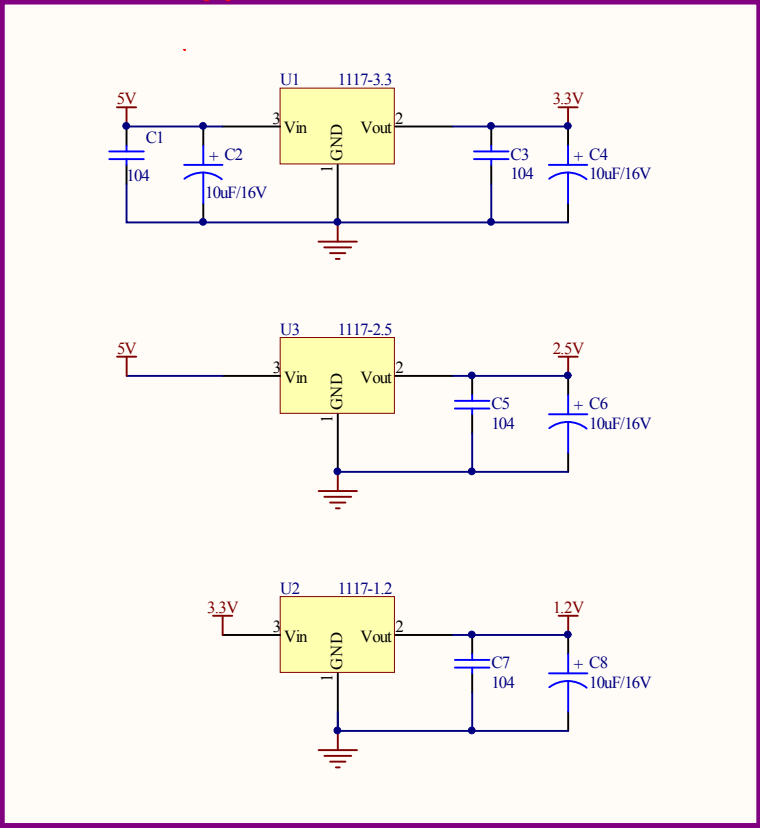


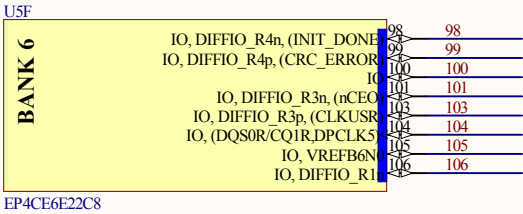
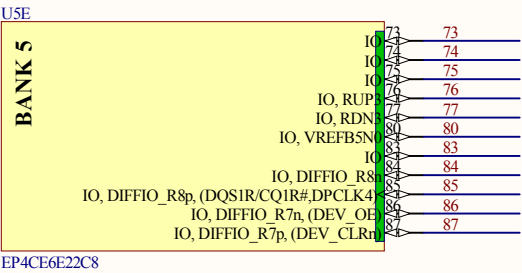
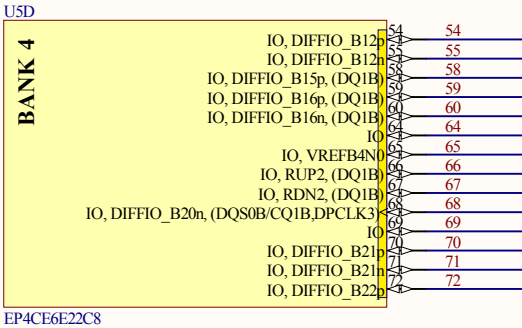
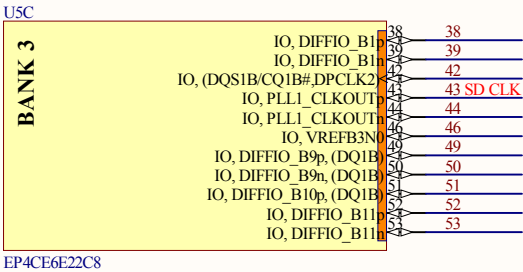
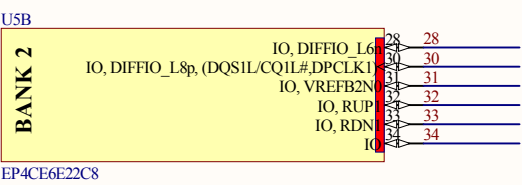
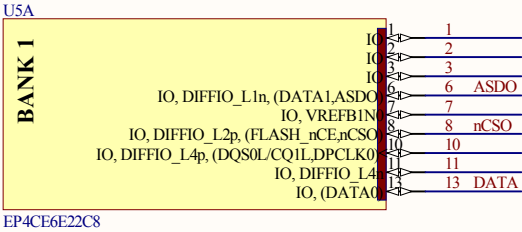
Power Indicator



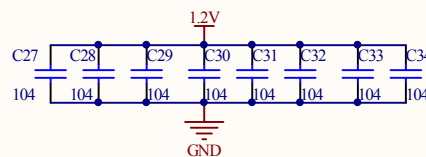
- Notes on the schematic diagram:
- 1. There are two ways to supply power to the development board, one is to connect the 5V power adapter externally or by connecting the USB B power supply. Only one can be connected, and the self-locking switch K can control two kinds of power supply switching
 - 2. There are three kinds of onboard power supply Power IC, respectively
AMS1117-3.3V - IO voltage,
AMS1117-2.5V - PLL voltage,
AMS1117-1.2V - is core voltage.

3-Power Supplies, 3.3V, 2.5V & 1.2V

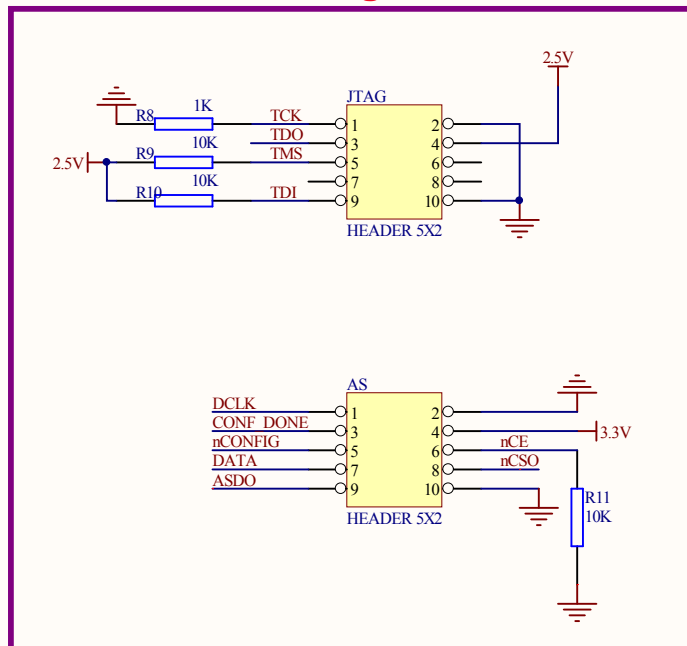
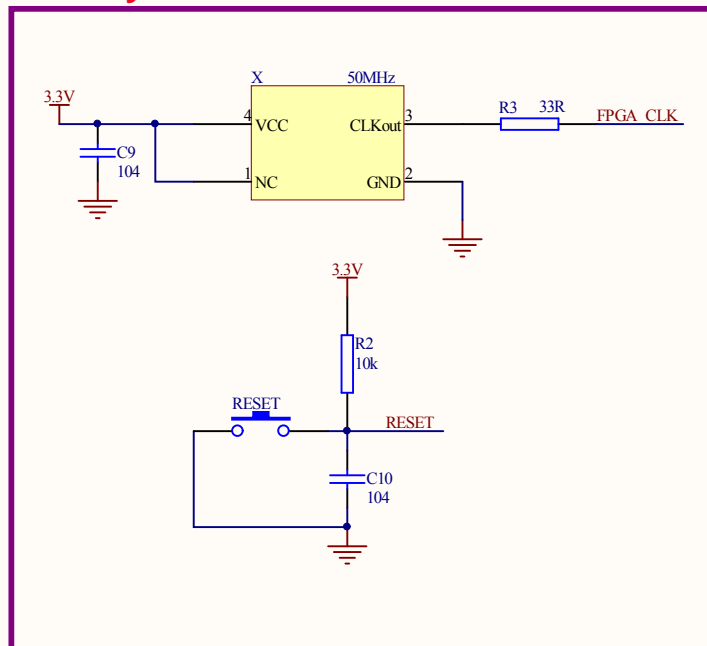




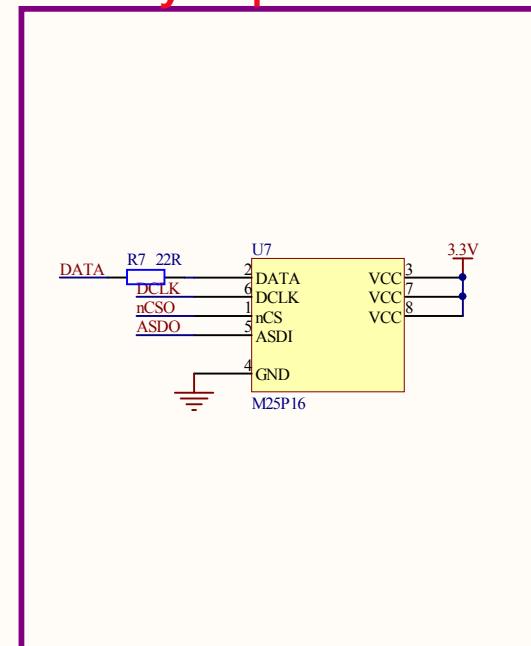
Schematic notes:
1. This page is the IO of the FPGA's BANK, the number marked on the pin, such as 98. It means the pin PIN98 of the FPGA chip
If there are two network labels on the same connection, it means that these two networks are connected together. The pure digital network labels on the schematic diagram represent the corresponding pins of the FPGA, and this rule applies to the entire diagram.



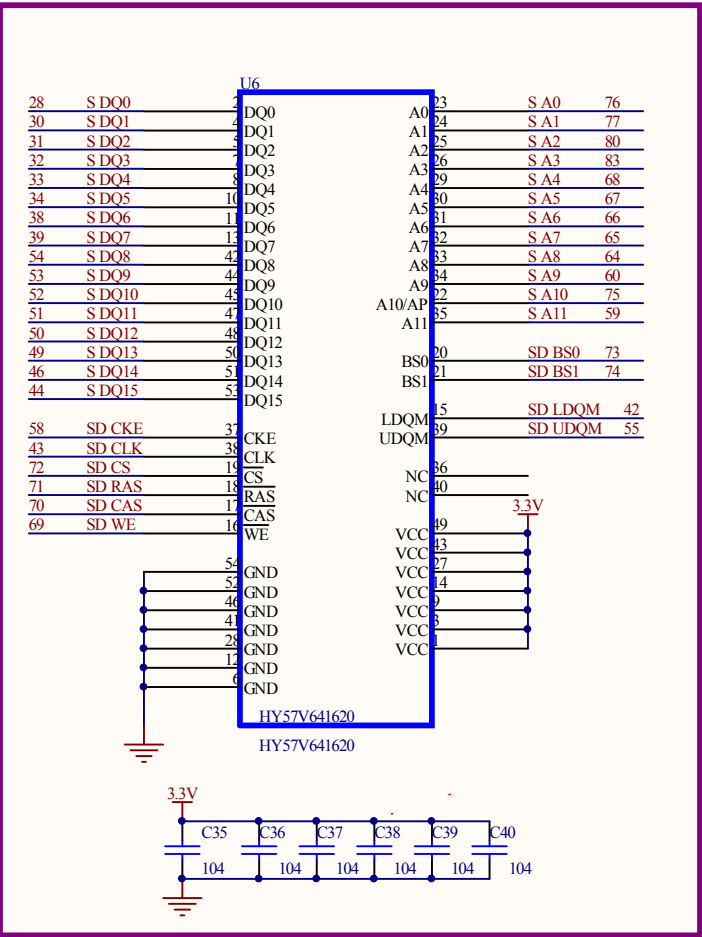
50M Crystal Oscillator and Reset Circuit JTAG and AS Configuration Circuit



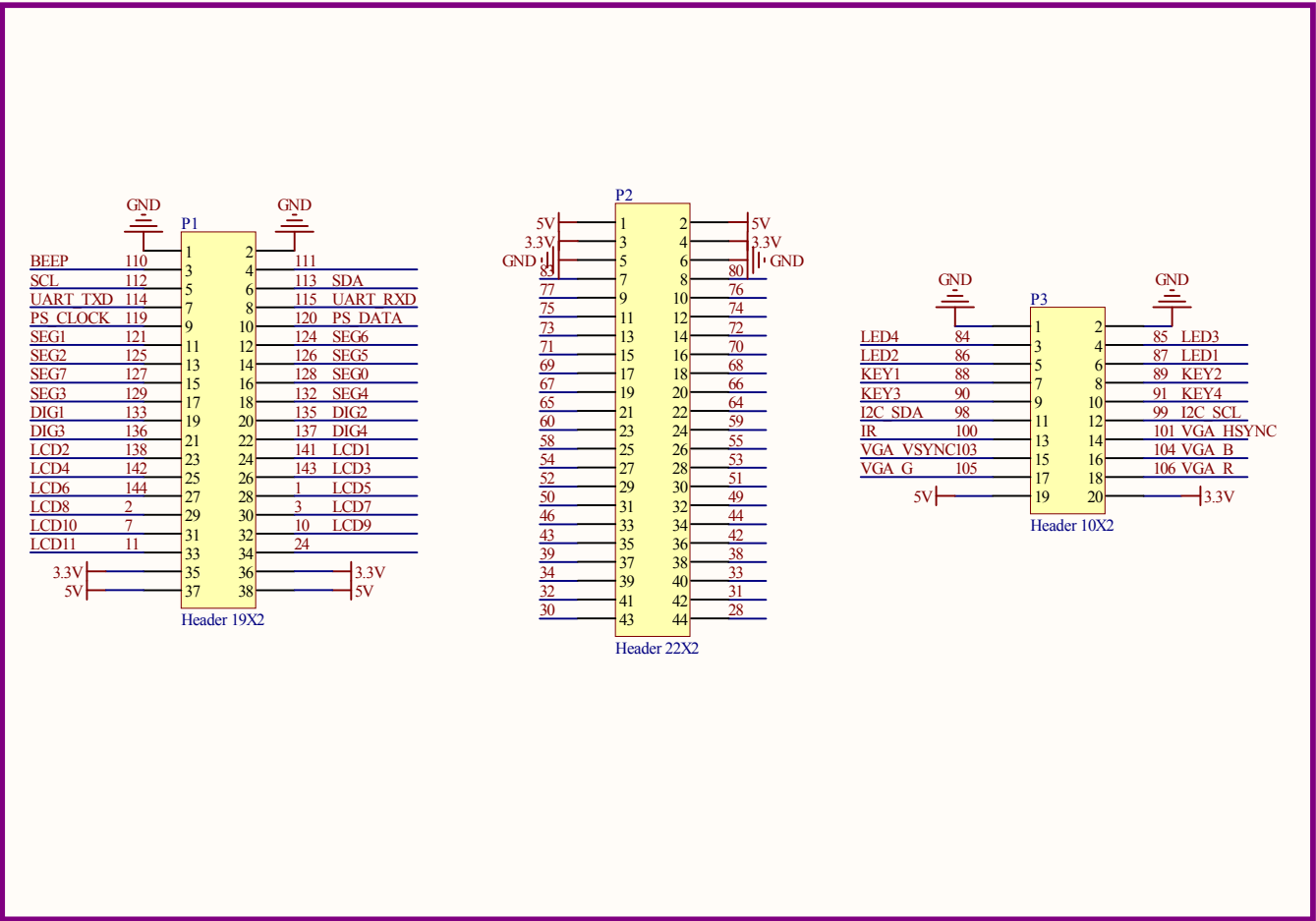
Memory Chip M25P16

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SDRAM Circuit

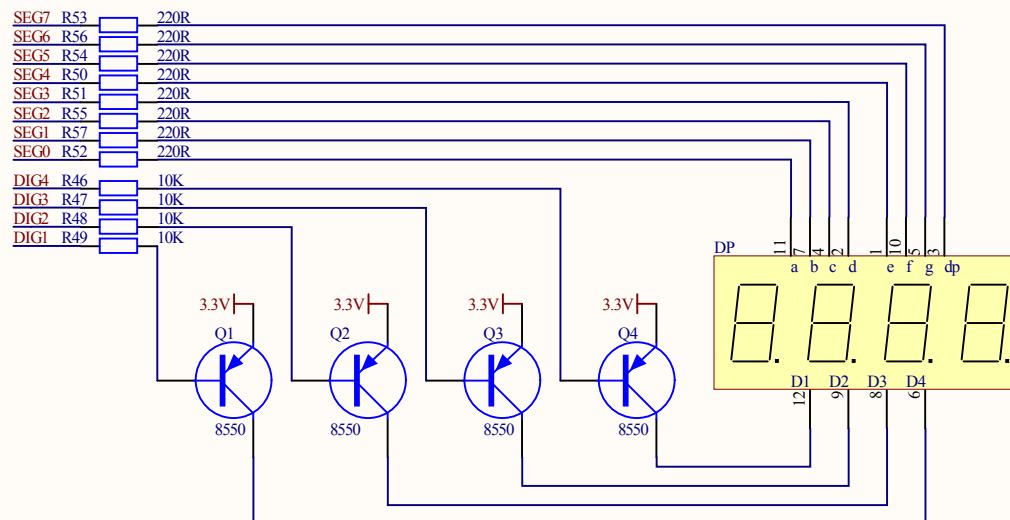


IO Outgoing Extensions

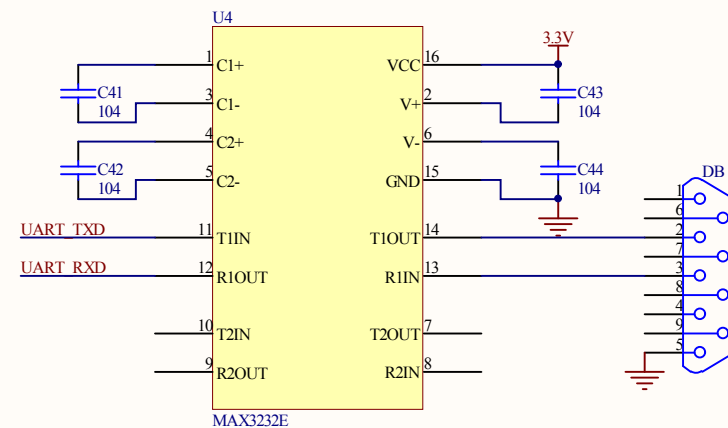


Schematic notes:
1. The number marked on the pin, such as 110. If the PIN110 pin of the FPGA chip has two network labels, it means that the two labels are connected together.
For example, BEEP110 indicates that the PIN110 pin of the FPGA is connected to this expansion pin and is connected to the BEEP network.

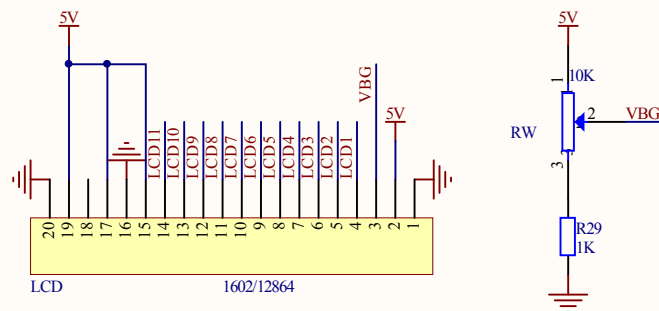
4 Digit - 7 Segment Display



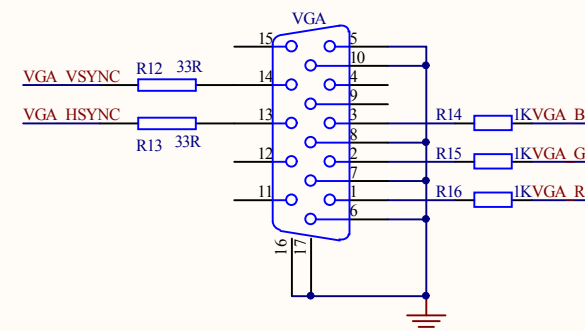
RS232 Serial Port



1602 12864 LCD

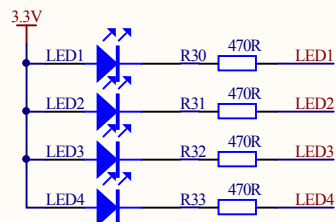


VGA Interface

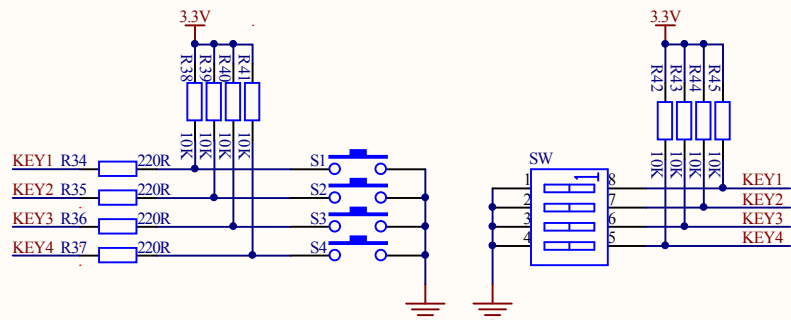


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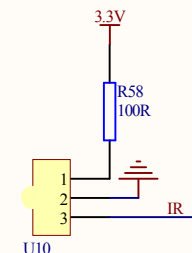
4 LEDs



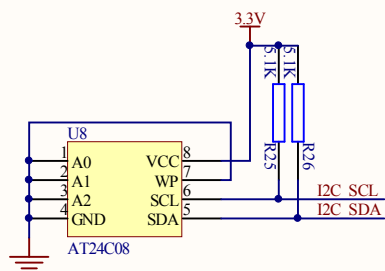
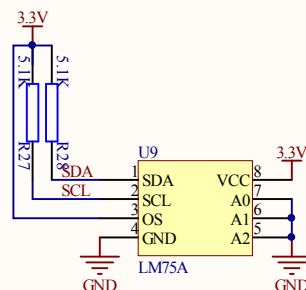
Independent key and DIP switch (IO multiplexing)



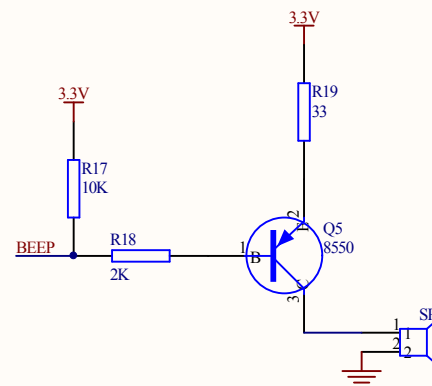
Infrared



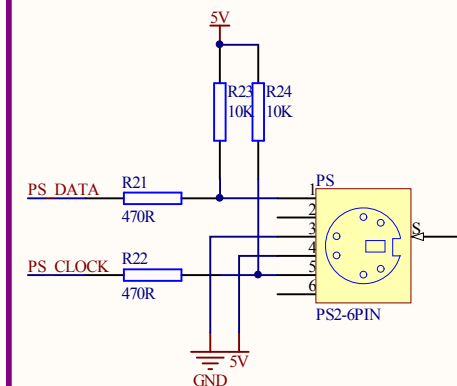
AT24C08

Temperature Sensor
LM75A

Buzzer



PS2 Interface

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