Overview

The IObundle CACHE is ....

Features

• iob-cache

Benefits

• iob-cache
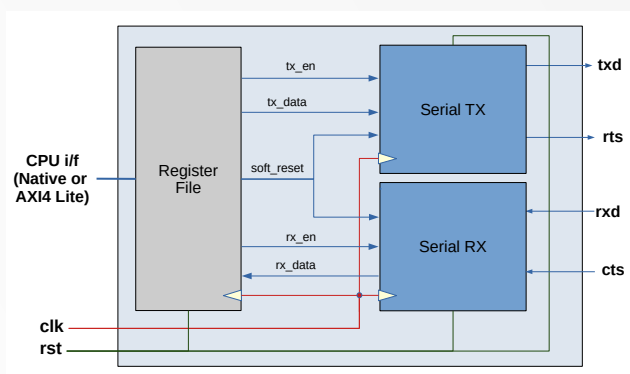
Deliverables

• iob-cache

Block Diagram

Figure 1: High-level block diagram.

Implementation Results

The following are FPGA implementation results for two FPGA families.

Resource	Used
LUTs	2168
Registers	1227
DSPs	0
BRAM	0

Resource	Used
ALM	826
FF	610
DSP	0
BRAM blocks	68
BRAM bits	72,768

Table 1: FPGA results for Kintex Ultrascale (left) and Cyclone V GT (right).