

IOB-CACHE

User Guide, 0.1 , Build 822d3a8



February 27, 2022





Contents

List of Tables

List of Figures



1 Introduction

The IObundle CACHE is

2 Symbol

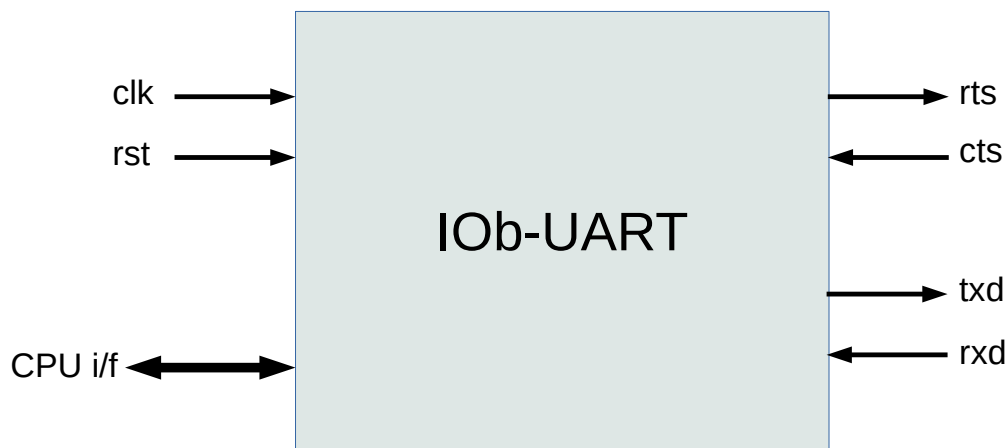


Figure 1: IP core symbol.

3 Features

- feature1
- feature2

4 Benefits

- Compact and easy to integrate hardware and software implementation
- Can fit many instances in low cost FPGAs and ASICs
- Low power consumption

5 Deliverables

- ASIC or FPGA synthesized netlist or Verilog source code, and respective synthesis and implementation scripts
- ASIC or FPGA verification environment by simulation and emulation
- Bare-metal software driver and example user software
- User documentation for easy system integration
- Example integration in IOb-SoC (optional)