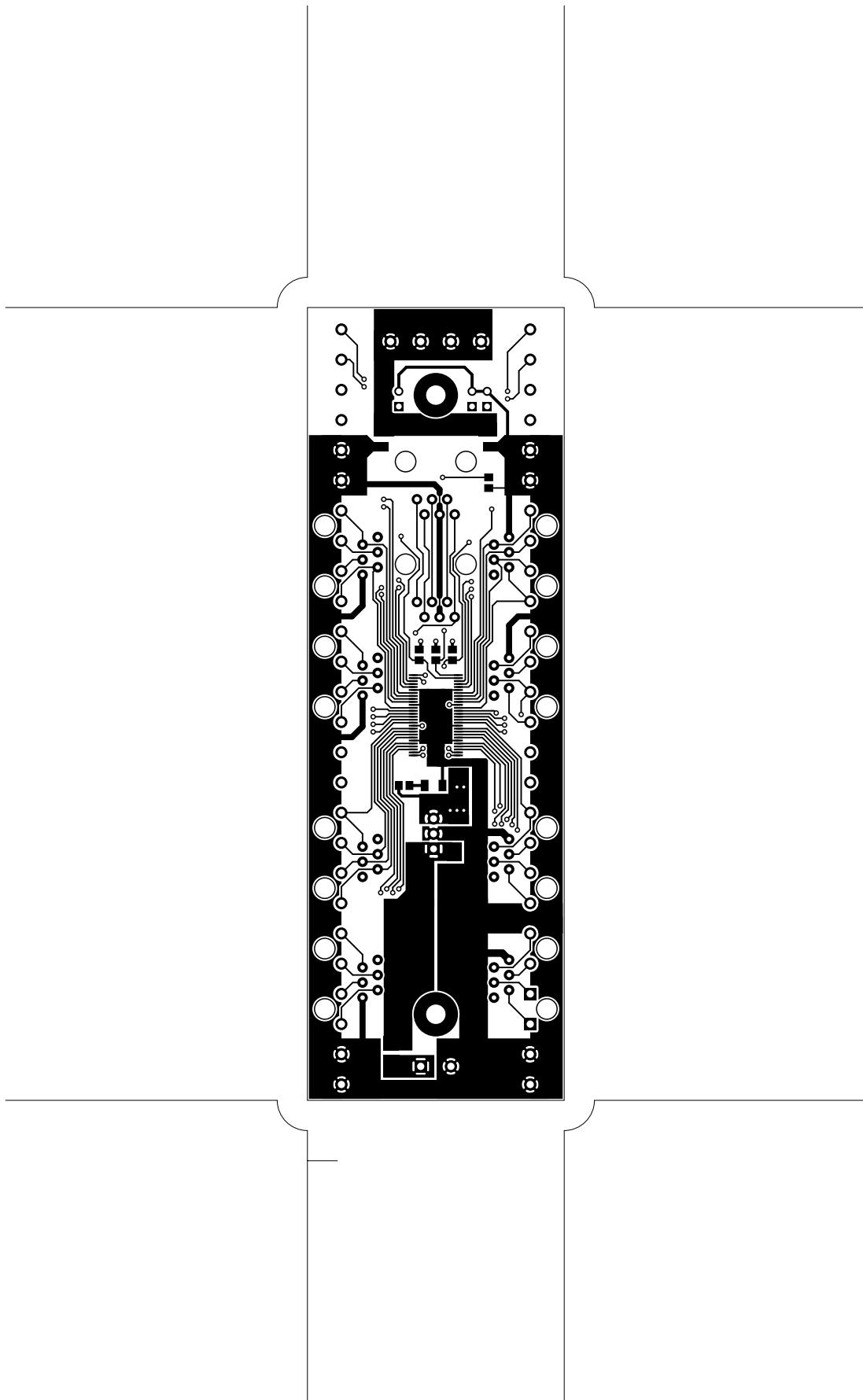
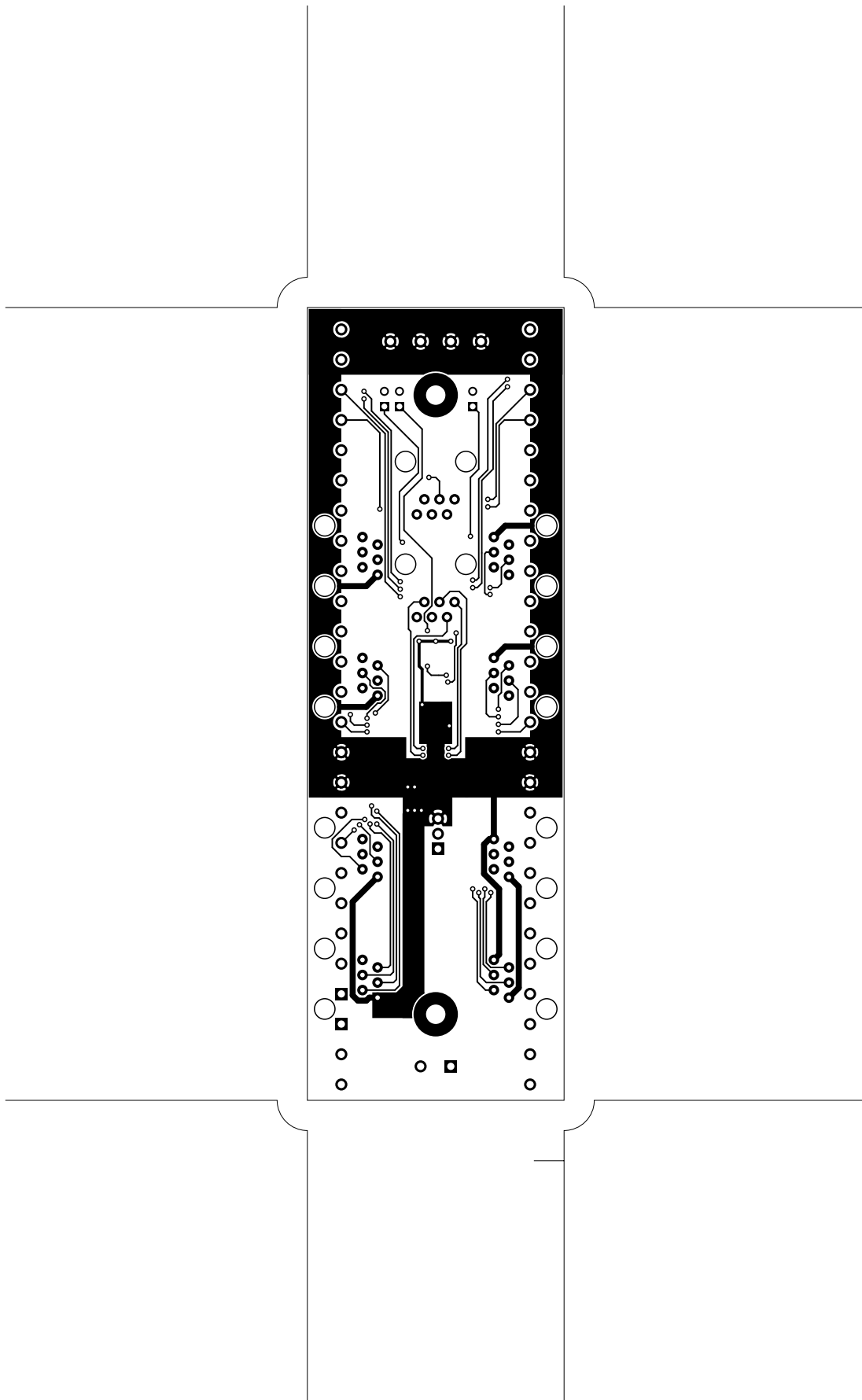


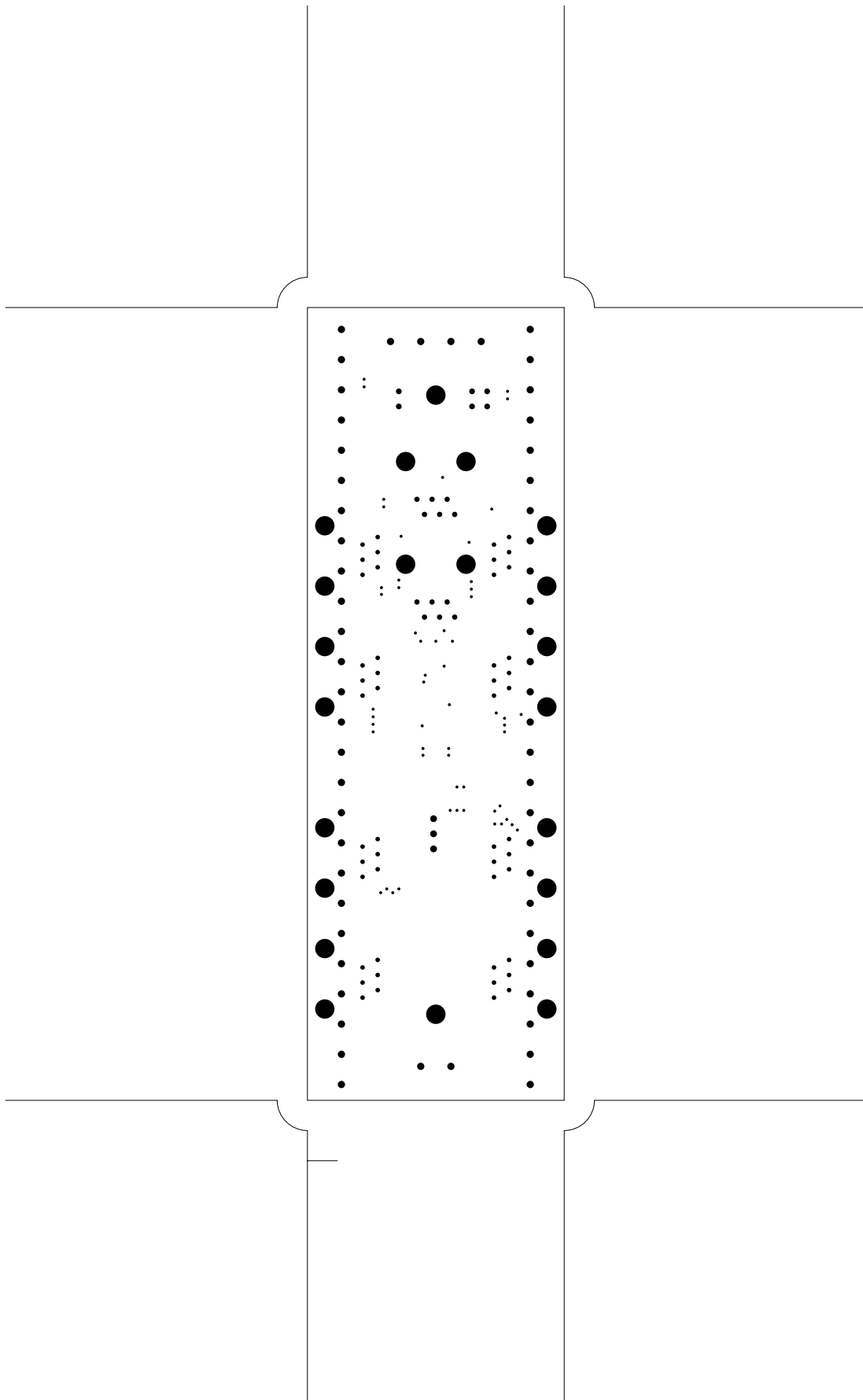
1. Table of Contents (This Page)  
Created on Wed Feb 18 10:35:01 2015

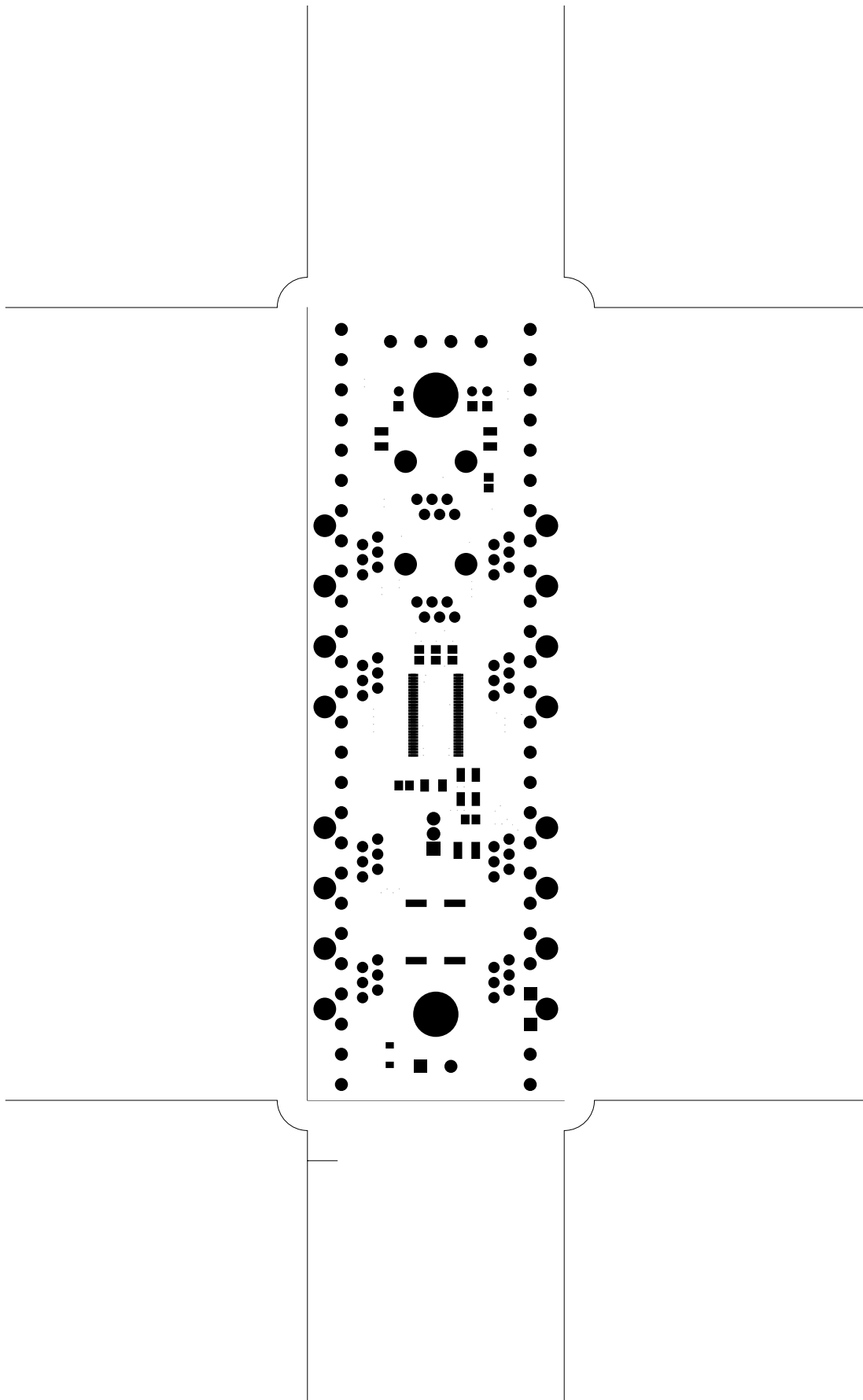
- 2. component
- 3. solder
- 4. plated-drill
- 5. componentmask
- 6. soldermask
- 7. topsilk
- 8. bottomsilk
- 9. toppaste
- 10. topassembly
- 11. bottomassembly
- 12. fab

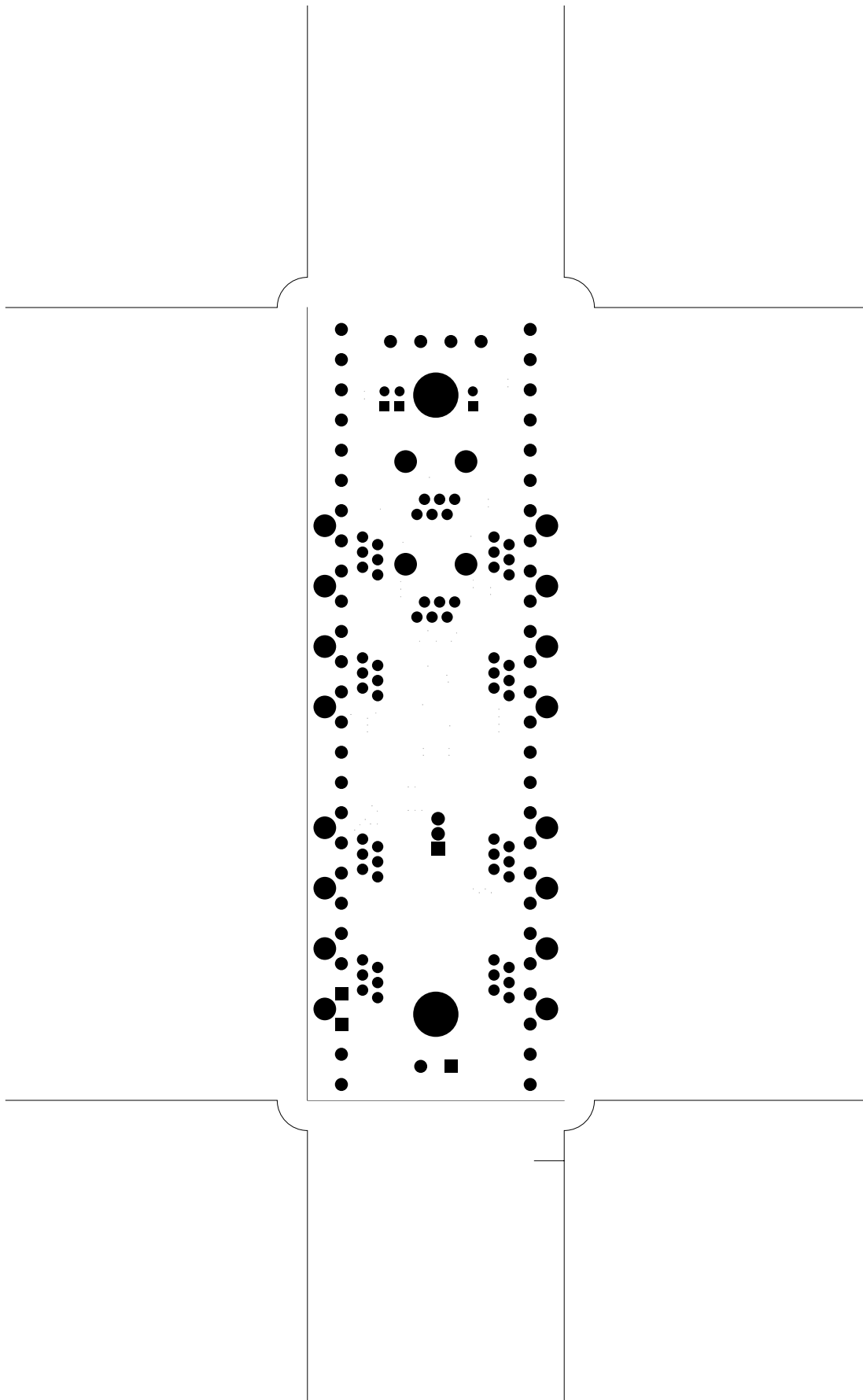




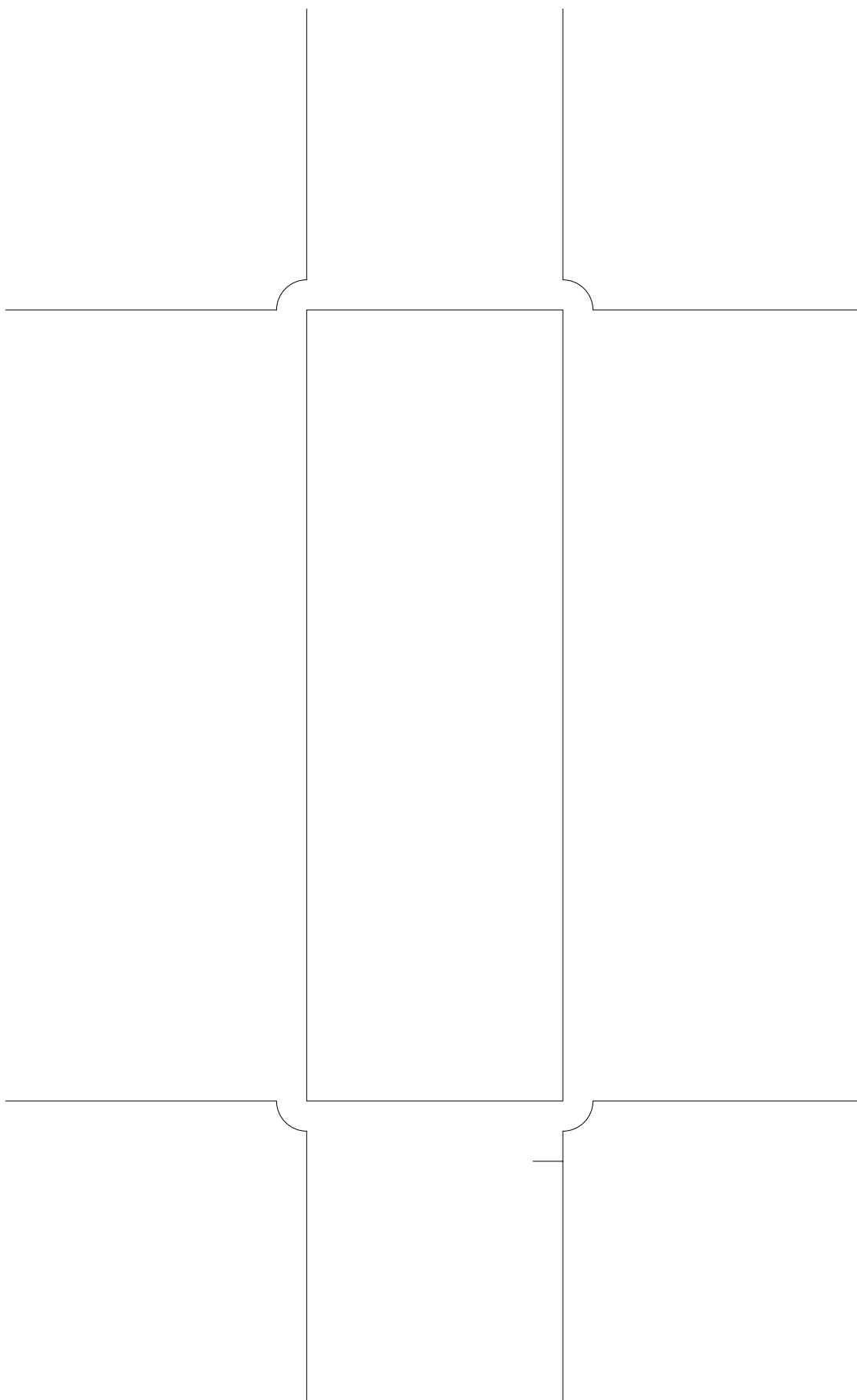
bottom (mirrored), scale = 1:1.000  
i2c-xio.pcb



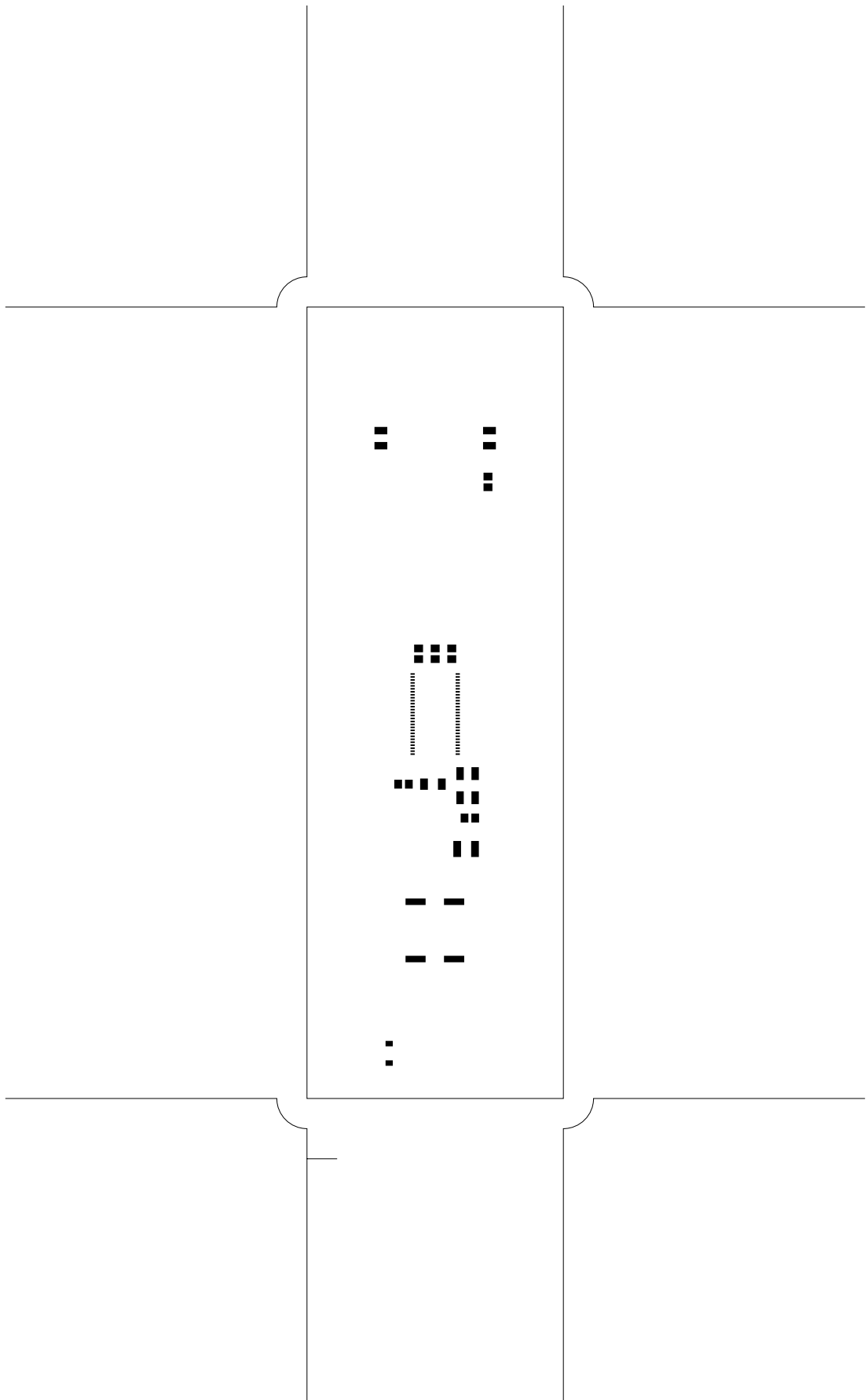


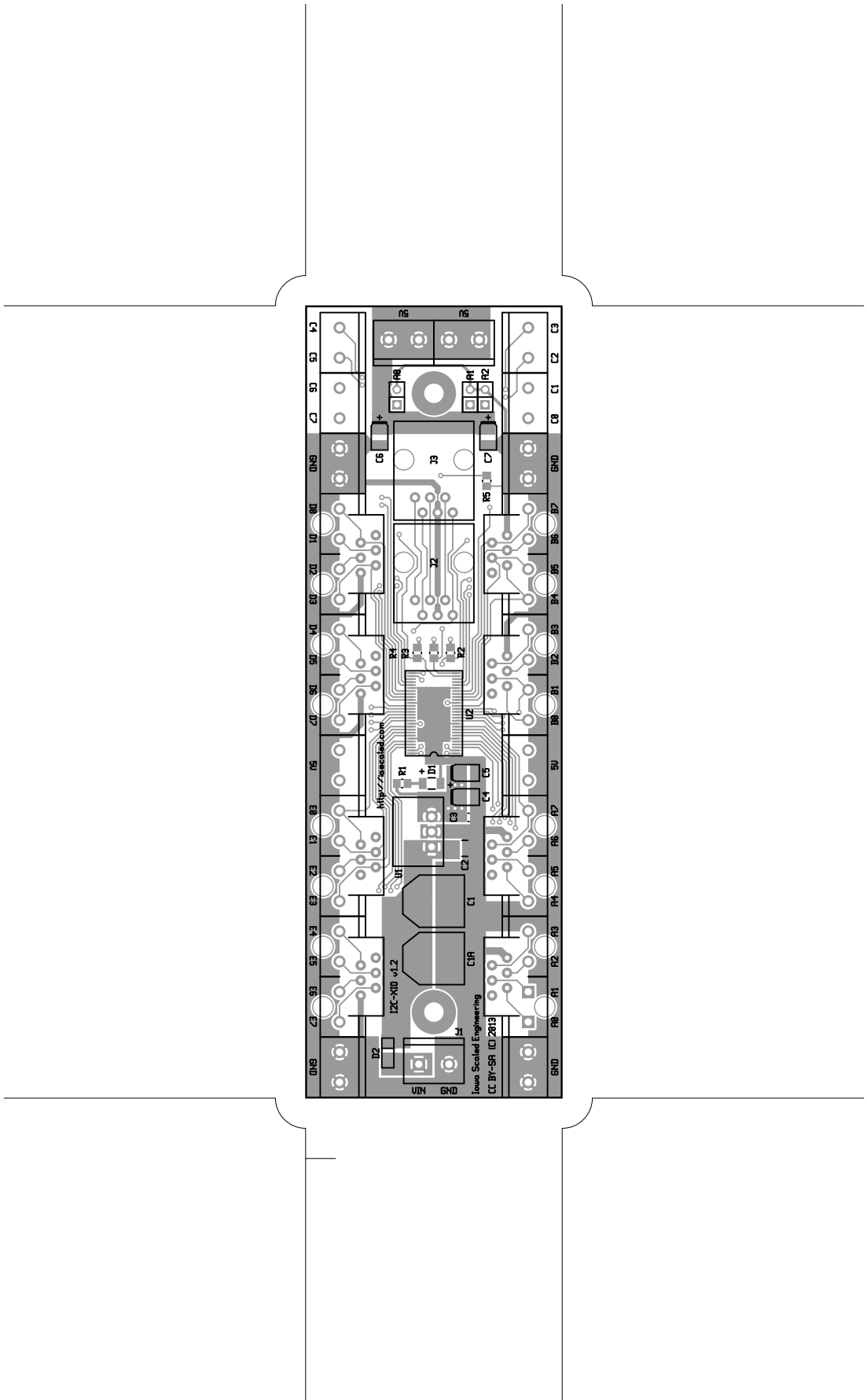


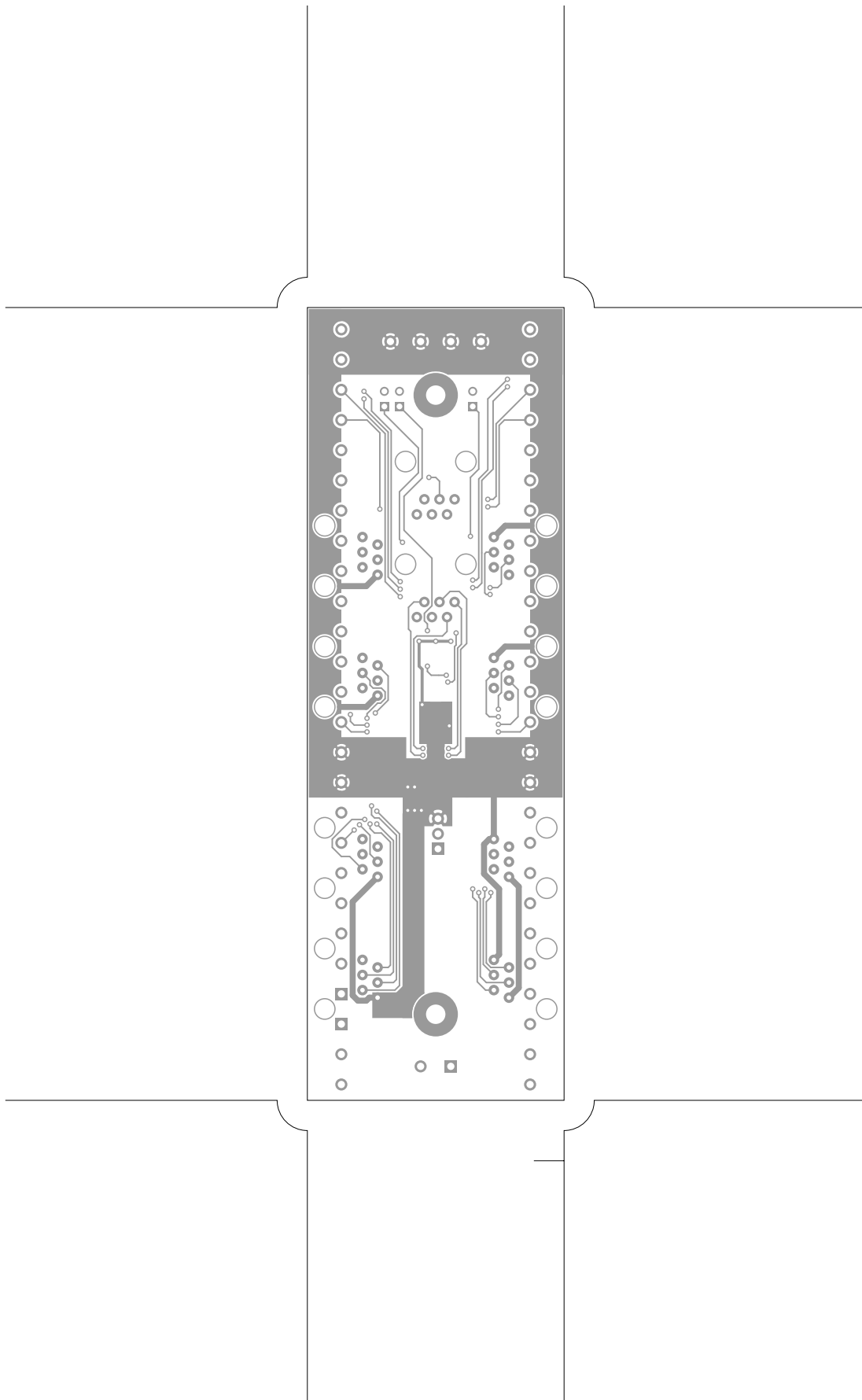








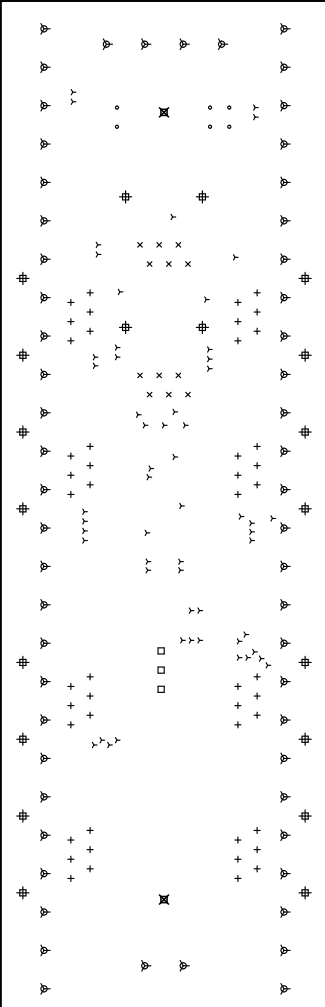




There are 8 different drill sizes used in this layout, 205 holes total

Symbol	Diam. (Inch)	Count	Plated?
γ	0.020	56	YES
+	0.030	48	YES
x	0.035	12	YES
•	0.038	6	YES
□	0.045	3	YES
▽	0.048	58	YES
⊕	0.128	20	YES
⊗	0.129	2	YES

Title: (unknown) - Fabrication Drawing  
Author: Nathan D. Holmes  
Date: Wed 18 Feb 2015 05:35:01 PM GMT UTC  
Maximum Dimensions: 5250.000000 mils wide, 1700.000000 mils high



Board outline is the centerline of this 8.000000 mil rectangle - 0.0 to 5250.000000,1700.000000 mils