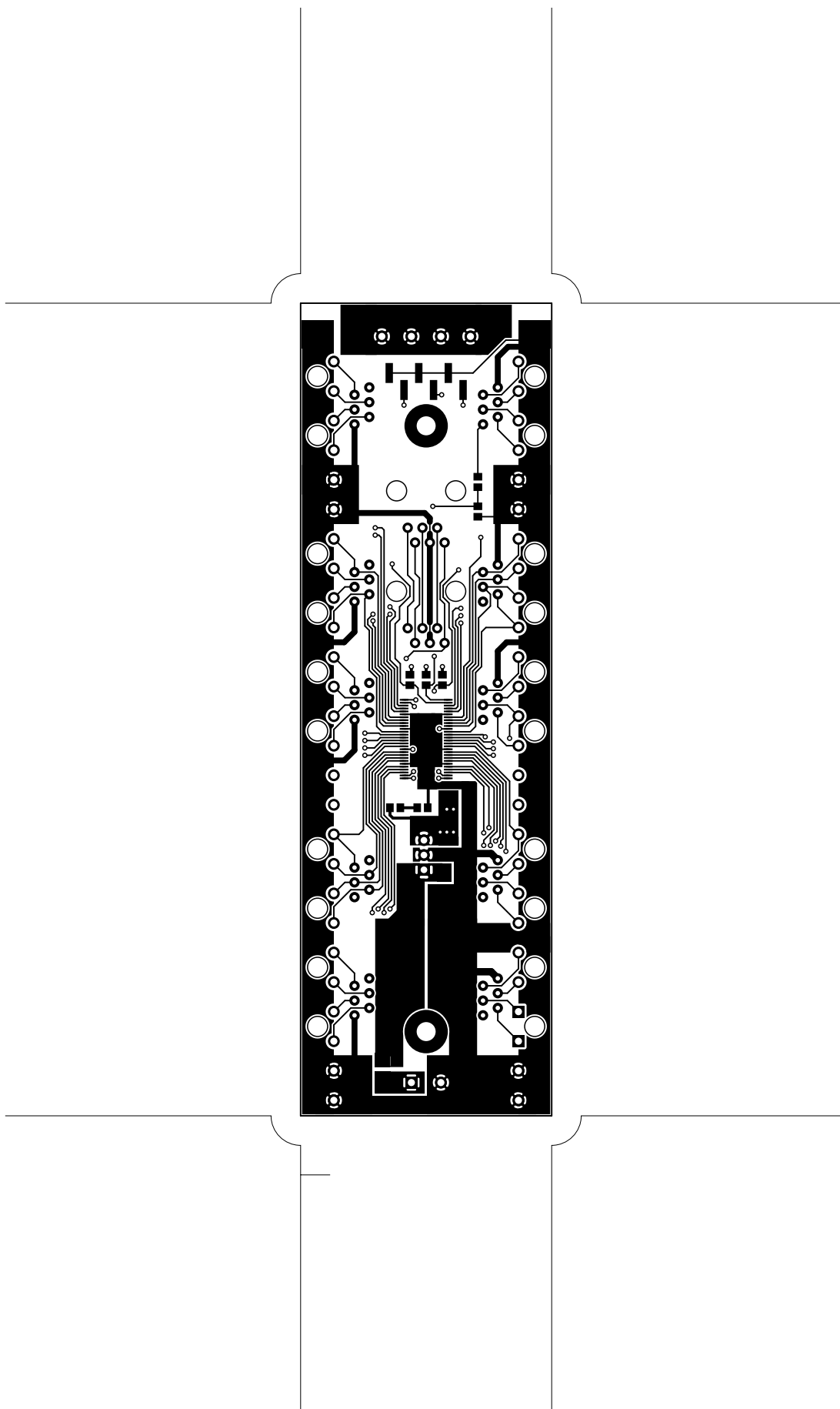
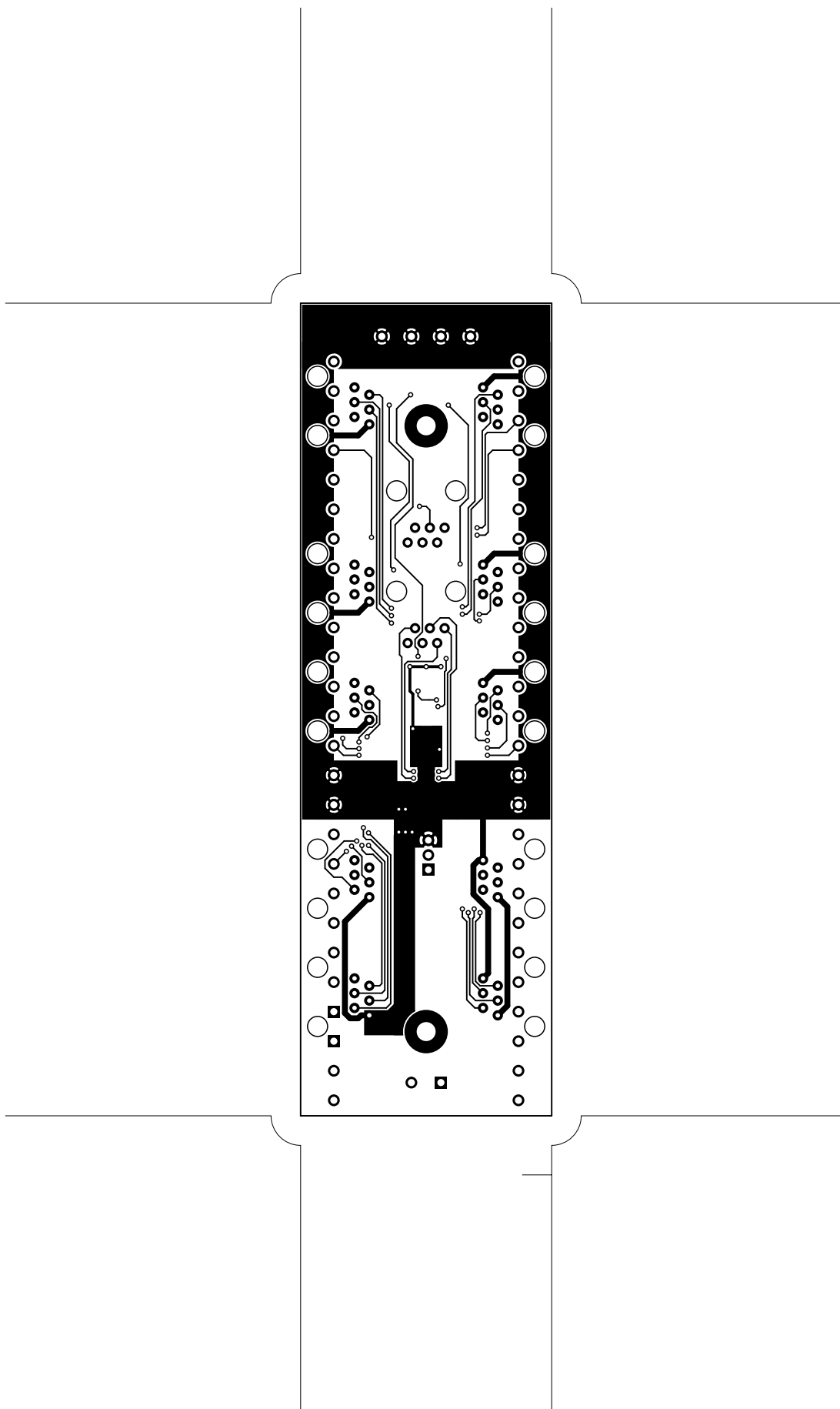
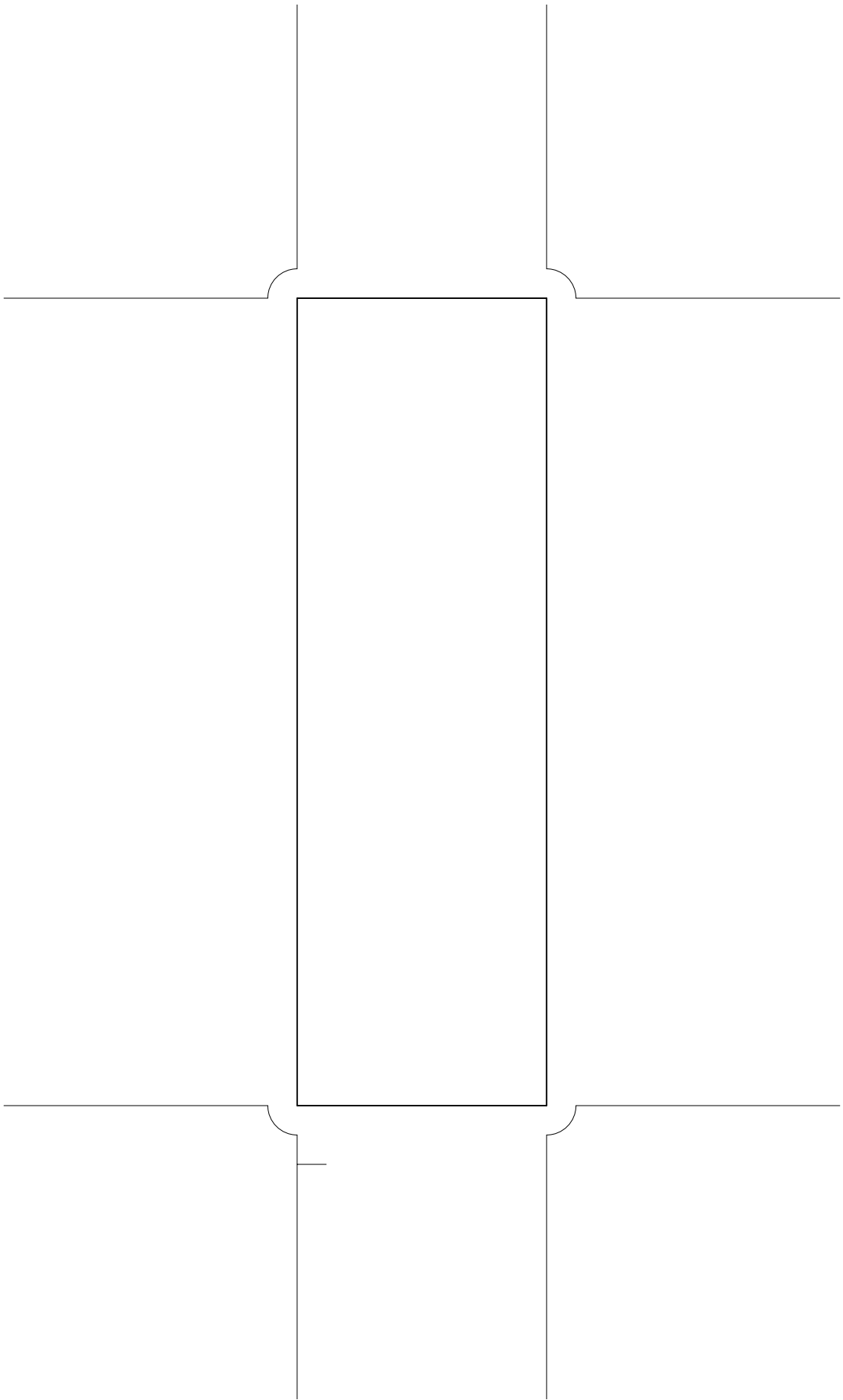


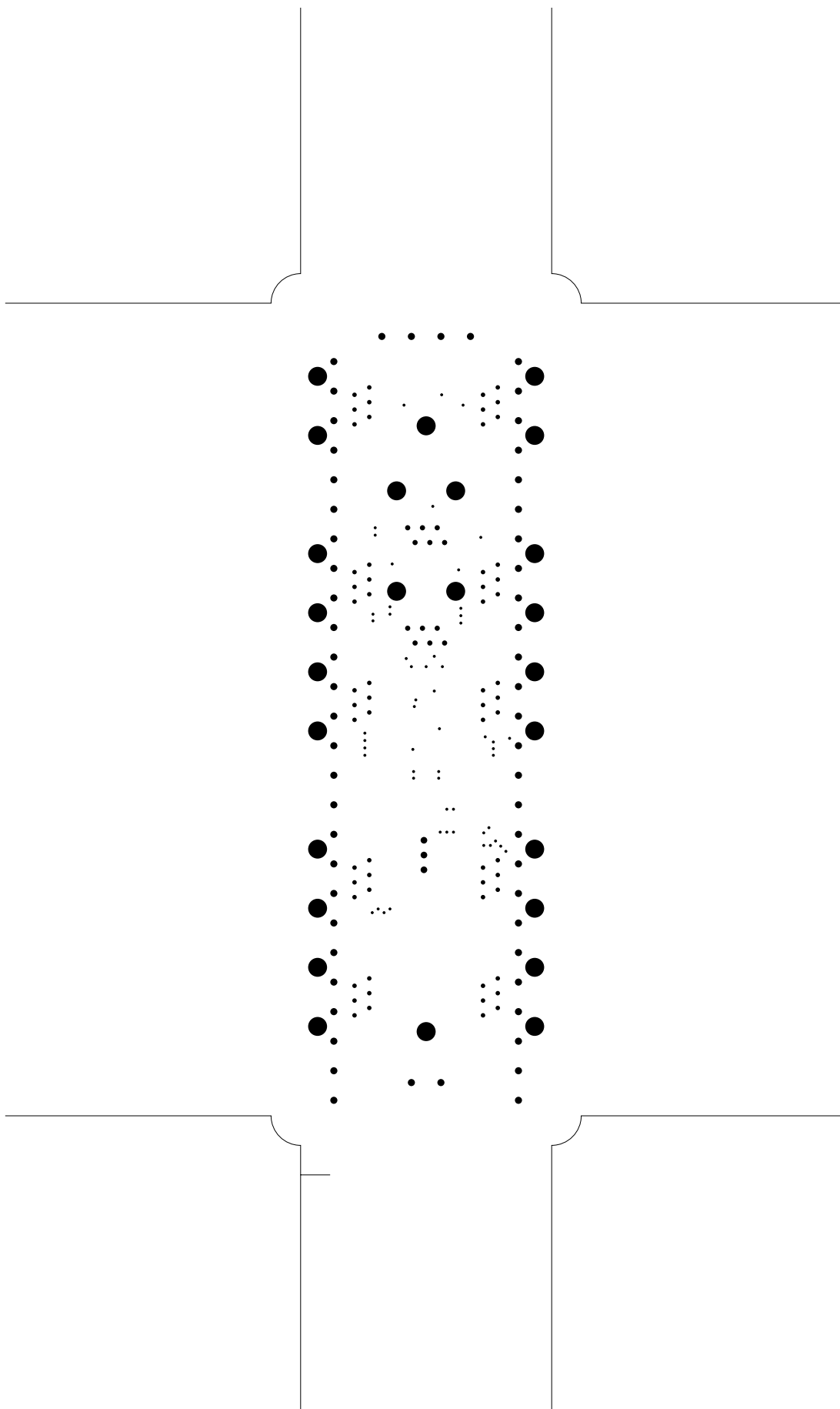
1. Table of Contents (This Page)  
Created on Sun Jan 28 18:09:37 2018

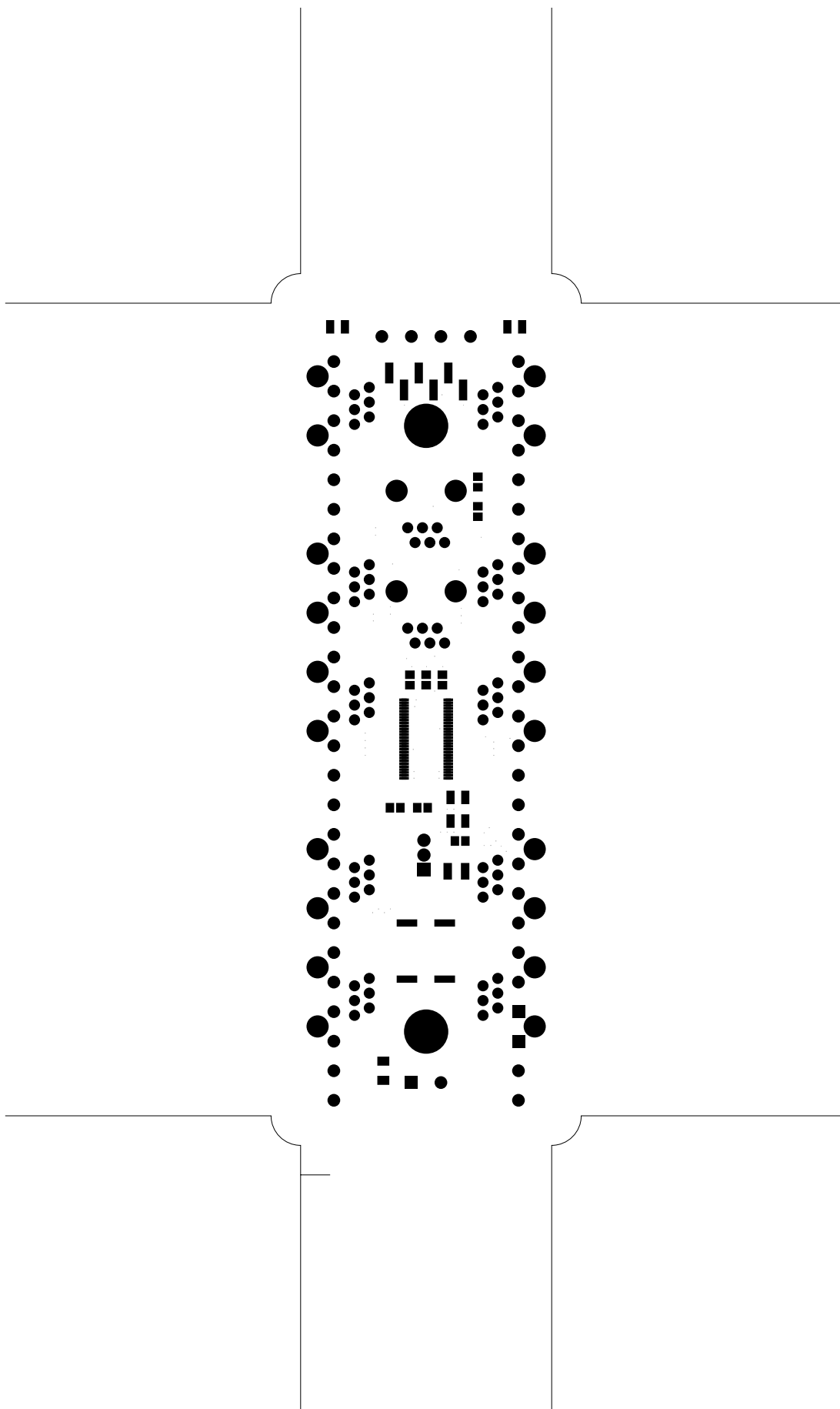
- 2. component
- 3. solder
- 4. outline
- 5. plated-drill
- 6. componentmask
- 7. soldermask
- 8. topsilk
- 9. bottomsilk
- 10. toppaste
- 11. topassembly
- 12. bottomassembly
- 13. fab

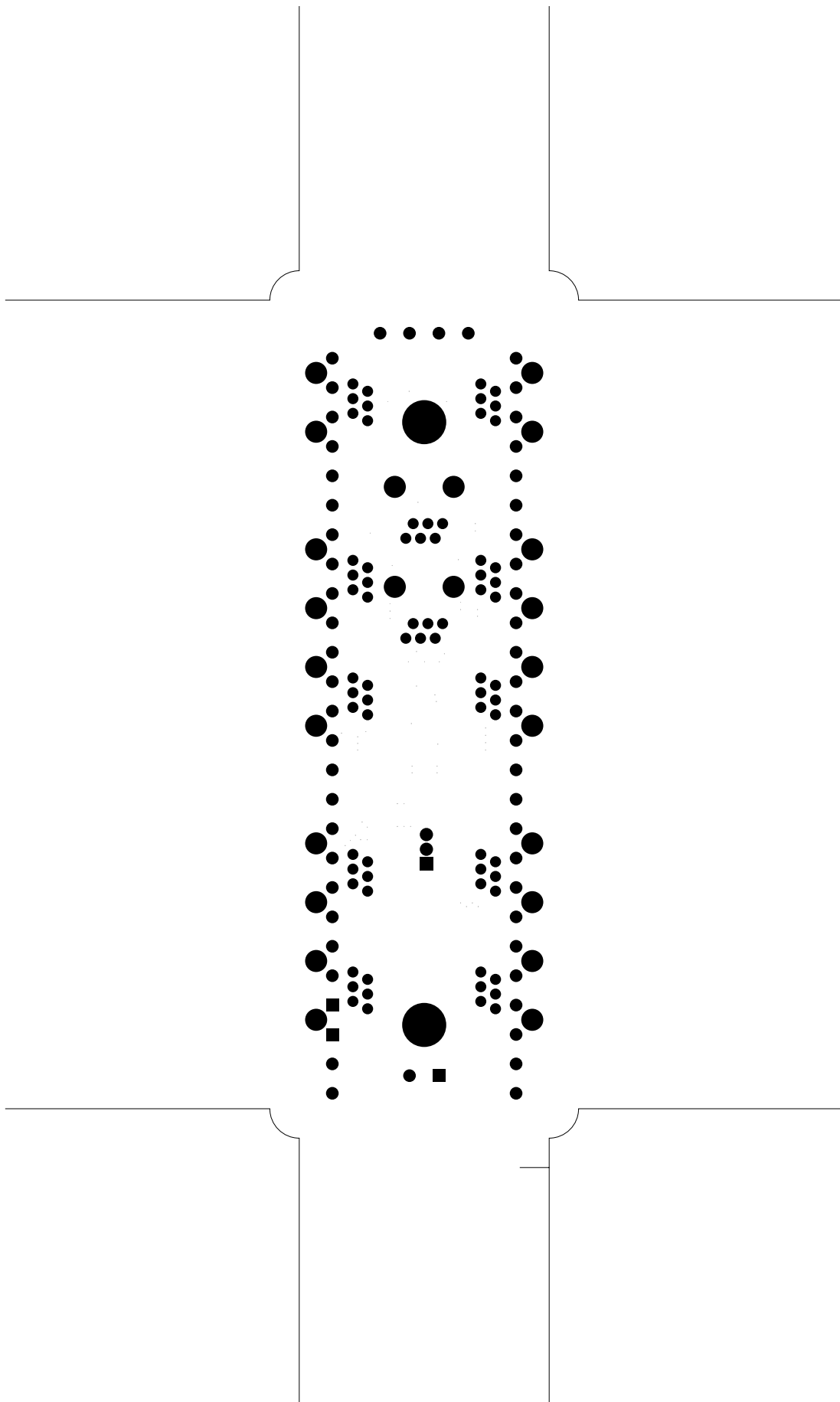








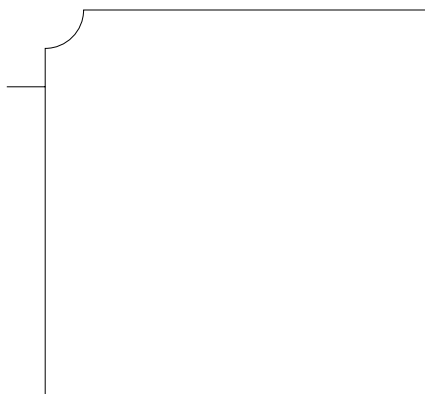
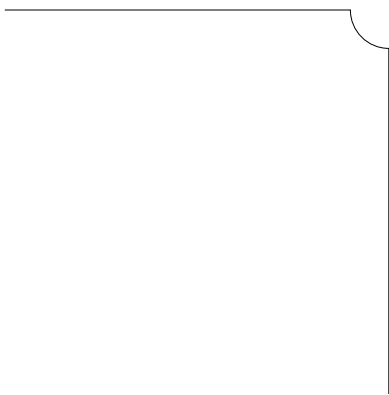
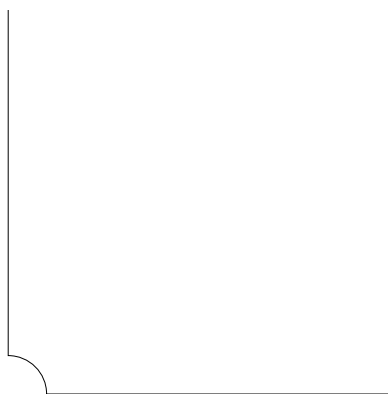
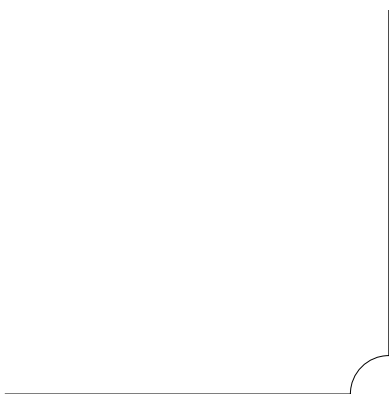


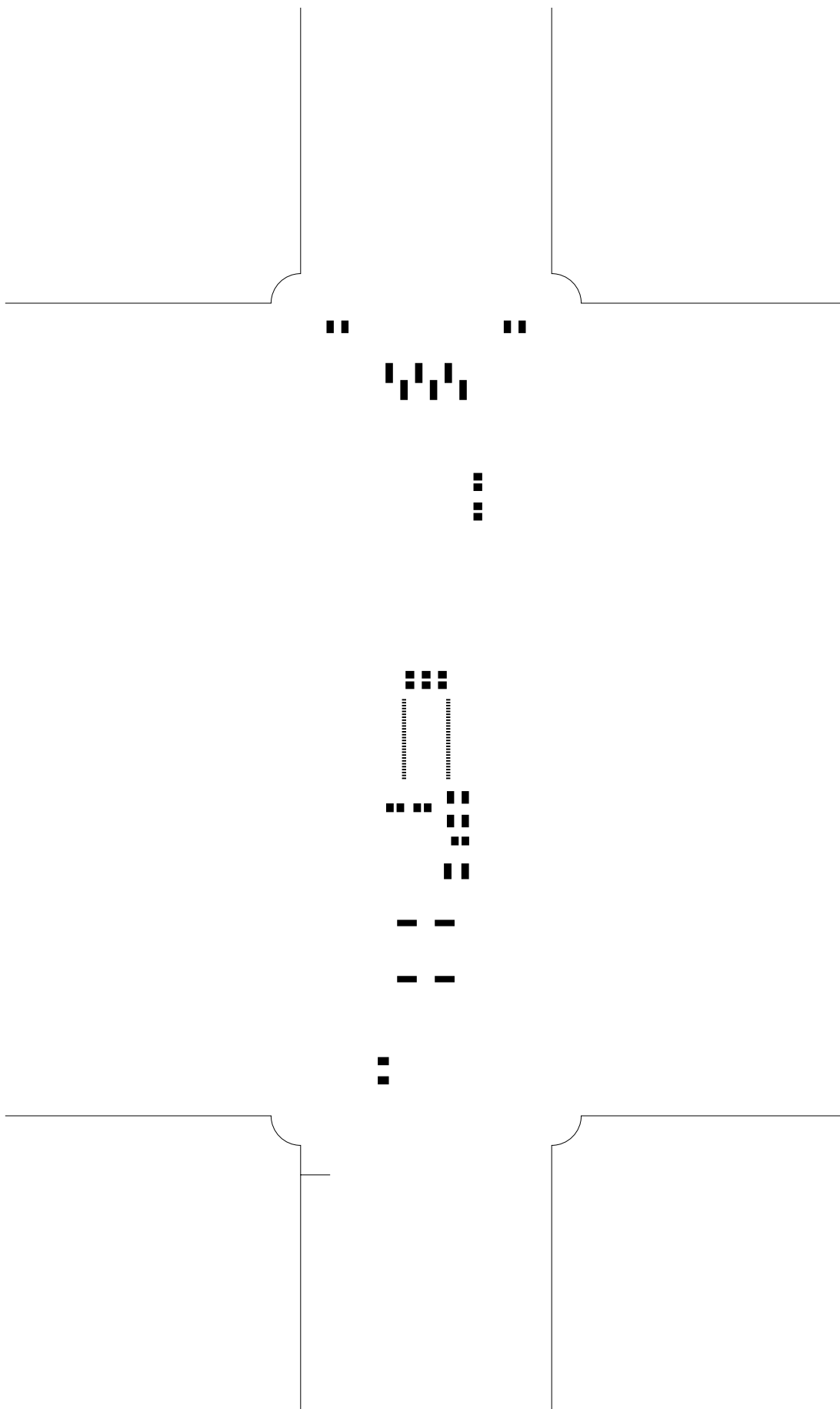


bottommask (mirrored), scale = 1:1.000  
i2c-xio.pcb

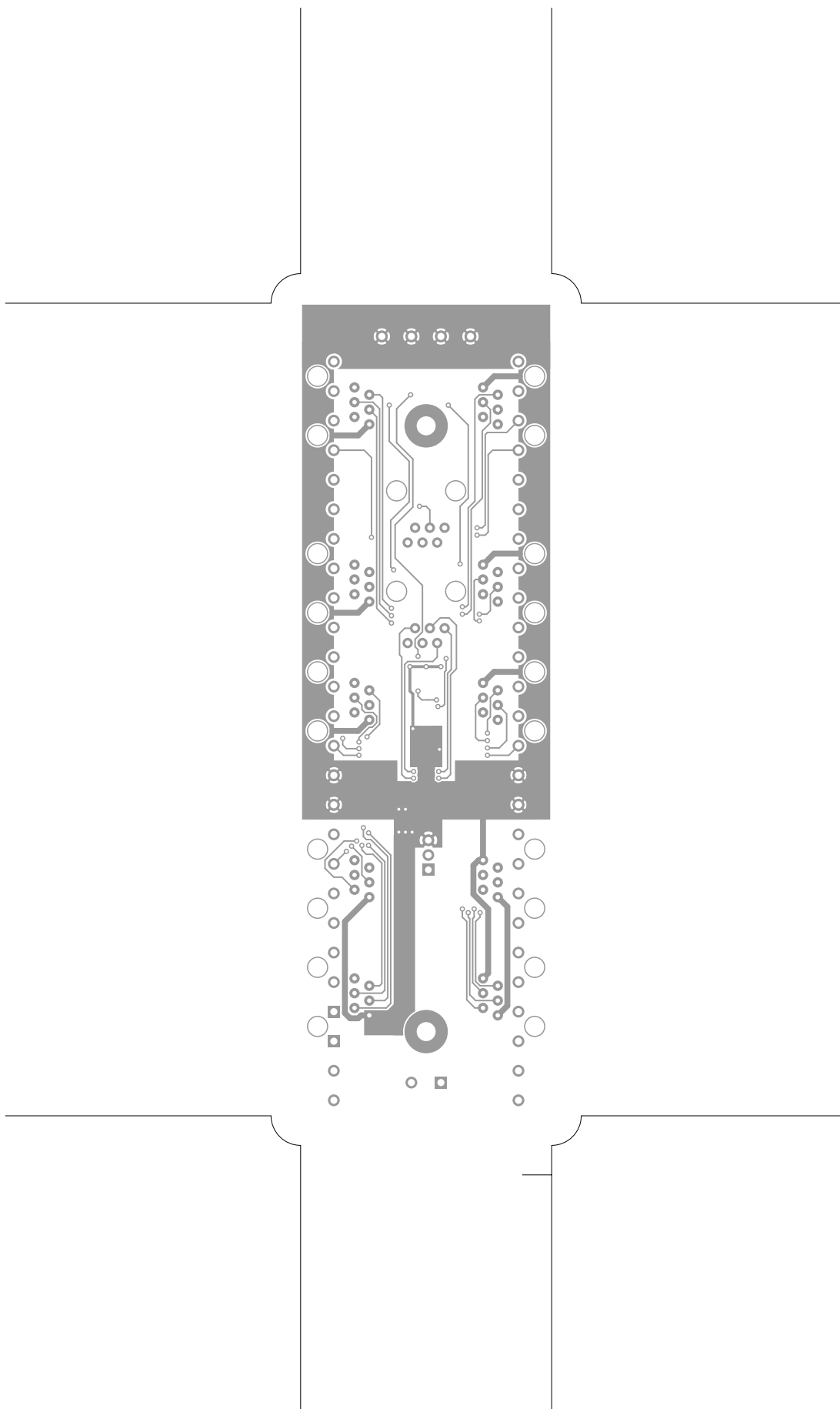










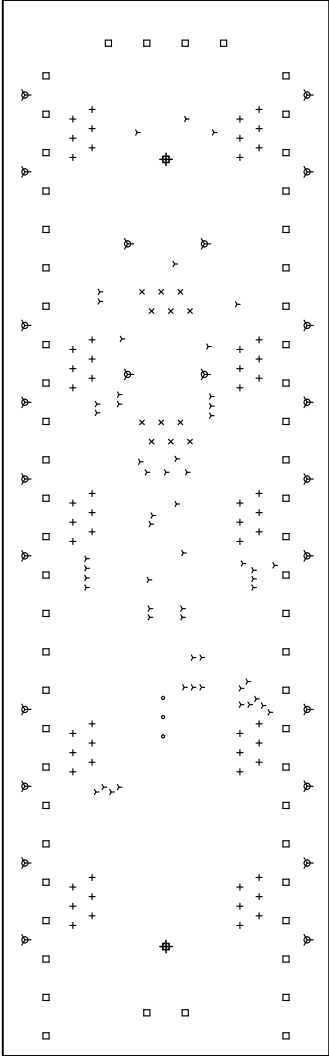


bottomassembly (mirrored), scale = 1:1.000  
i2c-xio.pcb

There are 7 different drill sizes used in this layout, 214 holes total

Symbol	Diam. (Inch)	Count	Plated?
Y	0.020	55	YES
+	0.030	60	YES
x	0.035	12	YES
o	0.045	3	YES
□	0.048	58	YES
▽	0.128	24	YES
#	0.129	2	YES

Title: (unknown) - Fabrication Drawing  
Author: Michael Petersen  
Date: Mon 29 Jan 2018 01:09:37 AM GMT UTC



Board outline is the centerline of this path