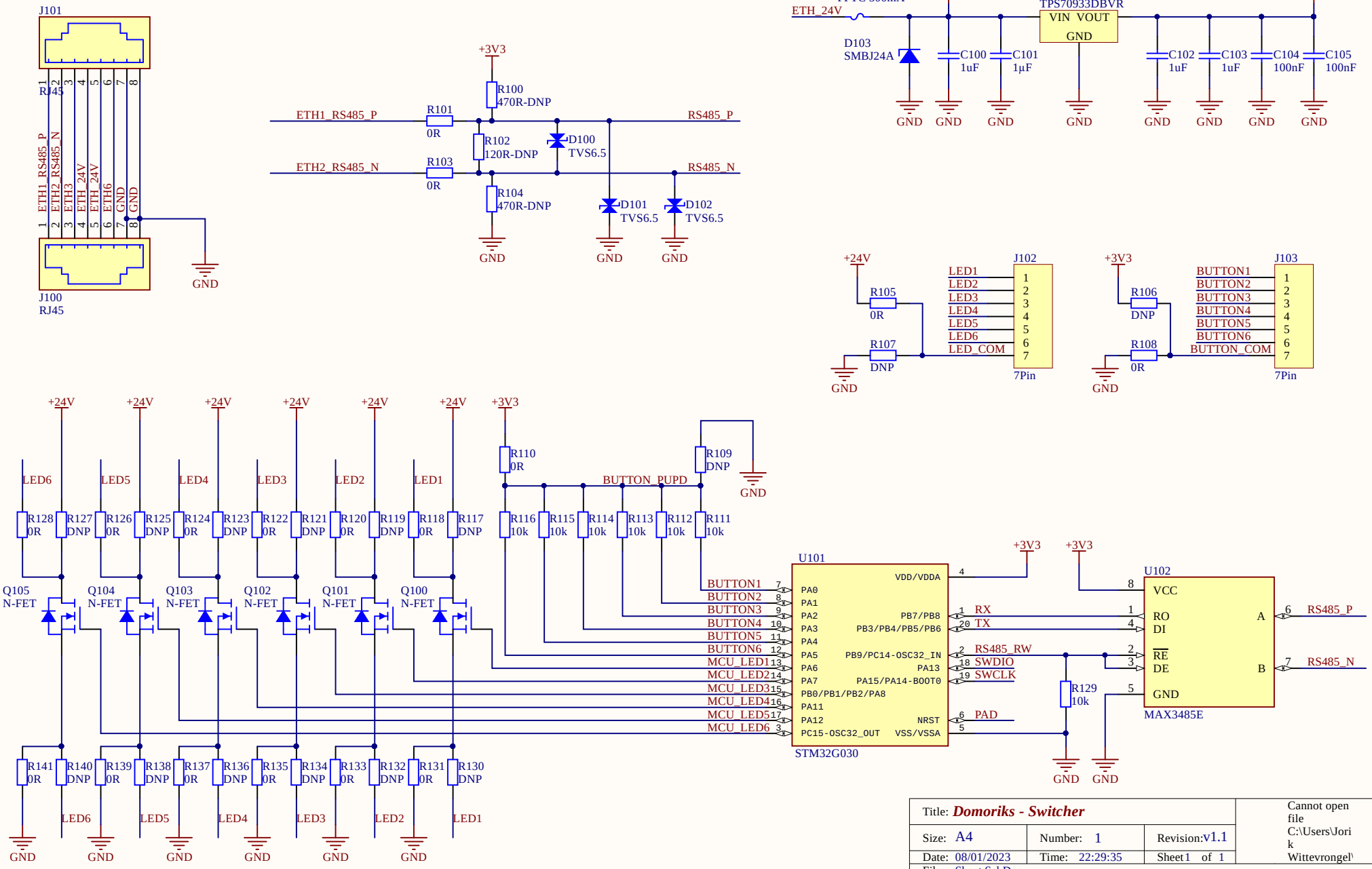
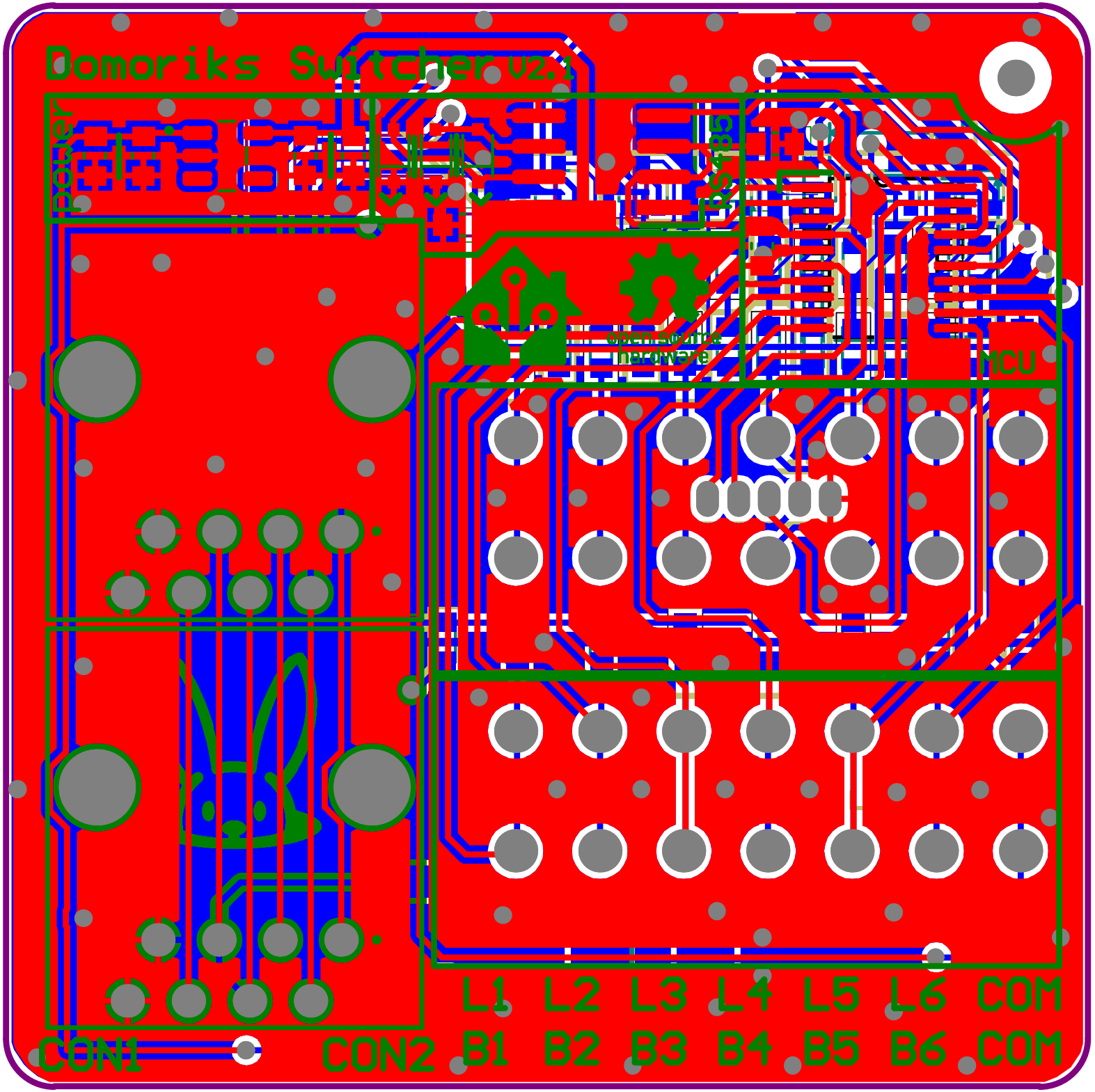

J101
RJ45
ETH1_RS485_P
ETH2_RS485_N
ETH3
ETH_24V
ETH_24V
ETH6
GND
GND
J100
RJ45
GND
+3V3
R100
470R-DNP
ETH1_RS485_P
R101
0R
RS485_P
R102
120R-DNP
D100
TVS6.5
ETH2_RS485_N
R103
0R
RS485_N
R104
470R-DNP
D101
TVS6.5
D102
TVS6.5
GND
GND
GND
F100
PPTC 500mA
ETH_24V
+24V
U100
TPS70933DBVR
VIN VOUT
GND
+3V3
D103
SMBJ24A
C100
1uF
C101
1µF
C102
1uF
C103
1uF
C104
100nF
C105
100nF
GND
GND
GND
GND
GND
GND
GND
GND
+24V
R105
0R
R107
DNP
LED1
LED2
LED3
LED4
LED5
LED6
LED_COM
J102
7Pin
GND
+3V3
R106
DNP
R108
0R
BUTTON1
BUTTON2
BUTTON3
BUTTON4
BUTTON5
BUTTON6
BUTTON_COM
J103
7Pin
GND
+24V
+24V
+24V
+24V
+24V
+24V
+3V3
LED6
LED5
LED4
LED3
LED2
LED1
R110
0R
R109
DNP
BUTTON_PUPD
GND
R128 0R
R127 DNP
R126 0R
R125 DNP
R124 0R
R123 DNP
R122 0R
R121 DNP
R120 0R
R119 DNP
R118 0R
R117 DNP
R116 10k
R115 10k
R114 10k
R113 10k
R112 10k
R111 10k
Q105
N-FET
Q104
N-FET
Q103
N-FET
Q102
N-FET
Q101
N-FET
Q100
N-FET
BUTTON1
BUTTON2
BUTTON3
BUTTON4
BUTTON5
BUTTON6
MCU_LED1
MCU_LED2
MCU_LED3
MCU_LED4
MCU_LED5
MCU_LED6
U101
VDD/VDDA
PA0
PA1
PA2
PB7/PB8
PA3
PB3/PB4/PB5/PB6
PA4
PA5
PB9/PC14-OSC32_IN
PA6
PA13
PA7
PA15/PA14-BOOT0
PB0/PB1/PB2/PA8
PA11
PA12
NRST
PC15-OSC32_OUT
VSS/VSSA
STM32G030
+3V3
+3V3
RX
TX
RS485_RW
SWDIO
SWCLK
PAD
R129
10k
U102
VCC
RO
DI
RE
DE
GND
A
B
MAX3485E
RS485_P
RS485_N
GND
GND
R141 0R
R140 DNP
R139 0R
R138 DNP
R137 0R
R136 DNP
R135 0R
R134 DNP
R133 0R
R132 DNP
R131 0R
R130 DNP
LED6
LED5
LED4
LED3
LED2
LED1
GND
GND
GND
GND
GND
GND
Title: Domoriks - Switcher
Size: A4
Number: 1
Revision: v1.1
Date: 08/01/2023
Time: 22:29:35
Sheet 1 of 1
File: Sheet.SchDoc
Cannot open file C:\Users\Jorik Wittevrongel\


Domoriks Switcher v2.1
open source
hardware
MCU
L1 L2 L3 L4 L5 L6 COM
CON1
CON2
B1 B2 B3 B4 B5 B6 COM

Design Rules Verification Report

Filename : C:\Users\Jorik\Desktop\Domoriks\PCB-Design\Domoriks_Switch\CircuitStudio\PCB1.CSPcbDoc

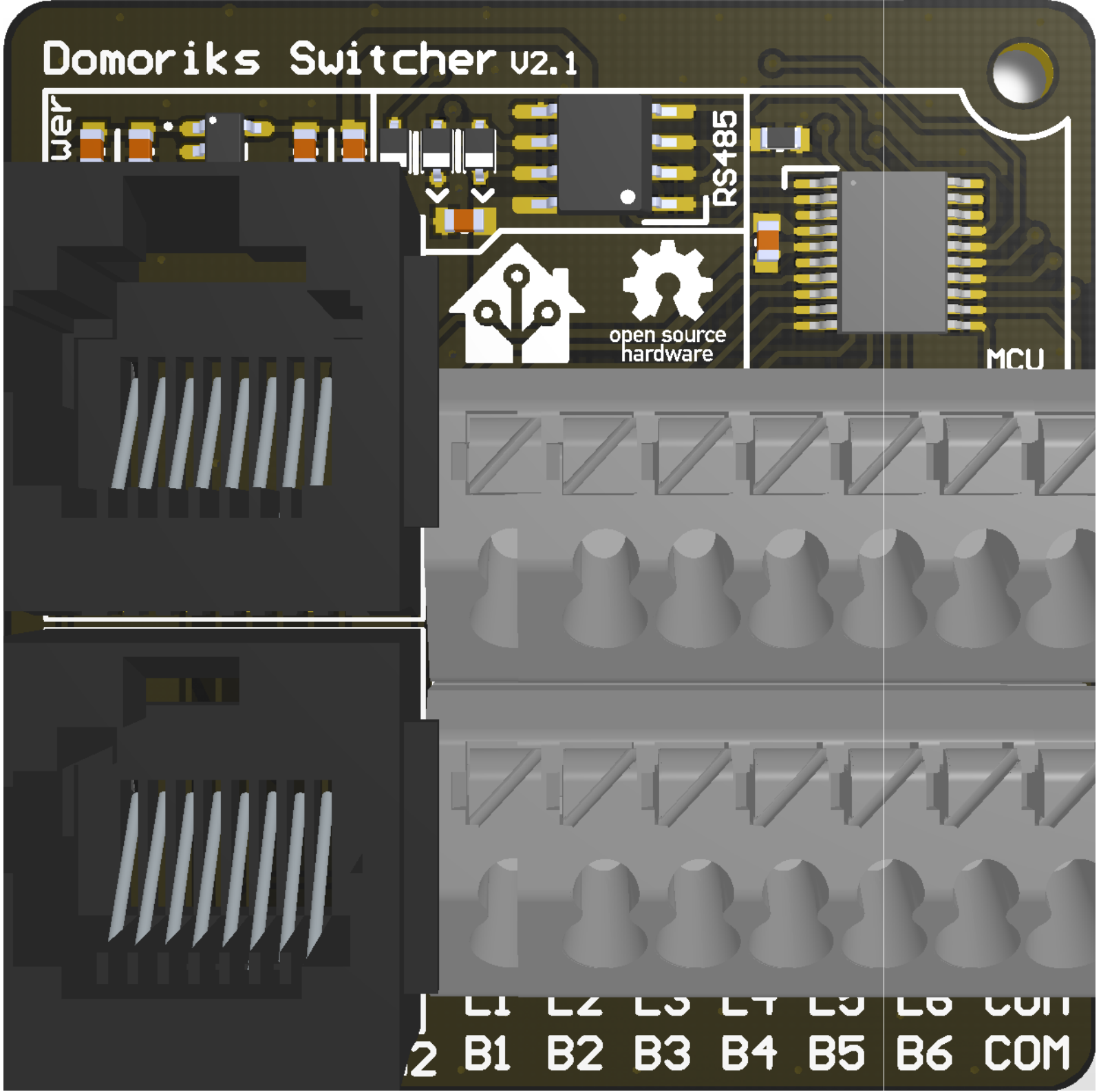
Warnings 0
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Clearance Constraint (Gap=0.25mm) (All),(All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.508mm) (Conductor Width=0.254mm)	0
Width Constraint (Min=0.15mm) (Max=1mm) (Preferred=0.254mm) (All)	0
Height Constraint (Min=0mm) (Max=25.4mm) (Prefered=12.7mm) (All)	0
Hole Size Constraint (Min=0.025mm) (Max=4mm) (All)	0
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0.05mm) (All),(All)	0
Silk To Solder Mask (Clearance=0.005mm) (IsPad),(All)	0
Silk to Silk (Clearance=0.254mm) (Disabled)(All),(All)	0
Silk primitive without silk layer	0
Unpoured Polygon (Allow unpoured: False)	0
Total	0

Electrical Rules Check Report

Class	Document	Message
		Successful Compile for Domoriks_Switch.PrjPcb


Domoriks Switcher V2.1
RS485
open source
hardware
MCU
B1 B2 B3 B4 B5 B6 COM