## Assignment-8

Due:  $4^{th}$  May, 2021

In the assignment, you have to generate intermediate code corresponding to a conditional expression. For the present assignment, you may assume that only if statements are present in the input code. The intermediate code is to be written in LLVM IR language. A sample input and output codes for the intermediate code generator is given below.

```
Input (input):
```

int main()

```
beg
    int a, b;
    a = 6;
    if(a > 5)
        b = 7;
    end
    return 0;
end
   Output (out.ll):
   define i32 @main() #0 {
 %1 = alloca i32, align 4
 %2 = alloca i32, align 4
 %3 = alloca i32, align 4
 store i32 0, i32* %1, align 4
 store i32 6, i32* %2, align 4
 %4 = load i32, i32* %2, align 4
 %5 = icmp sgt i32 %4, 5
 br i1 %5, label %6, label %7
                                       ; preds = %0
; <label>:6:
  store i32 7, i32* %3, align 4
 br label %7
; <label>:7:
                                       ; preds = %6, %0
 ret i32 0
```

Note that the sample output does not have header and footer for a typical llvm IR (.ll) file. The intermediate code has to be generated using backpatching method. As this method requires to insert labels to the branch statements, the entire file content has to be written to a file buffer. The buffer will be written to the file at the end. It is expected that the generated file out.ll can be subjected to the llvm interpreter lli without any error. More precisely, the command  $lli\ out.ll$ 

will not output any error. You may allow types integer and double and the relational operator '>' only for generating intermediate code.